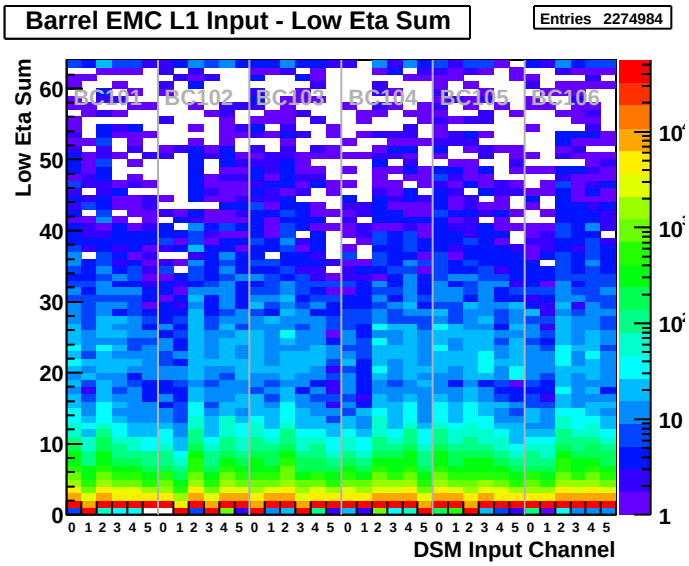
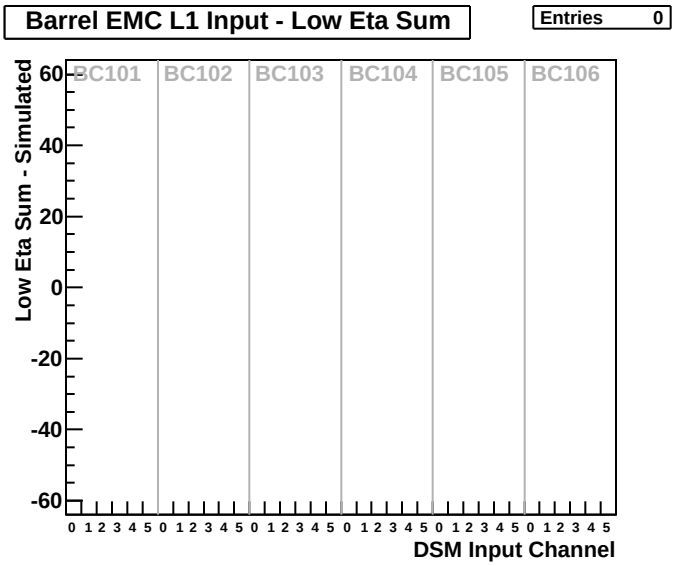
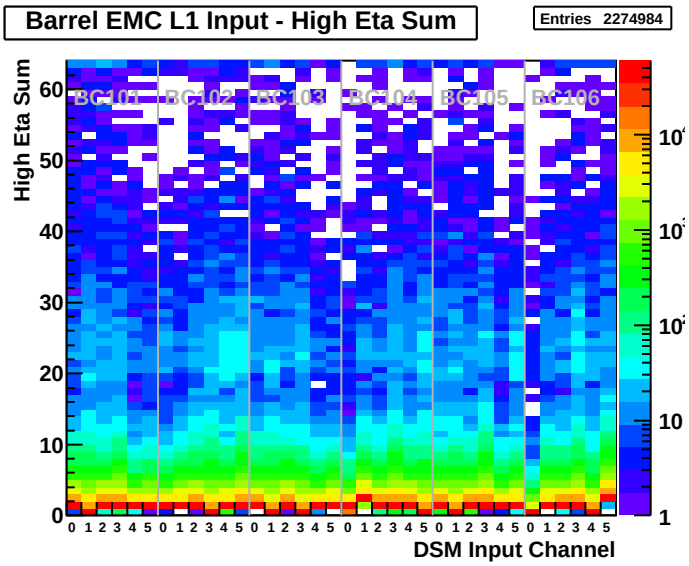
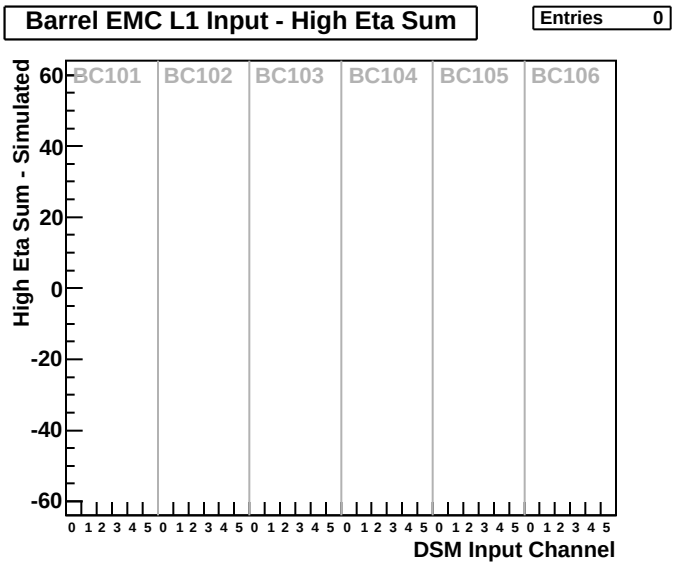
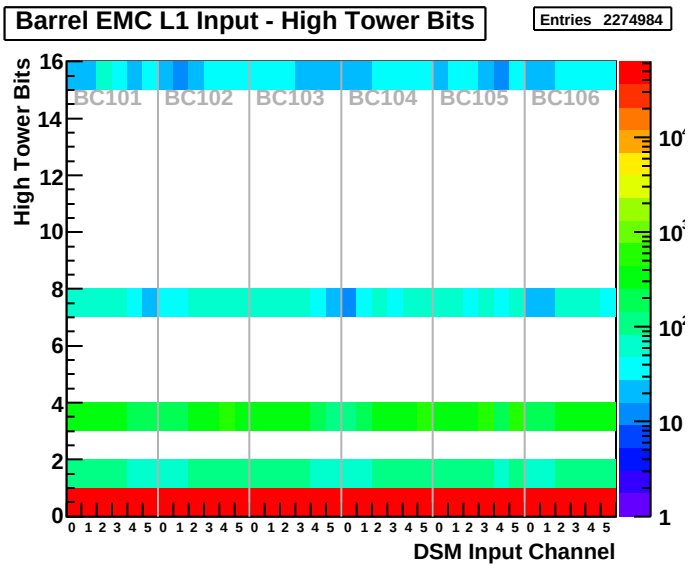
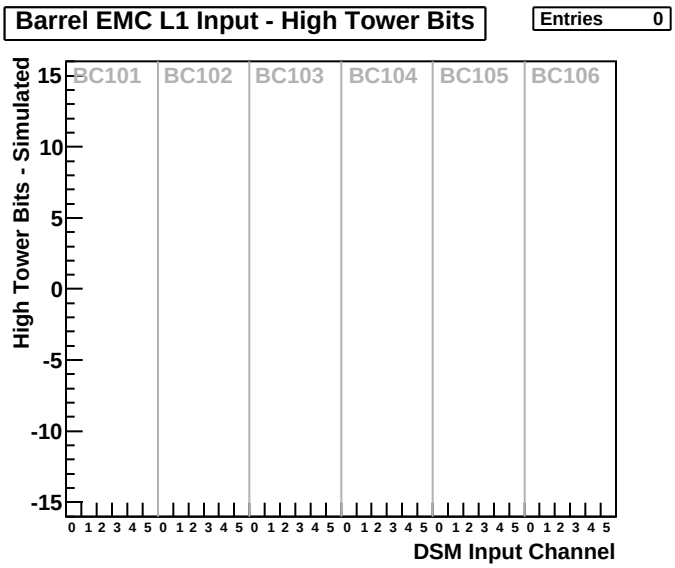

Barrel EMC L1 Input - Low Eta Sum
Entries 2274984
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

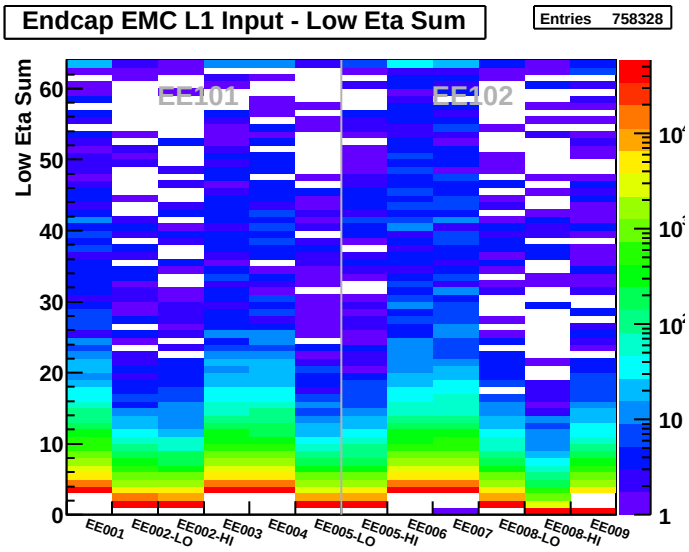

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

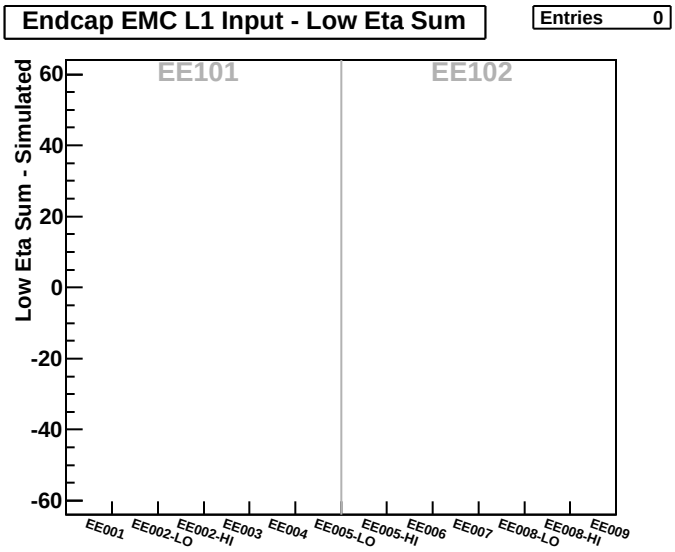

Barrel EMC L1 Input - High Eta Sum
Entries 2274984
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

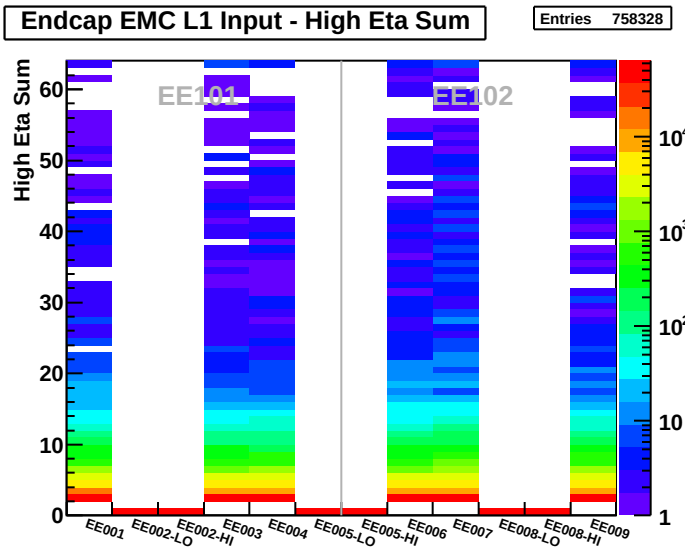

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

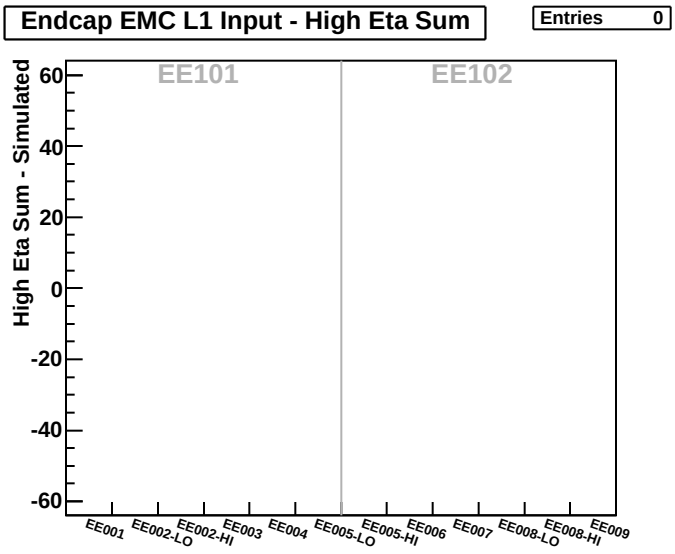

Barrel EMC L1 Input - High Tower Bits
Entries 2274984
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

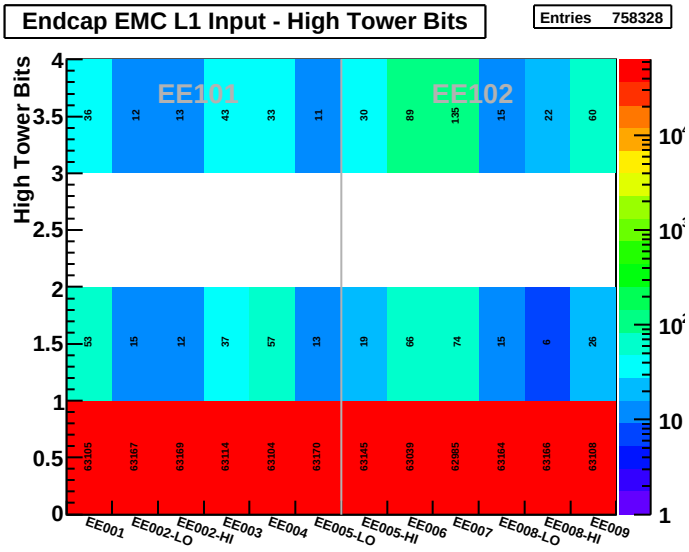

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

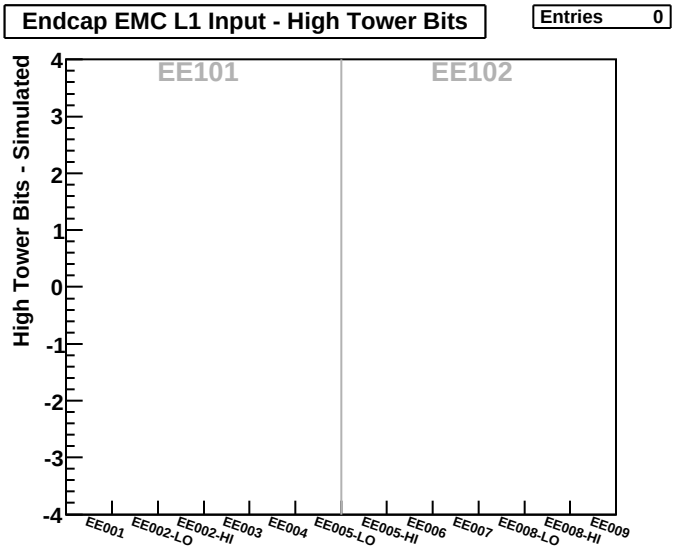

Endcap EMC L1 Input - Low Eta Sum
Entries 758328
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

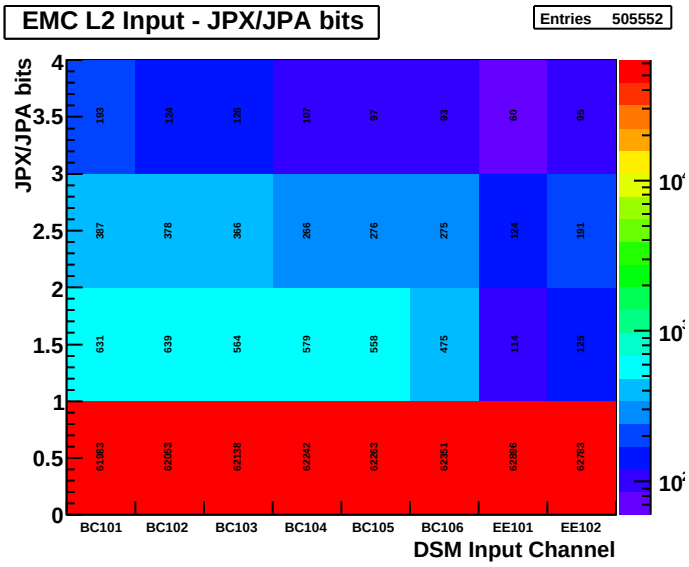

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

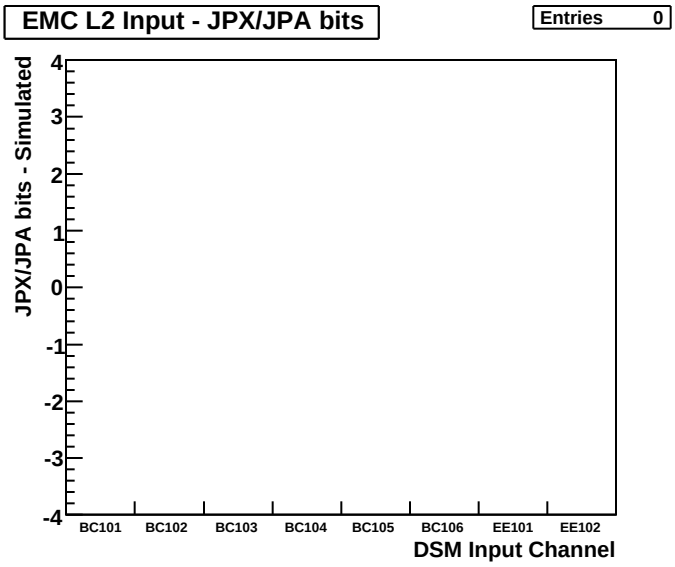

Endcap EMC L1 Input - High Eta Sum
Entries 758328
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

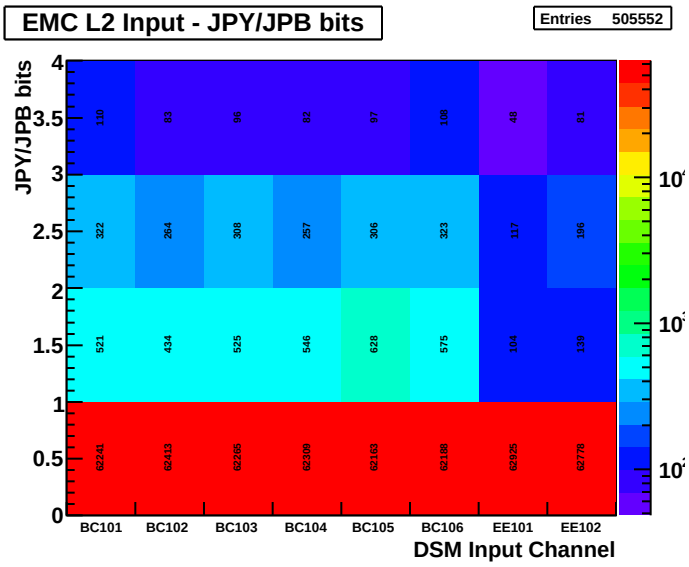

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

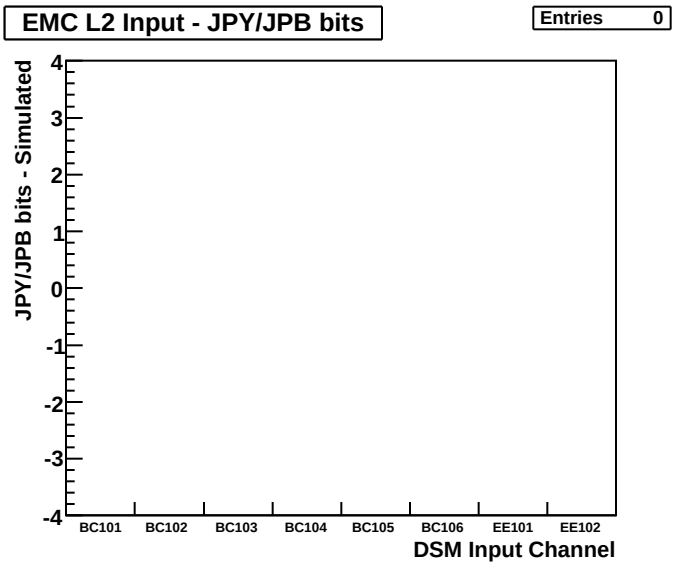

Endcap EMC L1 Input - High Tower Bits
Entries 758328
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

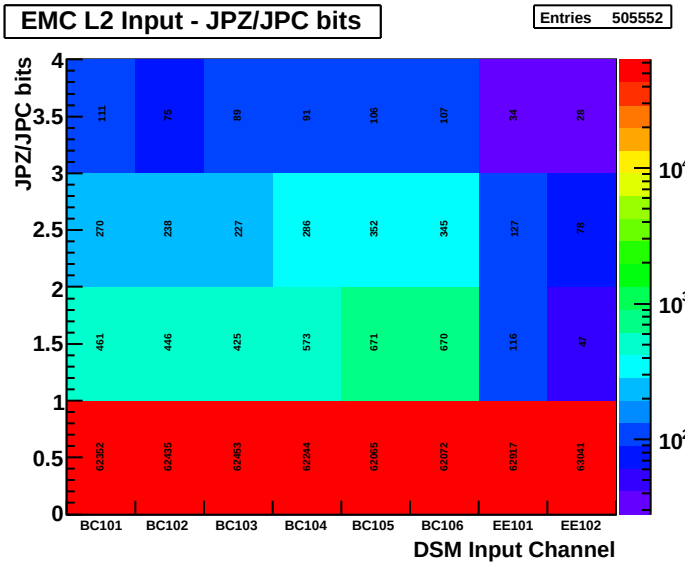

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

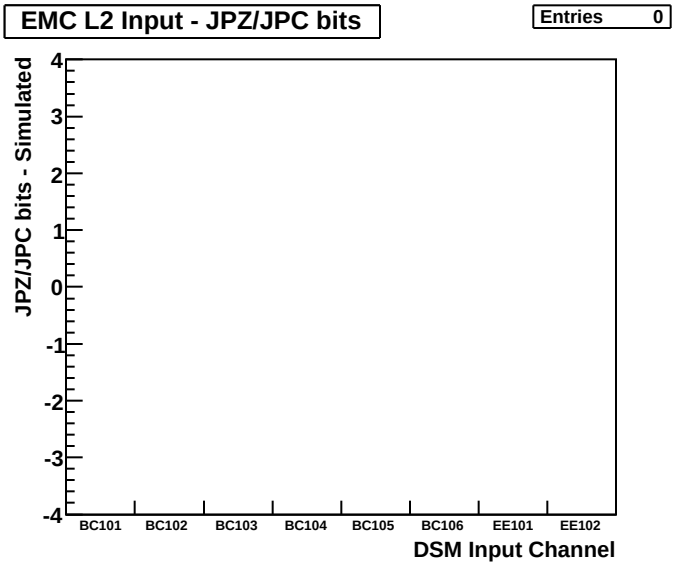

EMC L2 Input - JPX/JPA bits
Entries 505552
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
193 124 126 107 97 93 60 95
387 378 366 266 276 275 124 191
631 639 564 579 558 475 114 125
61983 62053 62138 62242 62263 62351 62896 62783

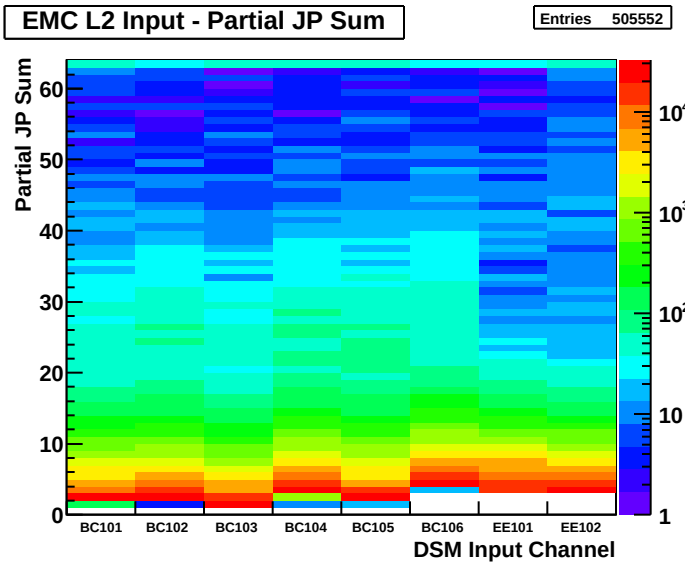

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

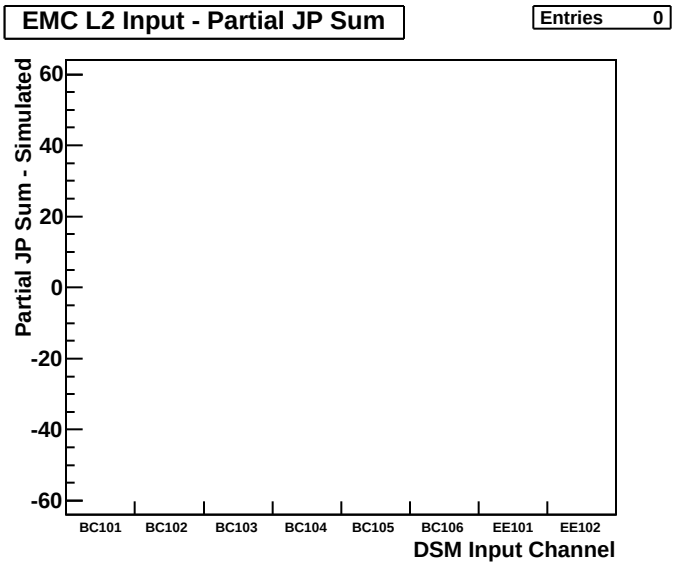

EMC L2 Input - JPY/JPB bits
Entries 505552
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
110 83 96 82 97 108 48 81
322 264 308 257 306 323 117 196
521 434 525 546 628 575 104 139
62241 62413 62265 62309 62163 62188 62925 62778

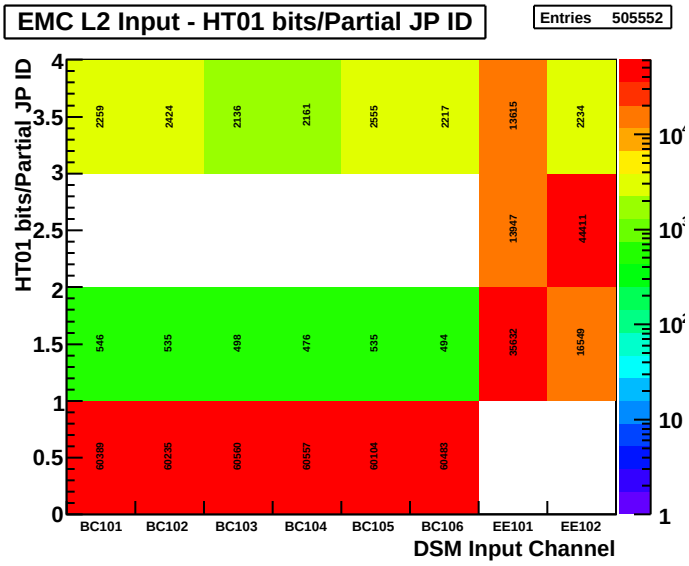

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

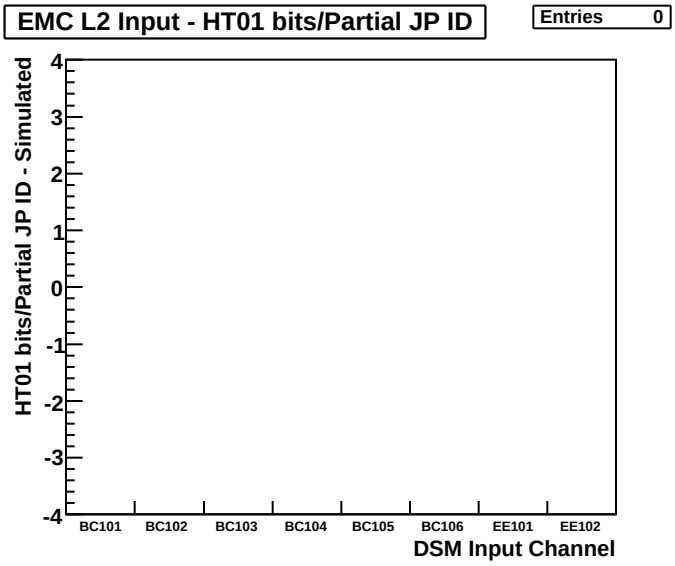

EMC L2 Input - JPZ/JPC bits
Entries 505552
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
111 75 89 91 106 107 34 28
270 238 227 286 352 345 127 78
461 446 425 573 671 670 116 47
62352 62435 62453 62244 62065 62072 62917 63041

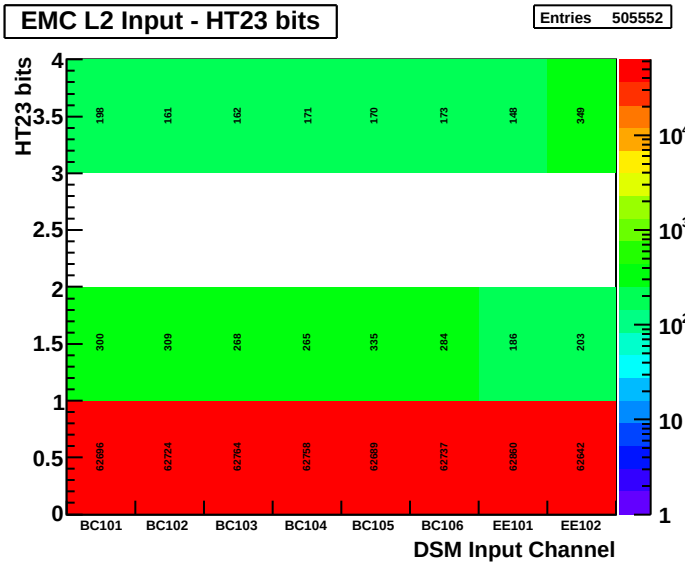

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

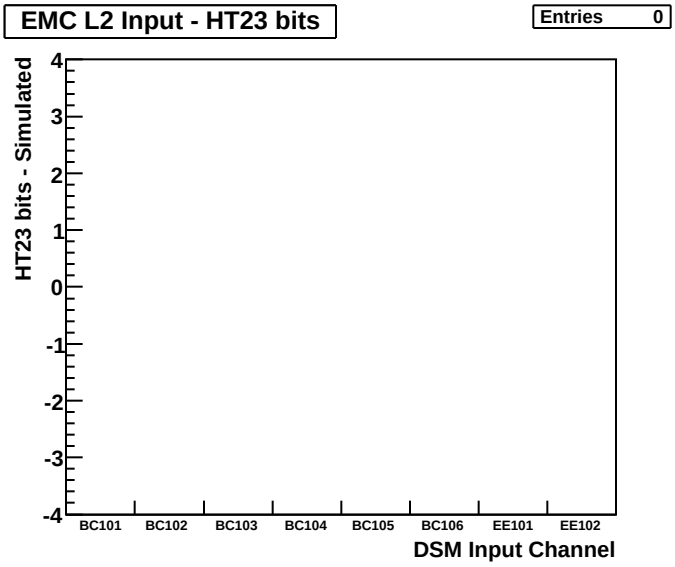

EMC L2 Input - Partial JP Sum
Entries 505552
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

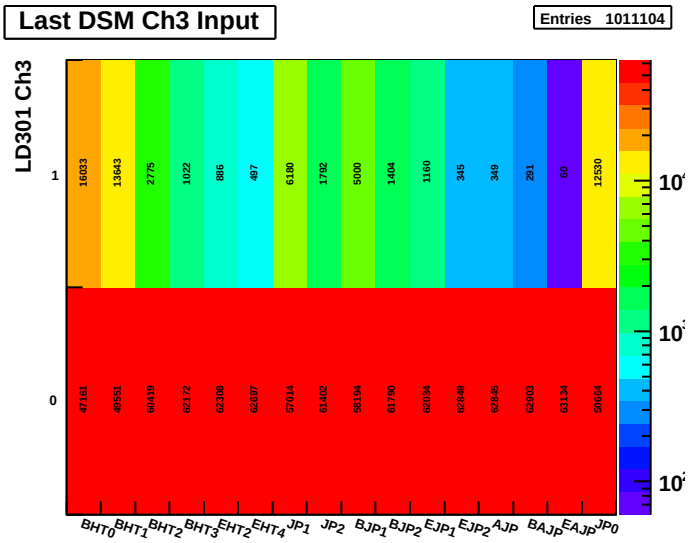

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

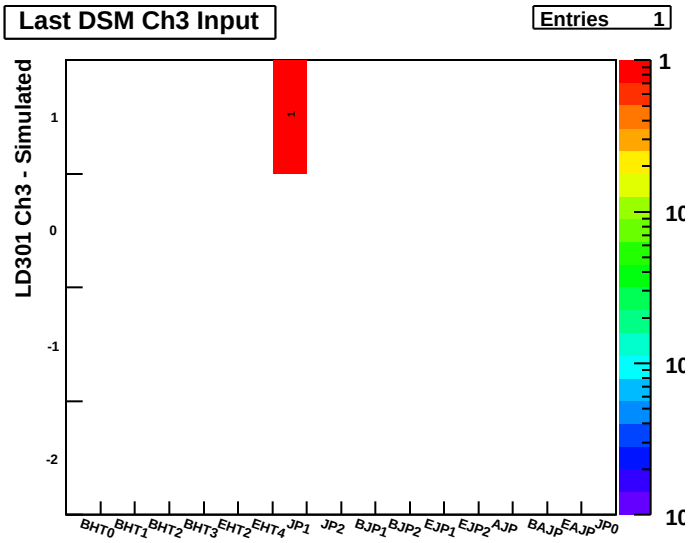

EMC L2 Input - HT01 bits/Partial JP ID
Entries 505552
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

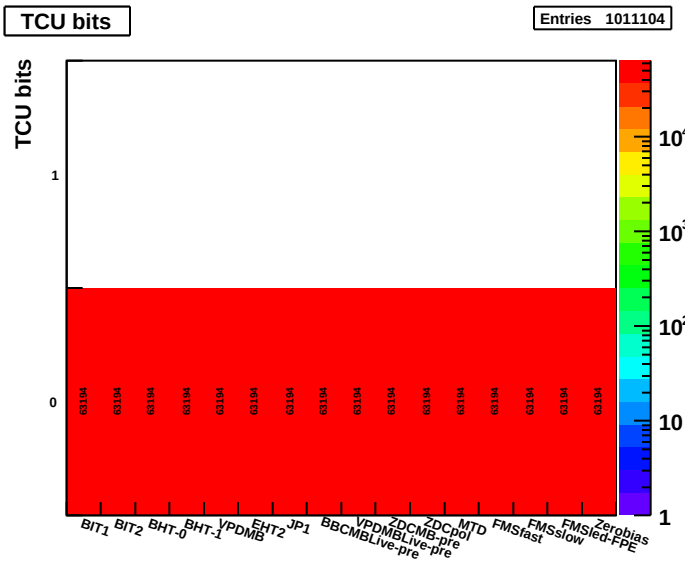

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

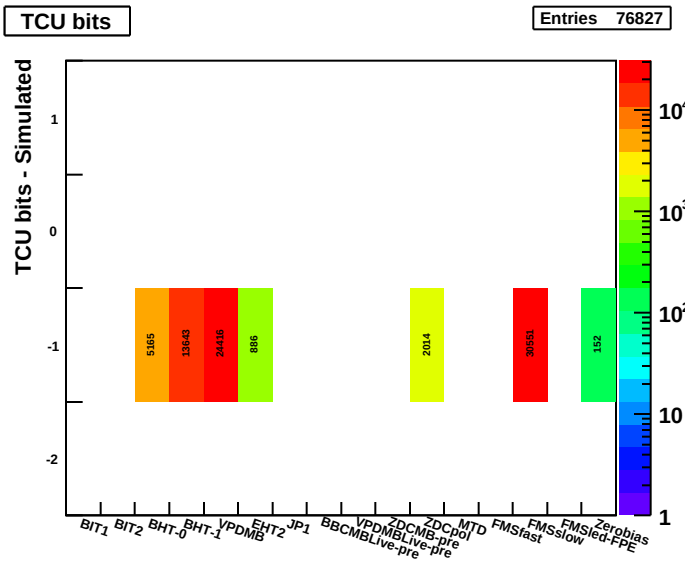

EMC L2 Input - HT23 bits
Entries 505552
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

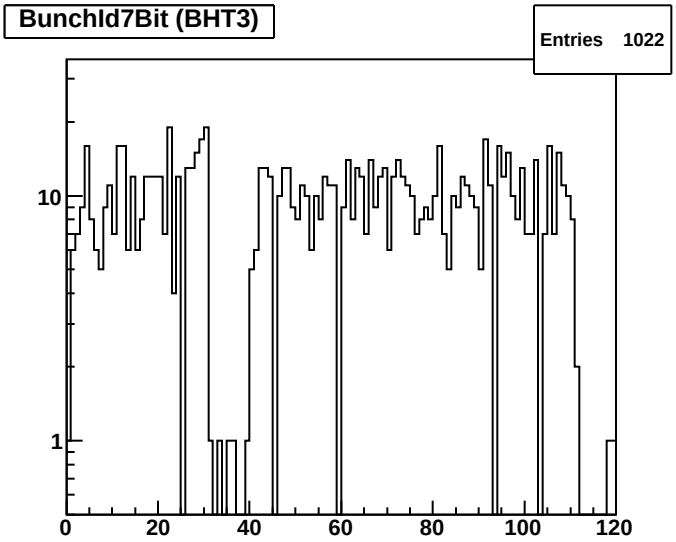

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

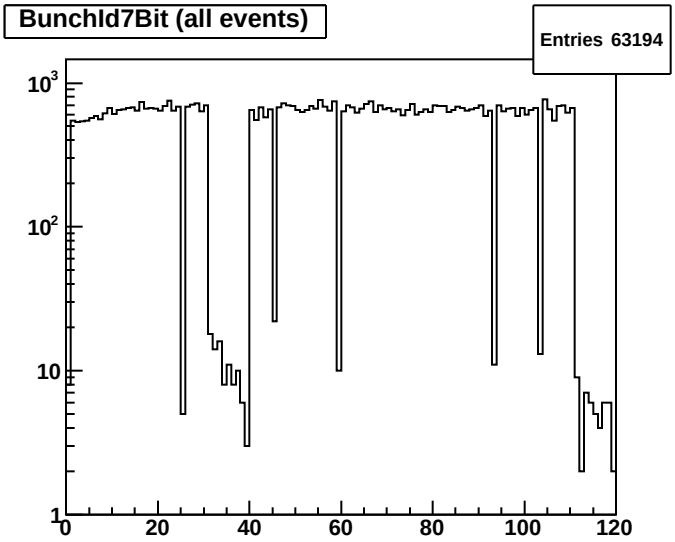

Last DSM Ch3 Input
Entries 1011104
LD301 Ch3
16033 13643 2775 1022 886 497 6180 1792 5000 1404 1160 345 349 291 60 12530
47161 49551 60419 62172 62308 62697 57014 61402 58194 61790 62034 62349 62845 62903 63134 50664
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

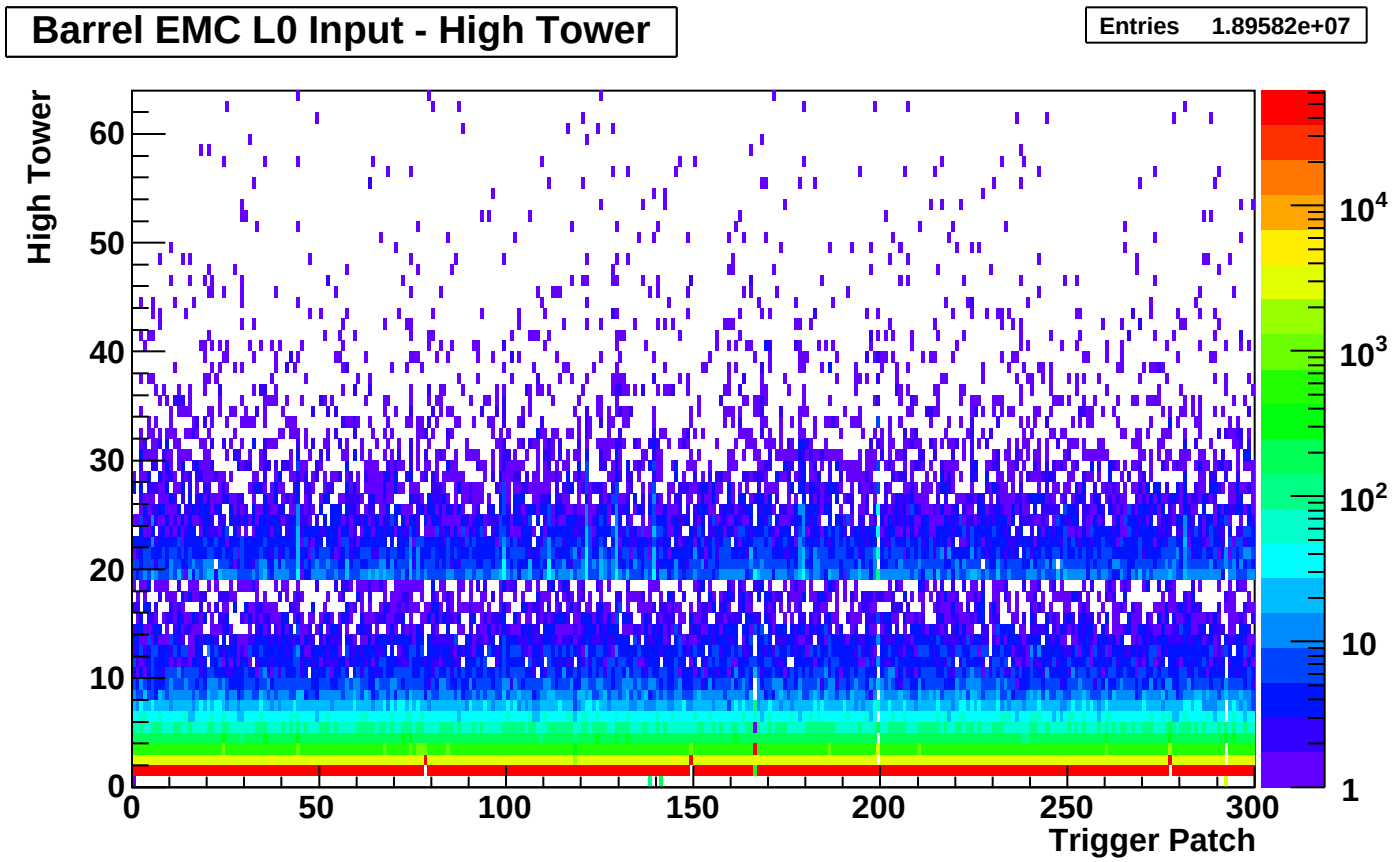

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

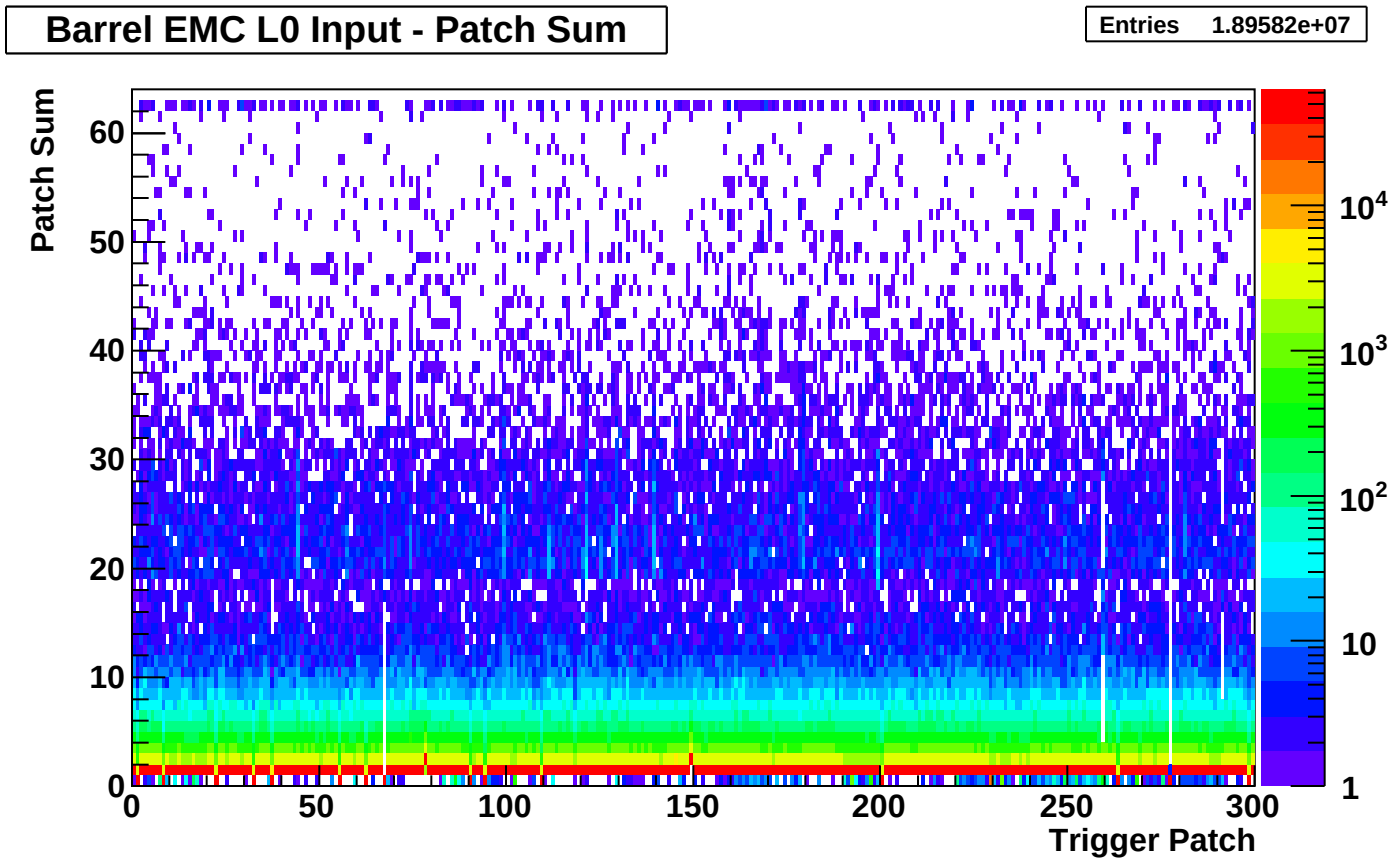

TCU bits
Entries 1011104
TCU bits
63194
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

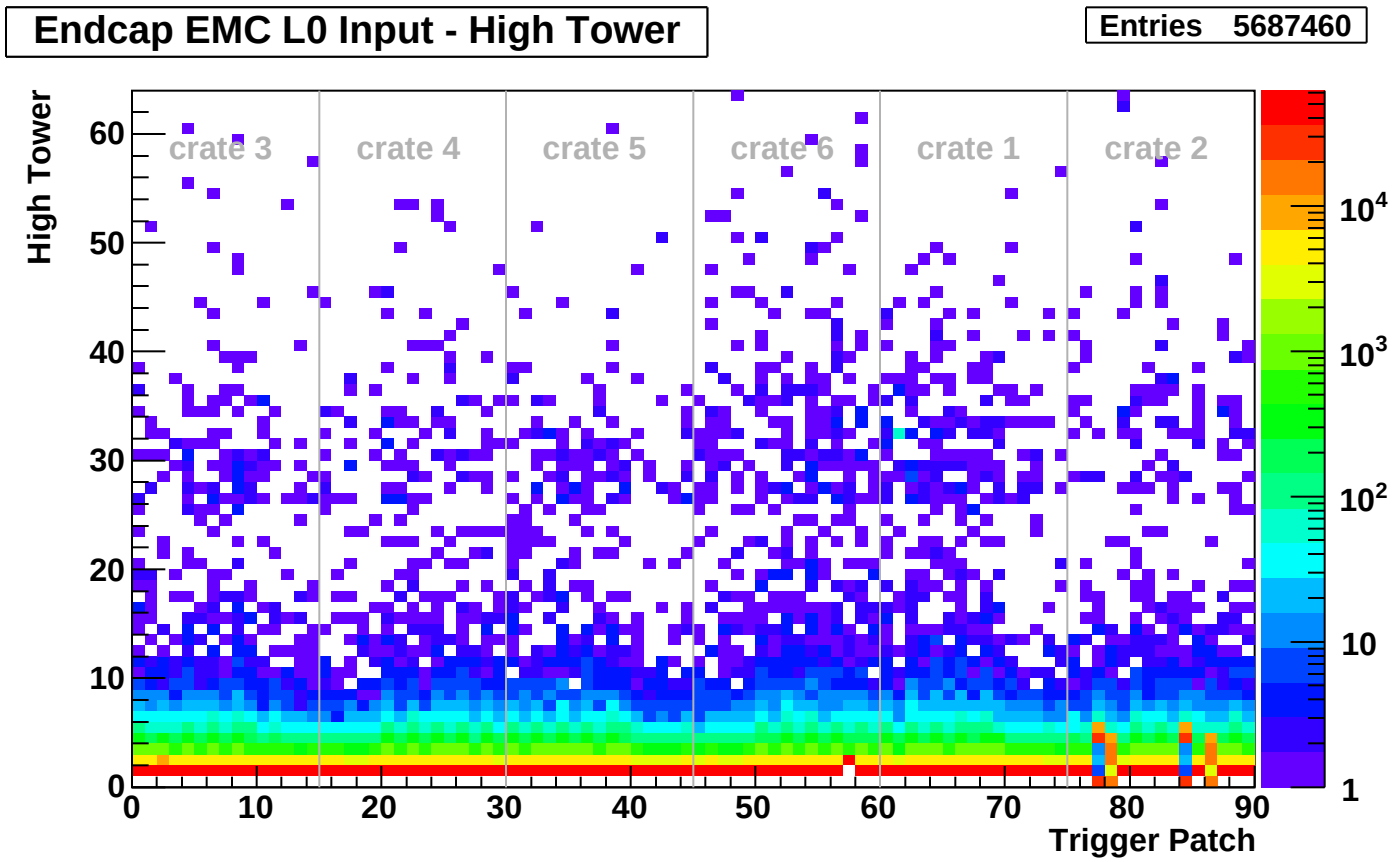

TCU bits
Entries 76827
TCU bits - Simulated
5165 13643 24416 886 2014 30551 152
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

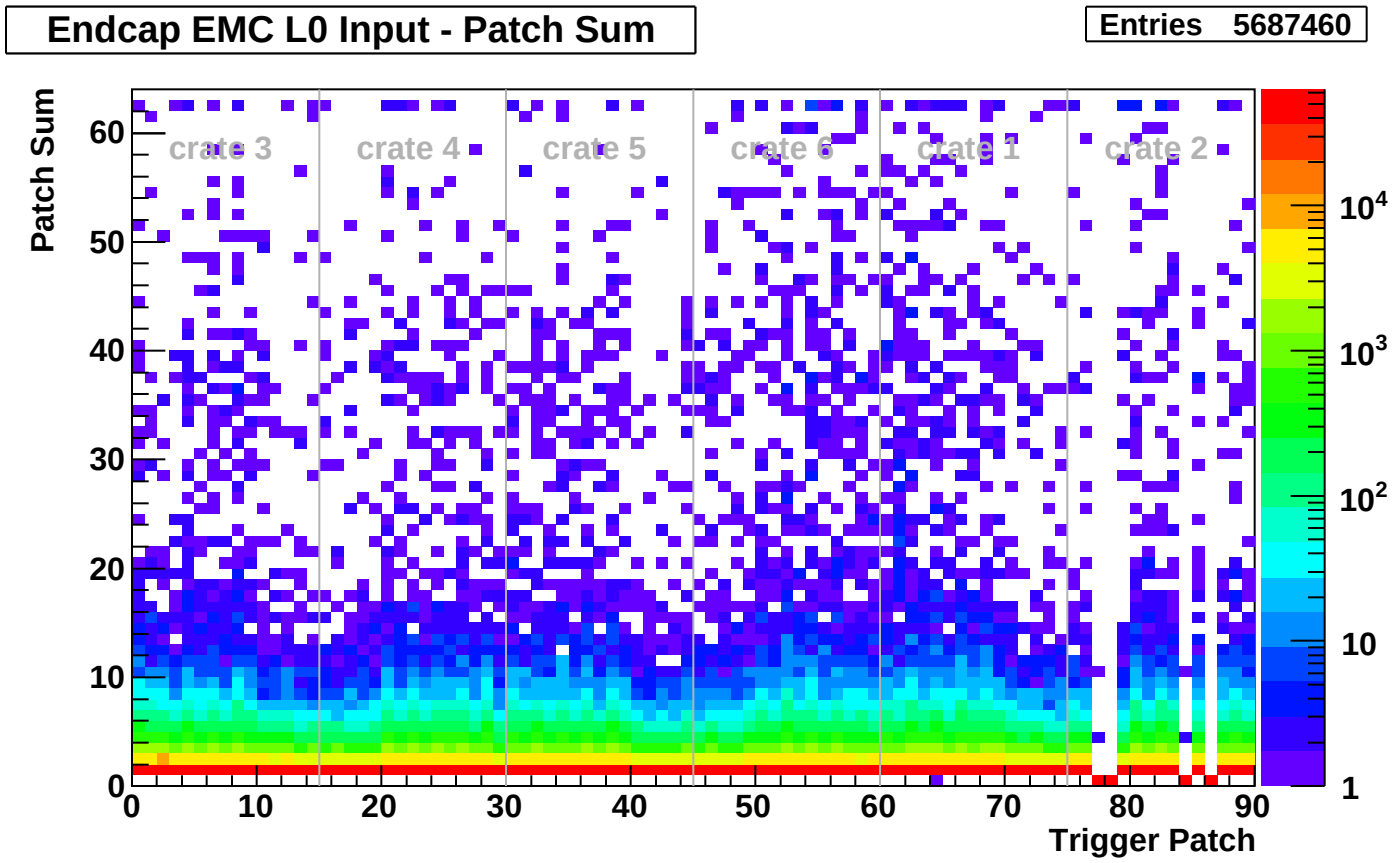

BunchId7Bit (BHT3)
Entries 1022

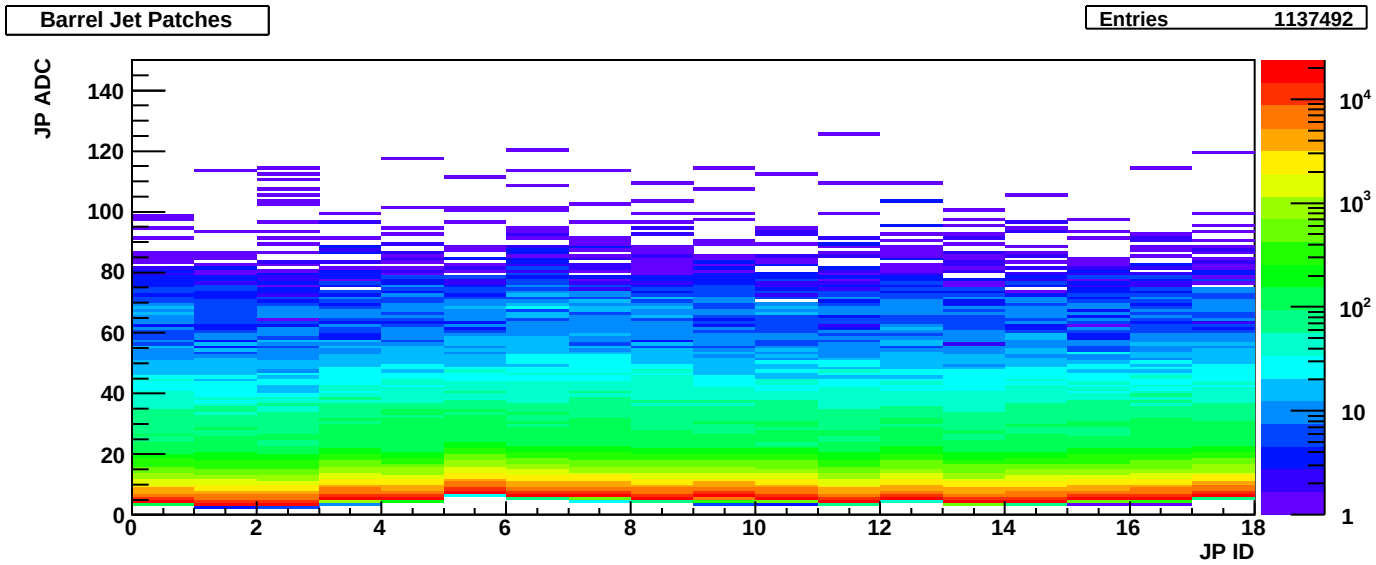

BunchId7Bit (all events)
Entries 63194

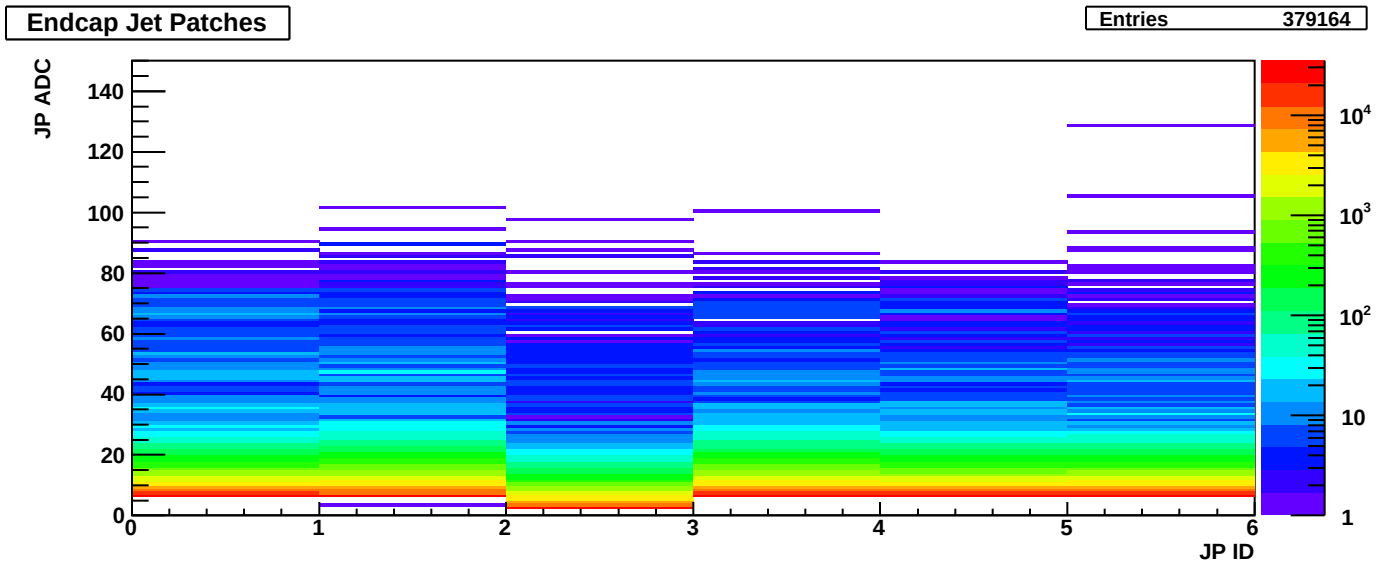

Barrel EMC L0 Input - High Tower
Entries 1.89582e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

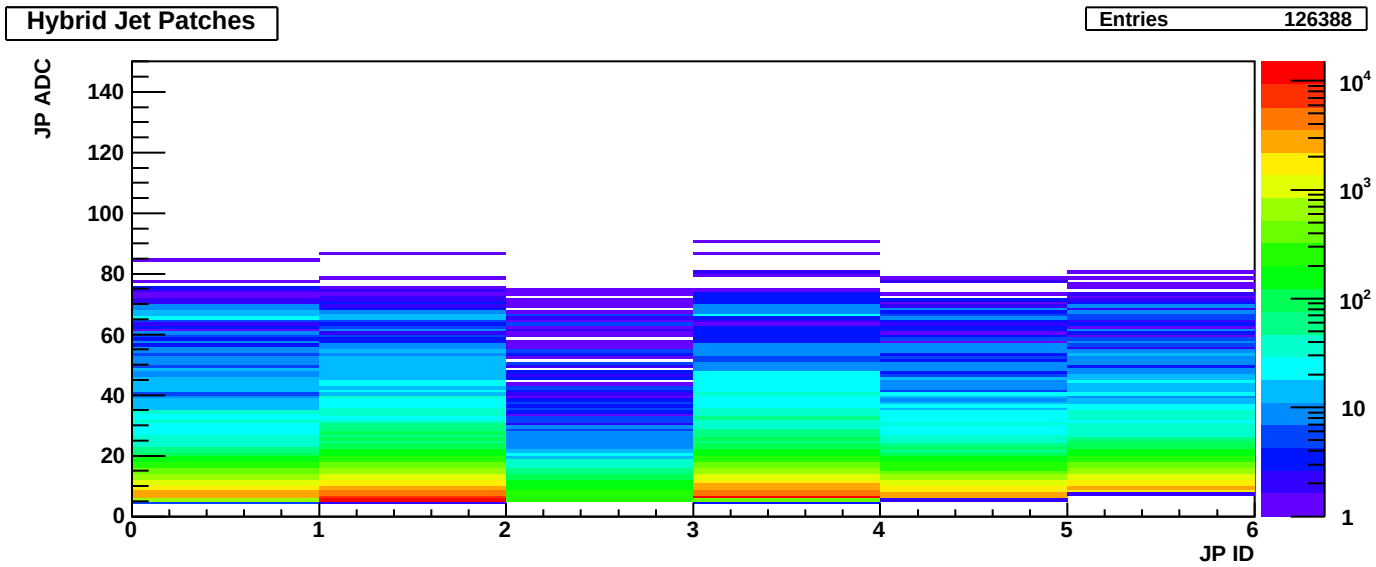

Barrel EMC L0 Input - Patch Sum
Entries 1.89582e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

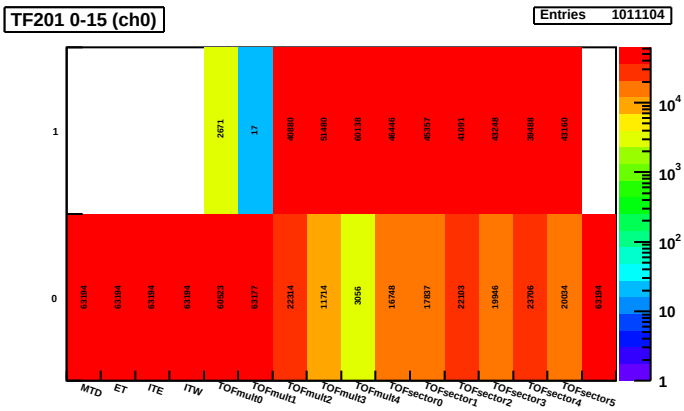

Endcap EMC L0 Input - High Tower
Entries 5687460
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

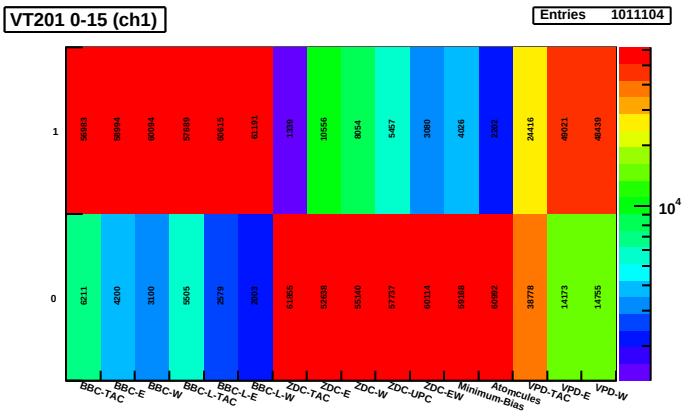

Endcap EMC L0 Input - Patch Sum
Entries 5687460
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

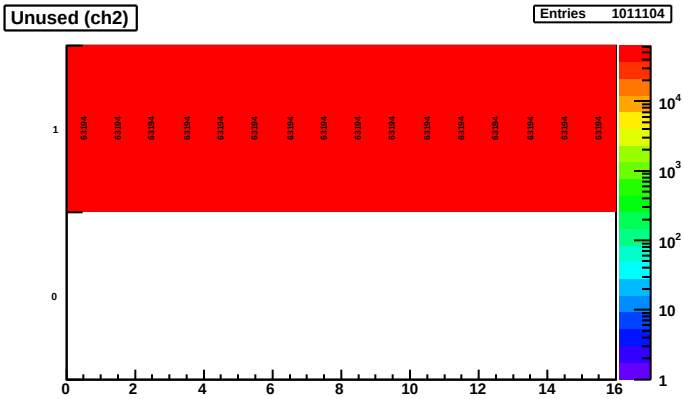

Barrel Jet Patches
Entries 1137492
JP ADC
JP ID

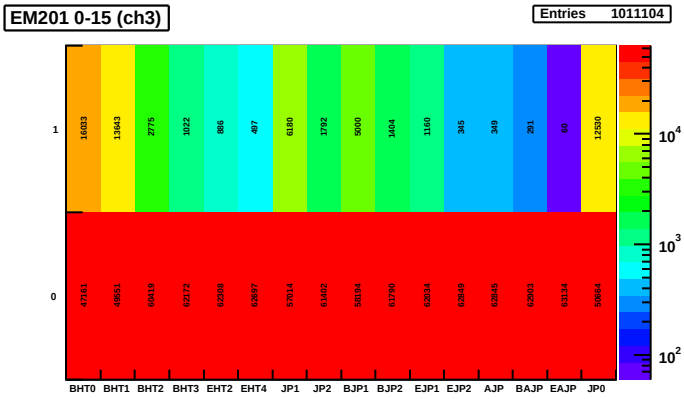

Endcap Jet Patches
Entries 379164
JP ADC
JP ID

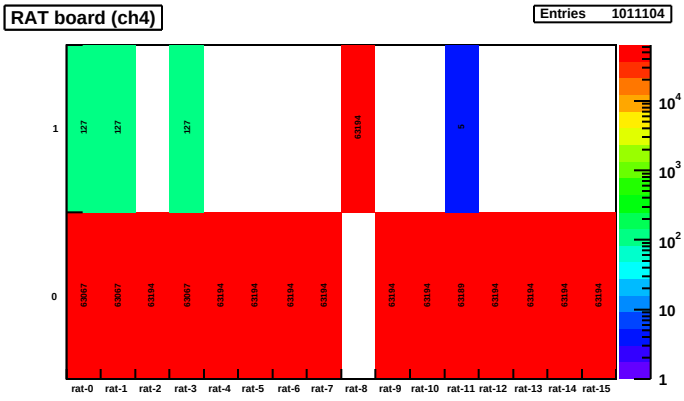

Hybrid Jet Patches
Entries 126388
JP ADC
JP ID

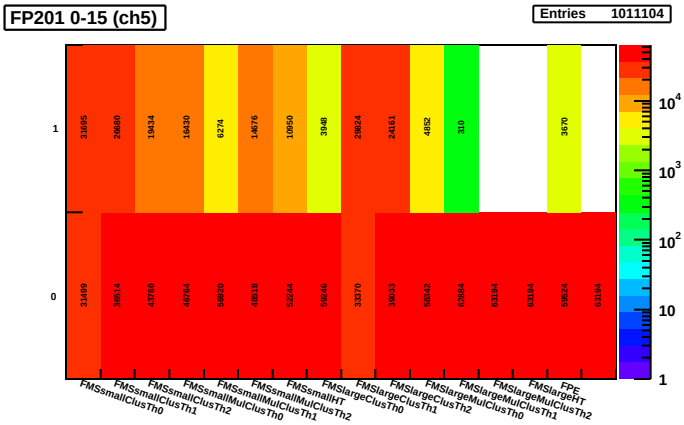

TF201 0-15 (ch0)
Entries 1011104
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

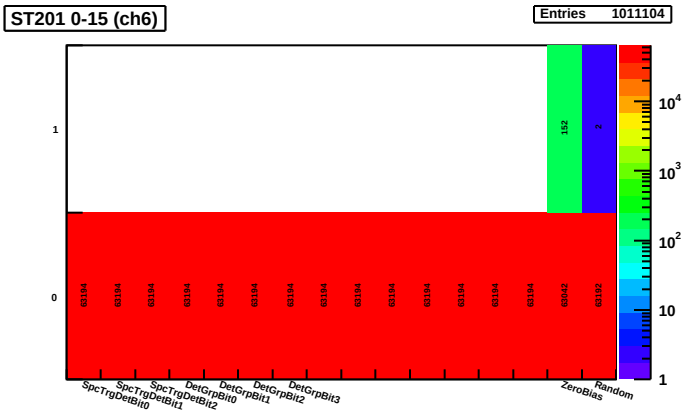

VT201 0-15 (ch1)
Entries 1011104
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

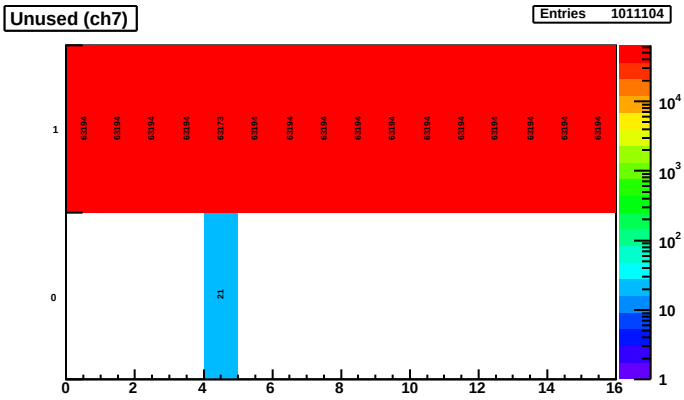

Unused (ch2)
Entries 1011104

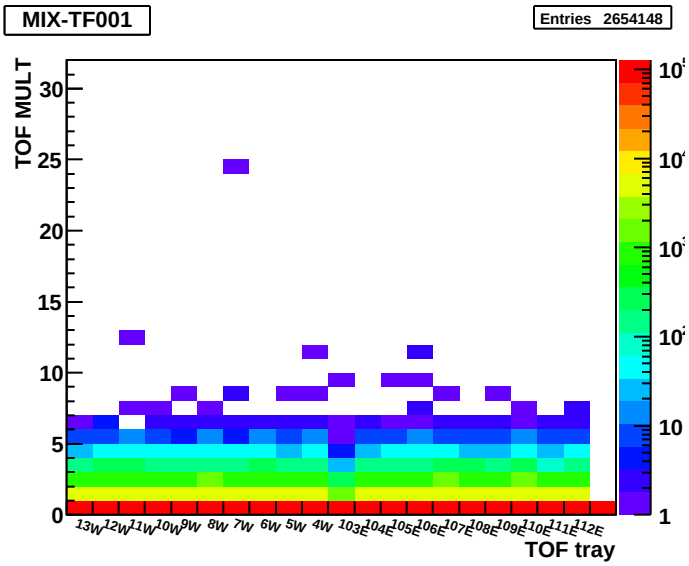

EM201 0-15 (ch3)
Entries 1011104
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

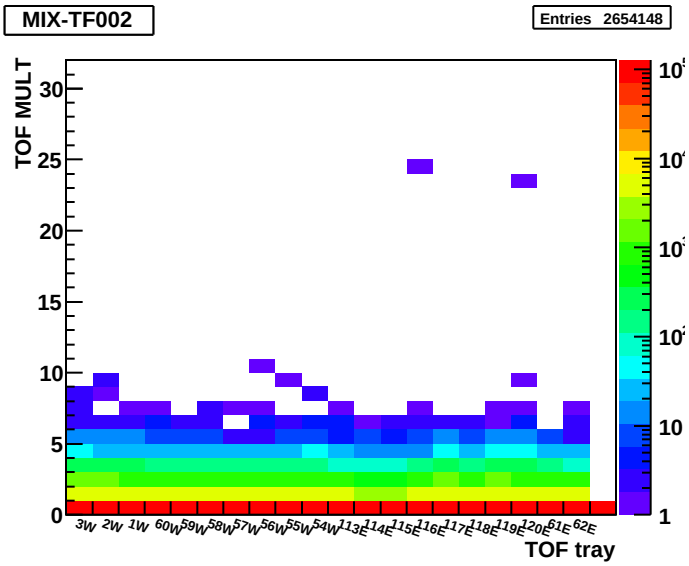

RAT board (ch4)
Entries 1011104
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

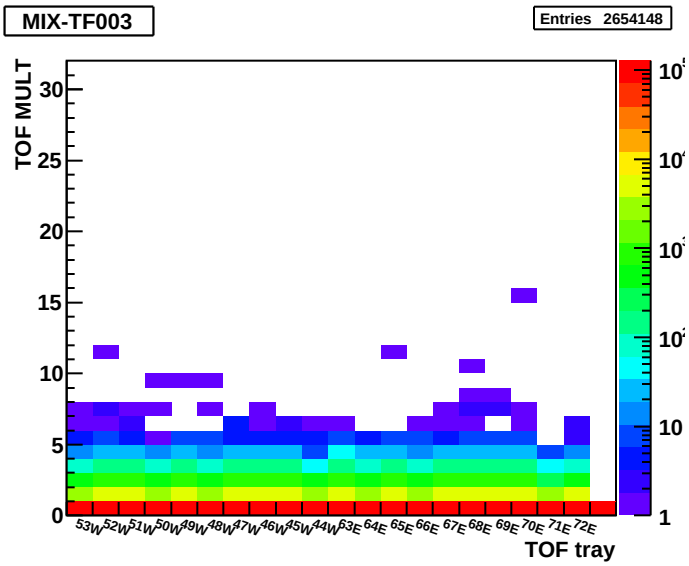

FP201 0-15 (ch5)
Entries 1011104
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

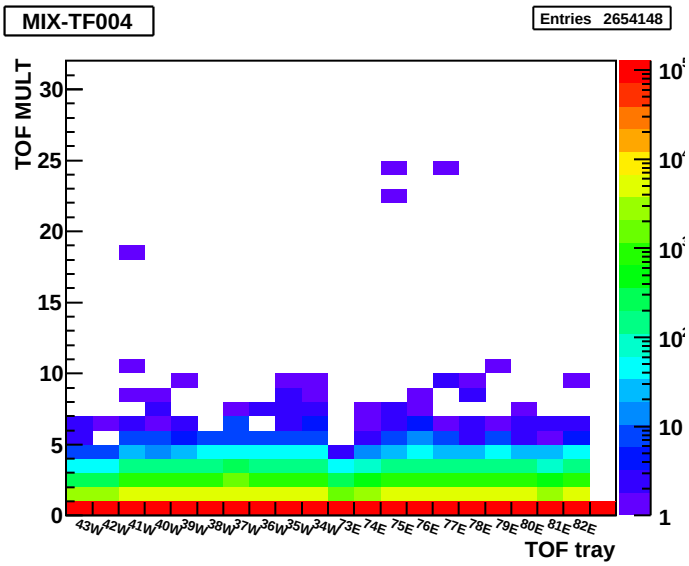

ST201 0-15 (ch6)
Entries 1011104
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

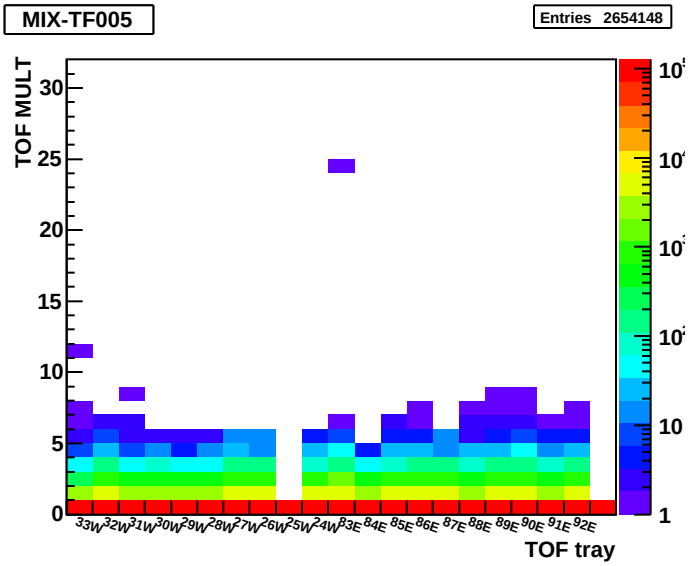

Unused (ch7)
Entries 1011104

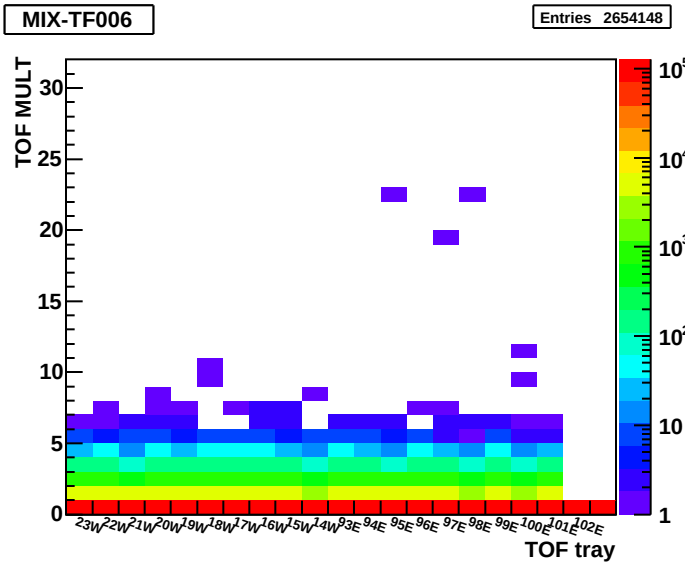

MIX-TF001
Entries 2654148
TOF MULT
TOF tray

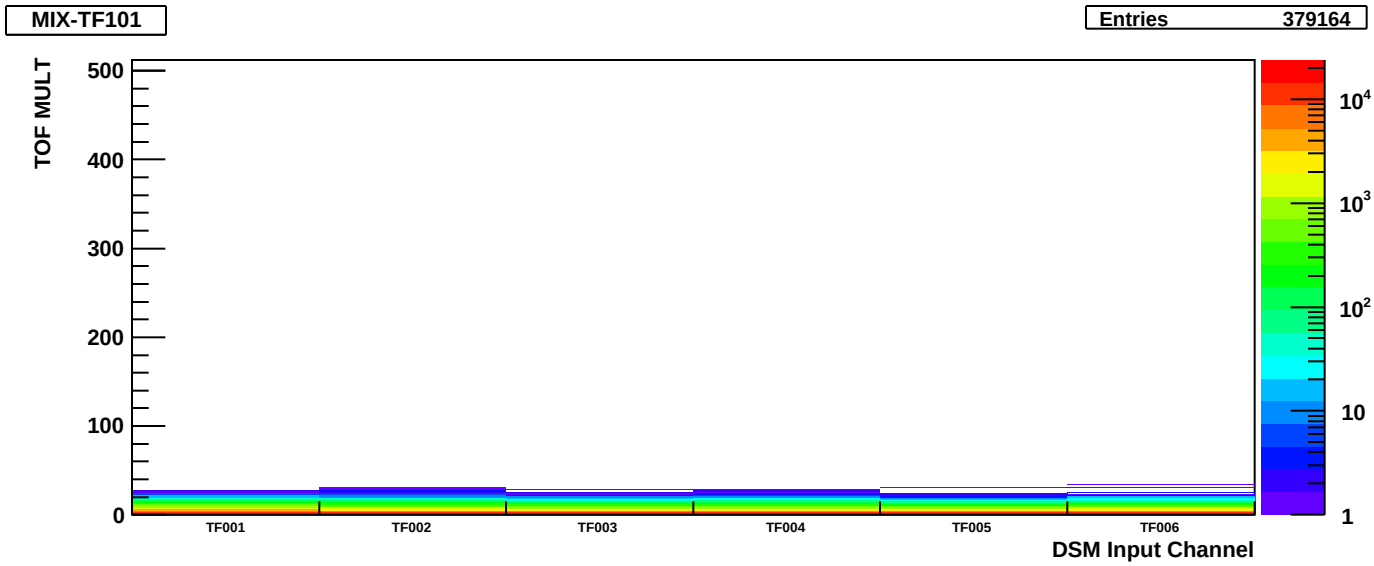

MIX-TF002
Entries 2654148
TOF MULT
TOF tray

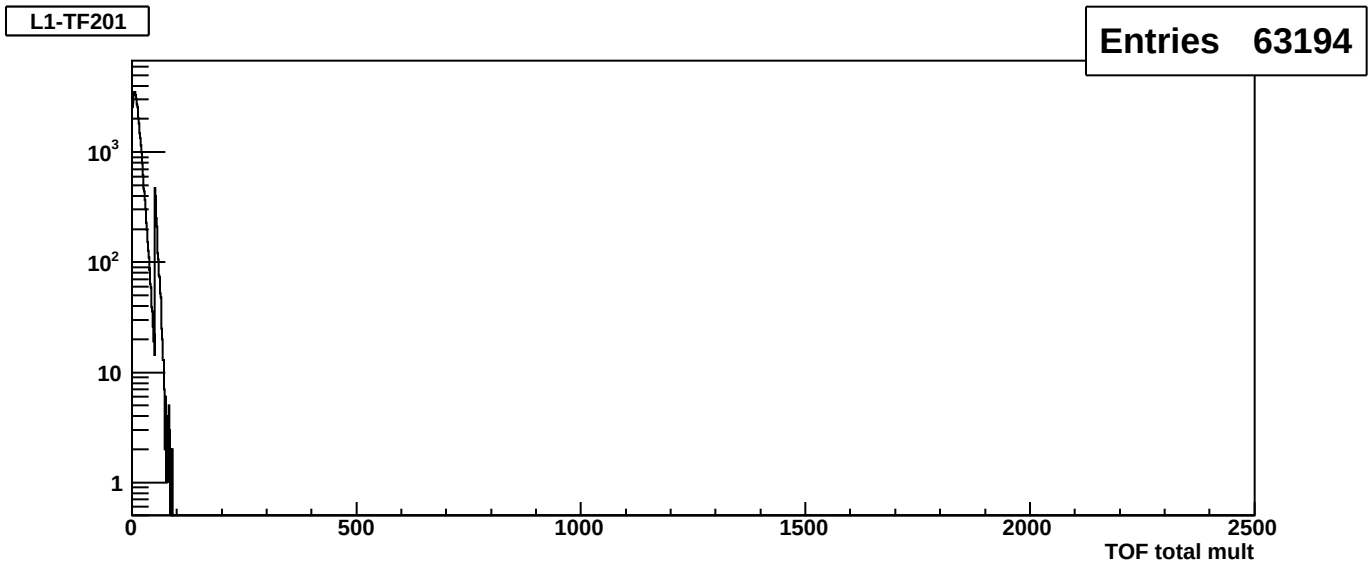

MIX-TF003
Entries 2654148
TOF MULT
TOF tray

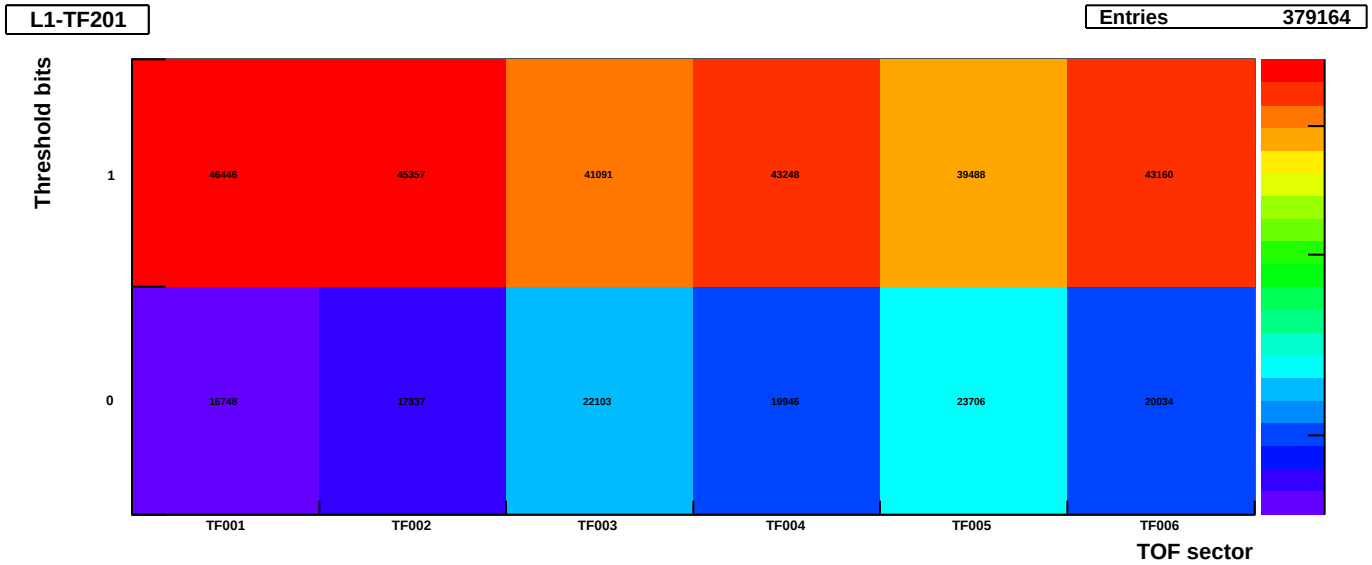

MIX-TF004
Entries 2654148
TOF MULT
TOF tray

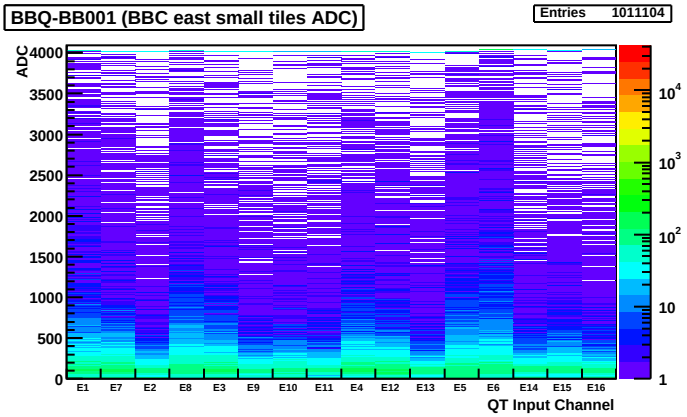

MIX-TF005
Entries 2654148
TOF MULT
TOF tray

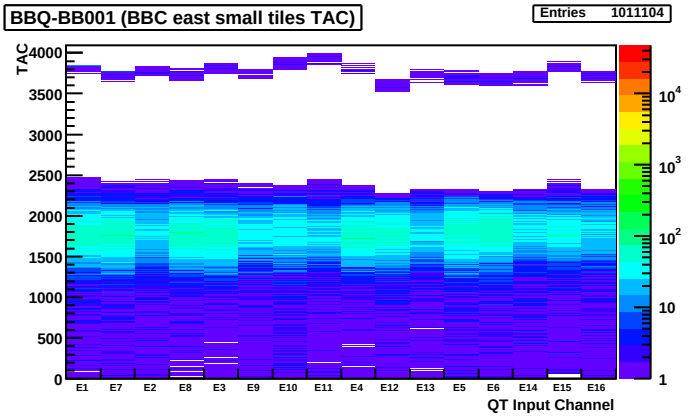

MIX-TF006
Entries 2654148
TOF MULT
TOF tray

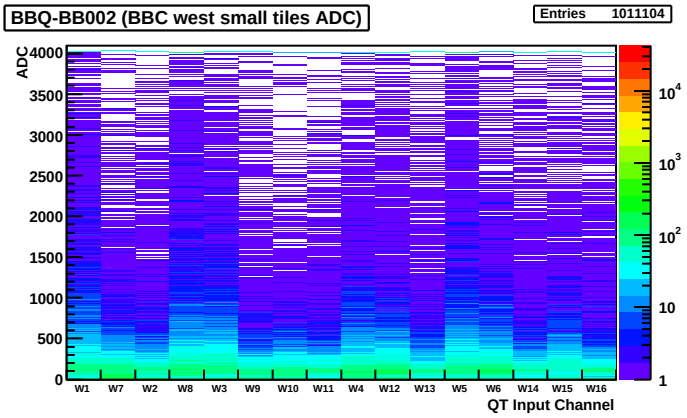

MIX-TF101
Entries 379164
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

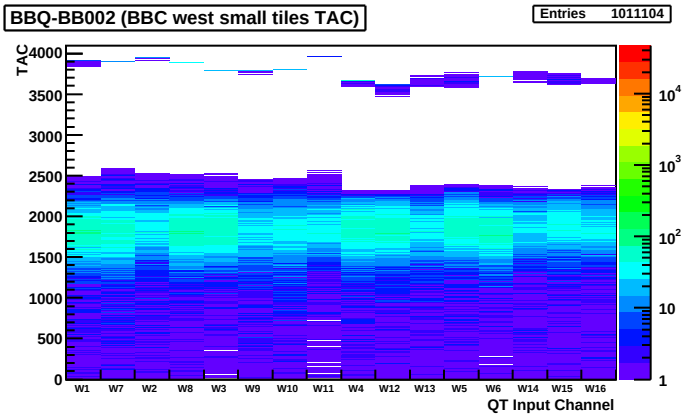

L1-TF201
Entries 63194
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

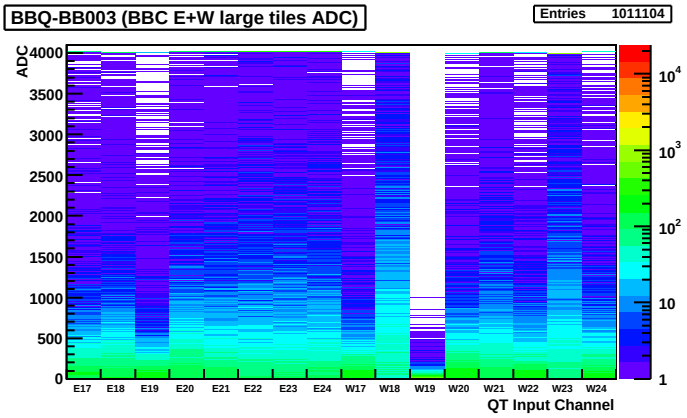

L1-TF201
Entries 379164
Threshold bits
1
0
46446
45357
41091
43248
39488
43160
16748
17837
22103
19946
23706
20034
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

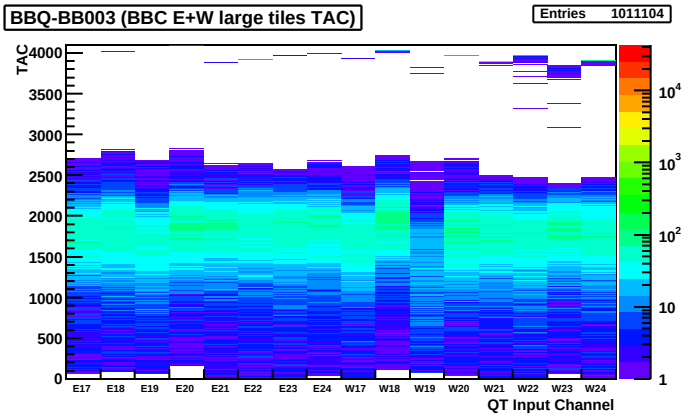

BBQ-BB001 (BBC east small tiles ADC)
Entries 1011104
ADC
QT Input Channel

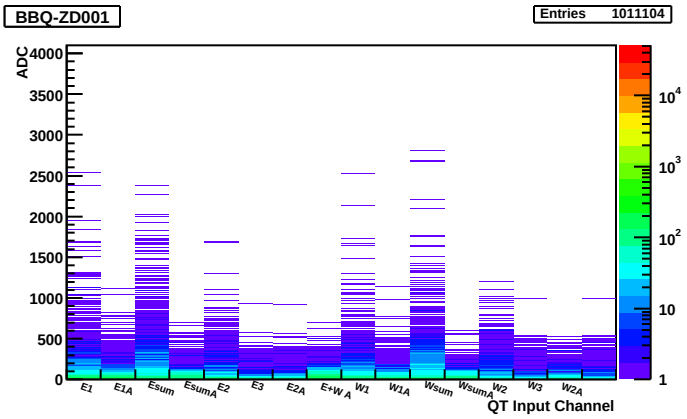

BBQ-BB001 (BBC east small tiles TAC)
Entries 1011104
TAC
QT Input Channel

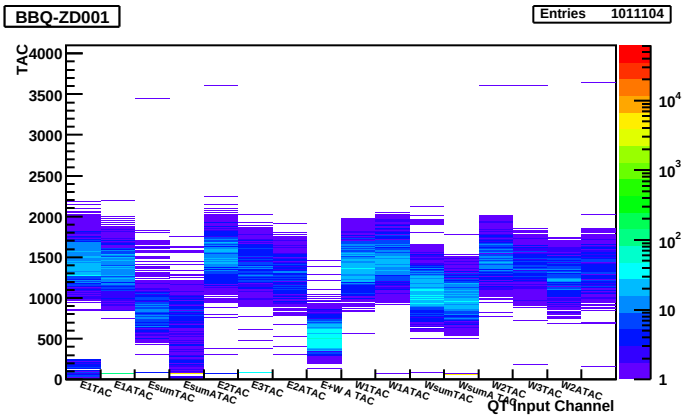

BBQ-BB002 (BBC west small tiles ADC)
Entries 1011104
ADC
QT Input Channel

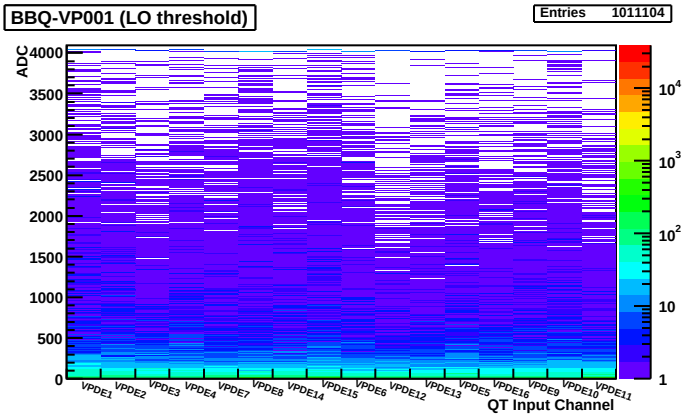

BBQ-BB002 (BBC west small tiles TAC)
Entries 1011104
TAC
QT Input Channel

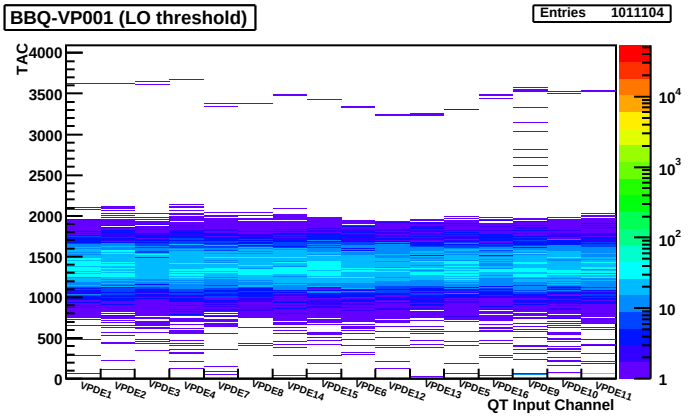

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1011104
ADC
QT Input Channel

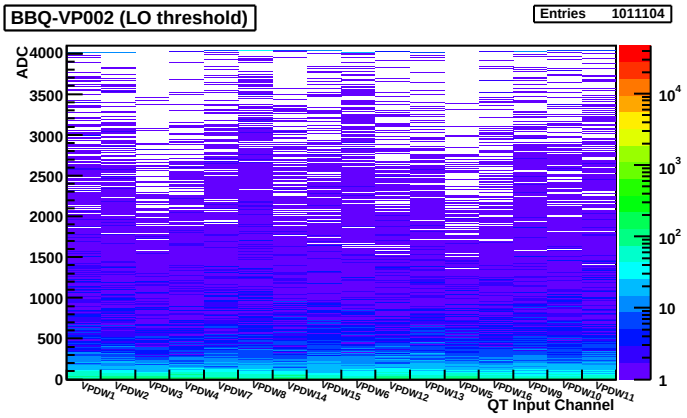

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1011104
TAC
QT Input Channel

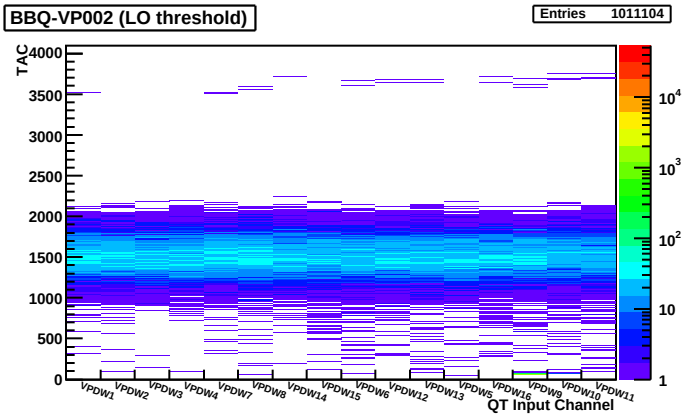

BBQ-ZD001
Entries 1011104
ADC
QT Input Channel

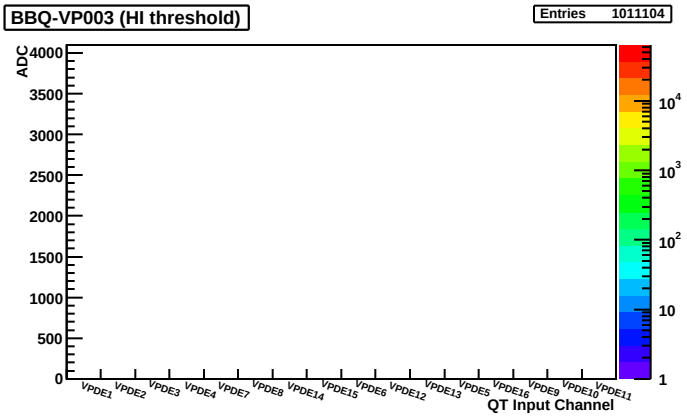

BBQ-ZD001
Entries 1011104
TAC
QT Input Channel

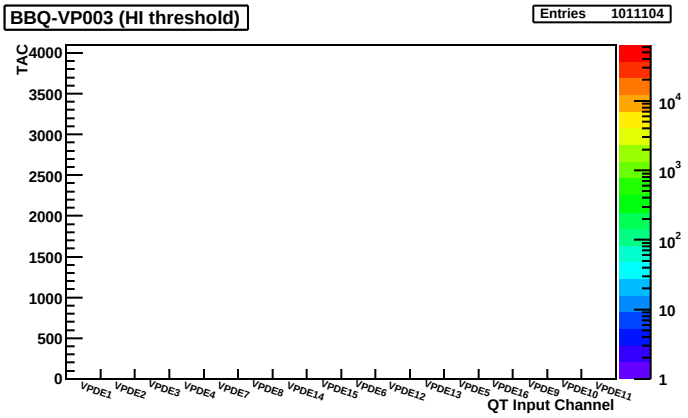

BBQ-VP001 (LO threshold)
Entries 1011104
ADC
QT Input Channel

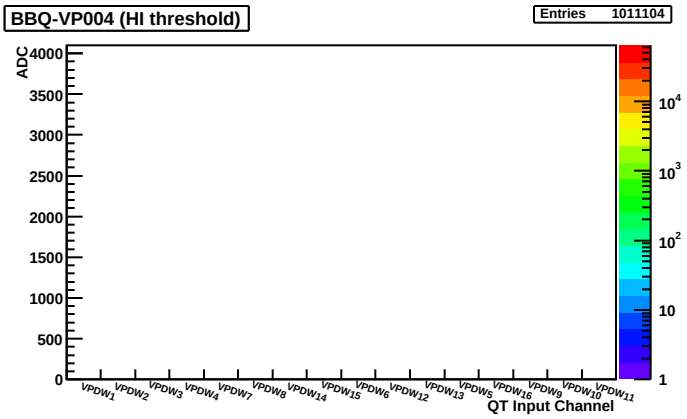

BBQ-VP001 (LO threshold)
Entries 1011104
TAC
QT Input Channel

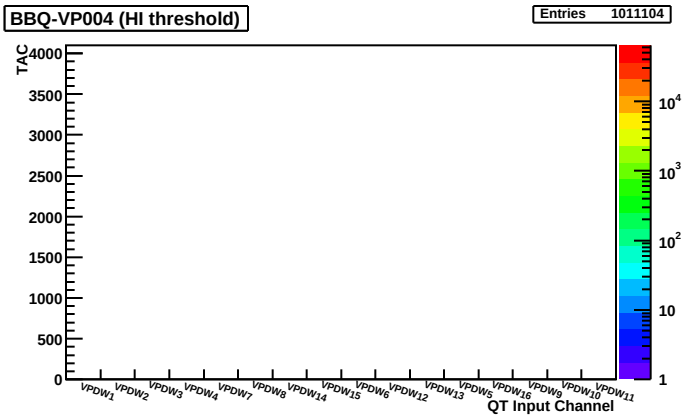

BBQ-VP002 (LO threshold)
Entries 1011104
ADC
QT Input Channel

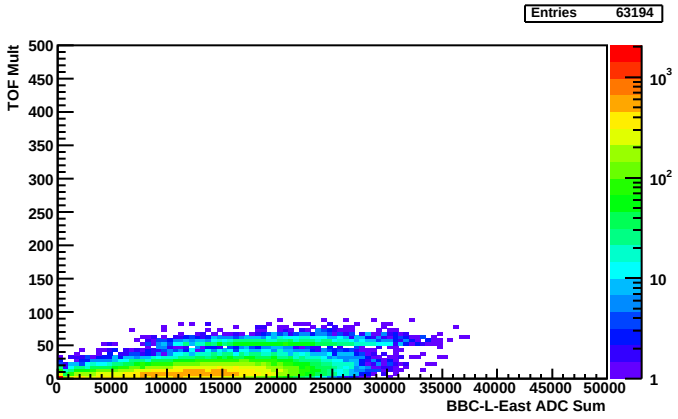

BBQ-VP002 (LO threshold)
Entries 1011104
TAC
QT Input Channel

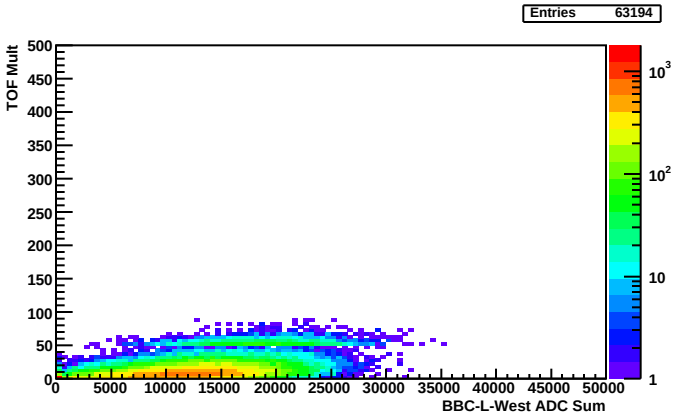

BBQ-VP003 (HI threshold)
Entries 1011104
ADC
QT Input Channel

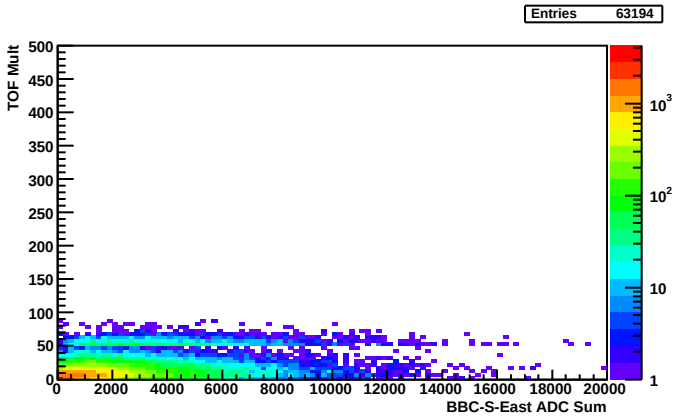

BBQ-VP003 (HI threshold)
Entries 1011104
TAC
QT Input Channel

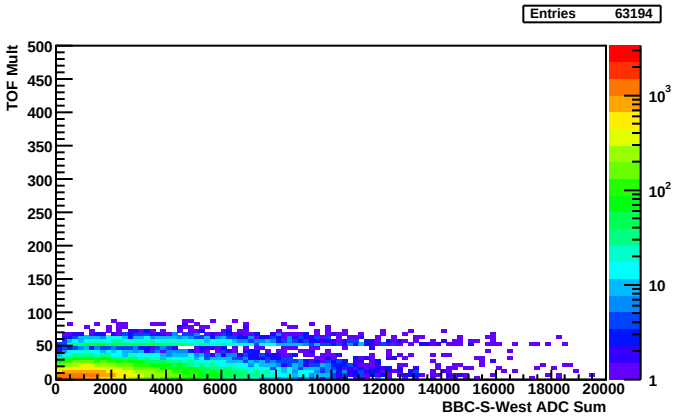

BBQ-VP004 (HI threshold)
Entries 1011104
ADC
QT Input Channel

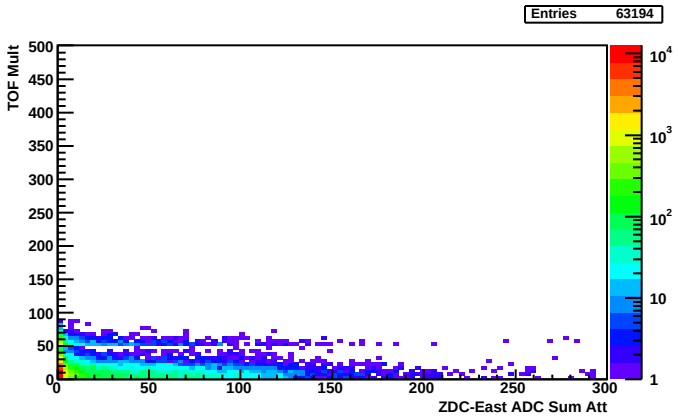

BBQ-VP004 (HI threshold)
Entries 1011104
TAC
QT Input Channel

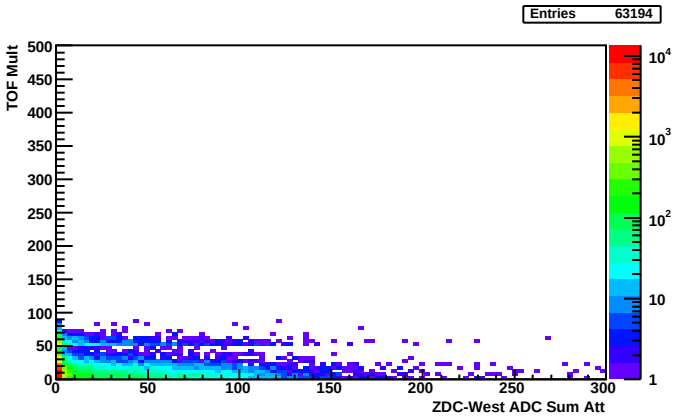

Entries 63194
TOF Mult
BBC-L-East ADC Sum

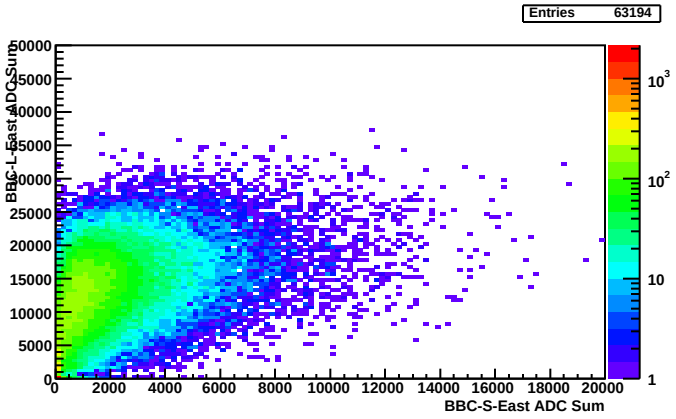

Entries 63194
TOF Mult
BBC-L-West ADC Sum

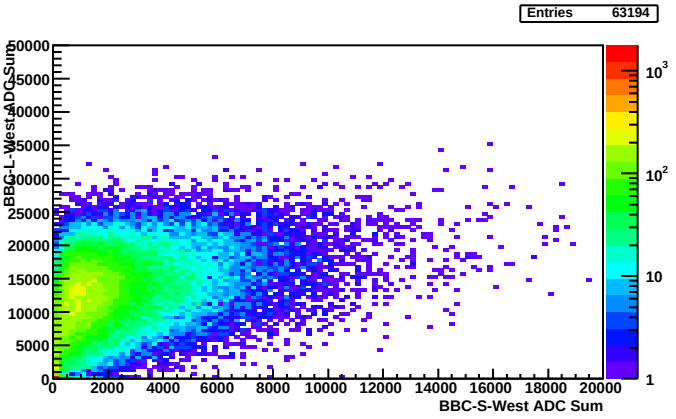

Entries 63194
TOF Mult
BBC-S-East ADC Sum

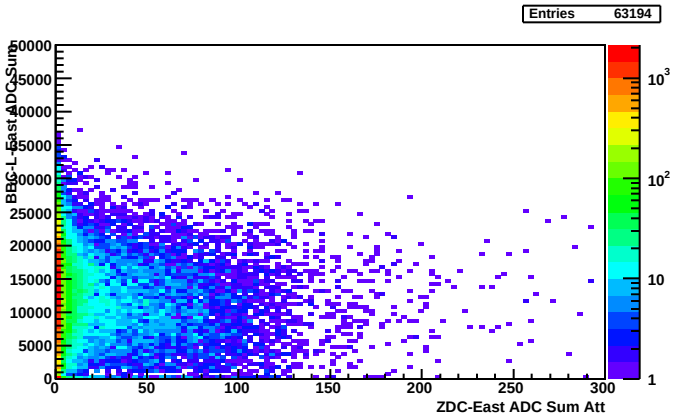

Entries 63194
TOF Mult
BBC-S-West ADC Sum

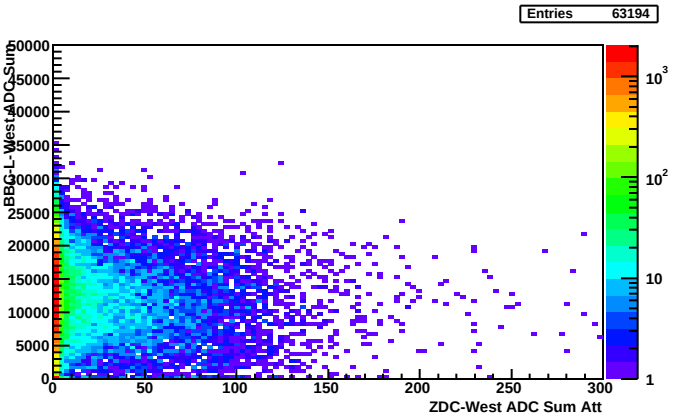

Entries 63194
TOF Mult
ZDC-East ADC Sum Att

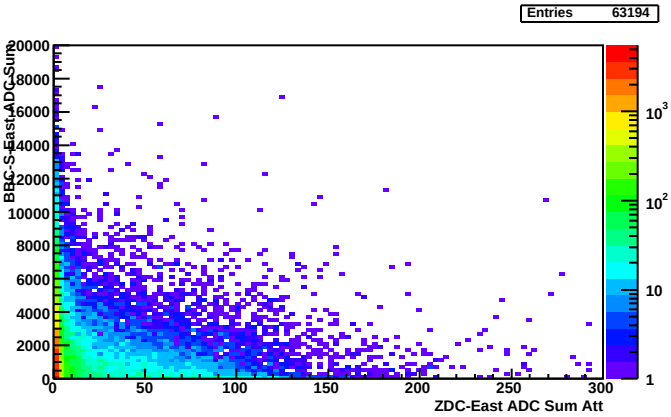

Entries 63194
TOF Mult
ZDC-West ADC Sum Att

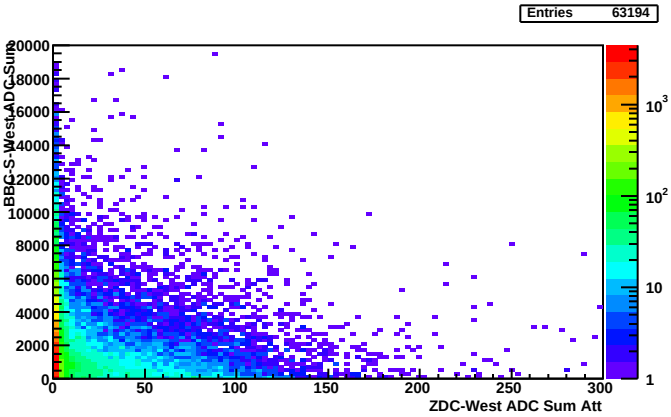

Entries 63194
BBC-L-East ADC Sum
BBC-S-East ADC Sum

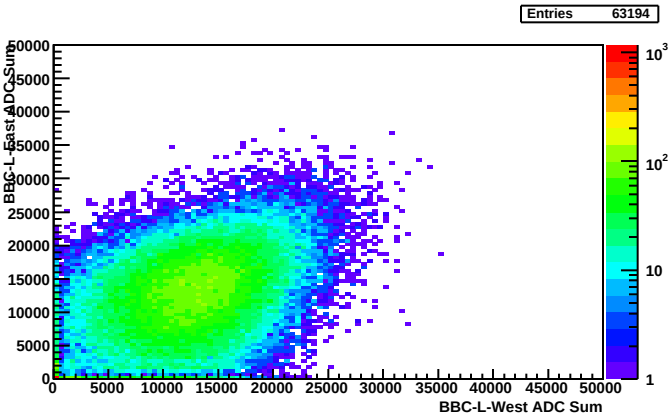

Entries 63194
BBC-L-West ADC Sum
BBC-S-West ADC Sum

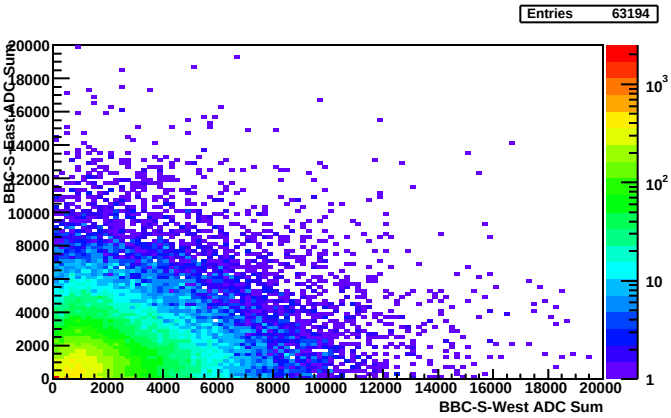

Entries 63194
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

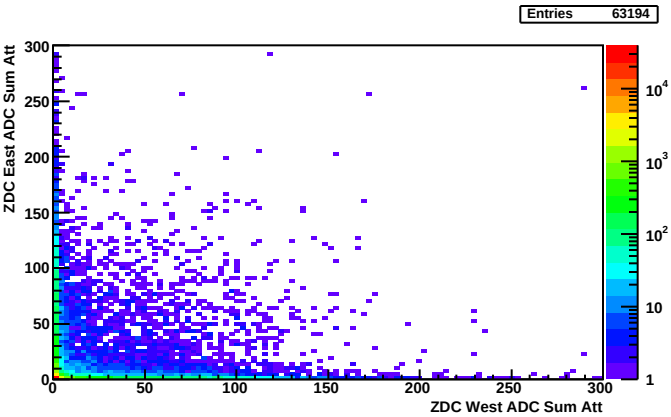

Entries 63194
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

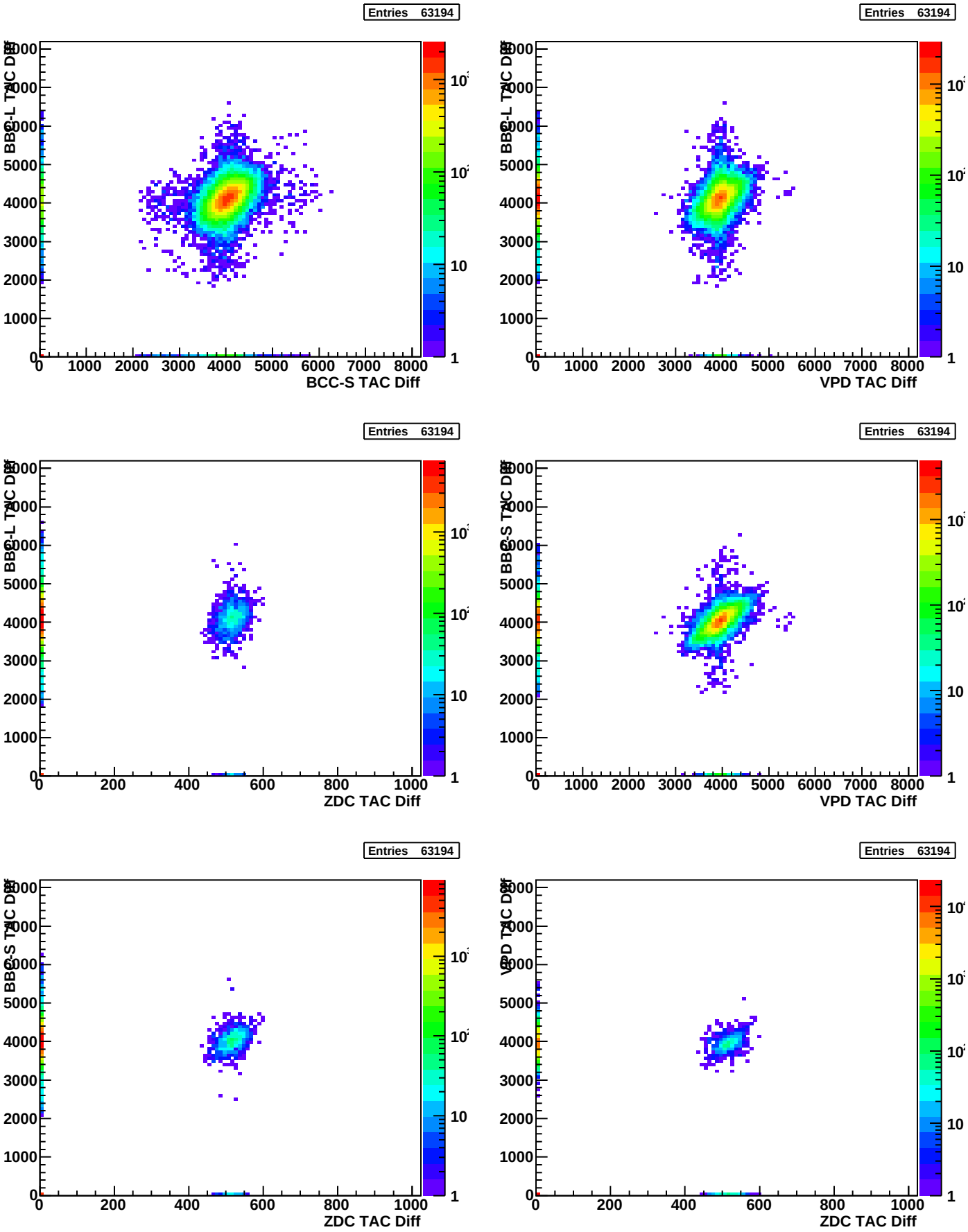

Entries 63194
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

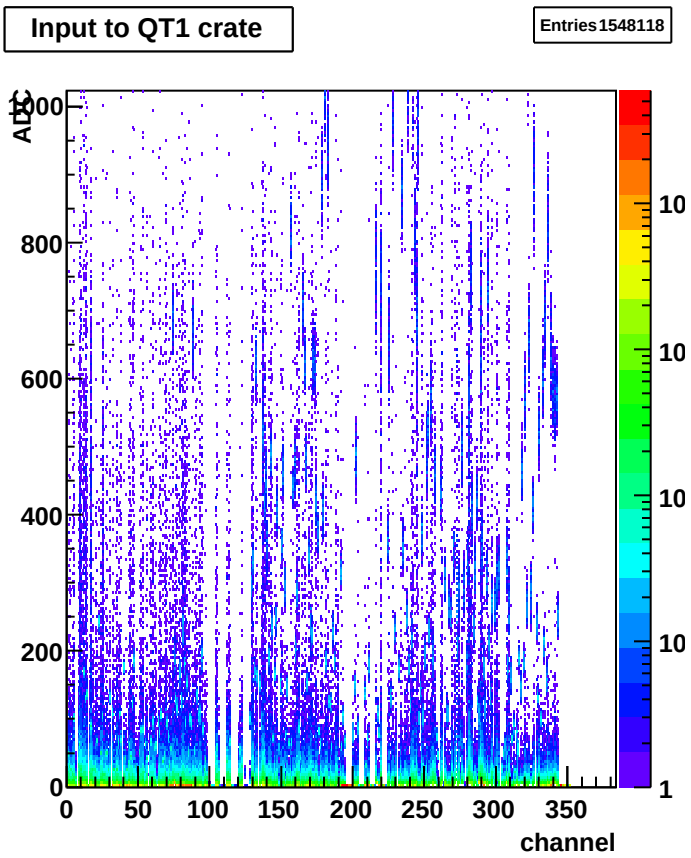

Entries 63194
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

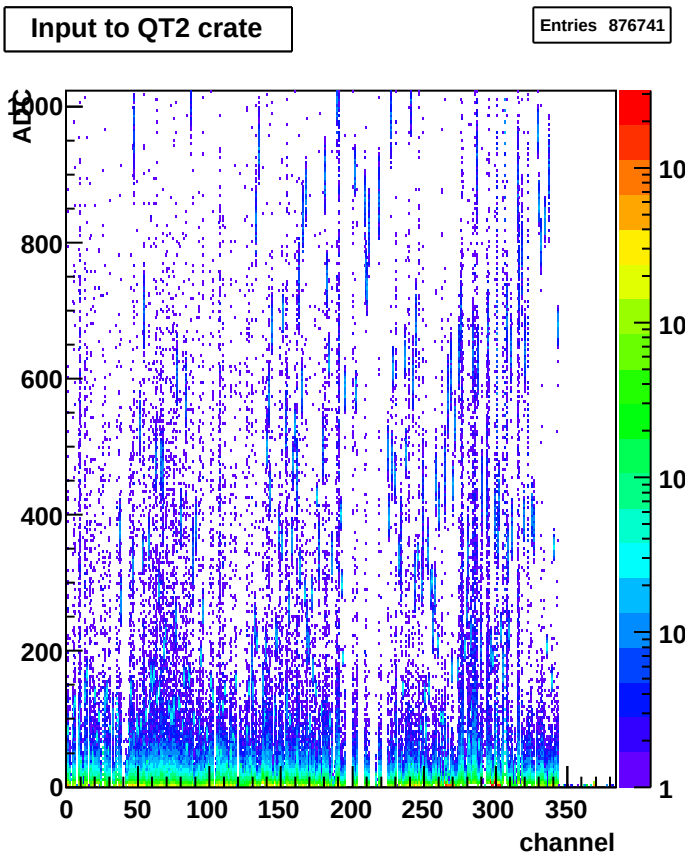

Entries 63194
BBC-L-East ADC Sum
BBC-L-West ADC Sum

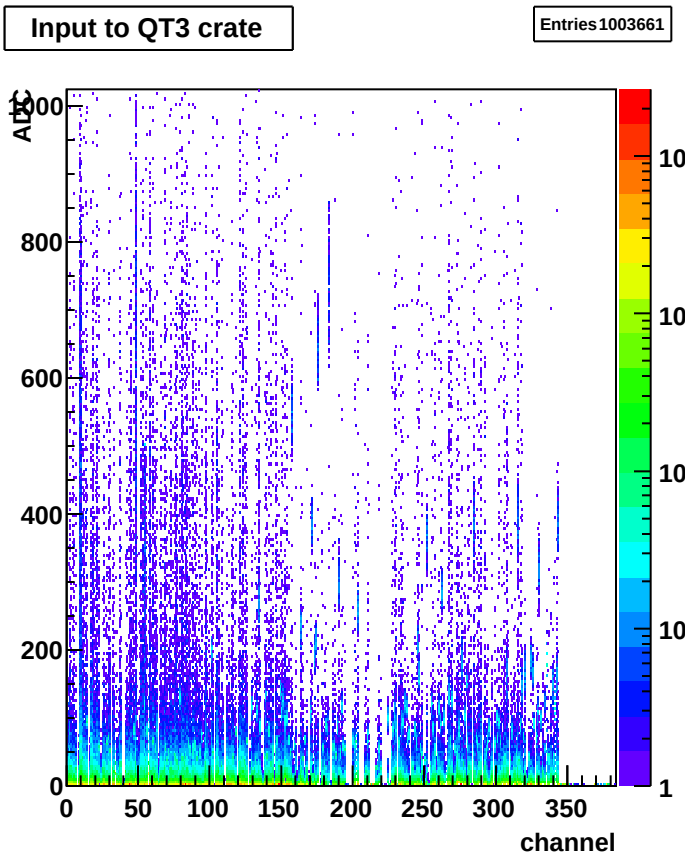

Entries 63194
BBC-S-East ADC Sum
BBC-S-West ADC Sum

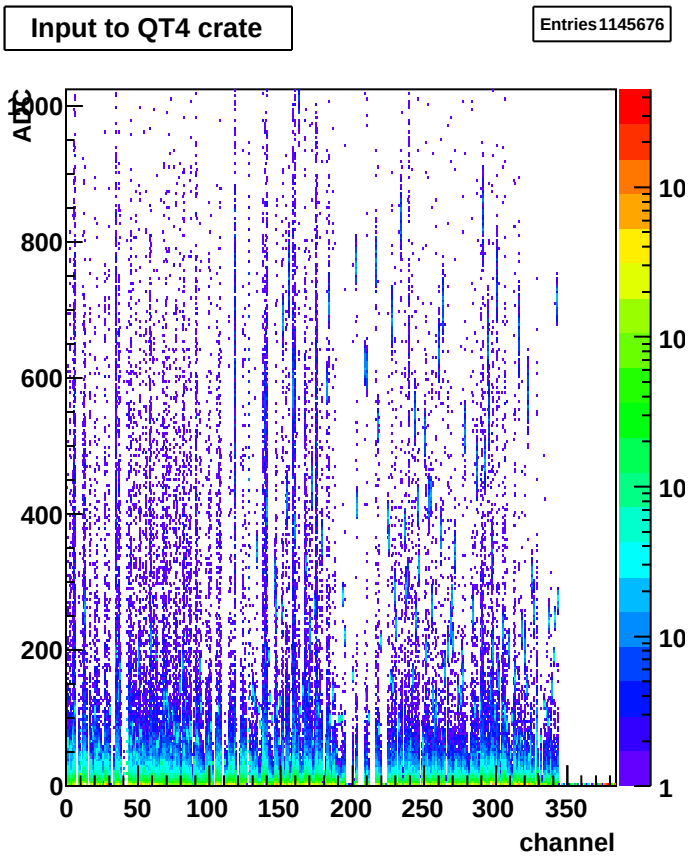

Entries 63194
ZDC East ADC Sum Att
ZDC West ADC Sum Att

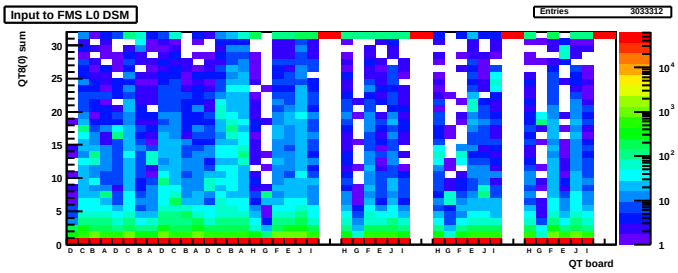

Entries 63194
BBC-L TAC Diff
BCC-S TAC Diff
Entries 63194
BBC-L TAC Diff
VPD TAC Diff
Entries 63194
BBC-L TAC Diff
ZDC TAC Diff
Entries 63194
BBC-S TAC Diff
VPD TAC Diff
Entries 63194
BBC-S TAC Diff
ZDC TAC Diff
Entries 63194
VPD TAC Diff
ZDC TAC Diff

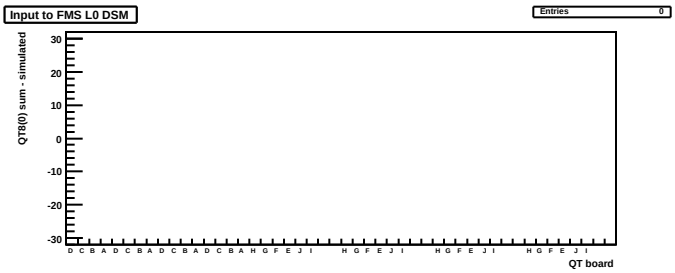

Input to QT1 crate
Entries 1548118
ADC
channel

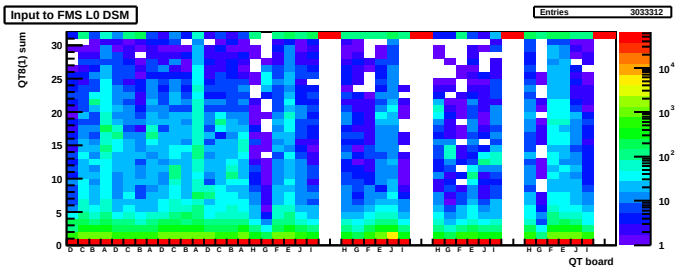

Input to QT2 crate
Entries 876741
ADC
channel

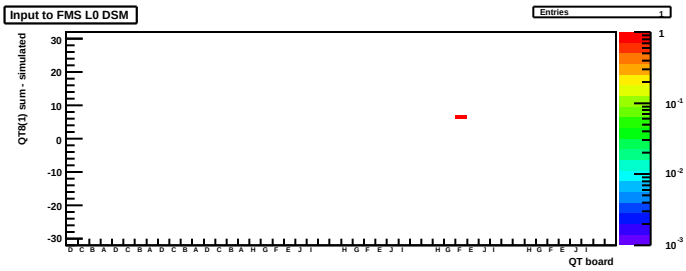

Input to QT3 crate
Entries 1003661
ADC
channel

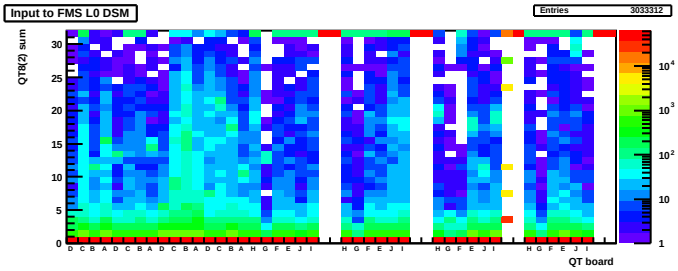

Input to QT4 crate
Entries 1145676
ADC
channel

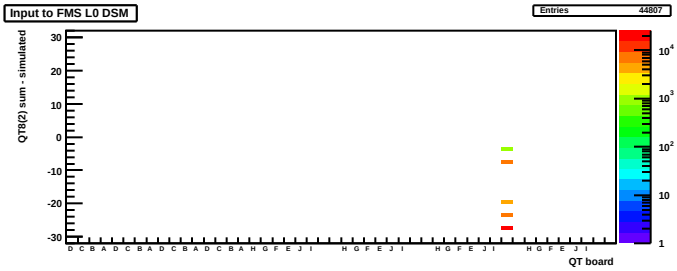

Input to FMS L0 DSM
Entries 3033312
QT8(0) sum
QT board

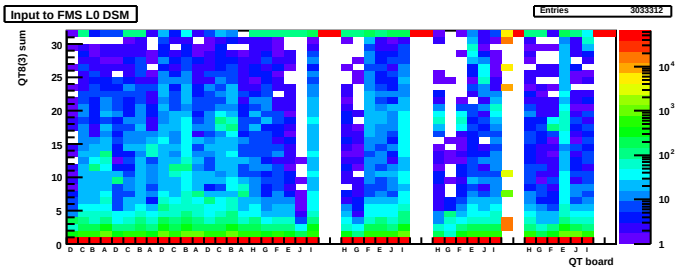

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

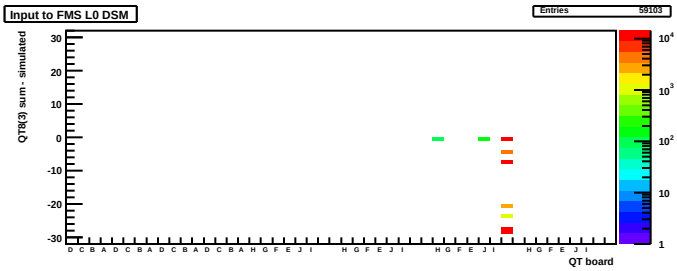

Input to FMS L0 DSM
Entries 3033312
QT8(1) sum
QT board

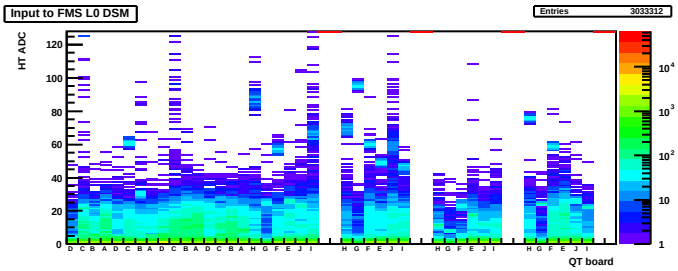

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

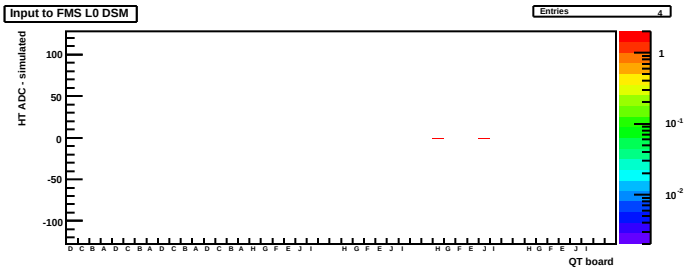

Input to FMS L0 DSM
Entries 3033312
QT8(2) sum
QT board

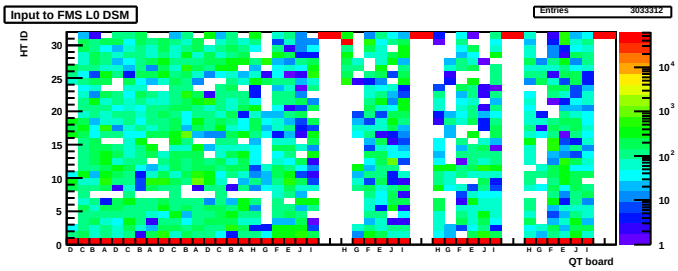

Input to FMS L0 DSM
Entries 44807
QT8(2) sum - simulated
QT board

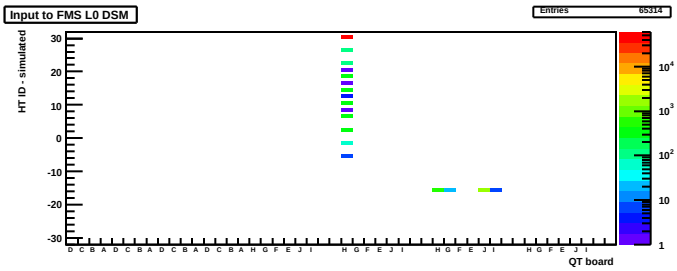

Input to FMS L0 DSM
Entries 3033312
QT8(3) sum
QT board

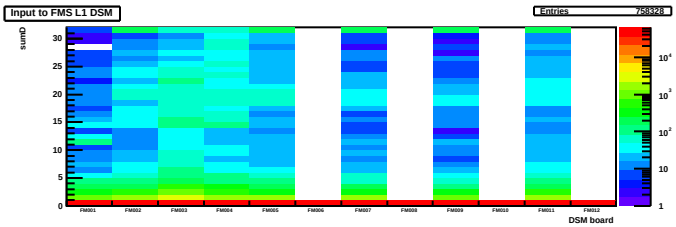

Input to FMS L0 DSM
Entries 59103
QT8(3) sum - simulated
QT board

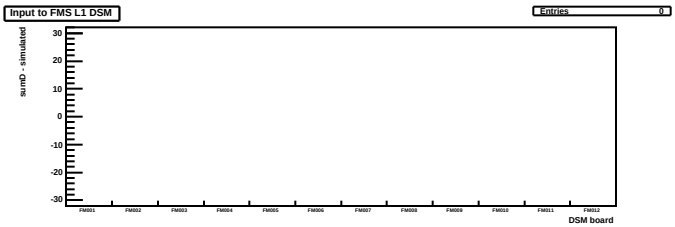

Input to FMS L0 DSM
Entries 3033312
HT ADC
QT board

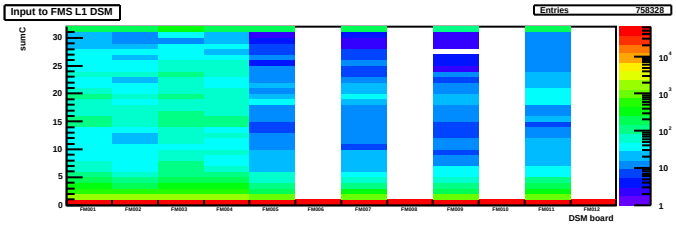

Input to FMS L0 DSM
Entries 4
HT ADC - simulated
QT board

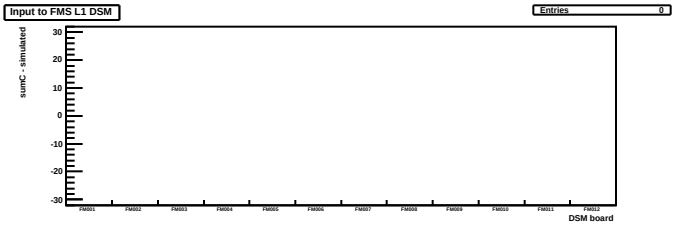

Input to FMS L0 DSM
Entries 3033312
HT ID
QT board

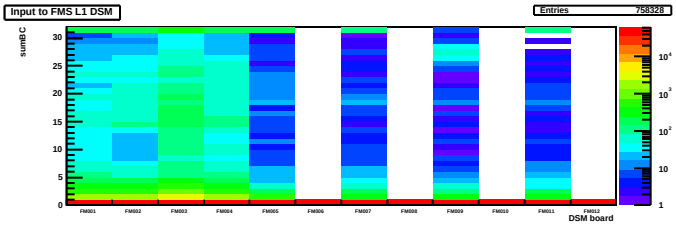

Input to FMS L0 DSM
Entries 65314
HT ID - simulated
QT board

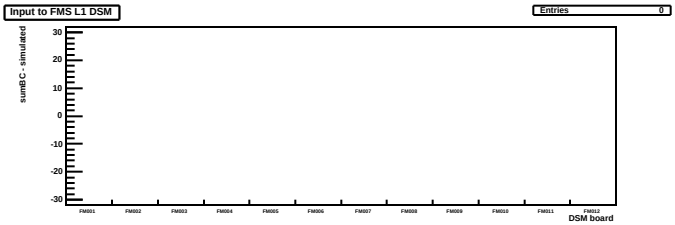

Input to FMS L1 DSM
Entries 758328
sumD
DSM board

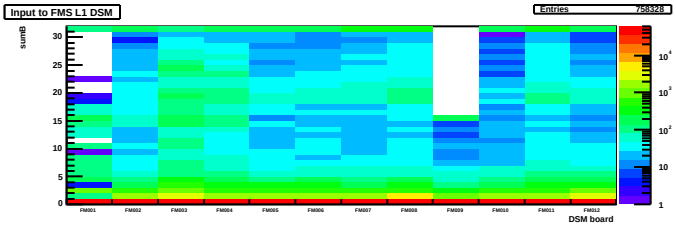

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

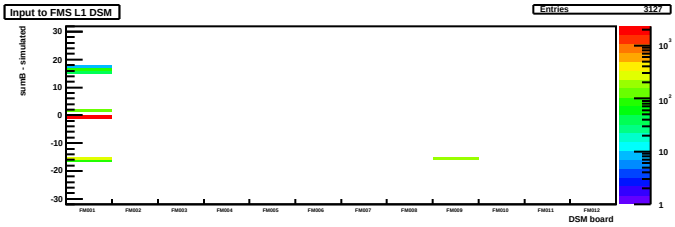

Input to FMS L1 DSM
Entries 758328
sumC
DSM board

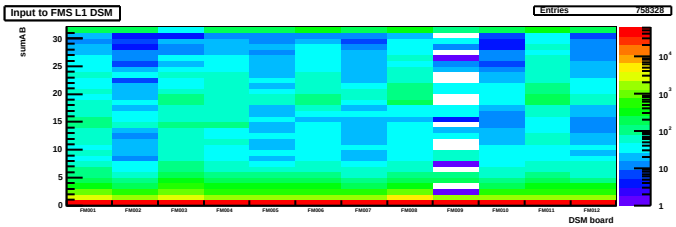

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

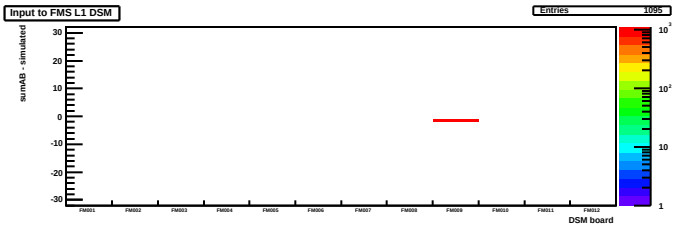

Input to FMS L1 DSM
Entries 758328
sumBC
DSM board

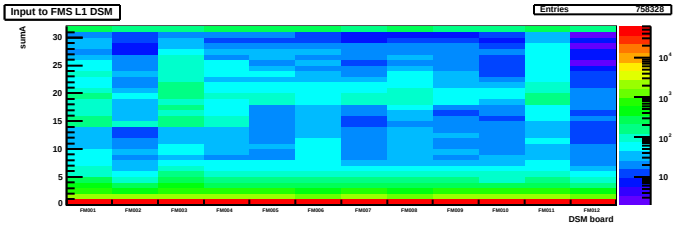

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

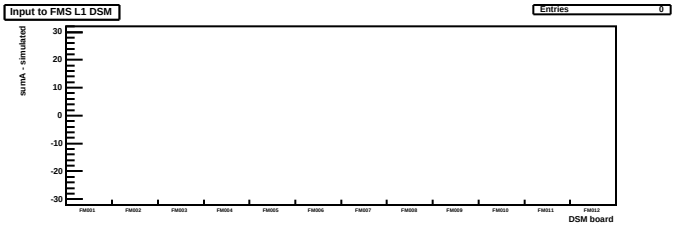

Input to FMS L1 DSM
Entries 758328
sumB
DSM board

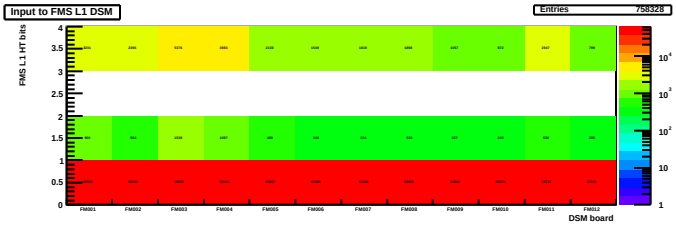

Input to FMS L1 DSM
Entries 3127
sumB - simulated
DSM board

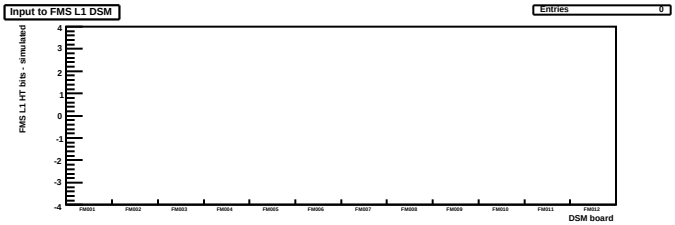

Input to FMS L1 DSM
Entries 758328
sumAB
DSM board

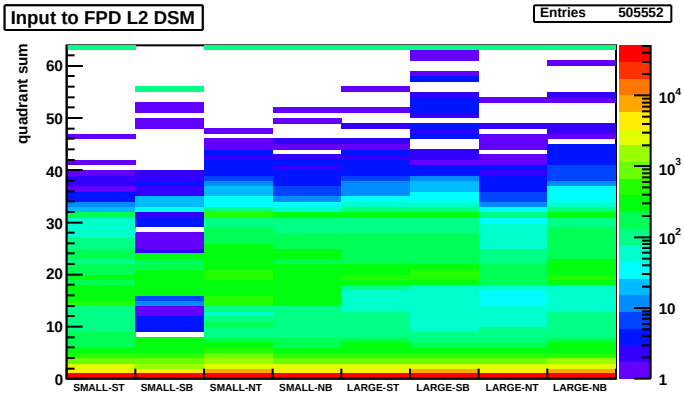

Input to FMS L1 DSM
Entries 1095
sumAB - simulated
DSM board

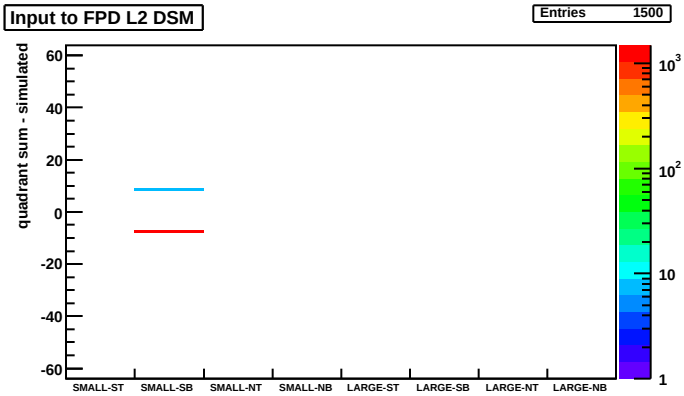

Input to FMS L1 DSM
Entries 758328
sumA
DSM board

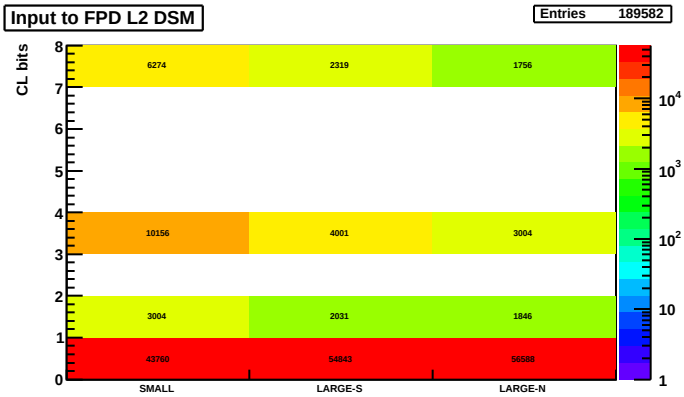

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

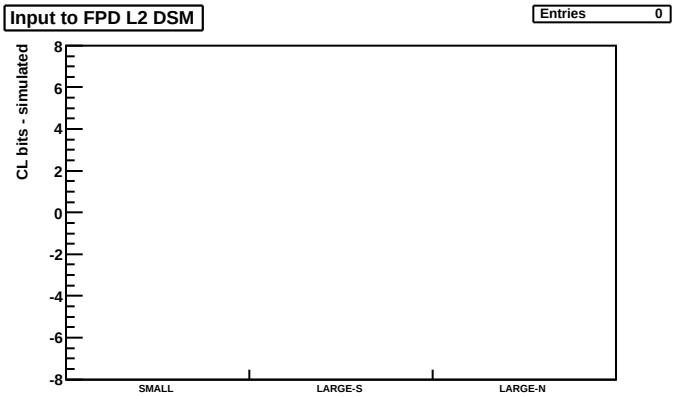

Input to FMS L1 DSM
Entries 758328
FMS L1 HT bits
DSM board

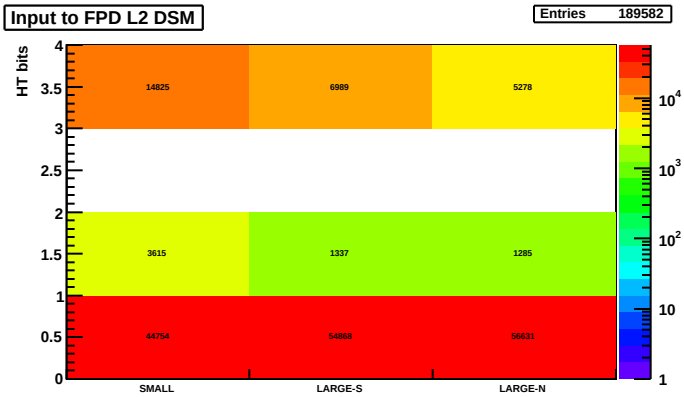

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

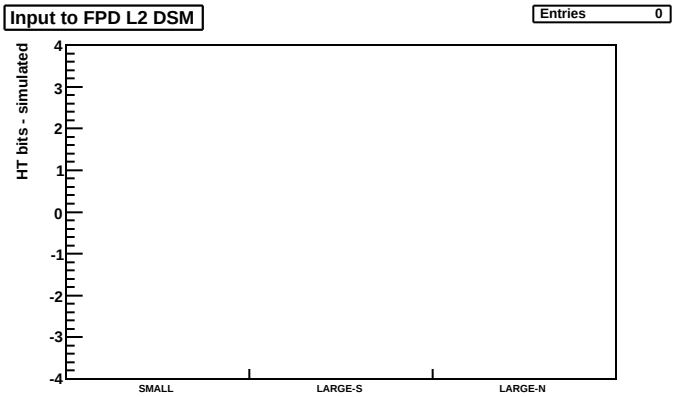

Input to FPD L2 DSM
Entries 505552
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

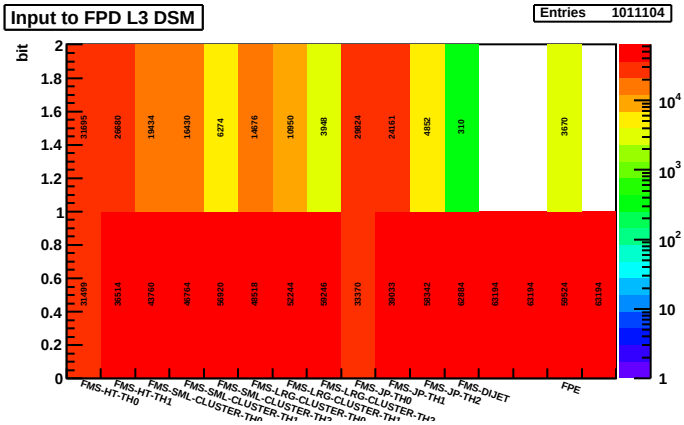

Input to FPD L2 DSM
Entries 1500
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

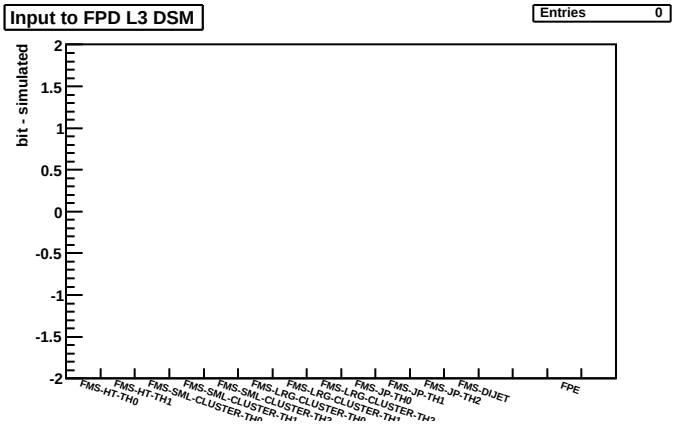

Input to FPD L2 DSM
Entries 189582
CL bits
6274
2319
1756
10156
4001
3004
3004
2031
1846
43760
54843
56588
SMALL
LARGE-S
LARGE-N

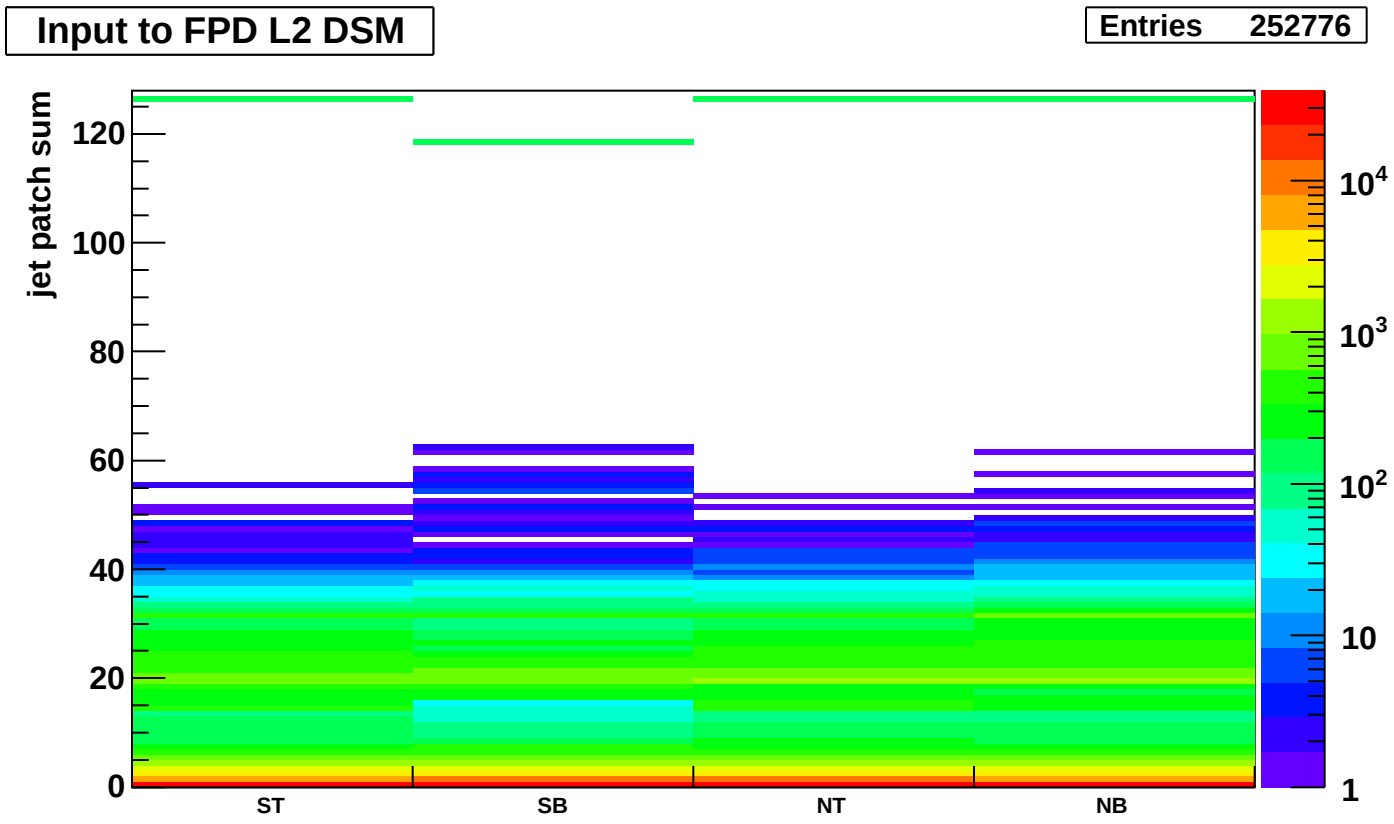

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

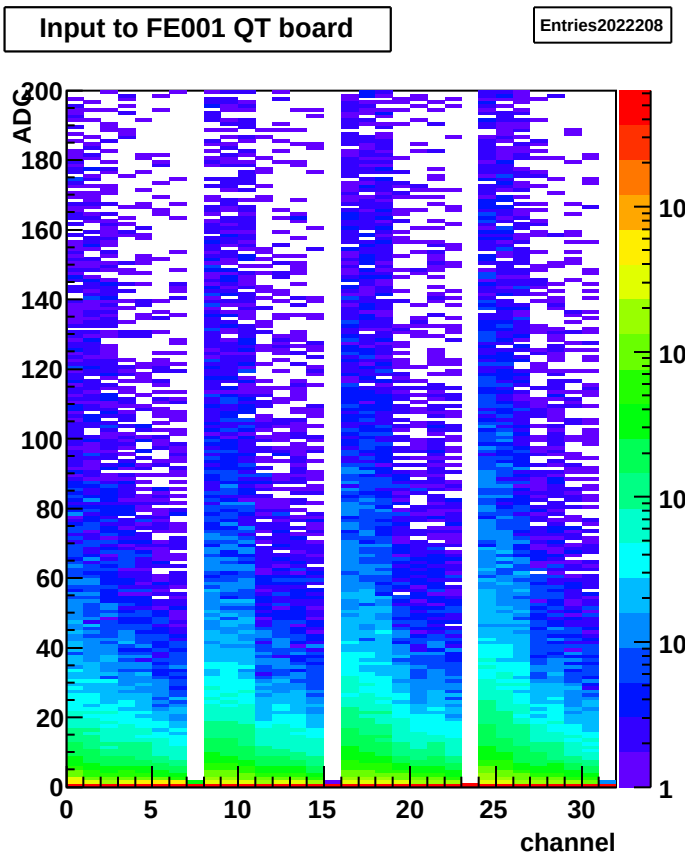

Input to FPD L2 DSM
Entries 189582
HT bits
14825
6989
5278
3615
1337
1285
44754
54868
56631
SMALL
LARGE-S
LARGE-N

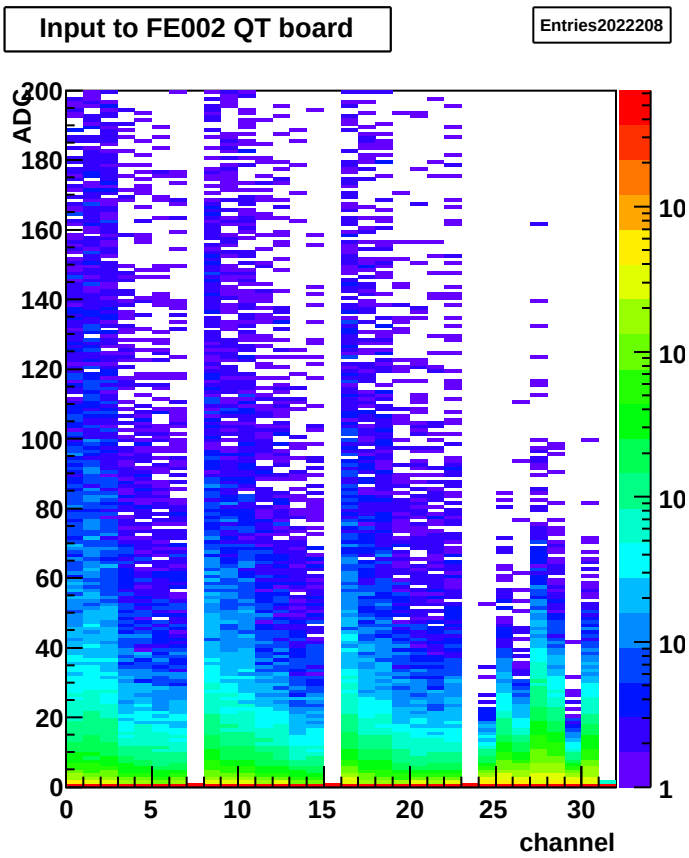

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

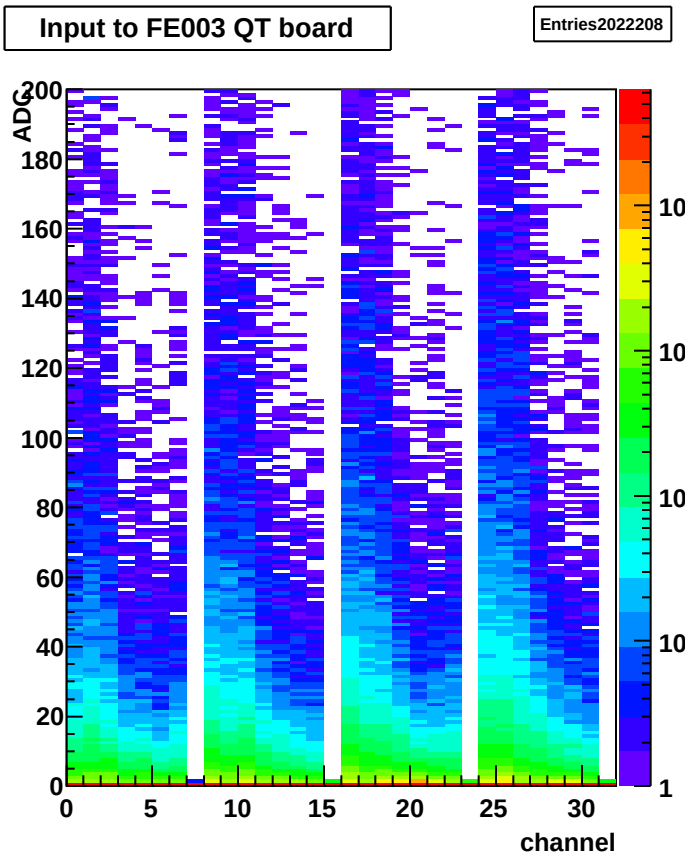

Input to FPD L3 DSM
Entries 1011104
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

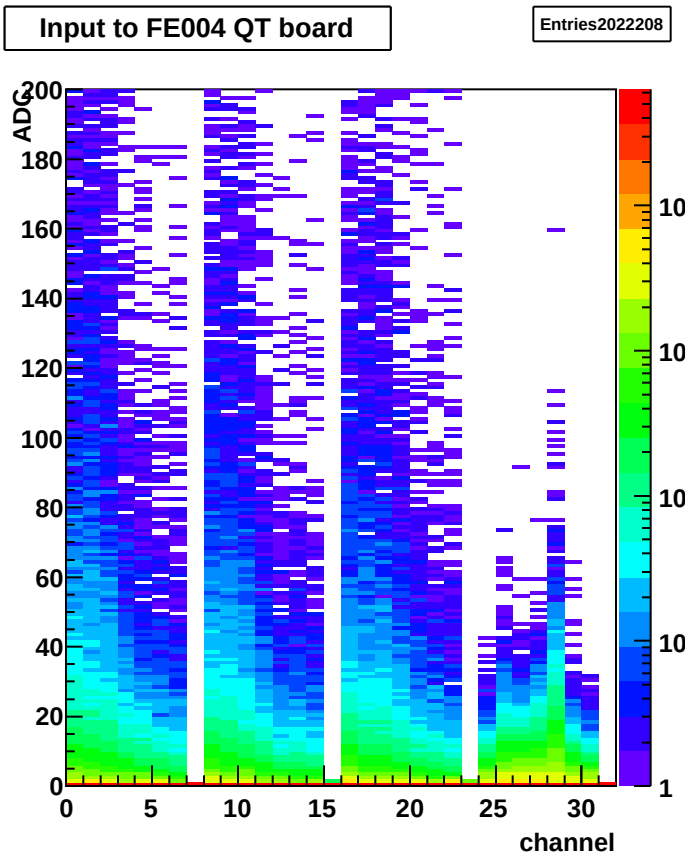

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

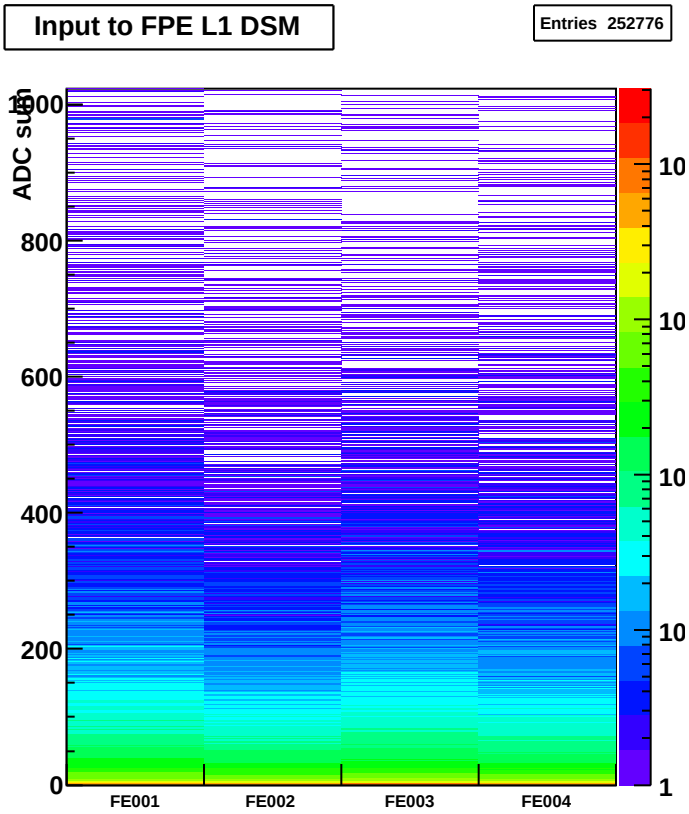

Input to FPD L2 DSM
Entries 252776
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

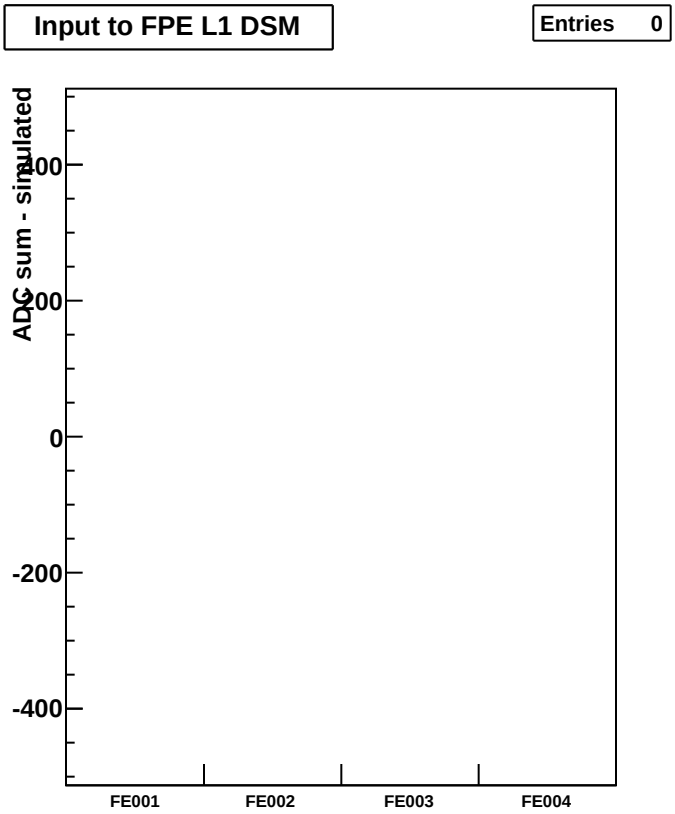

Input to FE001 QT board
Entries2022208
ADC
channel

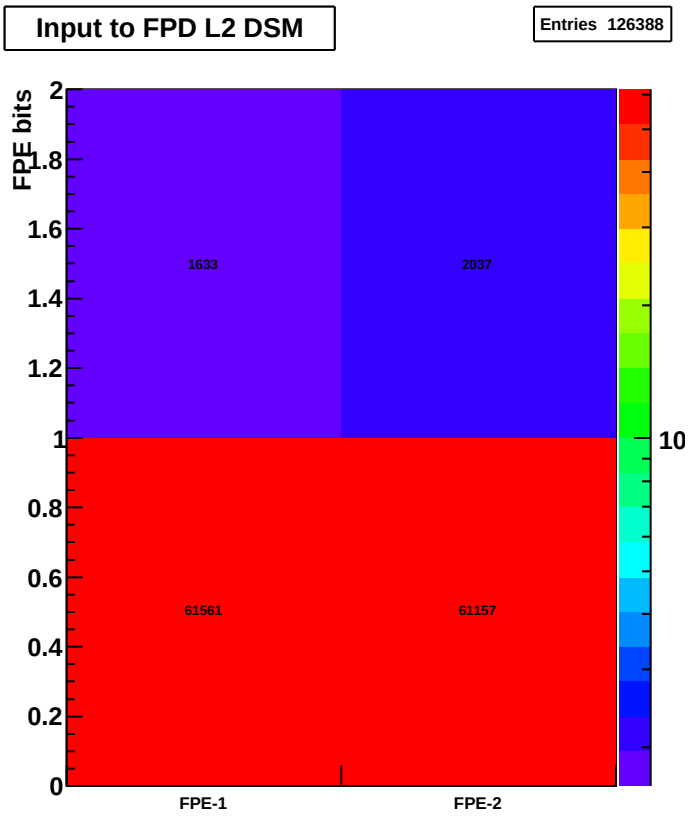

Input to FE002 QT board
Entries2022208
ADC
channel

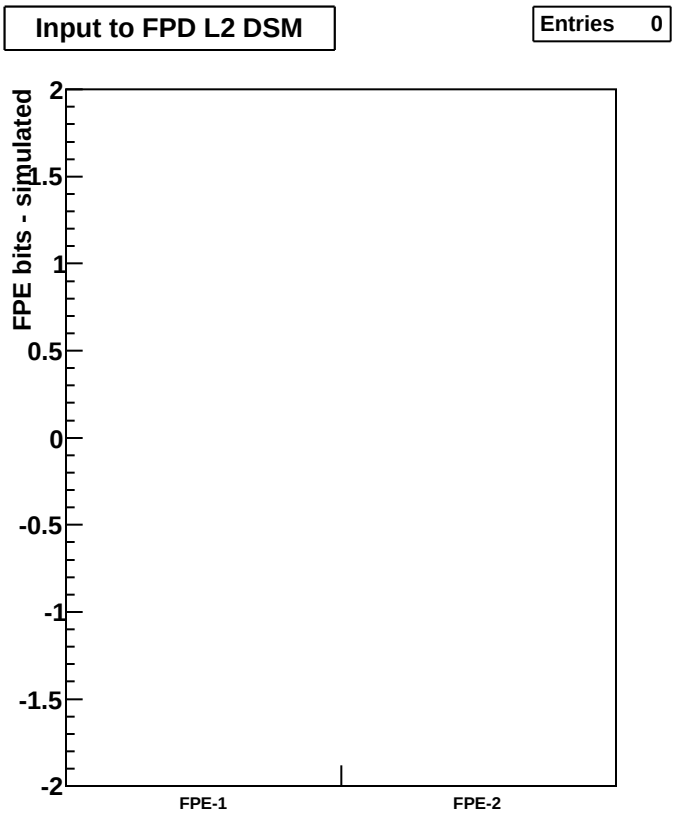

Input to FE003 QT board
Entries2022208
ADC
channel


Input to FE004 QT board
Entries2022208
ADC
channel


Input to FPE L1 DSM
Entries 252776
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 126388
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1633
2037
61561
61157
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2