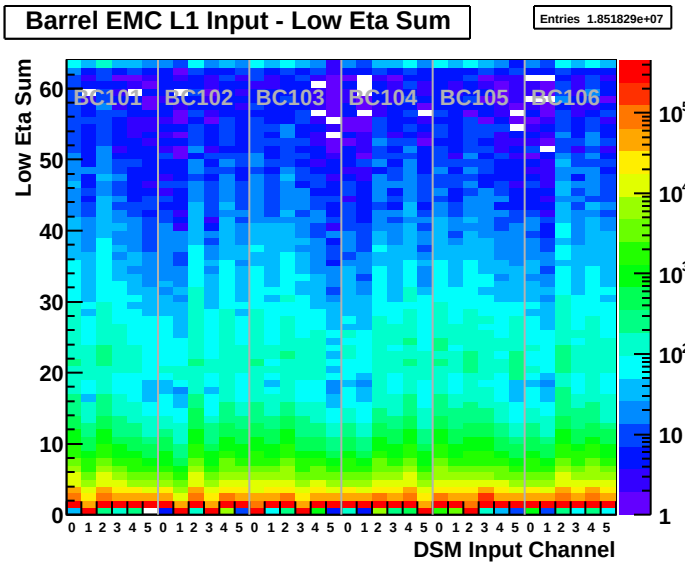
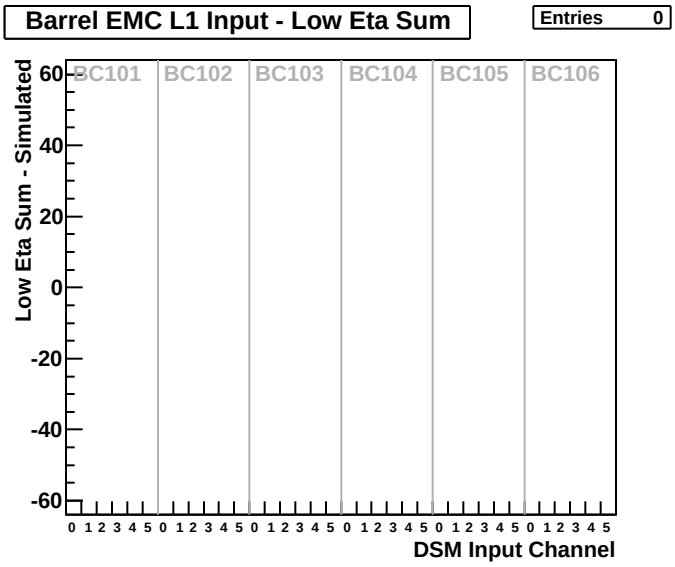
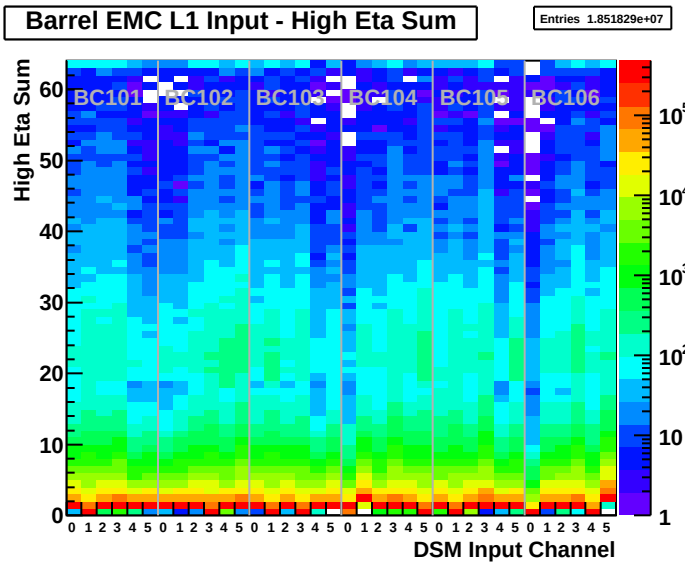
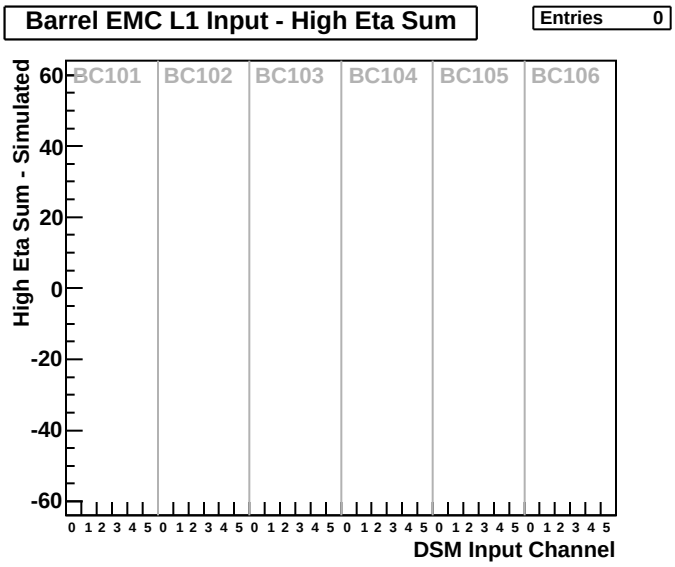
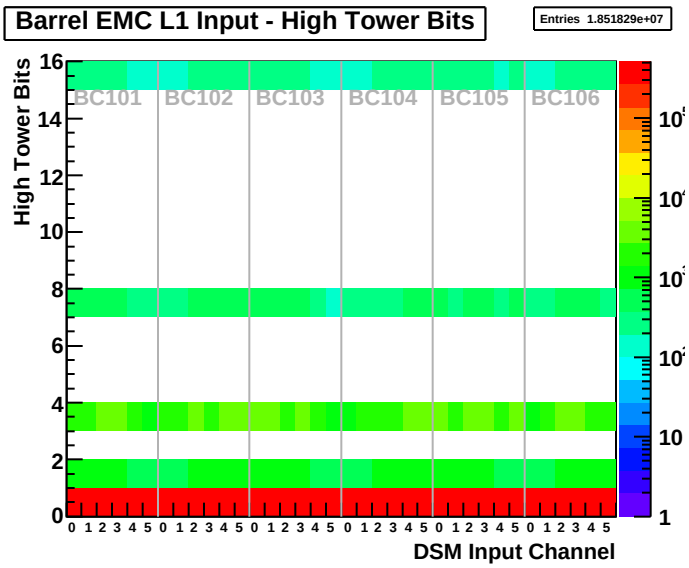
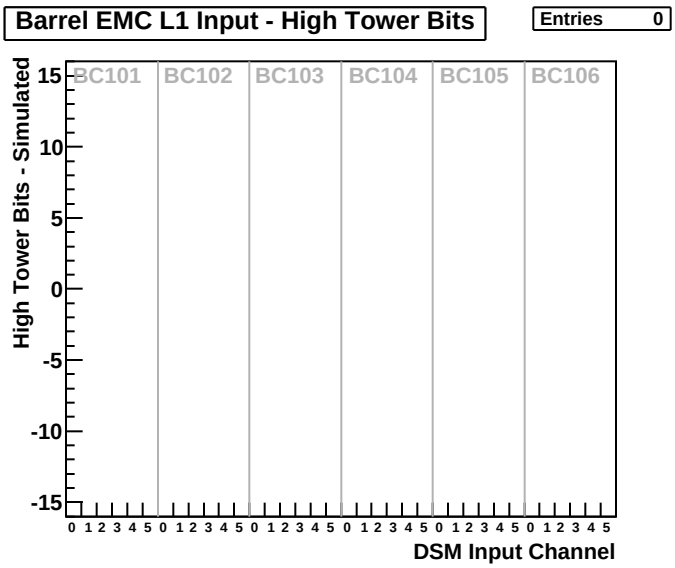

Barrel EMC L1 Input - Low Eta Sum
Entries 1.851829e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

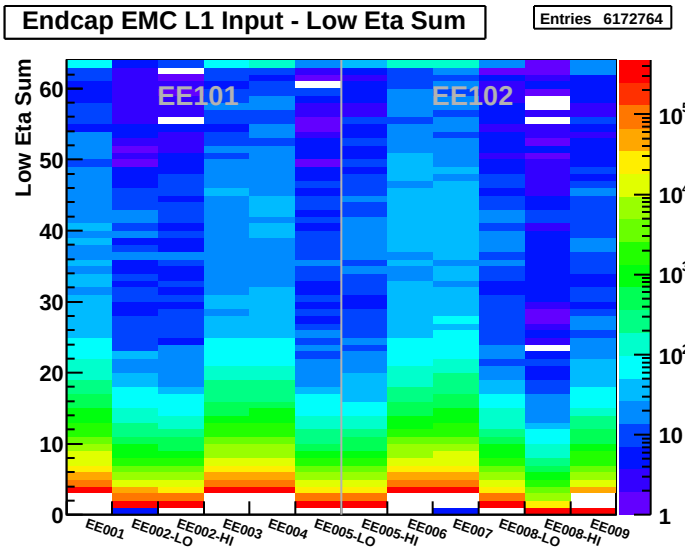

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

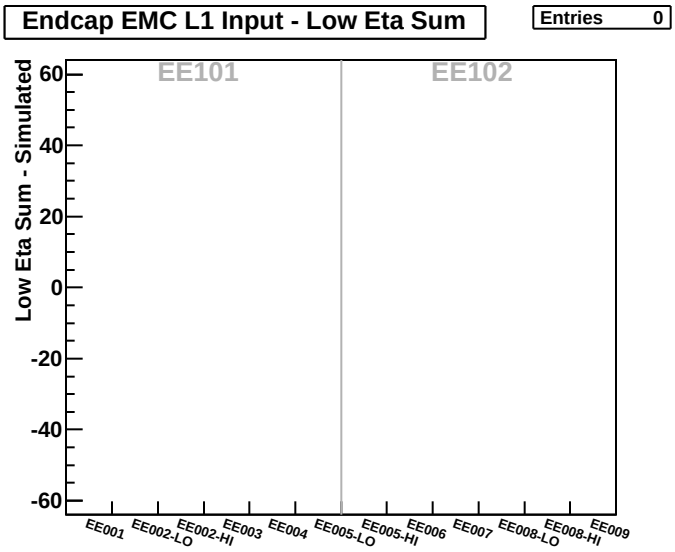

Barrel EMC L1 Input - High Eta Sum
Entries 1.851829e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

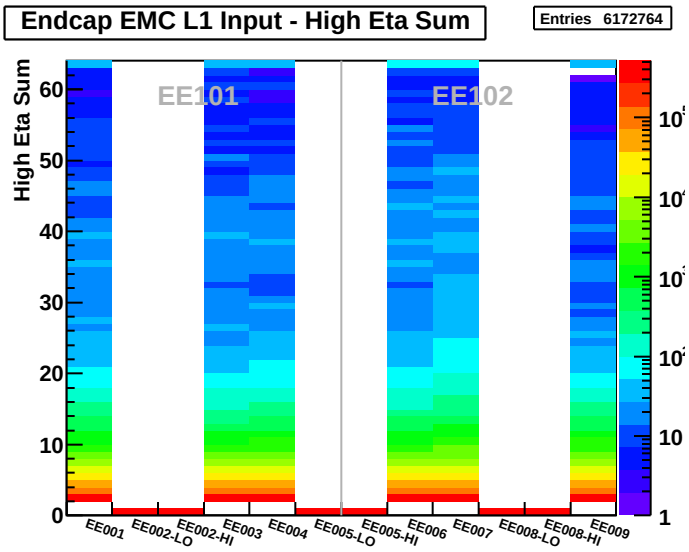

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

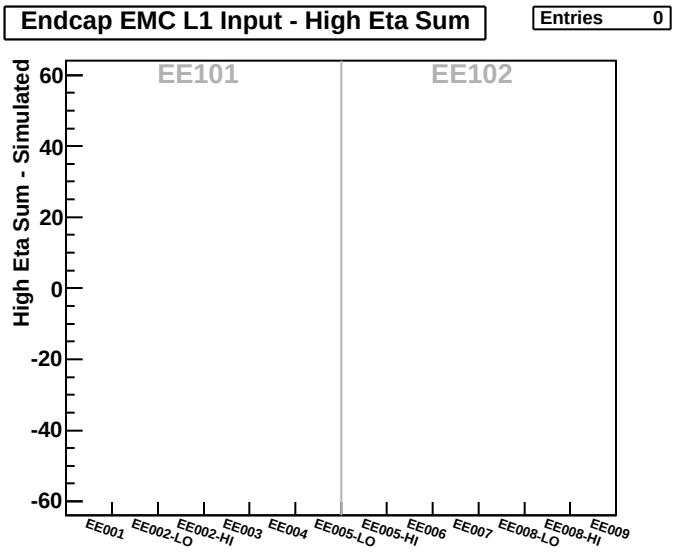

Barrel EMC L1 Input - High Tower Bits
Entries 1.851829e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

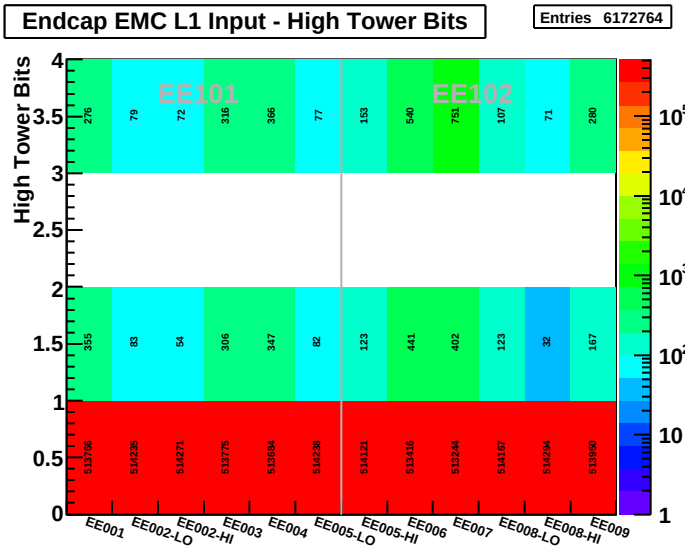

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

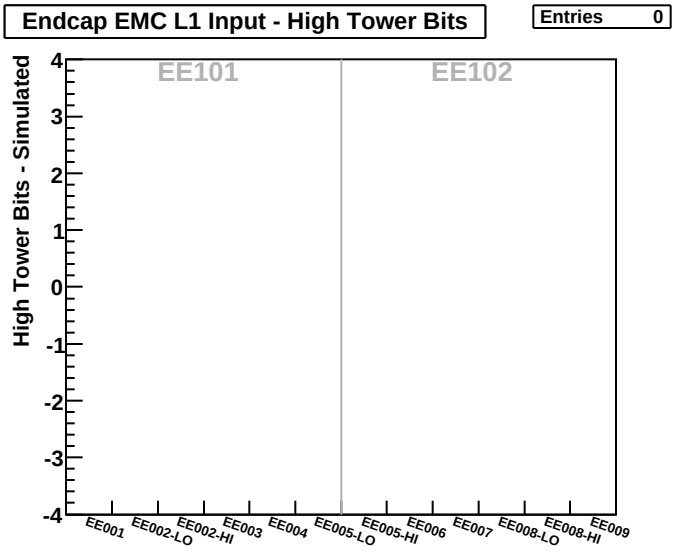

Endcap EMC L1 Input - Low Eta Sum
Entries 6172764
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

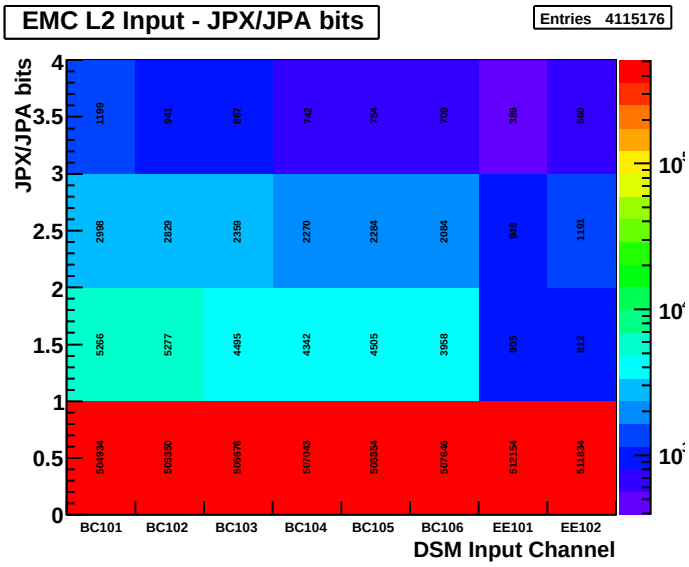

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

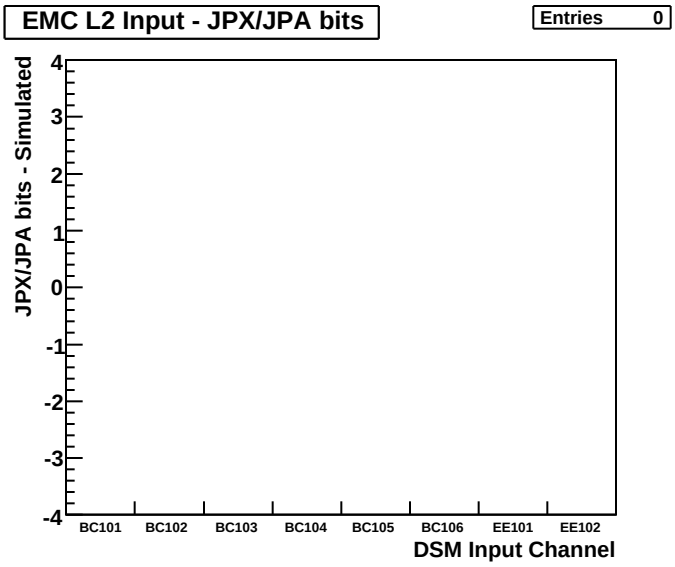

Endcap EMC L1 Input - High Eta Sum
Entries 6172764
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

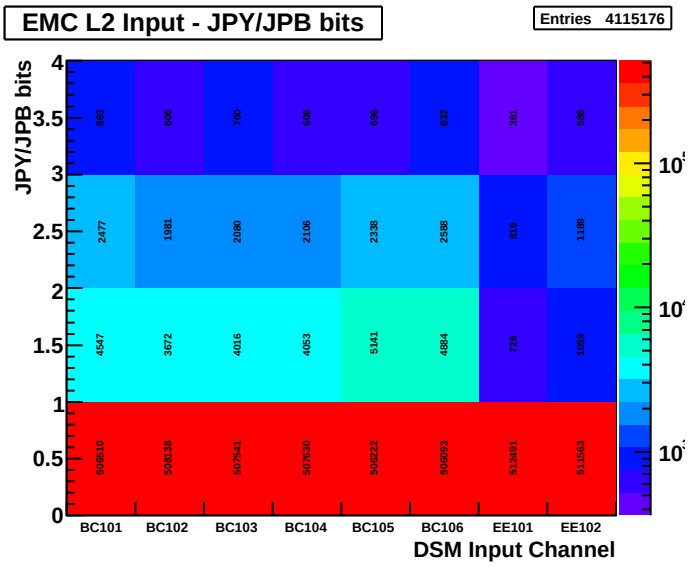

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

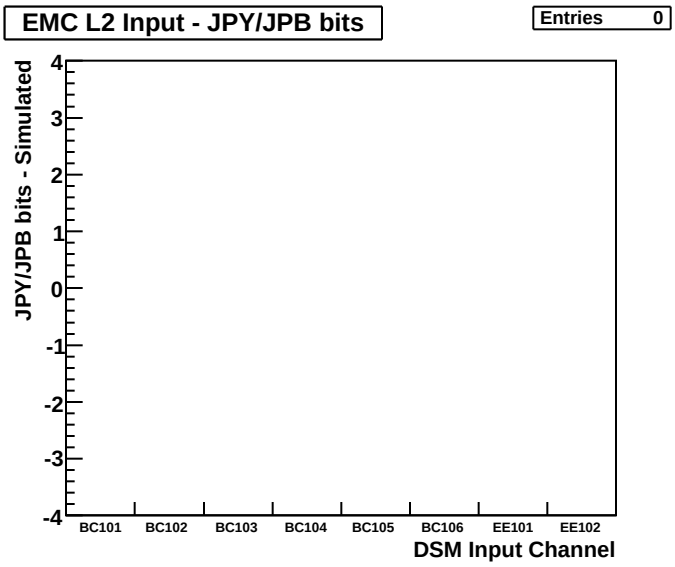

Endcap EMC L1 Input - High Tower Bits
Entries 6172764
High Tower Bits
EE101
EE102
276 79 72 316 366 77 153 540 751 107 71 280
355 83 54 306 347 82 123 441 402 123 32 167
513766 514235 514271 513775 513684 514238 514121 513416 513244 514167 514294 513950
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

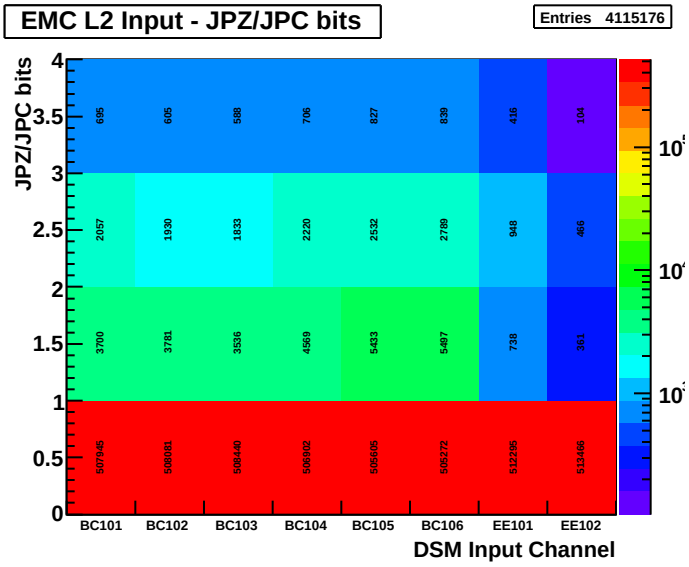

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

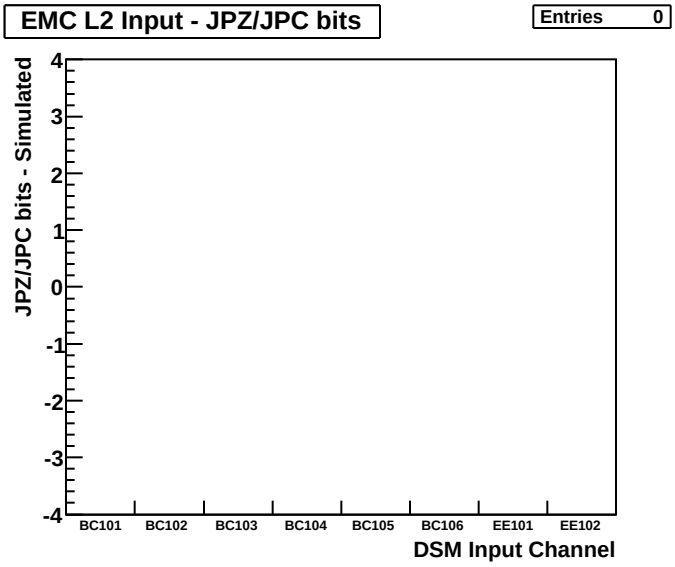

EMC L2 Input - JPX/JPA bits
Entries 4115176
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1199 941 867 742 754 709 389 560
2998 2829 2359 2270 2284 2084 949 1191
5266 5277 4495 4342 4505 3958 905 812
504934 505350 506676 507043 506854 507646 512154 511834

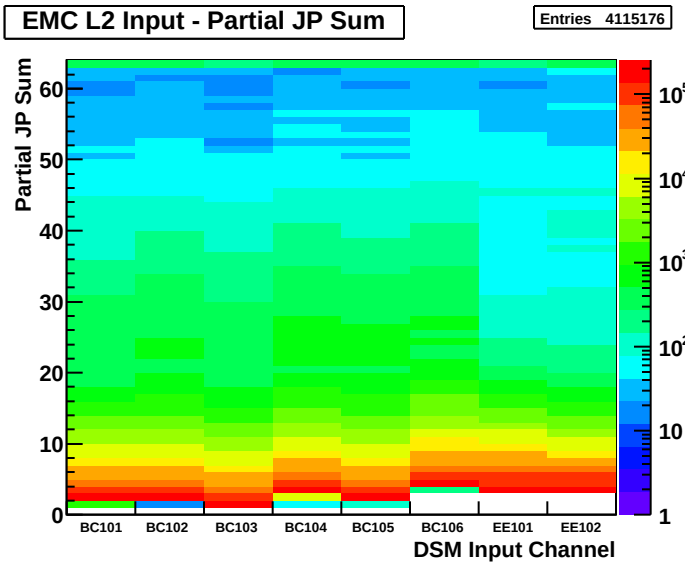

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

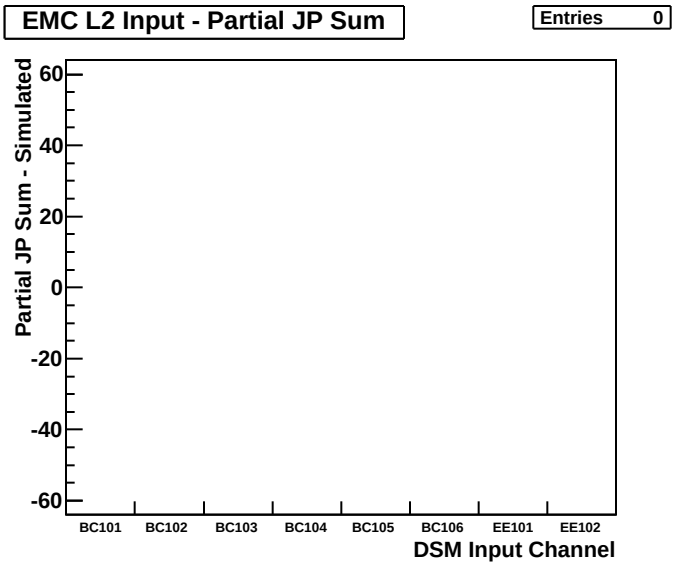

EMC L2 Input - JPY/JPB bits
Entries 4115176
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
665 606 760 608 696 832 361 586
2477 1981 2080 2106 2338 2588 819 1189
4547 3672 4016 4053 5141 4984 726 1059
506510 508138 507541 507630 508222 508093 512491 511563

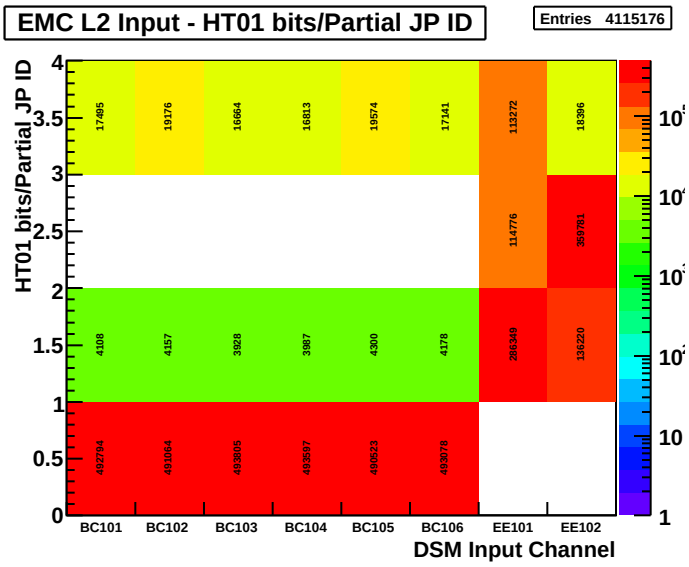

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

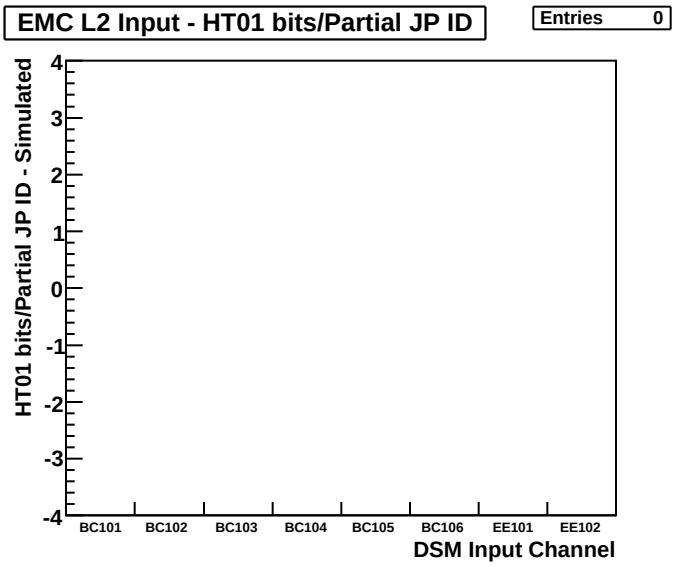

EMC L2 Input - JPZ/JPC bits
Entries 4115176
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
695 605 588 706 827 839 416 104
2057 1930 1833 2220 2532 2789 948 466
3700 3781 3536 4569 5433 5497 738 361
507945 508081 508440 506902 505605 505272 512295 513466

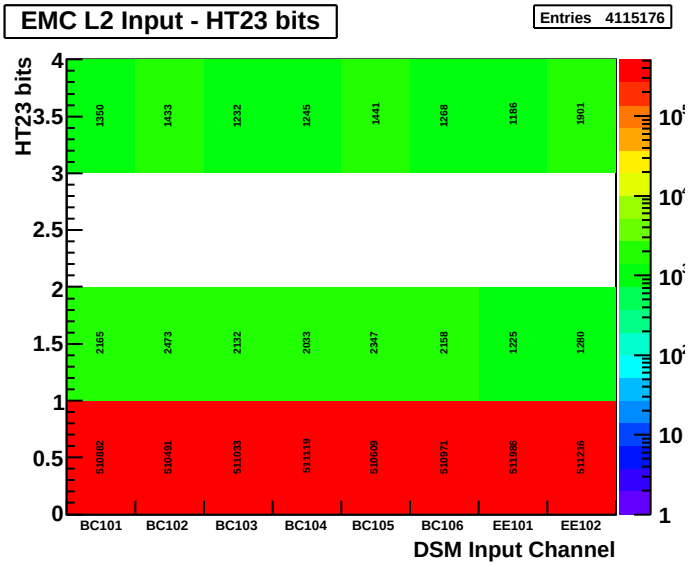

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

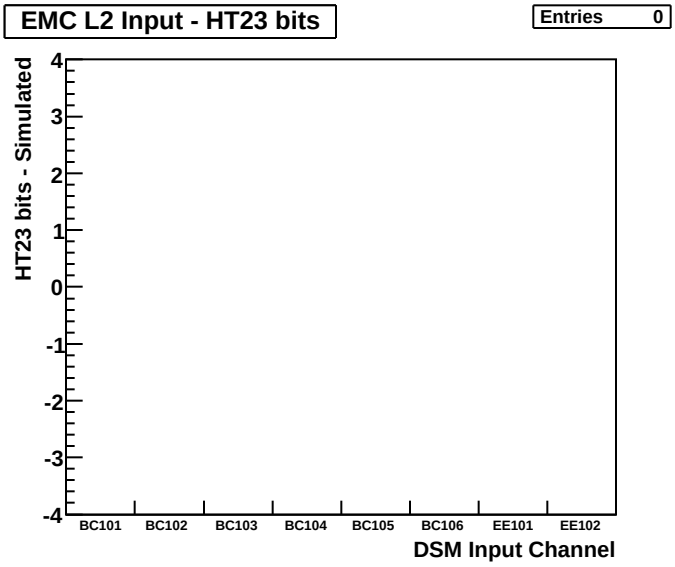

EMC L2 Input - Partial JP Sum
Entries 4115176
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

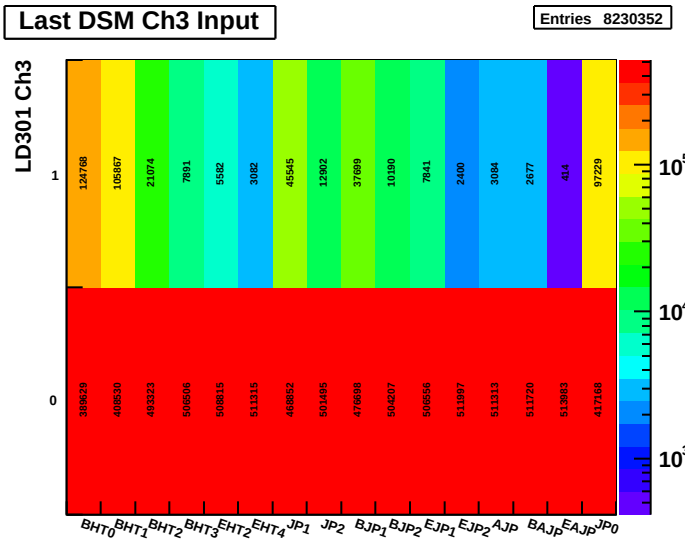

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

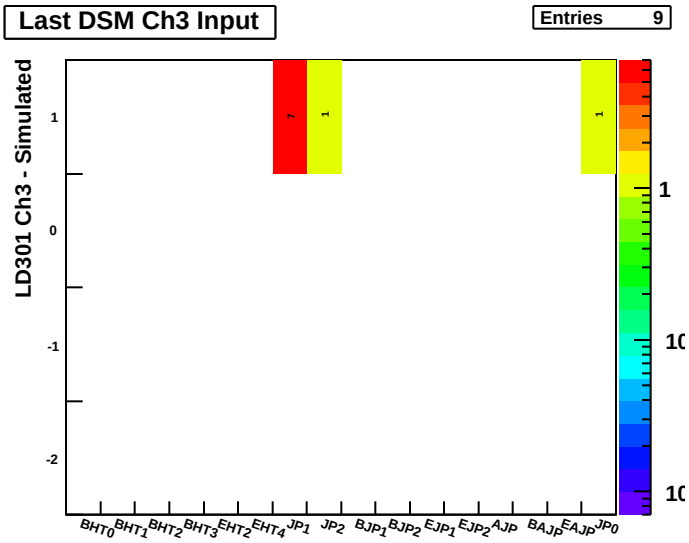

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4115176
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

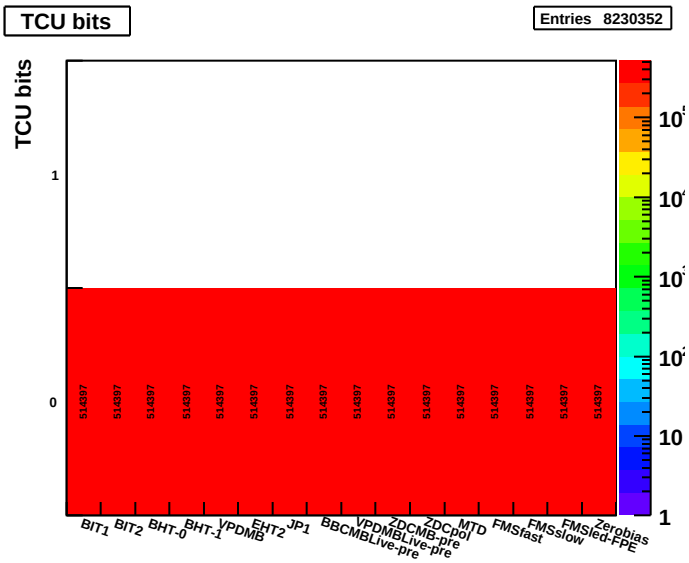

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

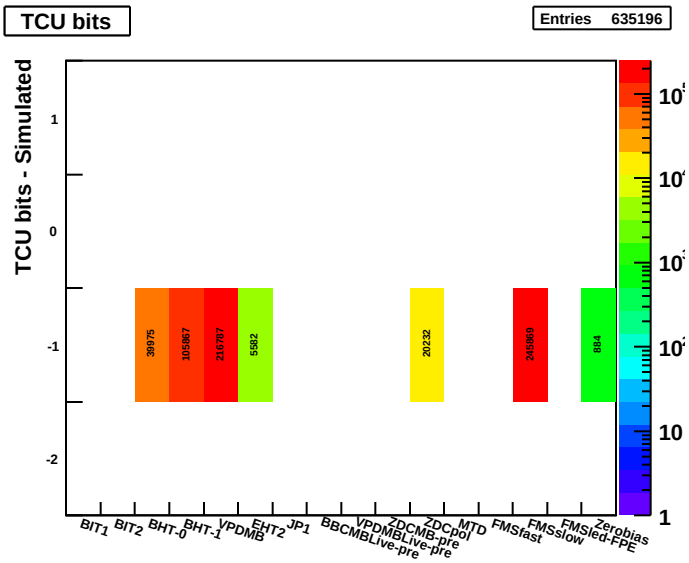

EMC L2 Input - HT23 bits
Entries 4115176
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

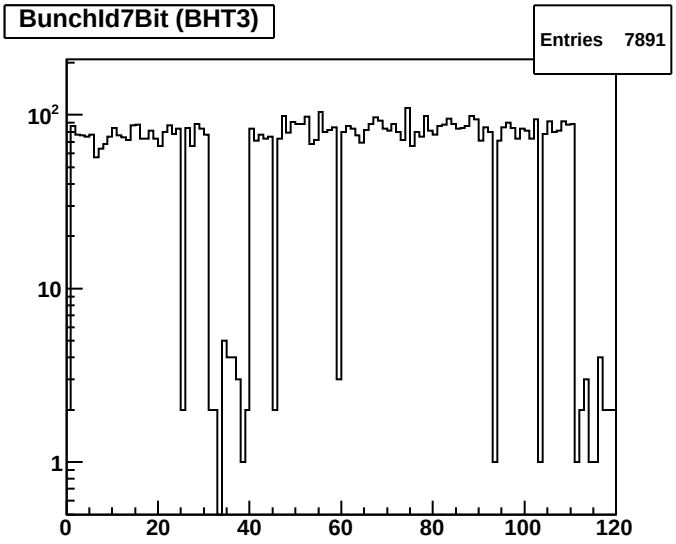

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

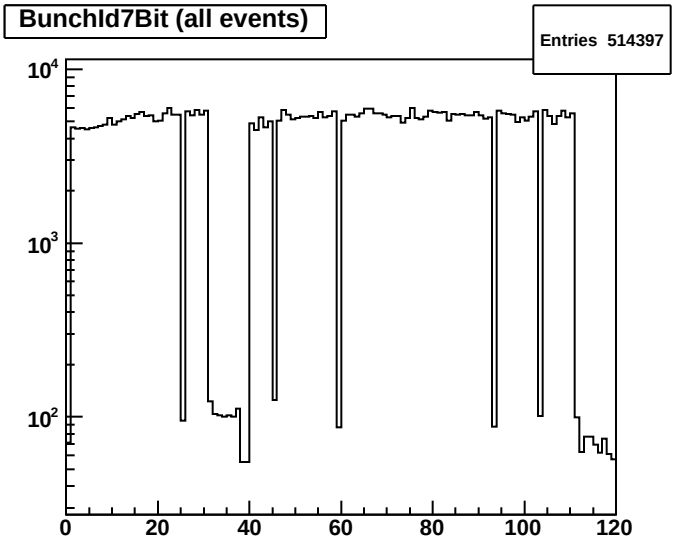

Last DSM Ch3 Input
Entries 8230352
LD301 Ch3
124768 105867 21074 7891 5582 3082 45545 12902 37699 10190 7841 2400 3084 2677 414 97229
389629 408530 493323 506506 508815 511315 468852 501495 476698 504207 506556 511997 511313 511720 513983 417168
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

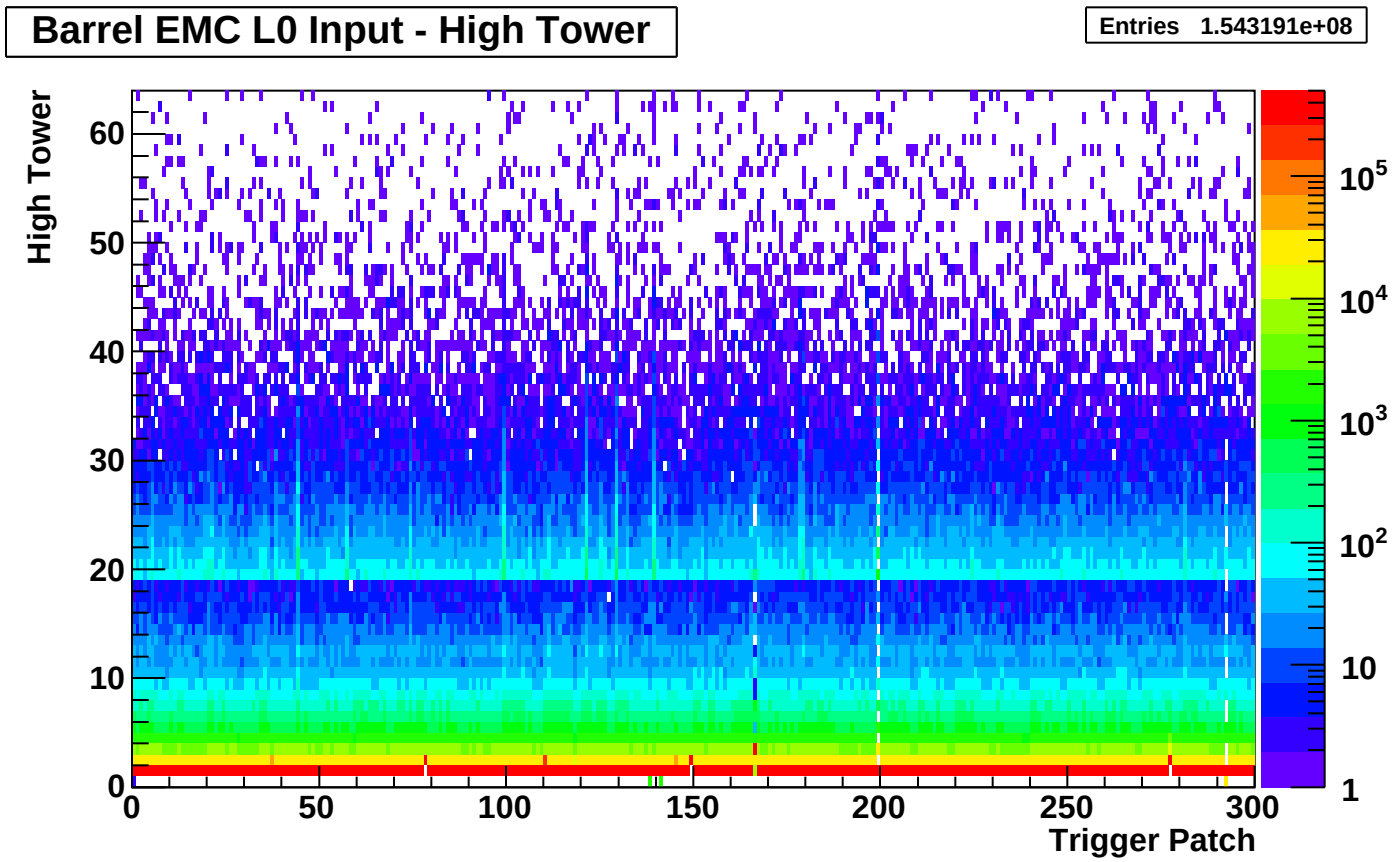

Last DSM Ch3 Input
Entries 9
LD301 Ch3 - Simulated
7 1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

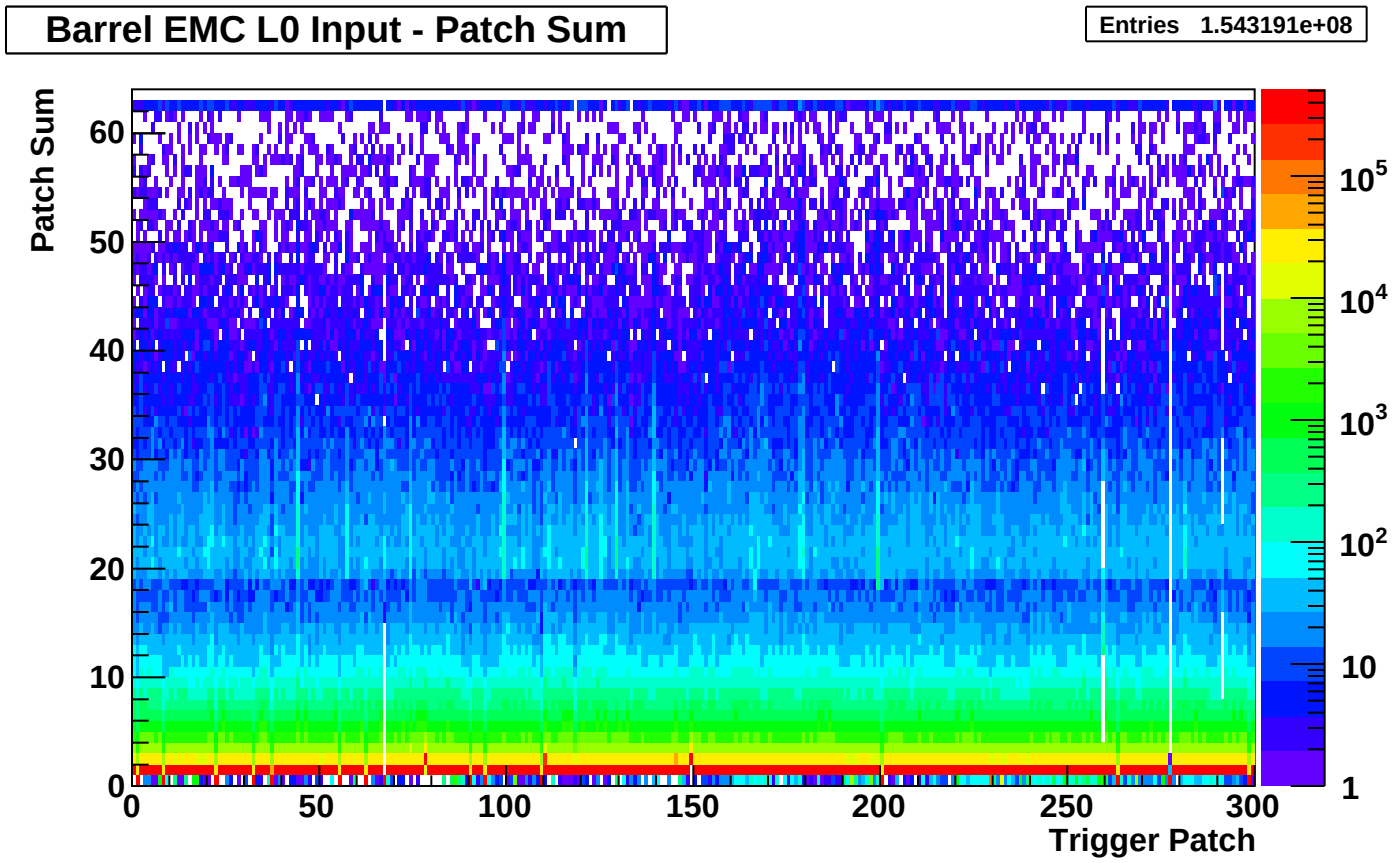

TCU bits
Entries 8230352
TCU bits
514397 514397 514397 514397 514397 514397 514397 514397 514397 514397 514397 514397 514397 514397 514397 514397
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

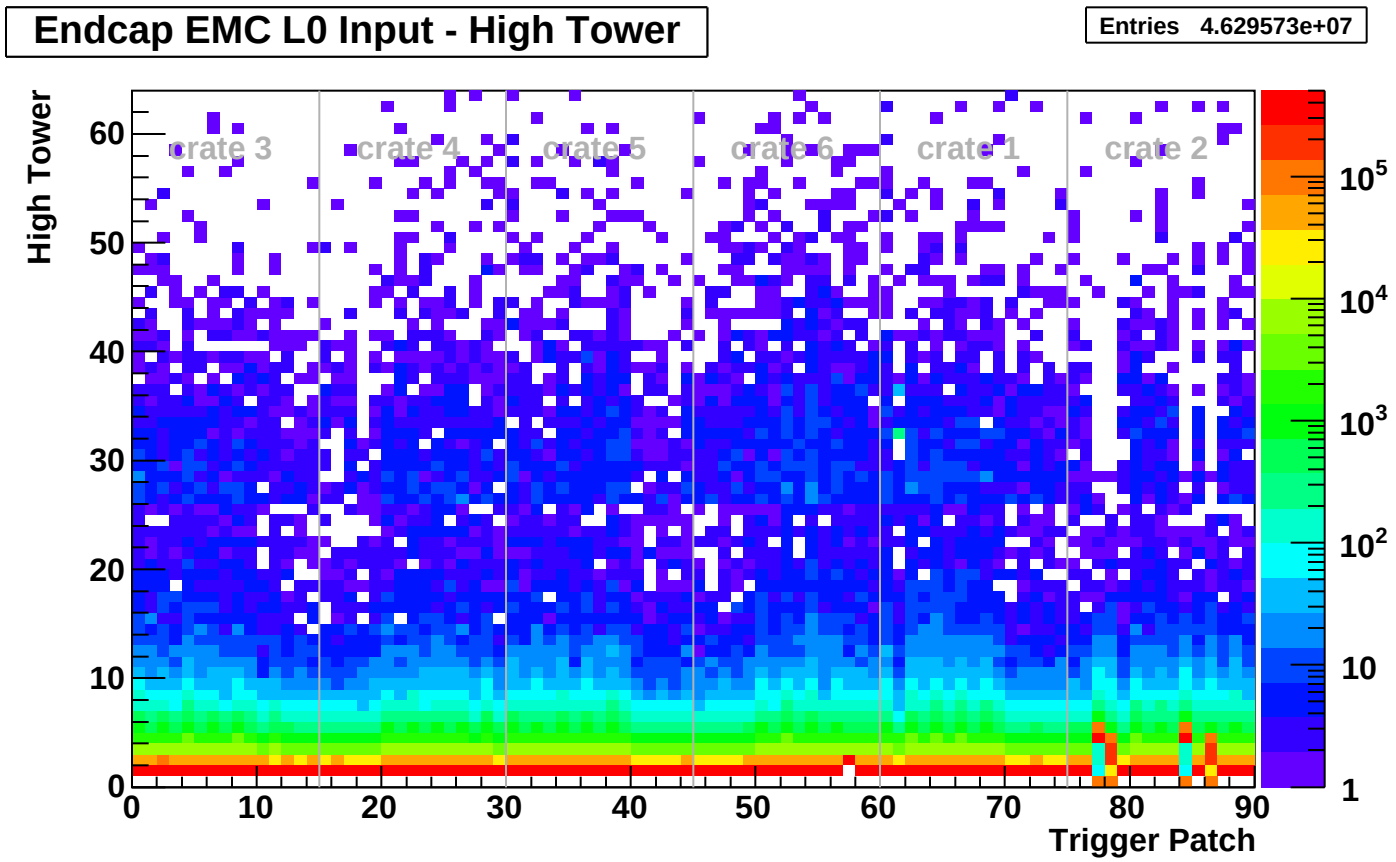

TCU bits
Entries 635196
TCU bits - Simulated
39975 105867 216787 5382 20232 245869 884
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

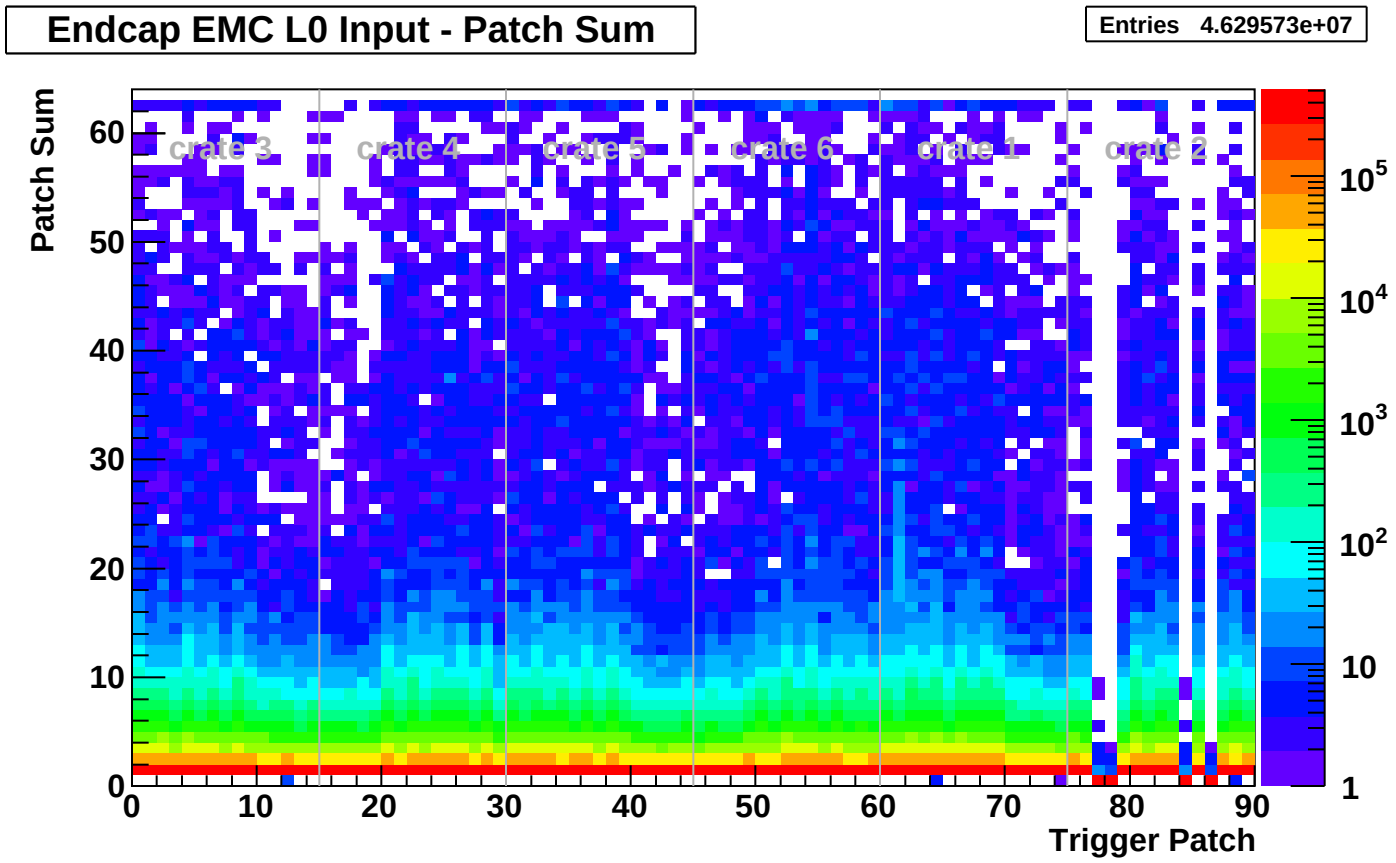

BunchId7Bit (BHT3)
Entries 7891

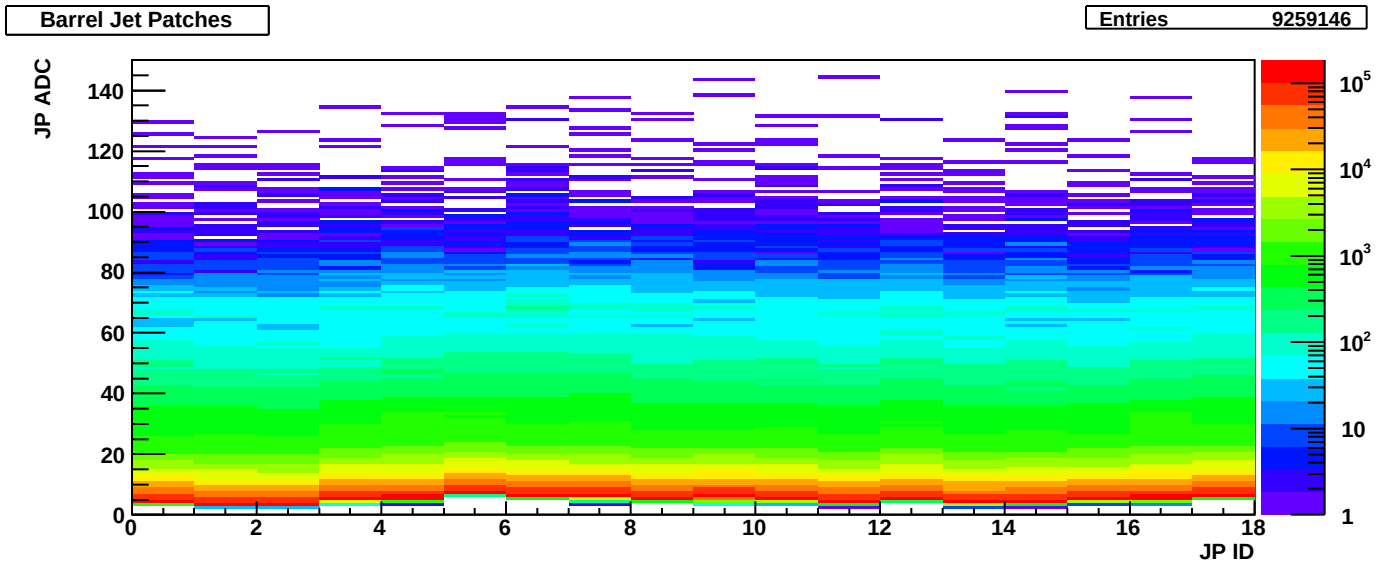

BunchId7Bit (all events)
Entries 514397

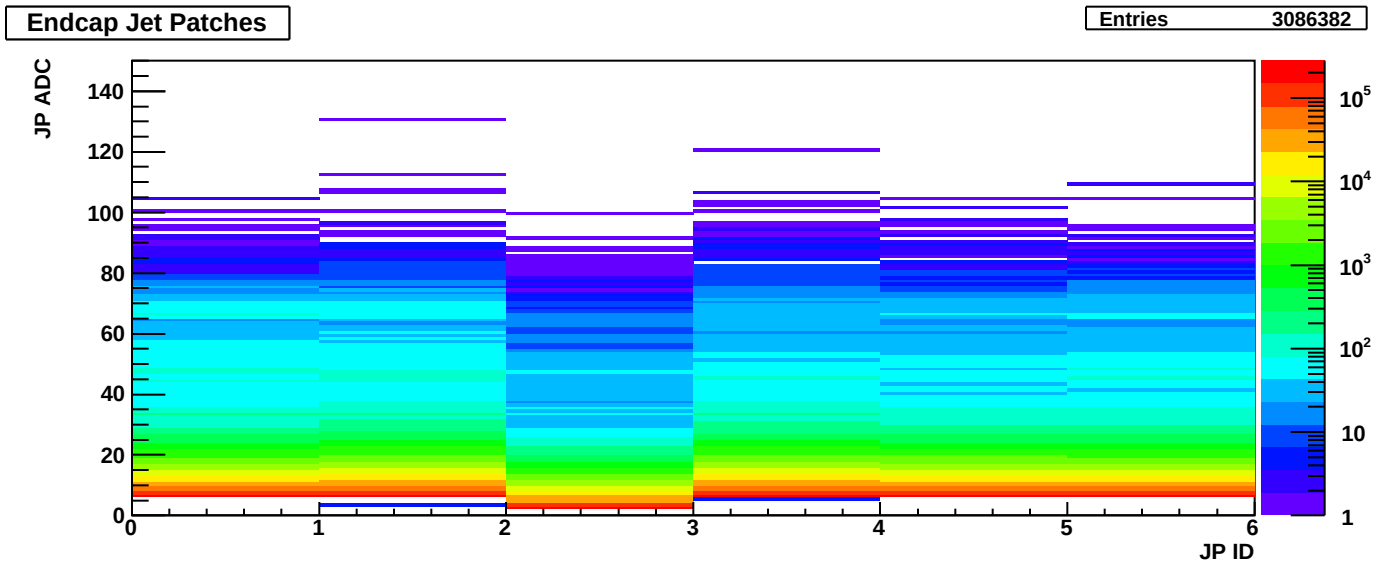

Barrel EMC L0 Input - High Tower
Entries 1.543191e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

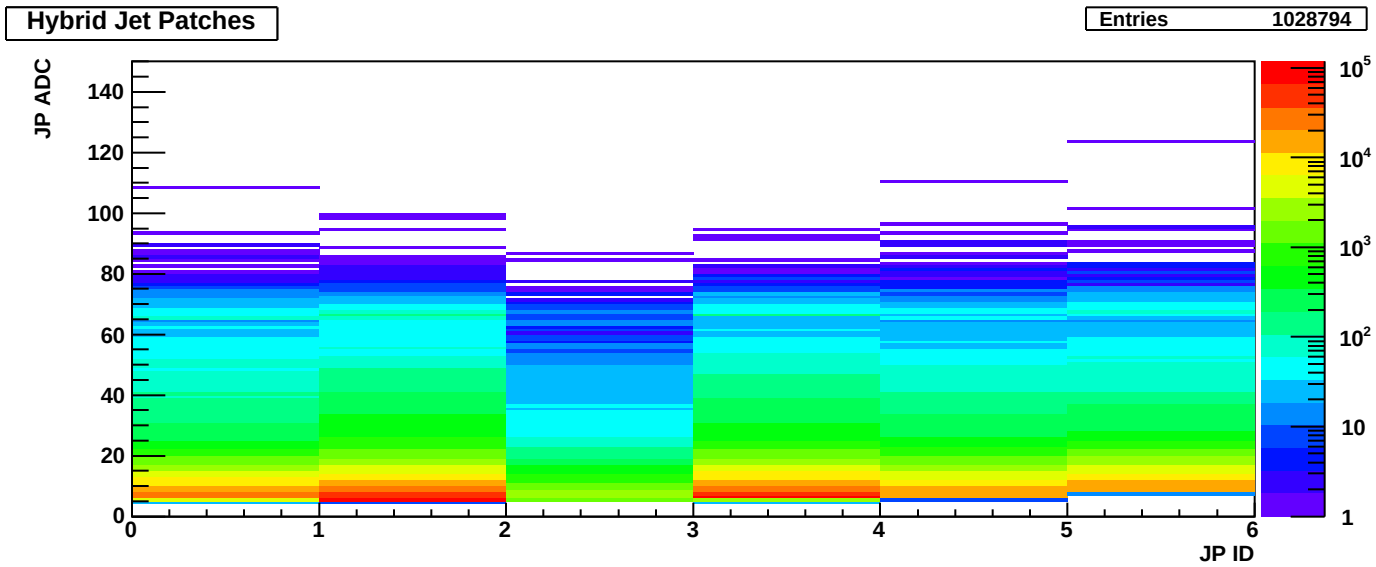

Barrel EMC L0 Input - Patch Sum
Entries 1.543191e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

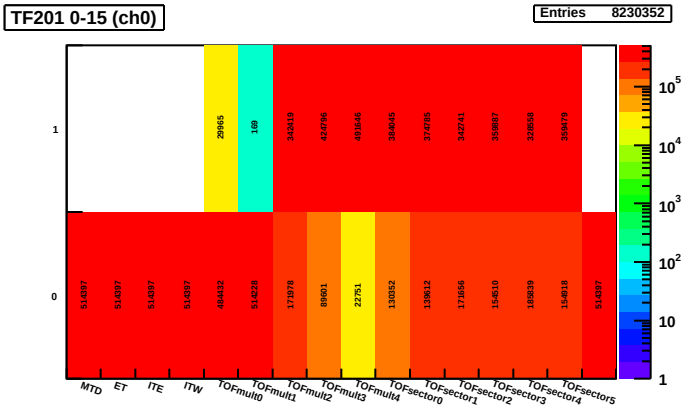

Endcap EMC L0 Input - High Tower
Entries 4.629573e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

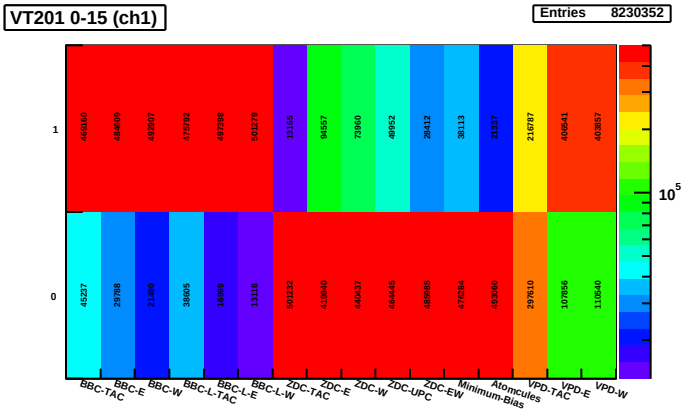

Endcap EMC L0 Input - Patch Sum
Entries 4.629573e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

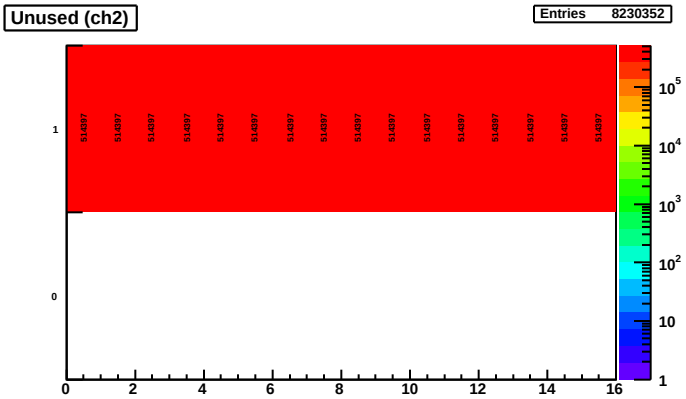

Barrel Jet Patches
Entries 9259146
JP ADC
JP ID

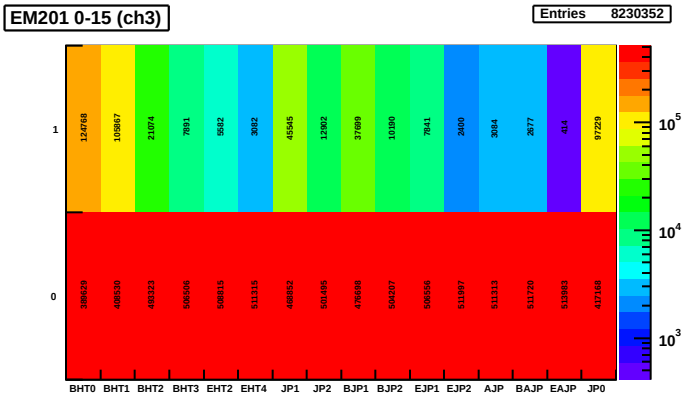

Endcap Jet Patches
Entries 3086382
JP ADC
JP ID

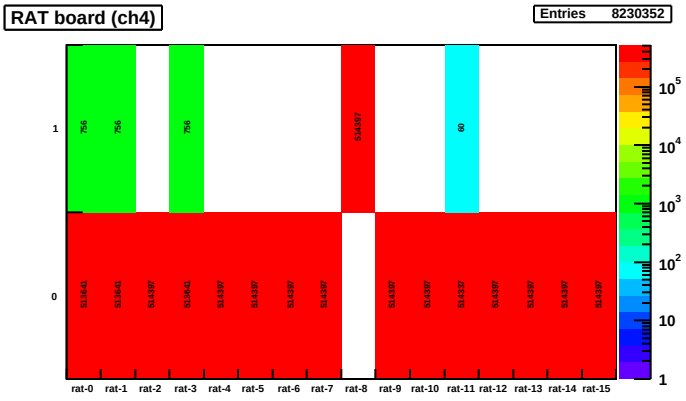

Hybrid Jet Patches
Entries 1028794
JP ADC
JP ID

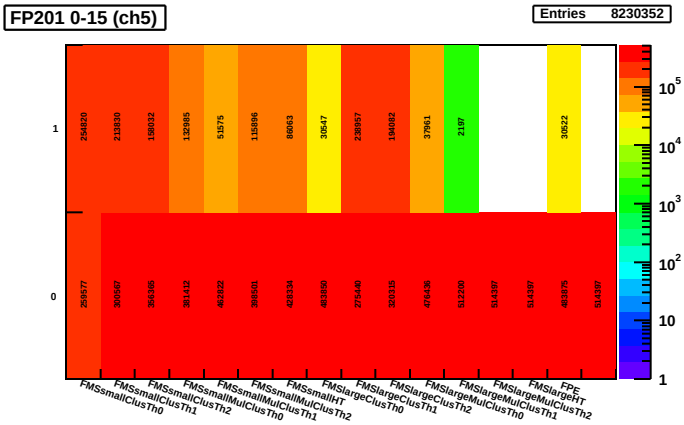

TF201 0-15 (ch0)
Entries 8230352
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

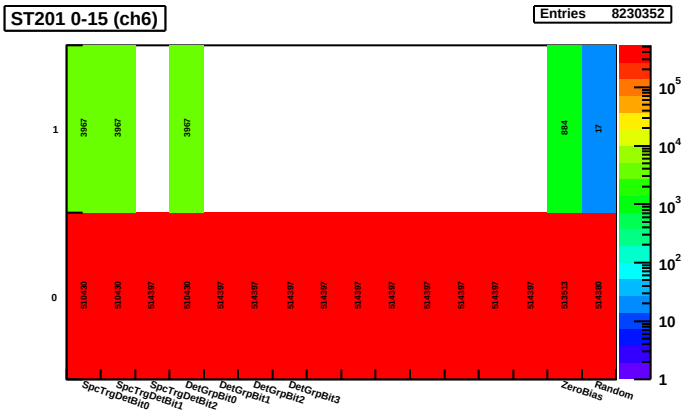

VT201 0-15 (ch1)
Entries 8230352
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

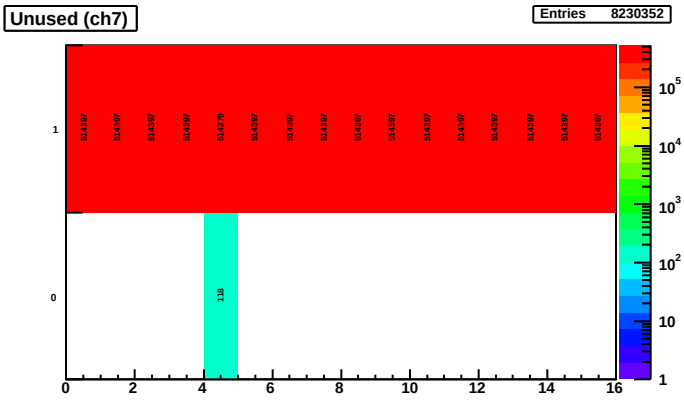

Unused (ch2)
Entries 8230352

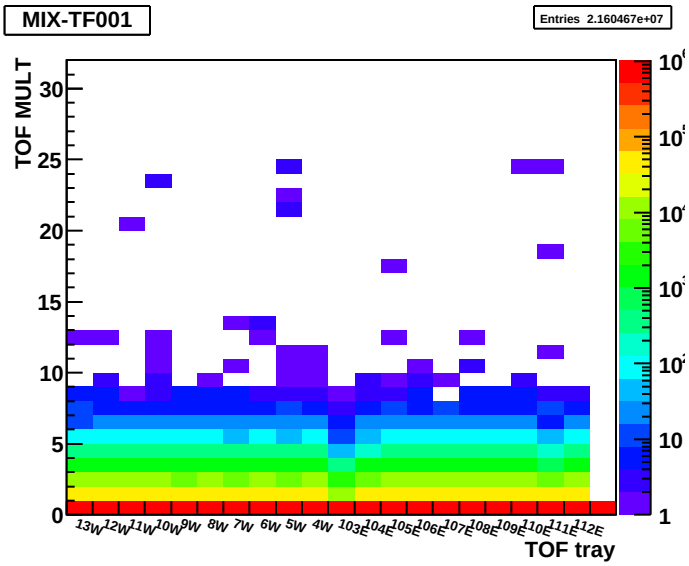

EM201 0-15 (ch3)
Entries 8230352
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

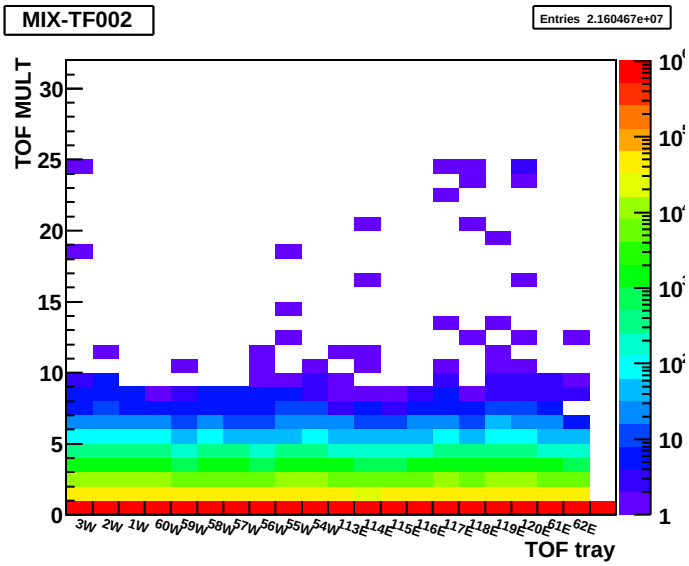

RAT board (ch4)
Entries 8230352
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

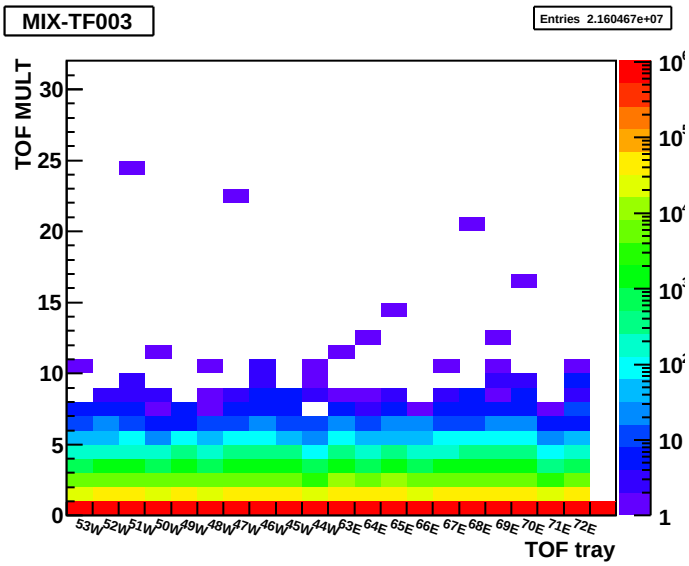

FP201 0-15 (ch5)
Entries 8230352
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

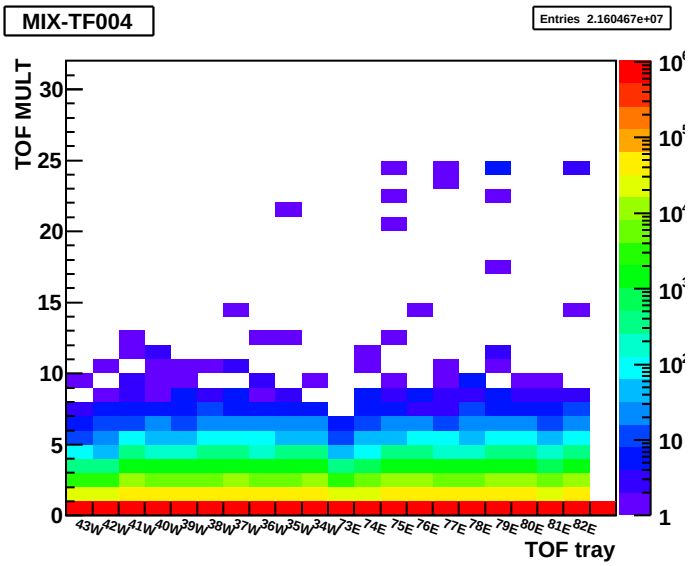

ST201 0-15 (ch6)
Entries 8230352
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

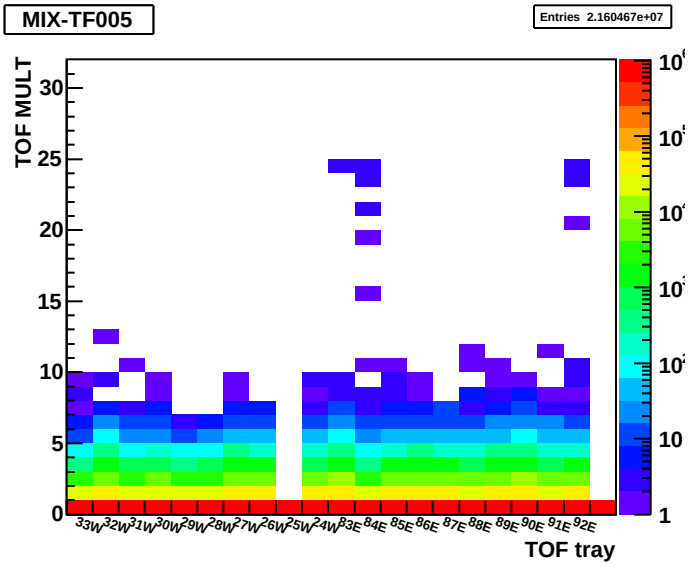

Unused (ch7)
Entries 8230352

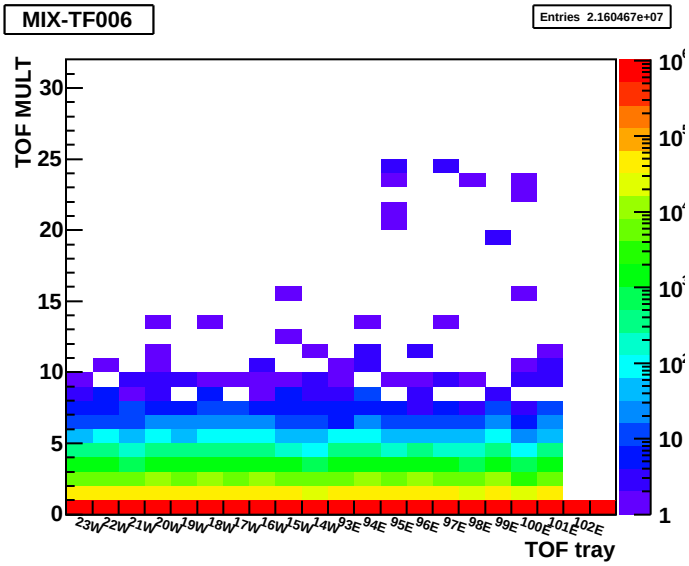

MIX-TF001
Entries 2.160467e+07
TOF MULT
TOF tray

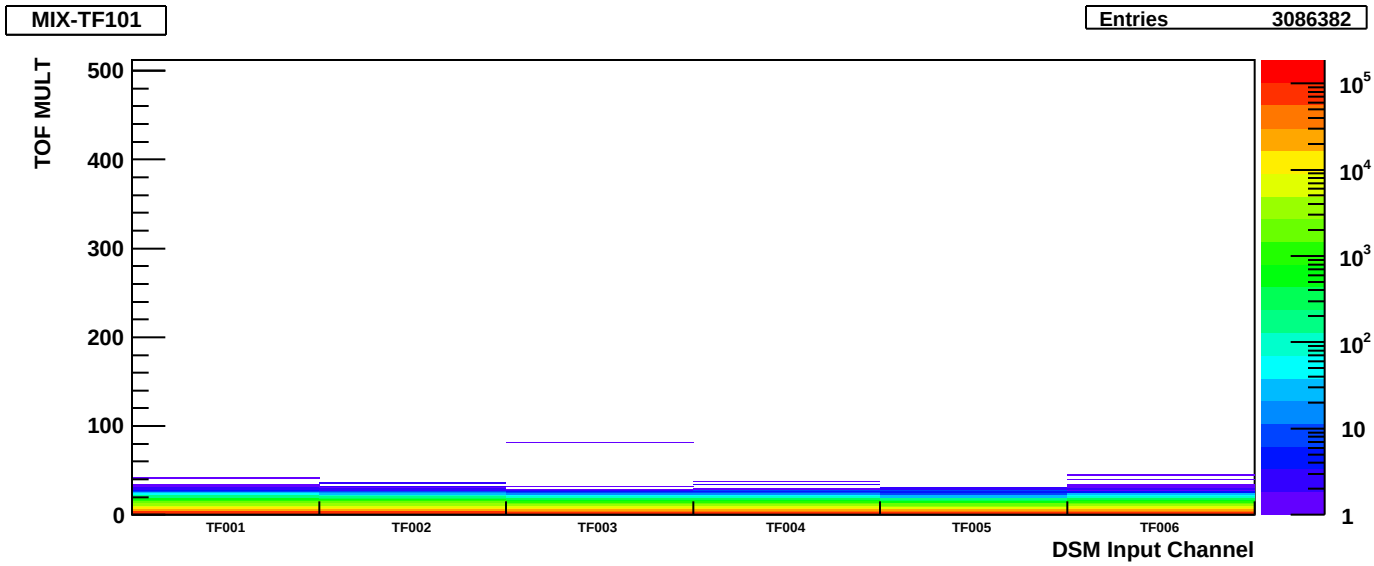

MIX-TF002
Entries 2.160467e+07
TOF MULT
TOF tray

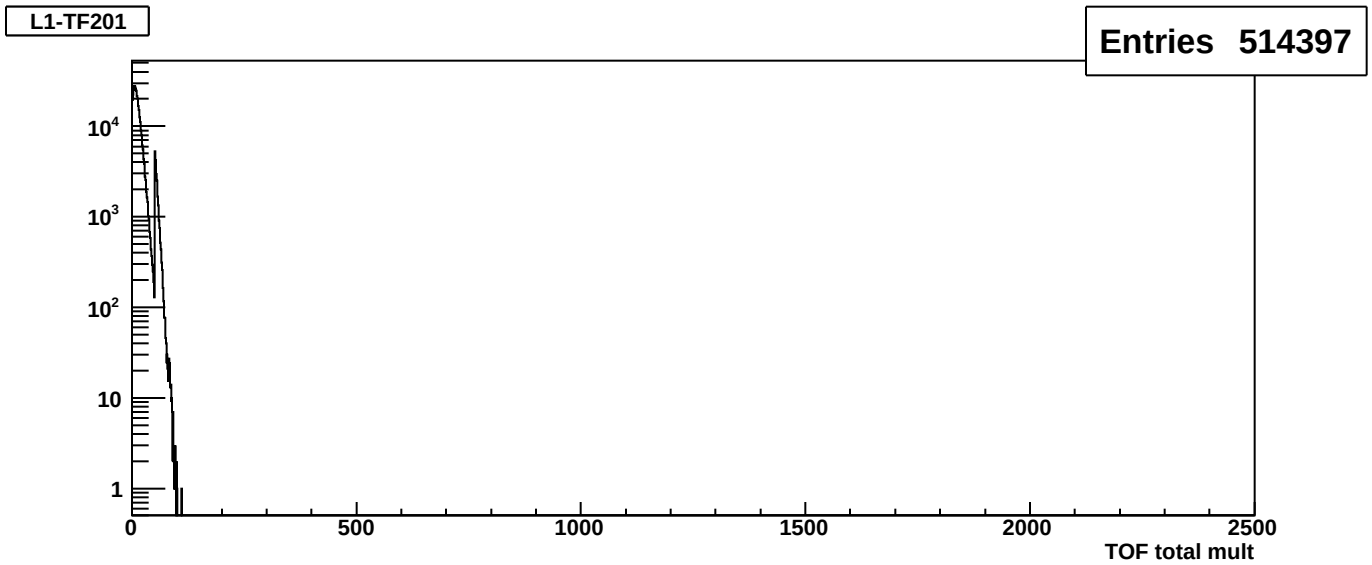

MIX-TF003
Entries 2.160467e+07
TOF MULT
TOF tray

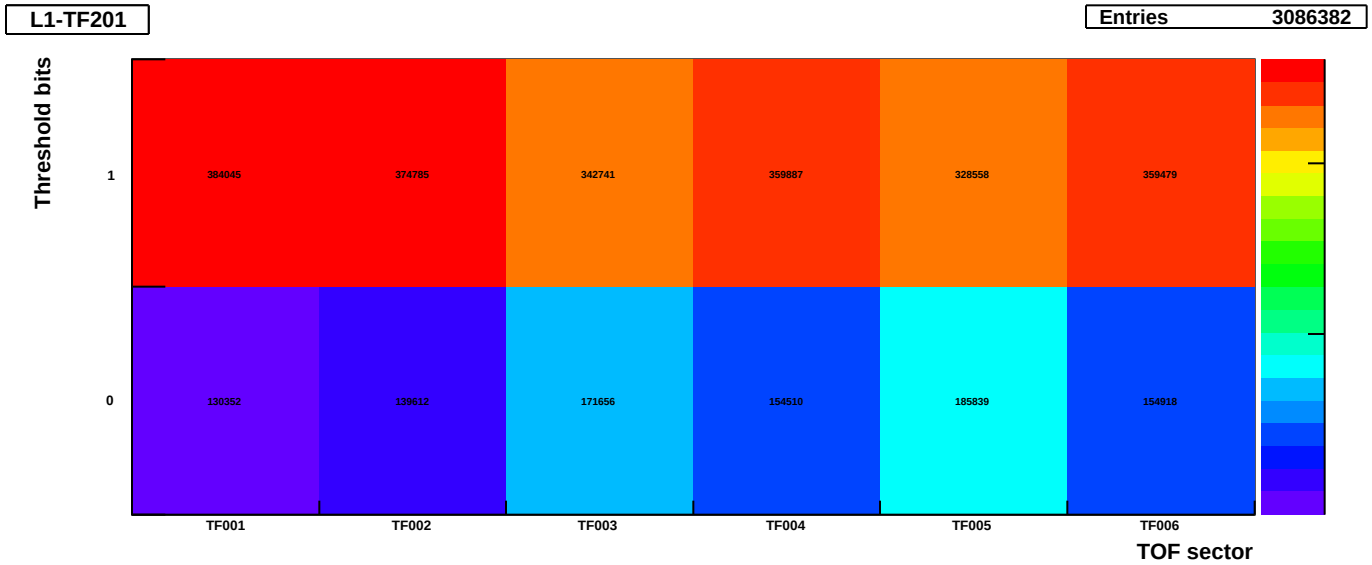

MIX-TF004
Entries 2.160467e+07
TOF MULT
TOF tray

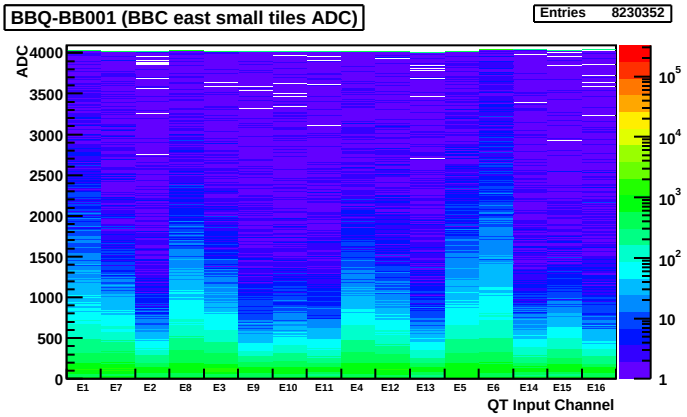

MIX-TF005
Entries 2.160467e+07
TOF MULT
TOF tray

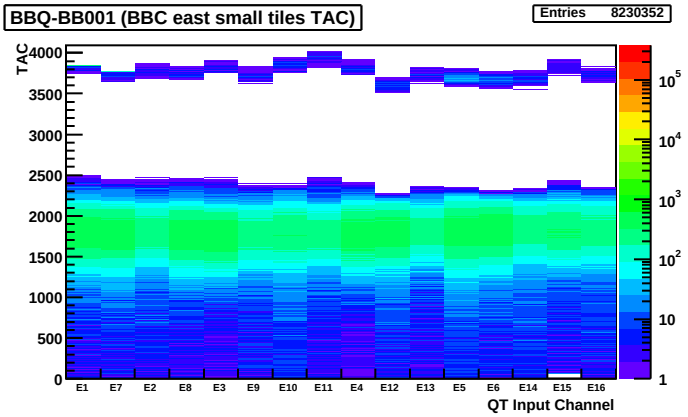

MIX-TF006
Entries 2.160467e+07
TOF MULT
TOF tray

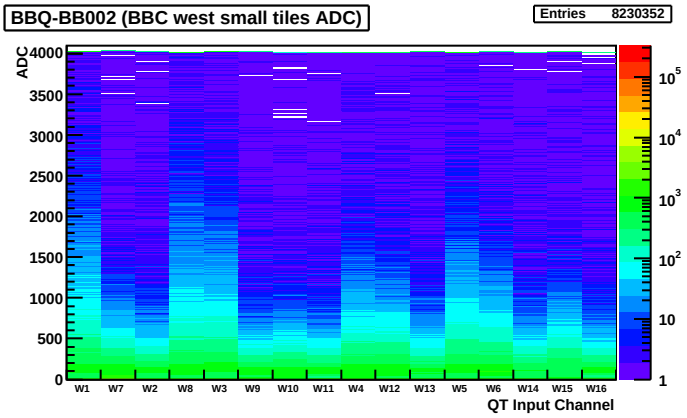

MIX-TF101
Entries 3086382
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

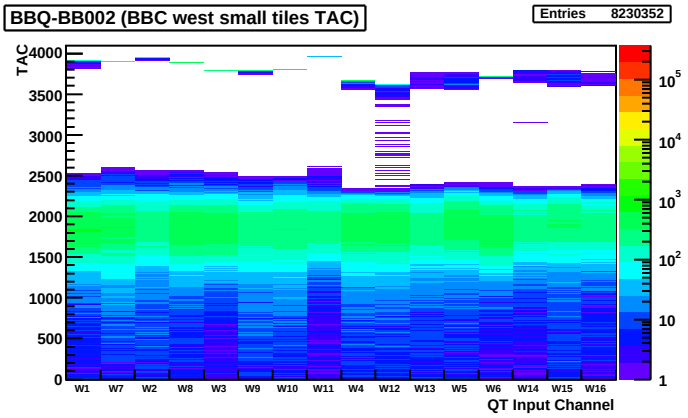

L1-TF201
Entries 514397
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

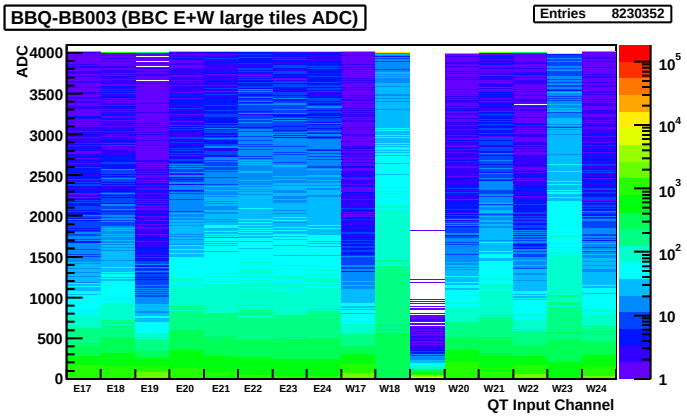

L1-TF201
Entries 3086382
Threshold bits
1
0
384045
374785
342741
359887
328558
359479
130352
139612
171656
154510
185839
154918
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

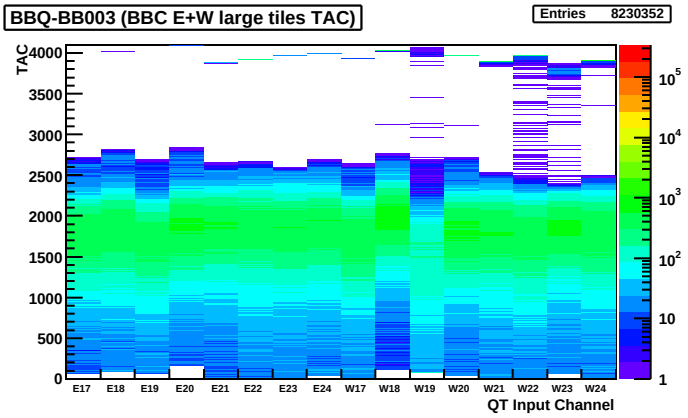

BBQ-BB001 (BBC east small tiles ADC)
Entries 8230352
ADC
QT Input Channel

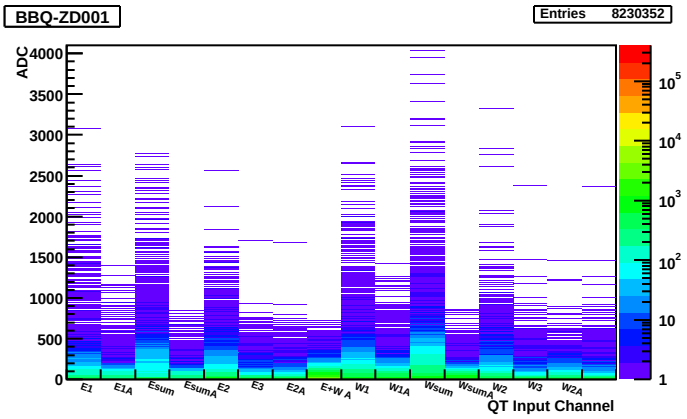

BBQ-BB001 (BBC east small tiles TAC)
Entries 8230352
TAC
QT Input Channel

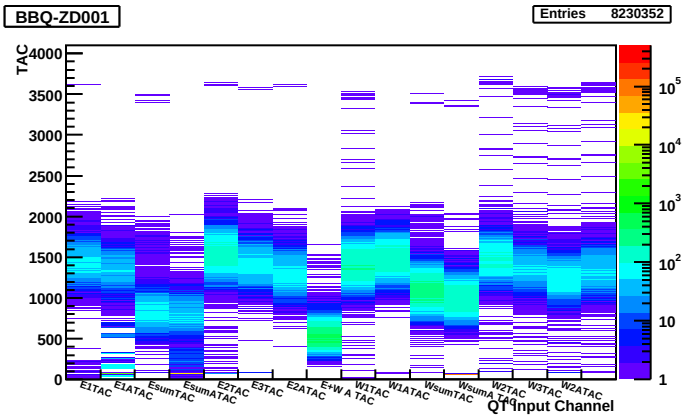

BBQ-BB002 (BBC west small tiles ADC)
Entries 8230352
ADC
QT Input Channel

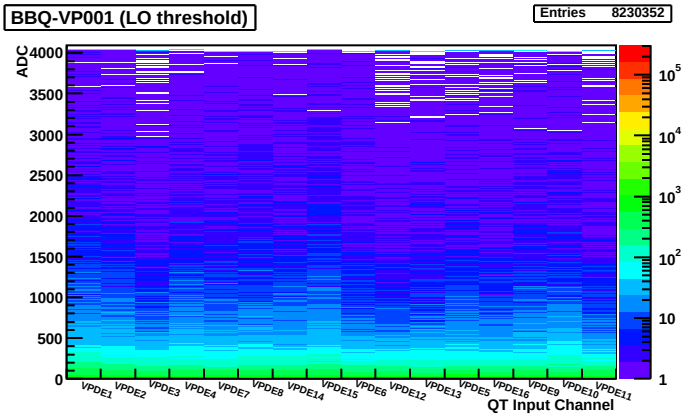

BBQ-BB002 (BBC west small tiles TAC)
Entries 8230352
TAC
QT Input Channel

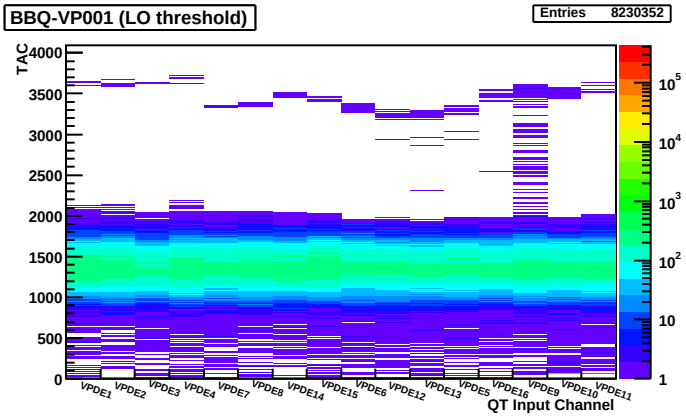

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8230352
ADC
QT Input Channel

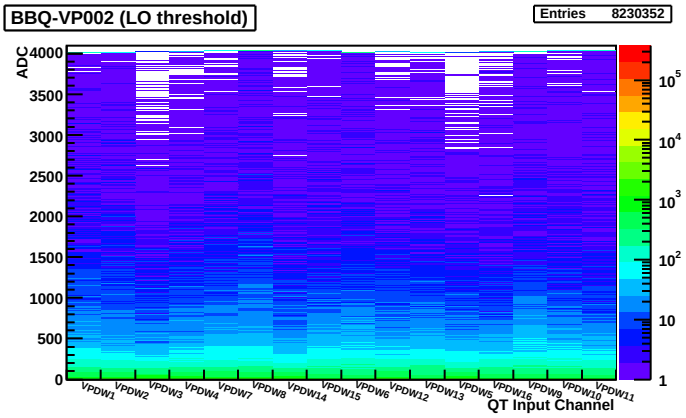

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8230352
TAC
QT Input Channel

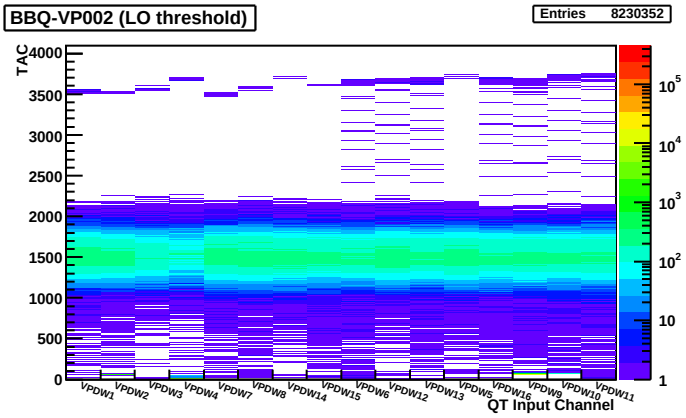

BBQ-ZD001
Entries 8230352
ADC
QT Input Channel

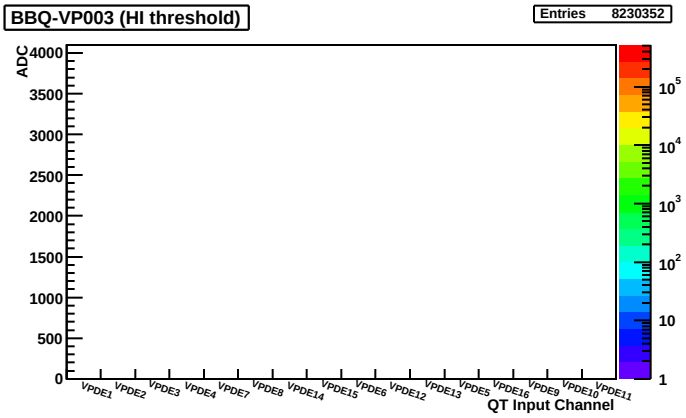

BBQ-ZD001
Entries 8230352
TAC
QT Input Channel

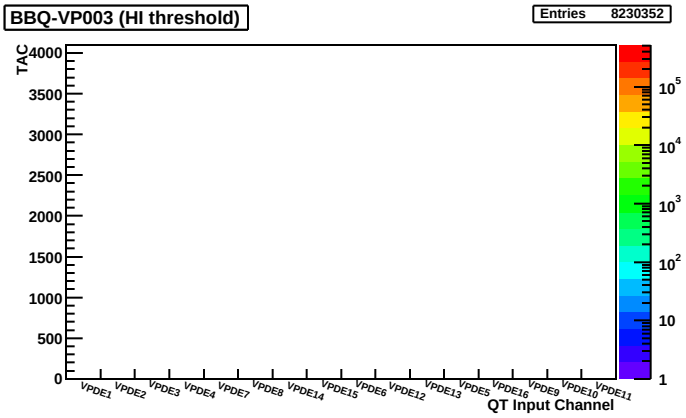

BBQ-VP001 (LO threshold)
Entries 8230352
ADC
QT Input Channel

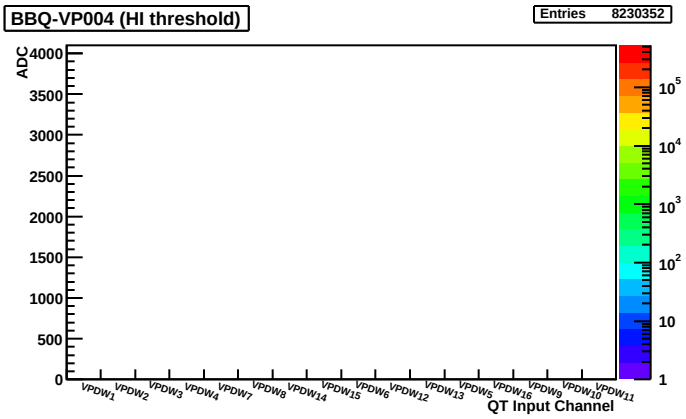

BBQ-VP001 (LO threshold)
Entries 8230352
TAC
QT Input Channel

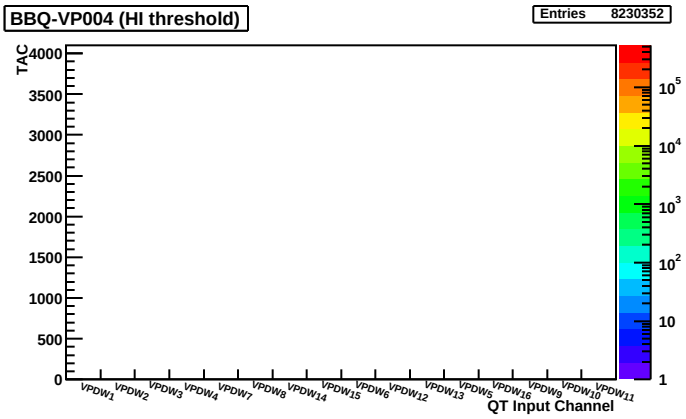

BBQ-VP002 (LO threshold)
Entries 8230352
ADC
QT Input Channel

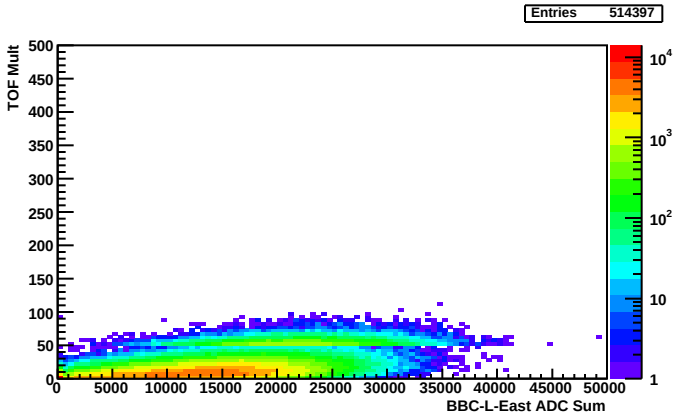

BBQ-VP002 (LO threshold)
Entries 8230352
TAC
QT Input Channel

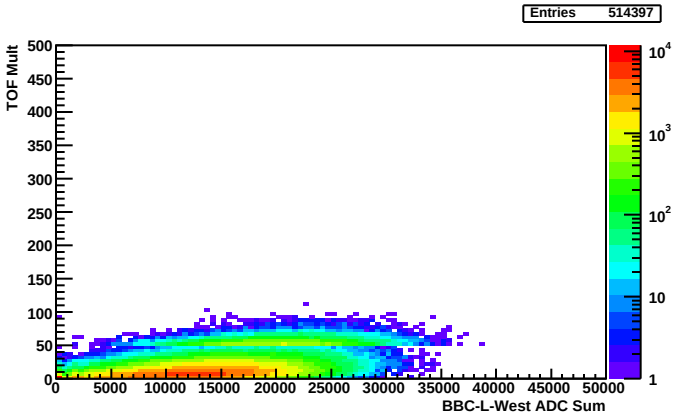

BBQ-VP003 (HI threshold)
Entries 8230352
ADC
QT Input Channel

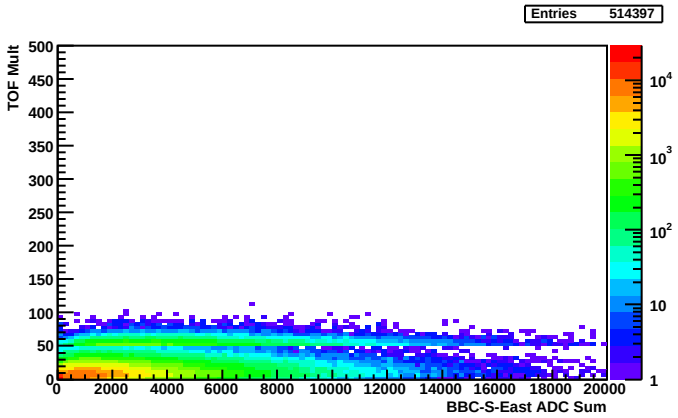

BBQ-VP003 (HI threshold)
Entries 8230352
TAC
QT Input Channel

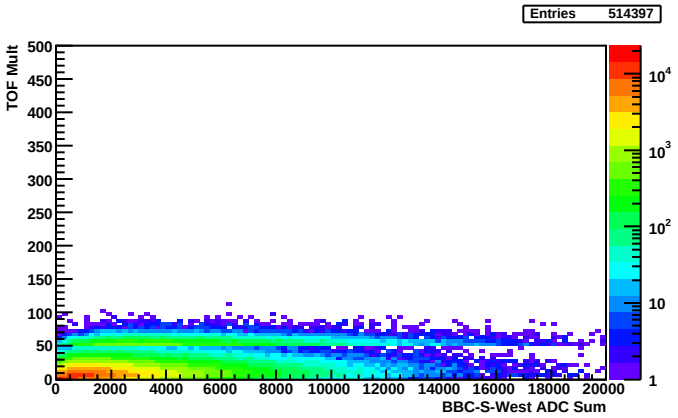

BBQ-VP004 (HI threshold)
Entries 8230352
ADC
QT Input Channel

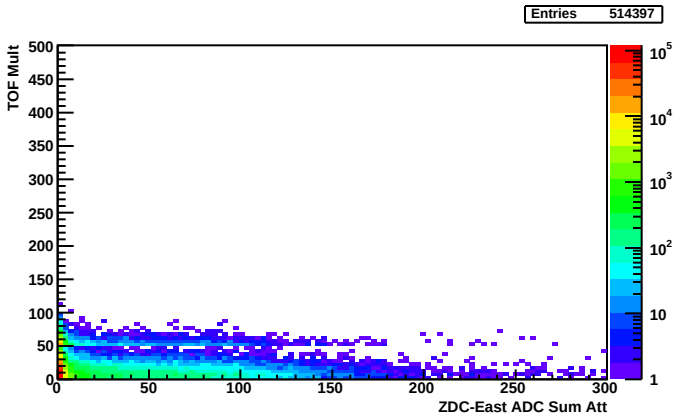

BBQ-VP004 (HI threshold)
Entries 8230352
TAC
QT Input Channel

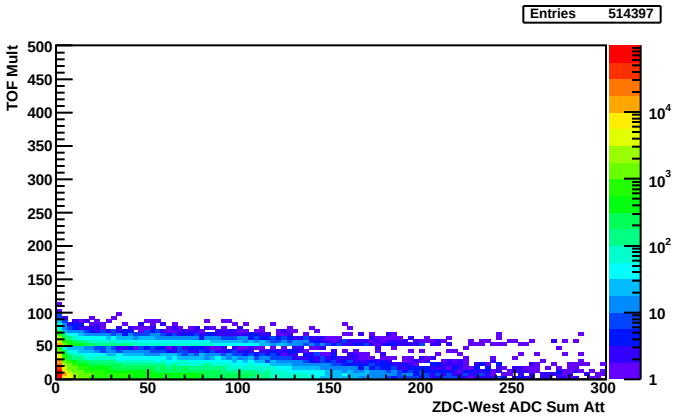

Entries 514397
TOF Mult
BBC-L-East ADC Sum

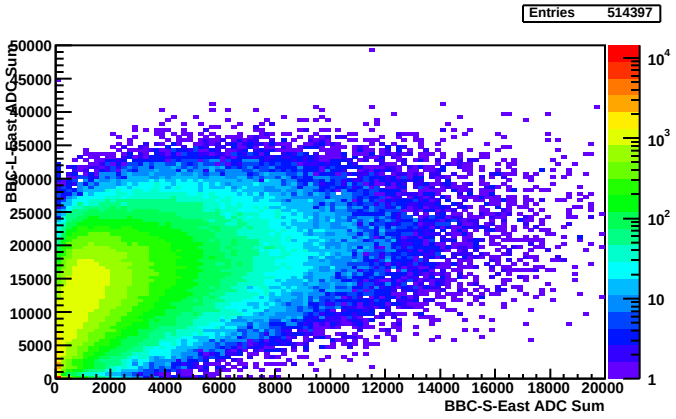

Entries 514397
TOF Mult
BBC-L-West ADC Sum

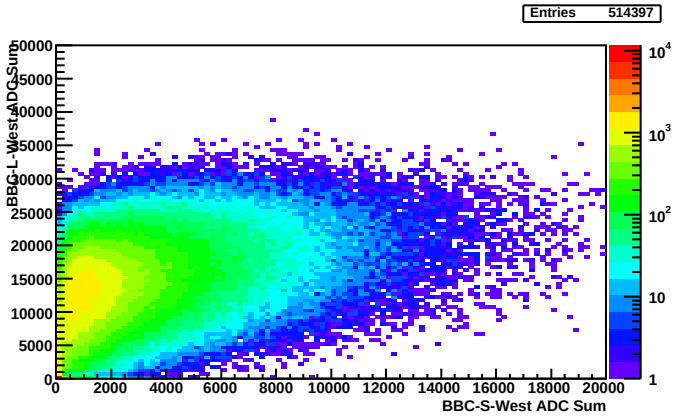

Entries 514397
TOF Mult
BBC-S-East ADC Sum

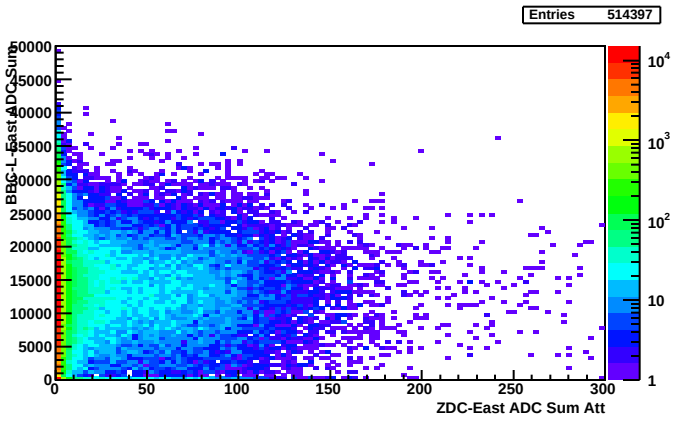

Entries 514397
TOF Mult
BBC-S-West ADC Sum

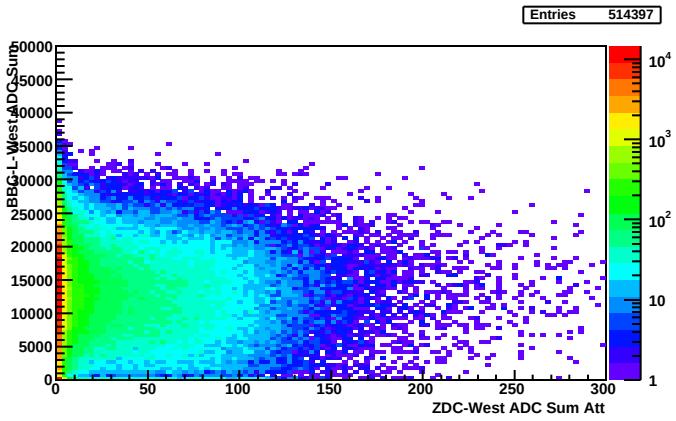

Entries 514397
TOF Mult
ZDC-East ADC Sum Att

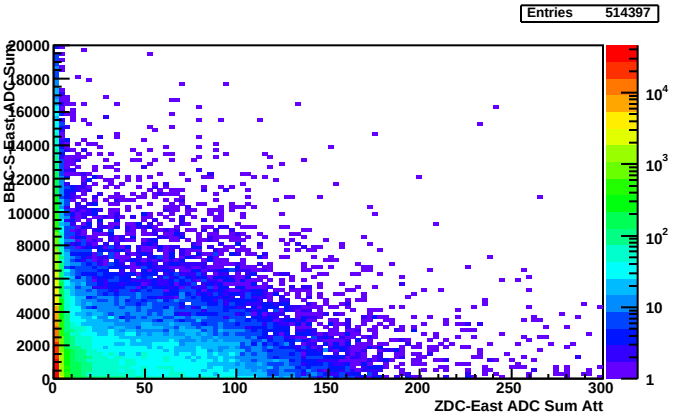

Entries 514397
TOF Mult
ZDC-West ADC Sum Att

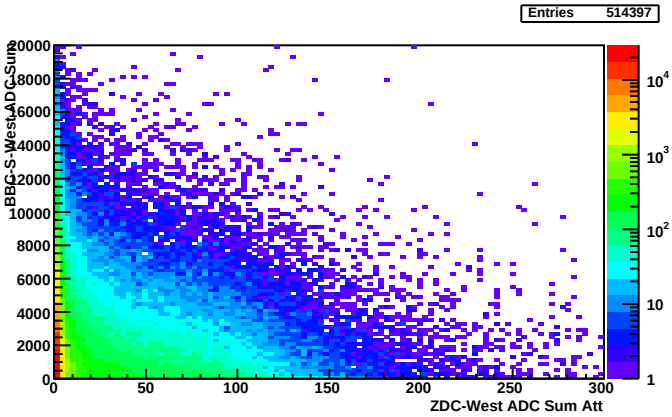

Entries 514397
BBC-L-East ADC Sum
BBC-S-East ADC Sum

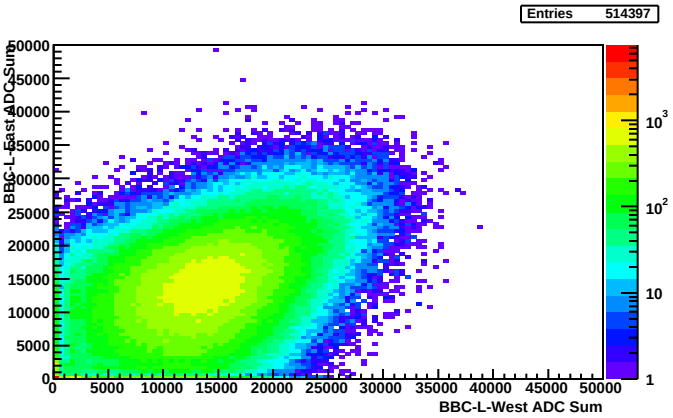

Entries 514397
BBC-L-West ADC Sum
BBC-S-West ADC Sum

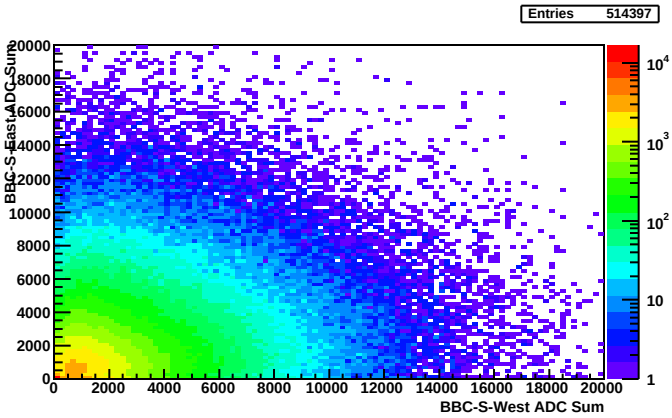

Entries 514397
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

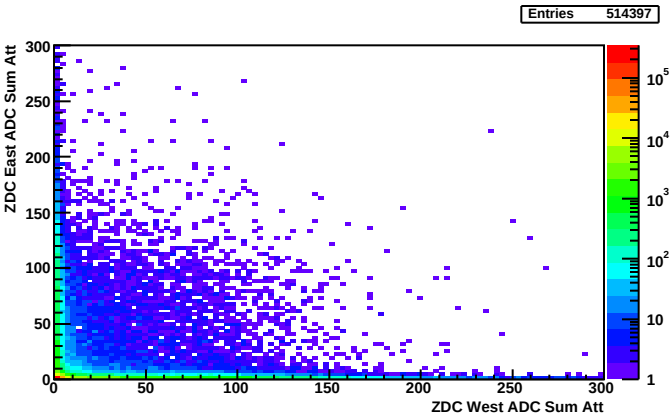

Entries 514397
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

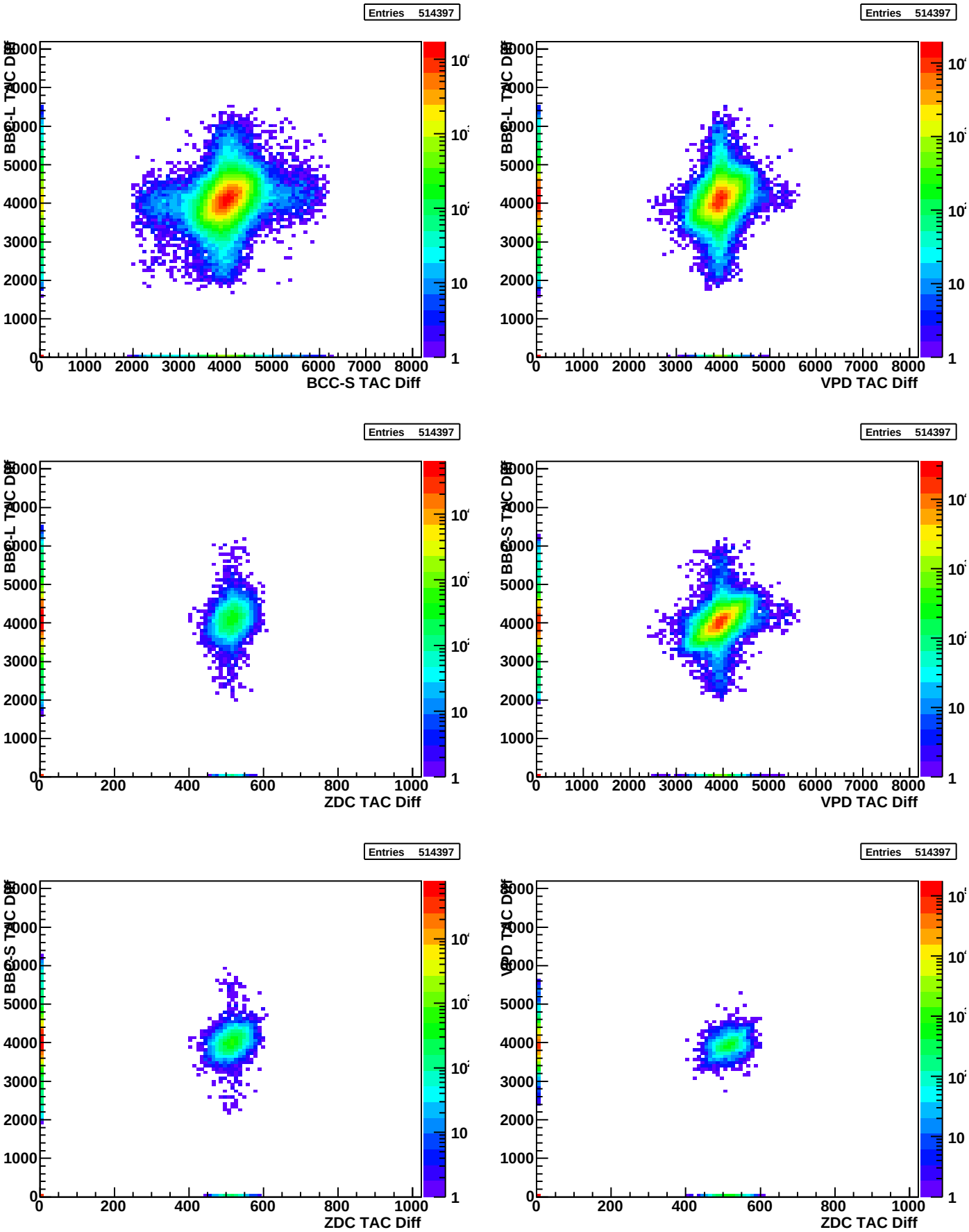

Entries 514397
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

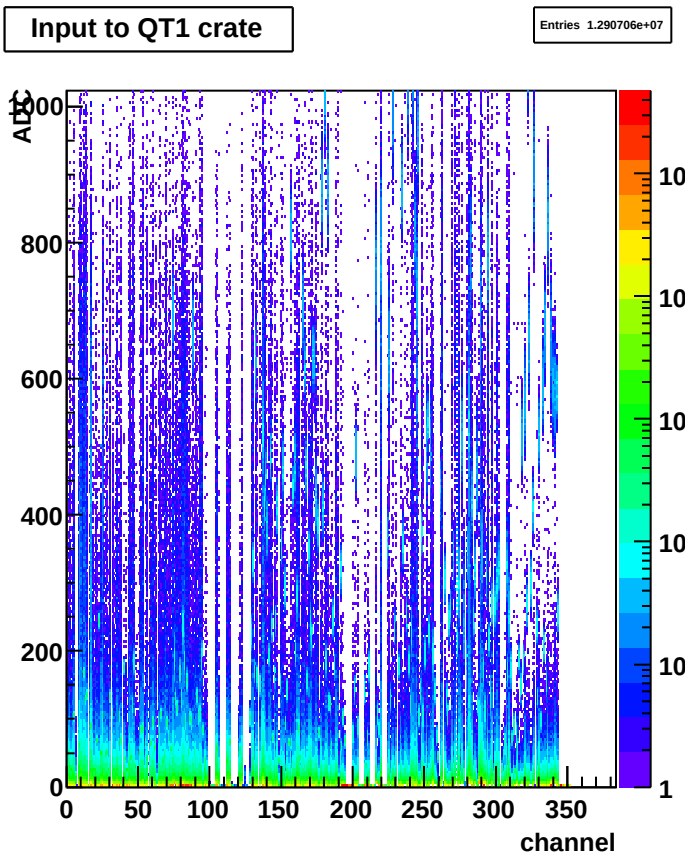

Entries 514397
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

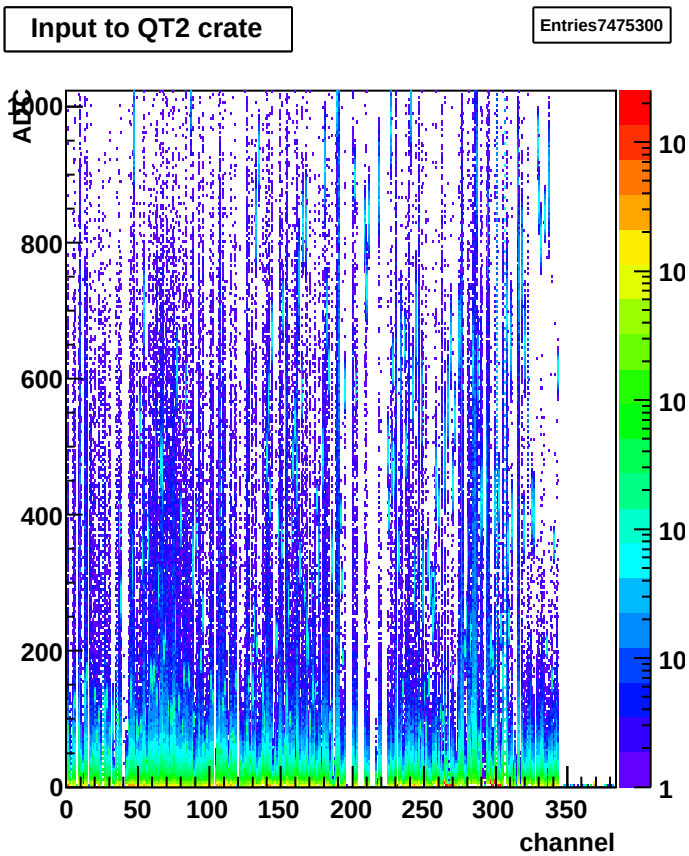

Entries 514397
BBC-L-East ADC Sum
BBC-L-West ADC Sum

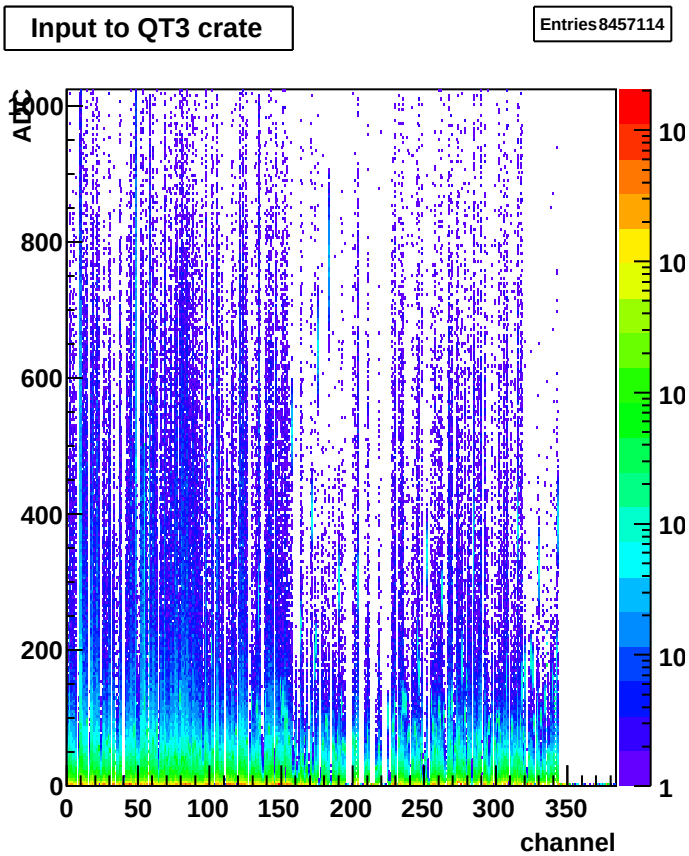

Entries 514397
BBC-S-East ADC Sum
BBC-S-West ADC Sum

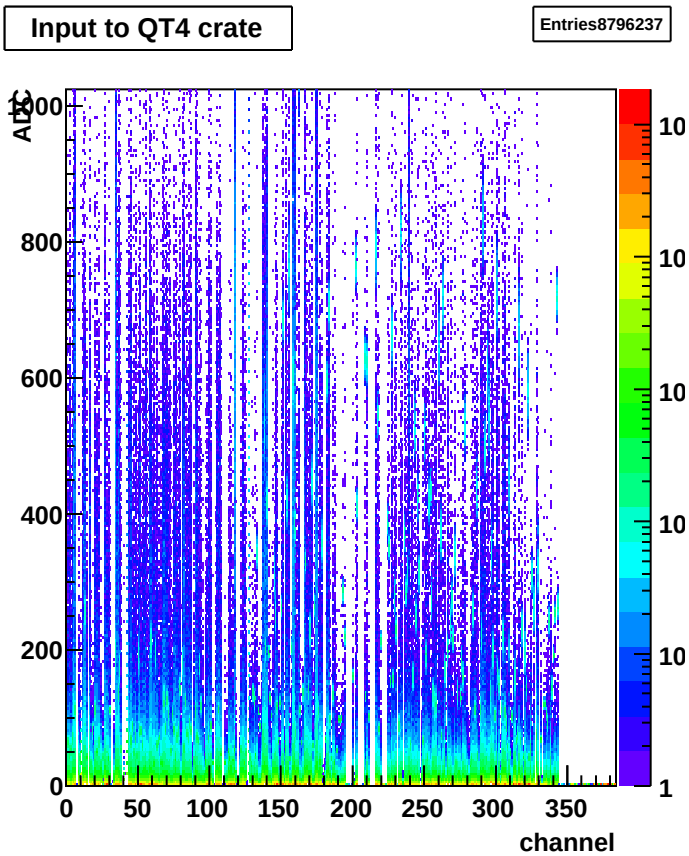

Entries 514397
ZDC East ADC Sum Att
ZDC West ADC Sum Att

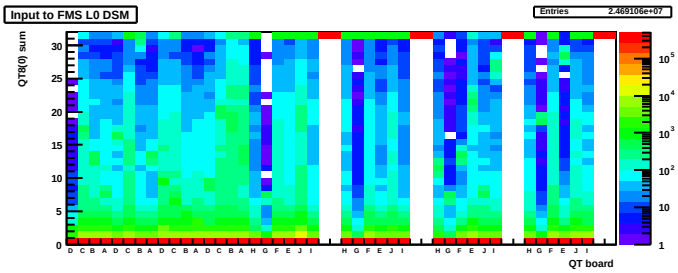

Entries 514397
BBC-L TAC Diff
BCC-S TAC Diff
Entries 514397
BBC-L TAC Diff
VPD TAC Diff
Entries 514397
BBC-L TAC Diff
ZDC TAC Diff
Entries 514397
BBC-S TAC Diff
VPD TAC Diff
Entries 514397
BBC-S TAC Diff
ZDC TAC Diff
Entries 514397
VPD TAC Diff
ZDC TAC Diff

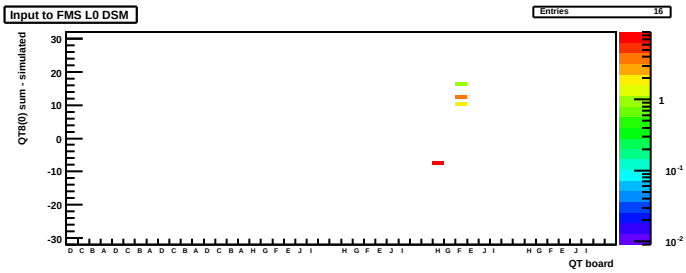

Input to QT1 crate
Entries 1.290706e+07
ADC
channel

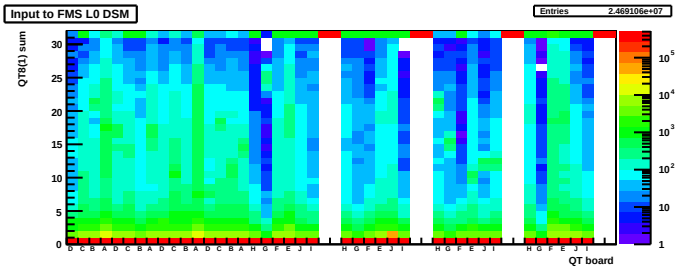

Input to QT2 crate
Entries7475300
ADC
channel

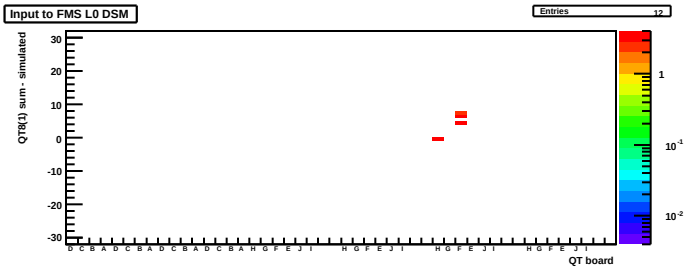

Input to QT3 crate
Entries8457114
ADC
channel

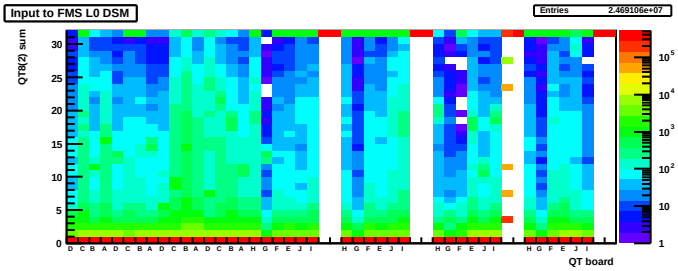

Input to QT4 crate
Entries8796237
ADC
channel

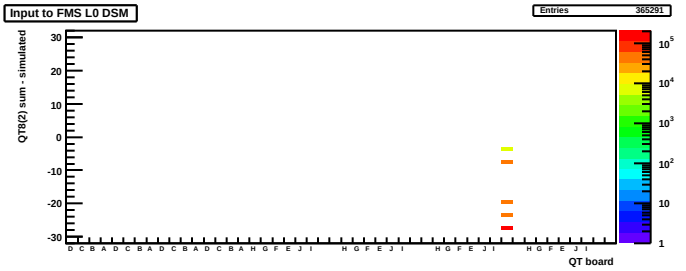

Input to FMS L0 DSM
Entries 2.469106e+07
QT8(0) sum
QT board

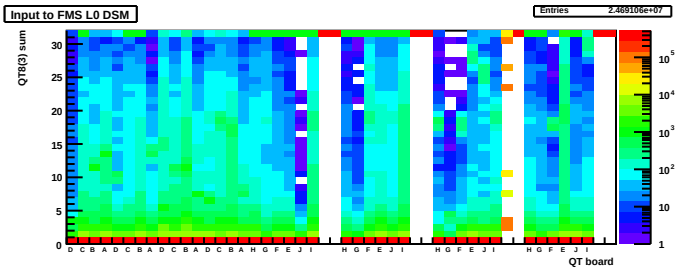

Input to FMS L0 DSM
Entries 16
QT8(0) sum - simulated
QT board

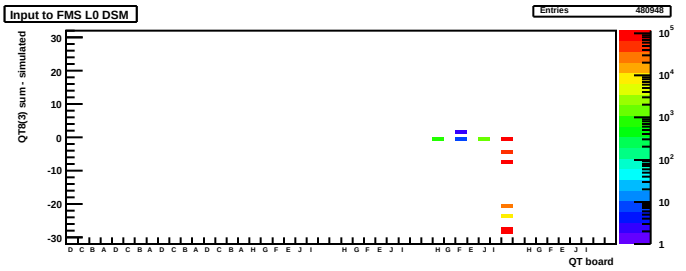

Input to FMS L0 DSM
Entries 2.469106e+07
QT8(1) sum
QT board

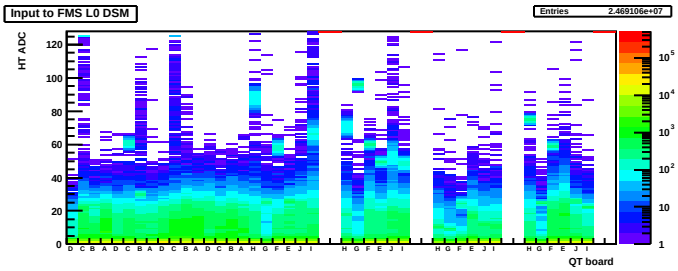

Input to FMS L0 DSM
Entries 12
QT8(1) sum - simulated
QT board

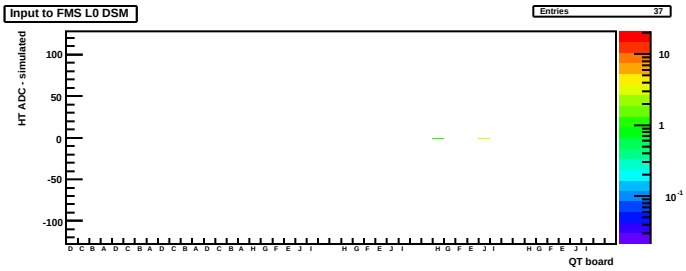

Input to FMS L0 DSM
Entries 2.469106e+07
QT8(2) sum
QT board

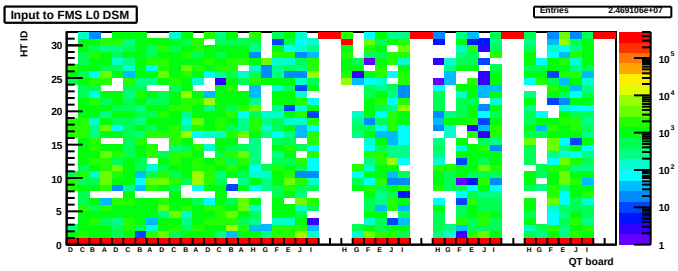

Input to FMS L0 DSM
Entries 365291
QT8(2) sum - simulated
QT board

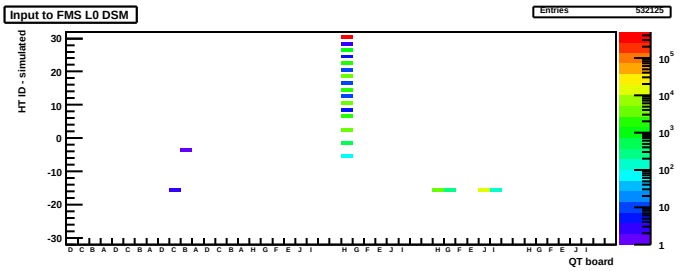

Input to FMS L0 DSM
Entries 2.469106e+07
QT8(3) sum
QT board

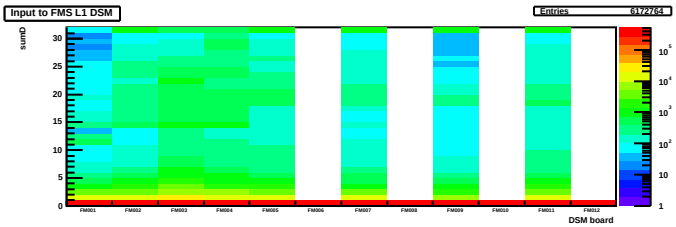

Input to FMS L0 DSM
Entries 480948
QT8(3) sum - simulated
QT board

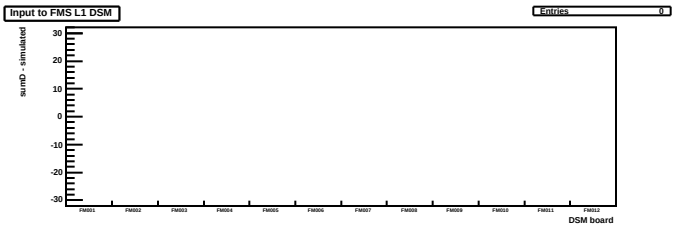

Input to FMS L0 DSM
Entries 2.469106e+07
HT ADC
QT board

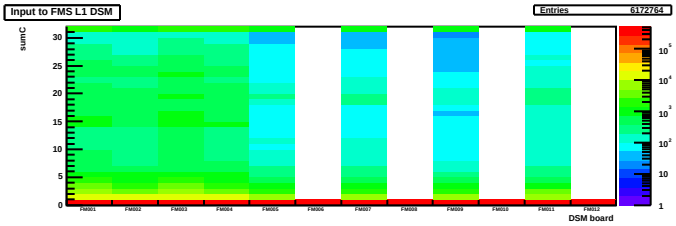

Input to FMS L0 DSM
Entries 37
HT ADC - simulated
QT board

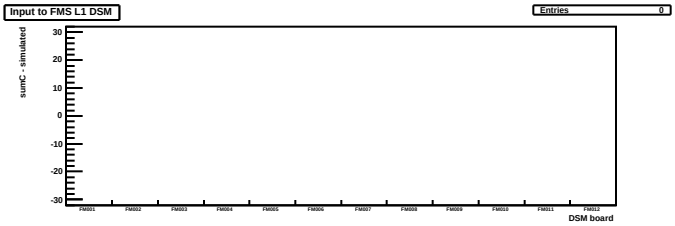

Input to FMS L0 DSM
Entries 2.469106e+07
HT ID
QT board

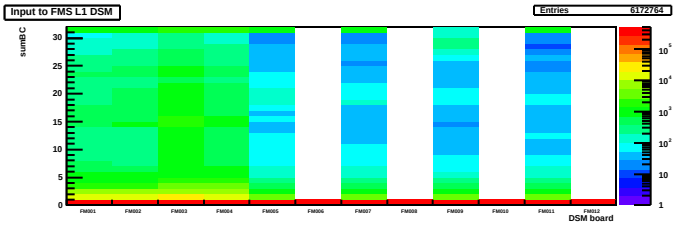

Input to FMS L0 DSM
Entries 532125
HT ID - simulated
QT board

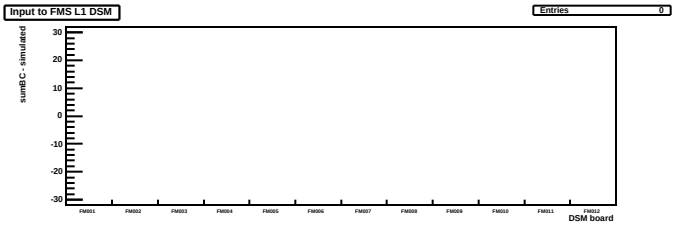

Input to FMS L1 DSM
Entries 6172764
sumD
DSM board

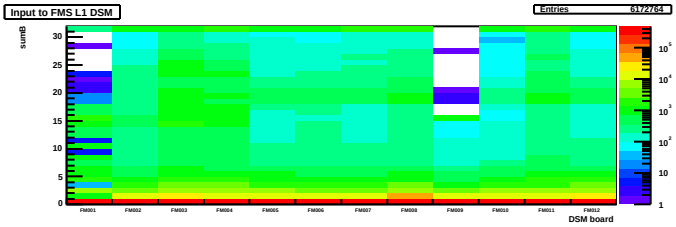

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

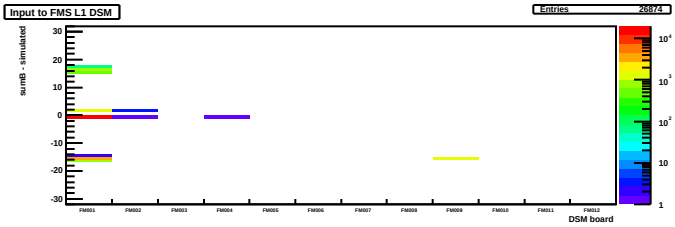

Input to FMS L1 DSM
Entries 6172764
sumC
DSM board

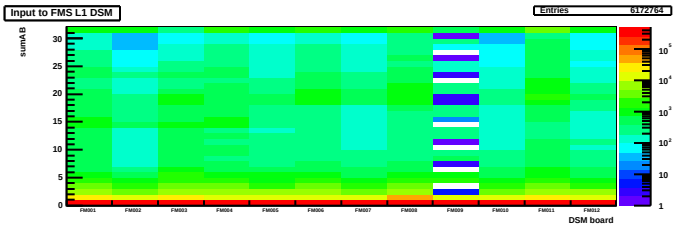

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

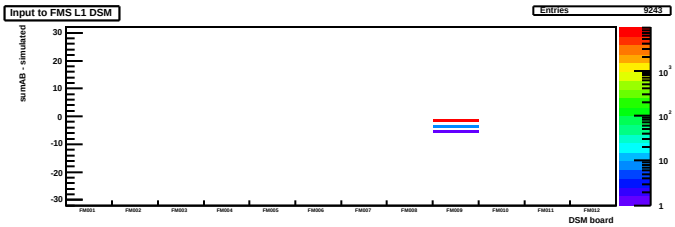

Input to FMS L1 DSM
Entries 6172764
sumBC
DSM board

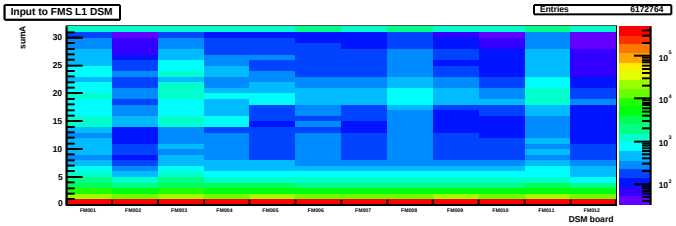

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

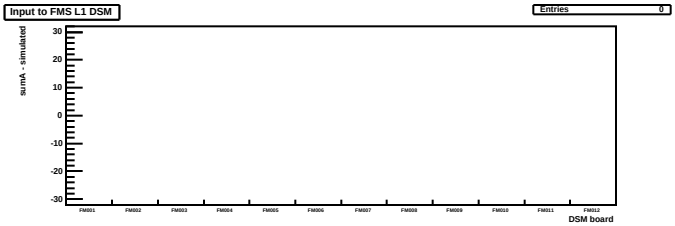

Input to FMS L1 DSM
Entries 6172764
sumB
DSM board

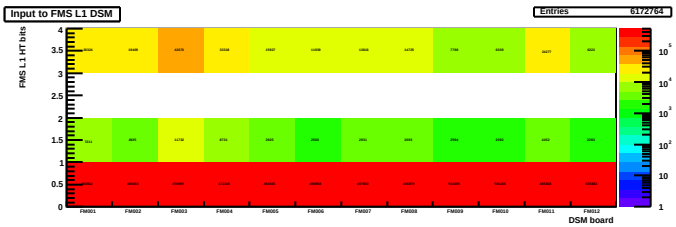

Input to FMS L1 DSM
Entries 26874
sumB - simulated
DSM board

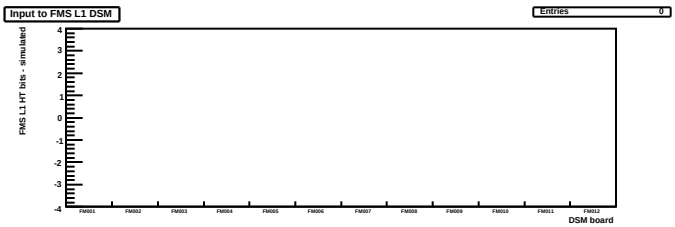

Input to FMS L1 DSM
Entries 6172764
sumAB
DSM board

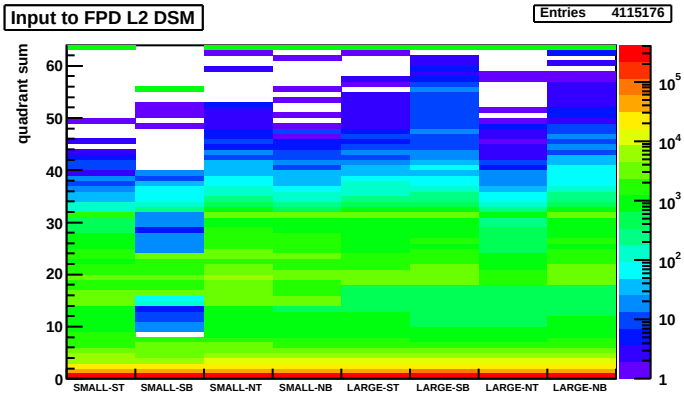

Input to FMS L1 DSM
Entries 9243
sumAB - simulated
DSM board

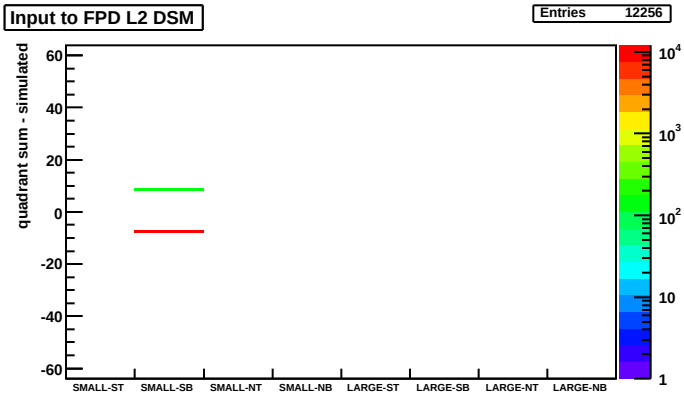

Input to FMS L1 DSM
Entries 6172764
sumA
DSM board

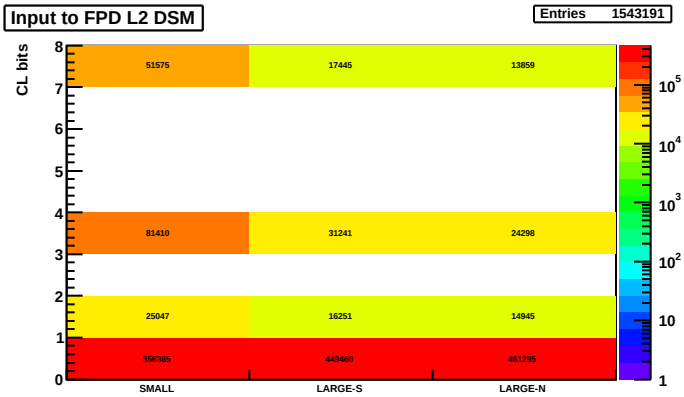

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

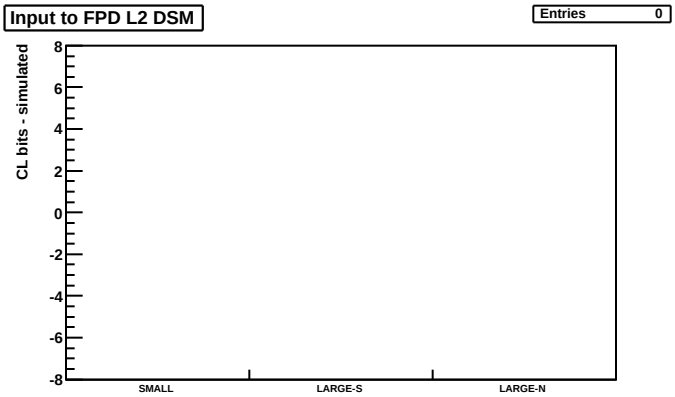

Input to FMS L1 DSM
Entries 6172764
FMS L1 HT bits
DSM board

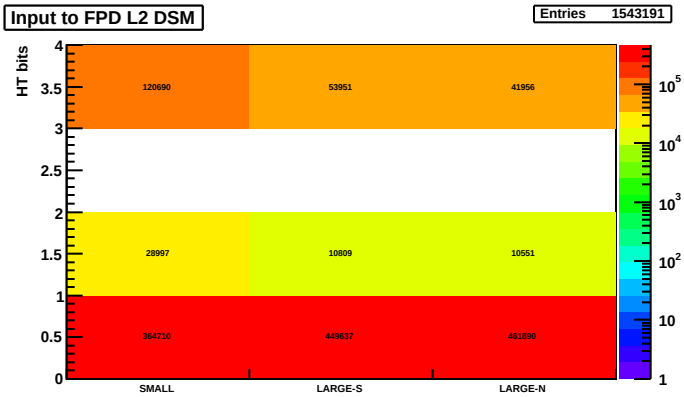

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

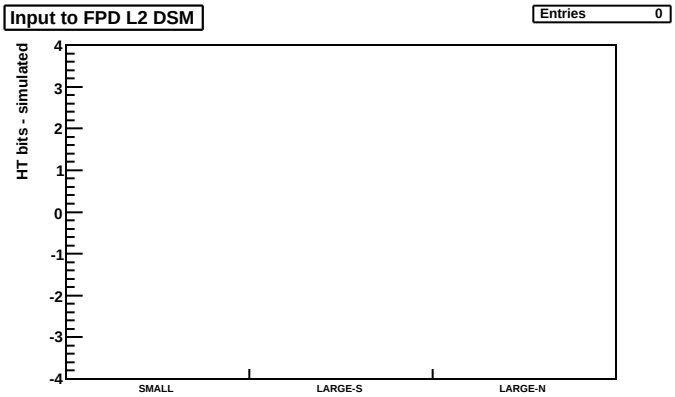

Input to FPD L2 DSM
Entries 4115176
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

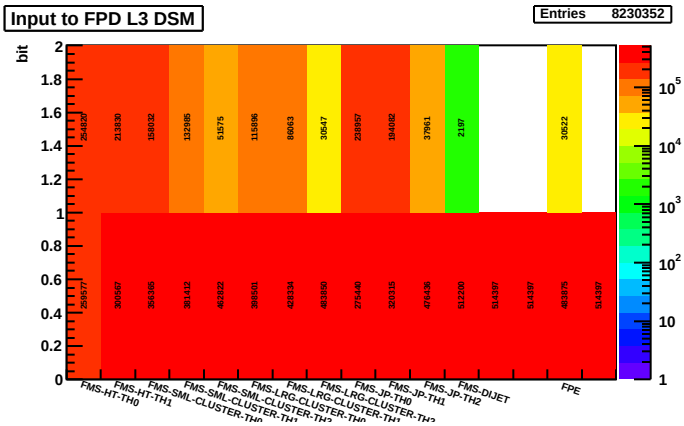

Input to FPD L2 DSM
Entries 12256
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

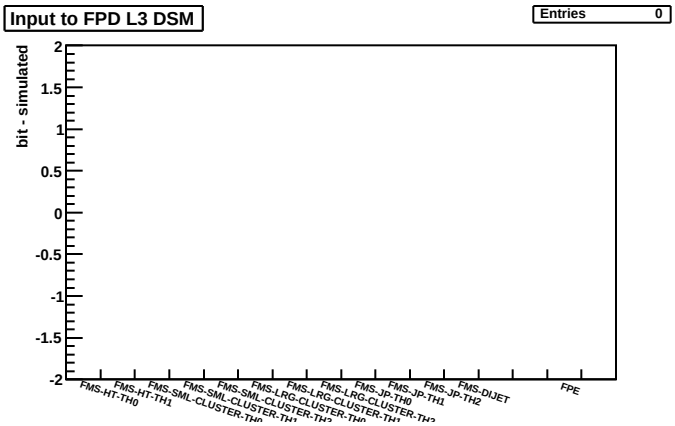

Input to FPD L2 DSM
Entries 1543191
CL bits
51575
17445
13859
81410
31241
24298
25047
16251
14945
356365
449460
461295
SMALL
LARGE-S
LARGE-N

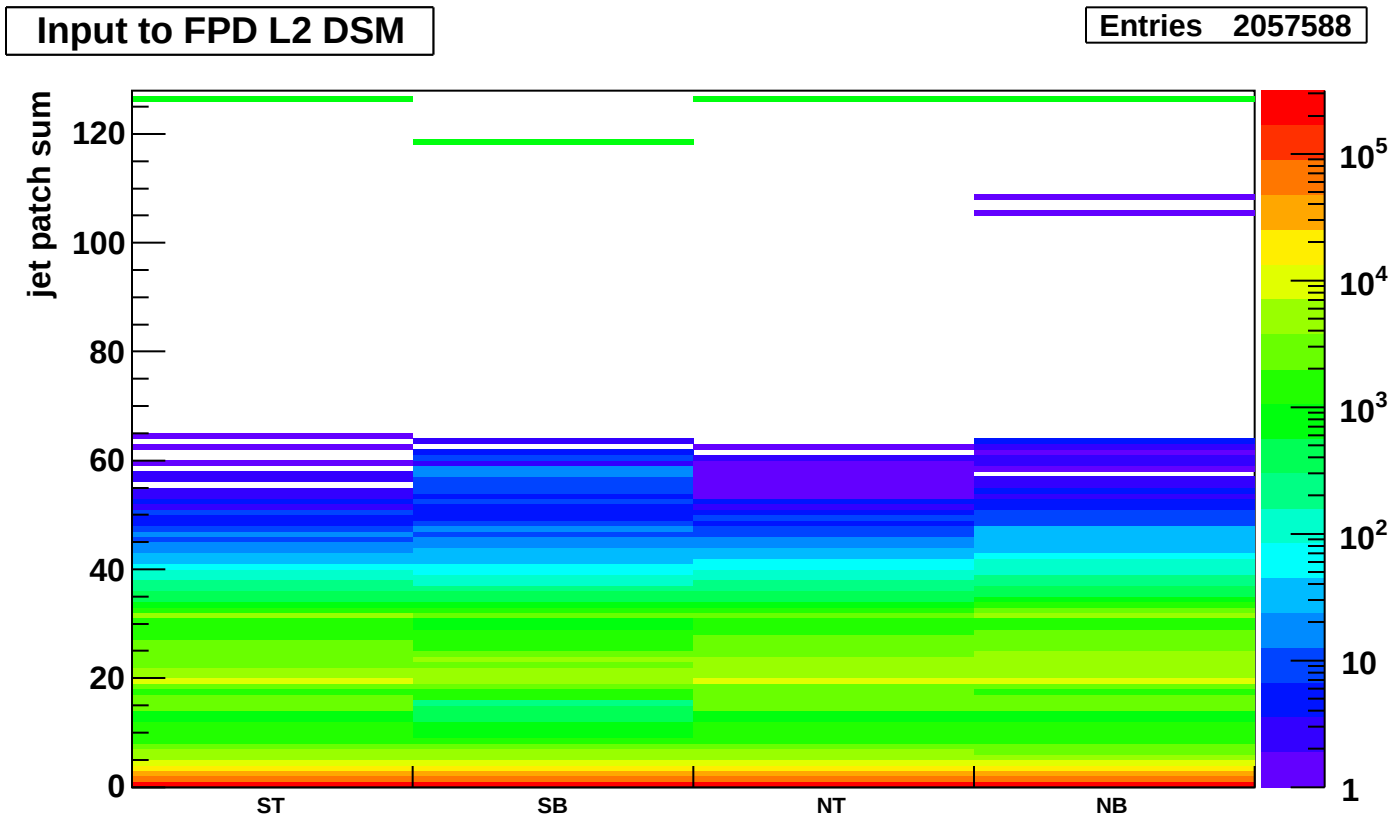

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

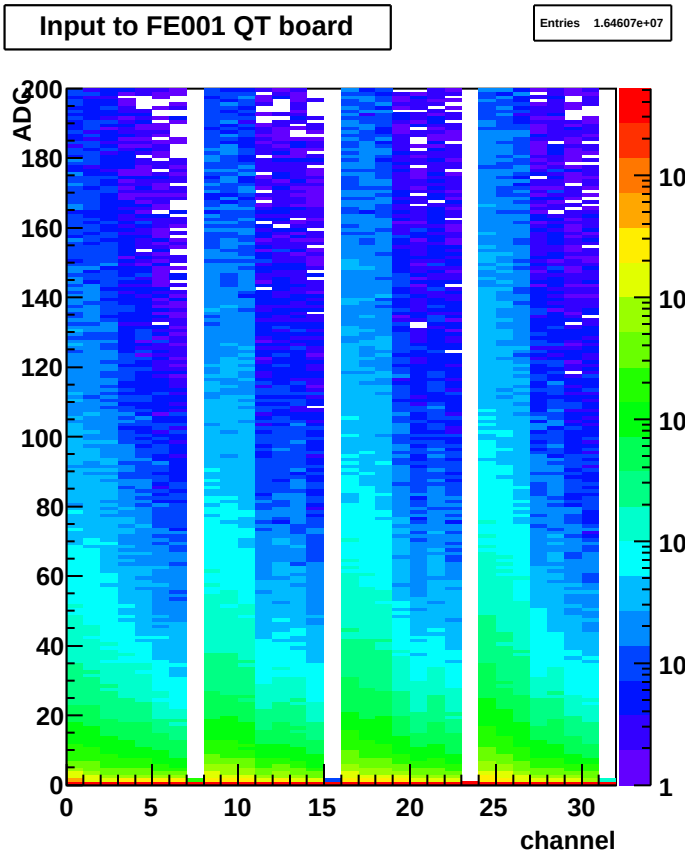

Input to FPD L2 DSM
Entries 1543191
HT bits
120690
53951
41956
28997
10809
10551
364710
449637
461890
SMALL
LARGE-S
LARGE-N

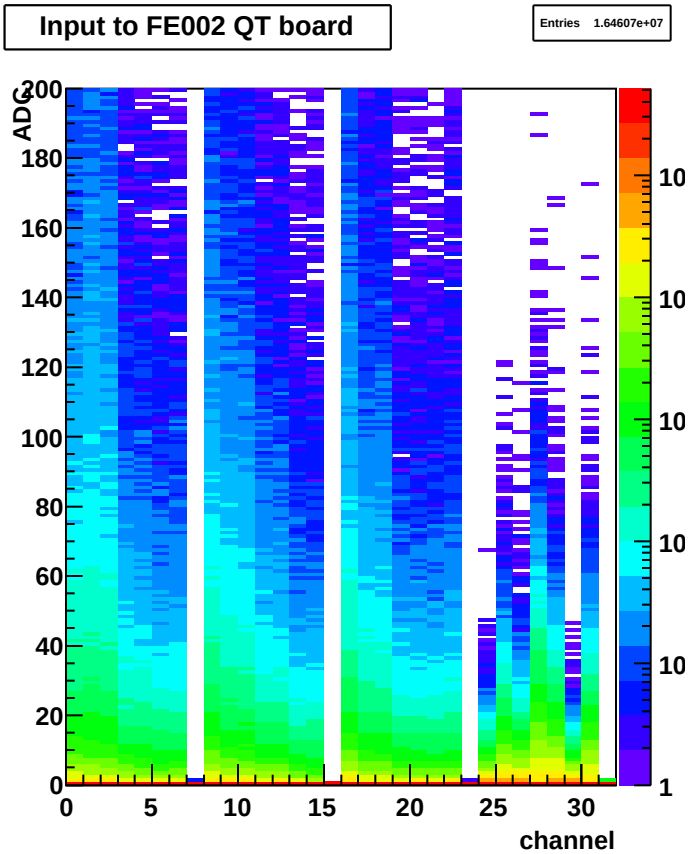

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

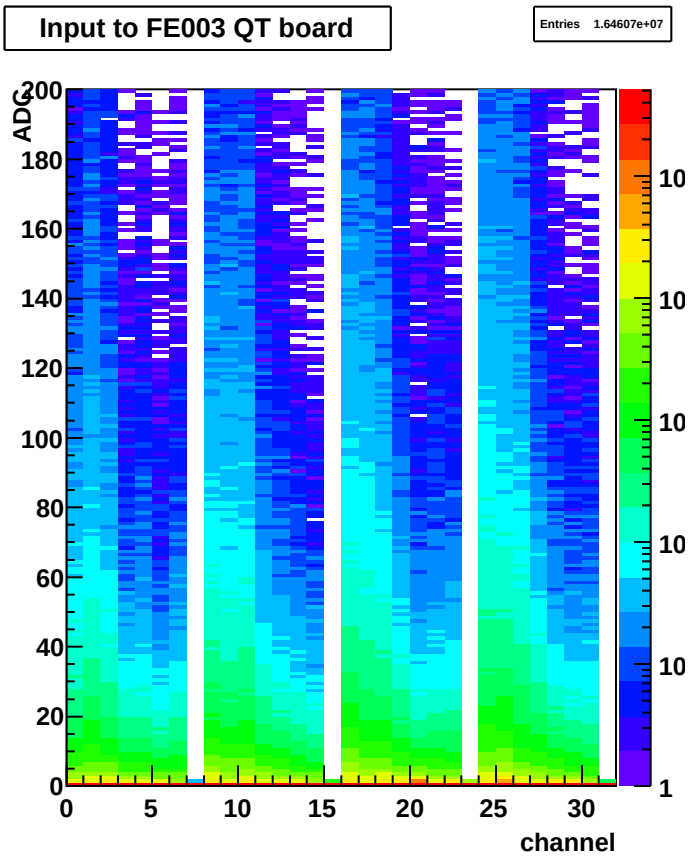

Input to FPD L3 DSM
Entries 8230352
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

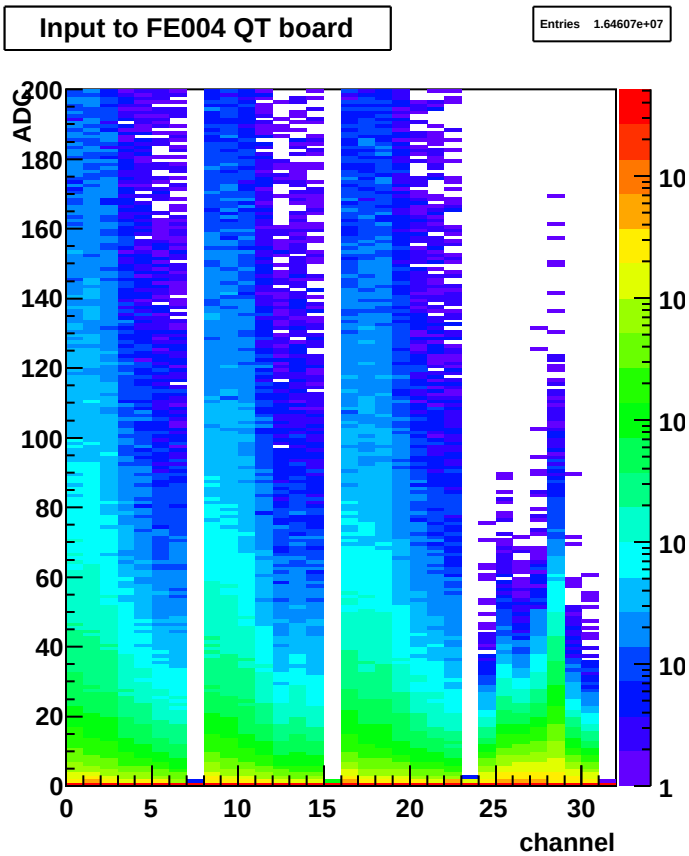

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

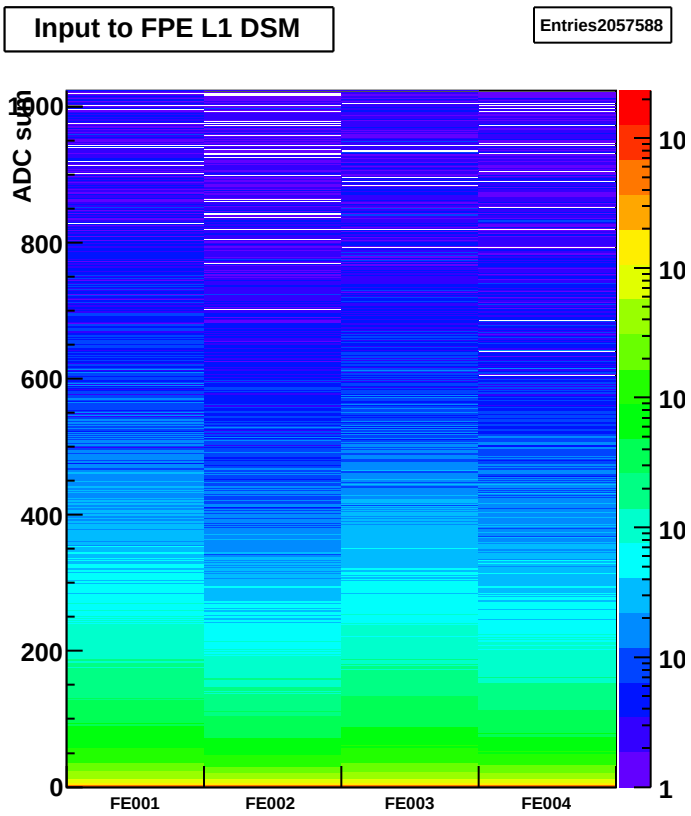

Input to FPD L2 DSM
Entries 2057588
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

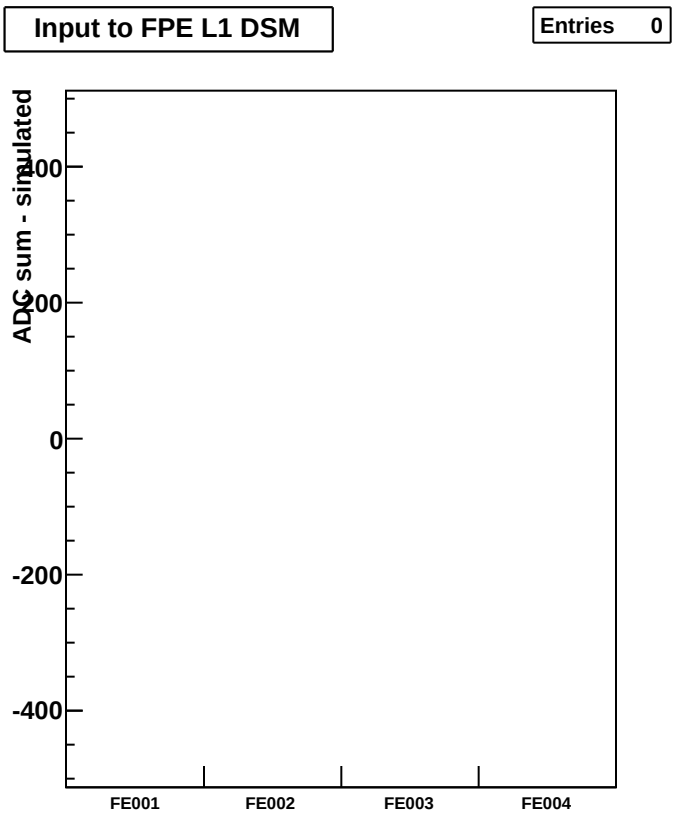

Input to FE001 QT board
Entries 1.64607e+07
ADC
channel

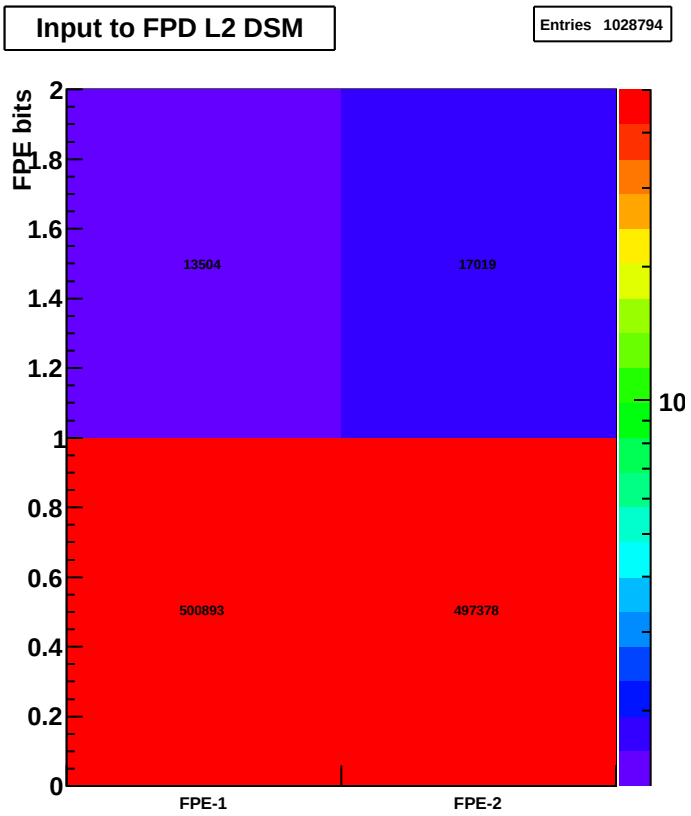

Input to FE002 QT board
Entries 1.64607e+07
ADC
channel

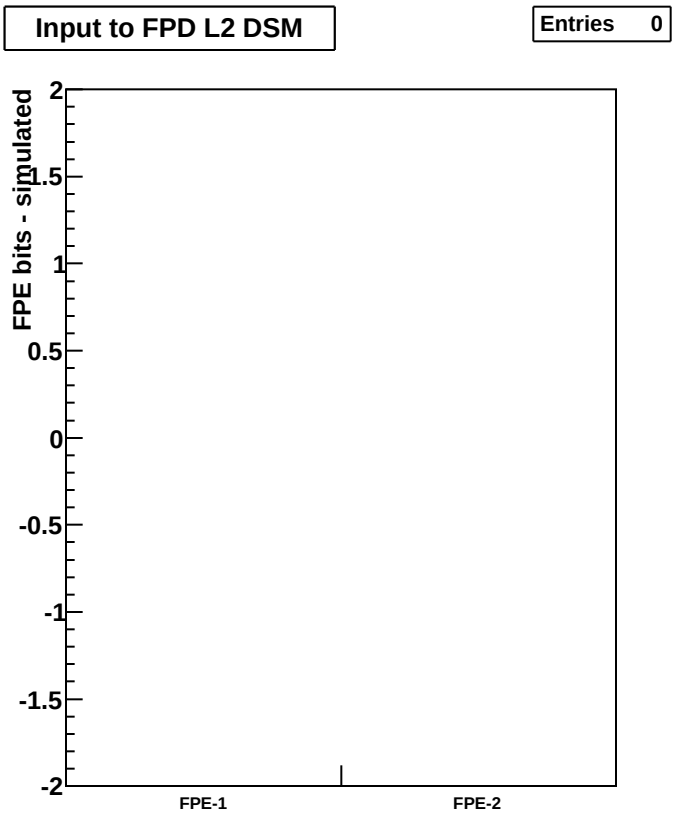

Input to FE003 QT board
Entries 1.64607e+07
ADC
channel


Input to FE004 QT board
Entries 1.64607e+07
ADC
channel


Input to FPE L1 DSM
Entries2057588
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1028794
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
13504
17019
500893
497378
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2