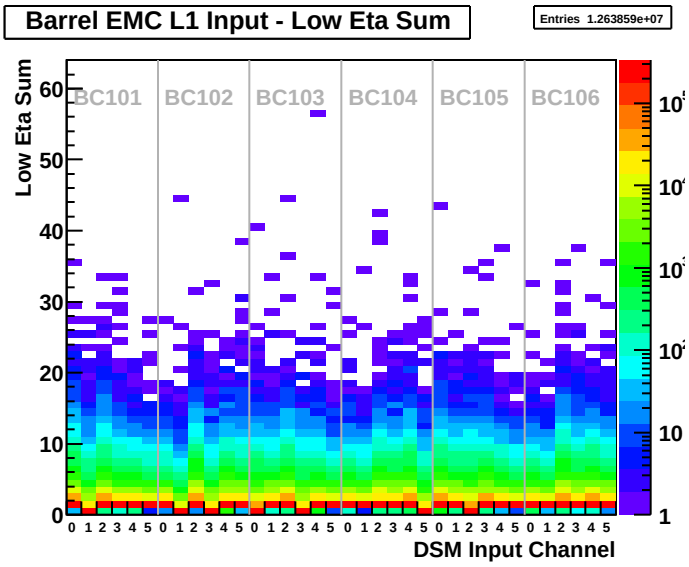
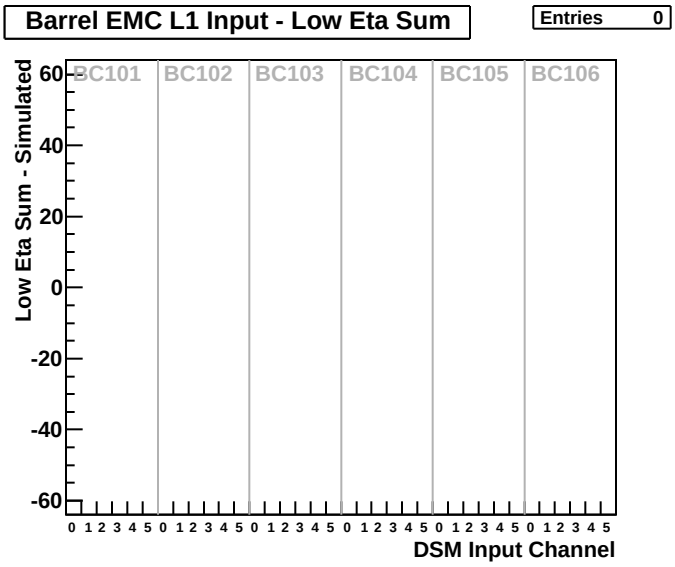
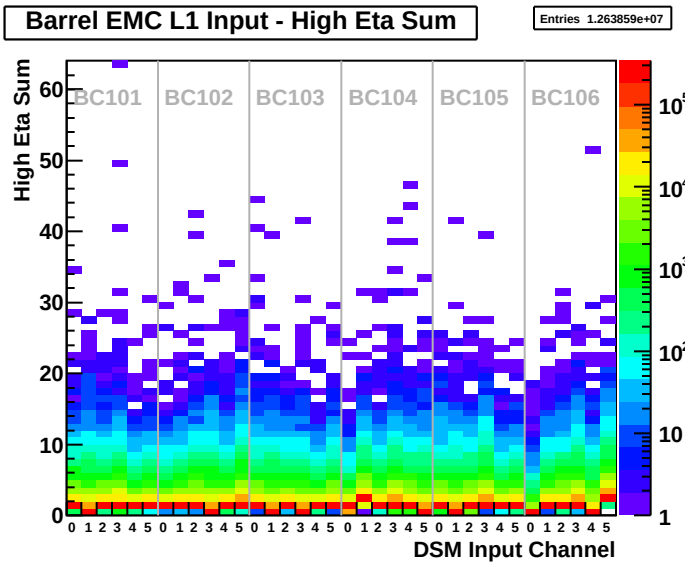
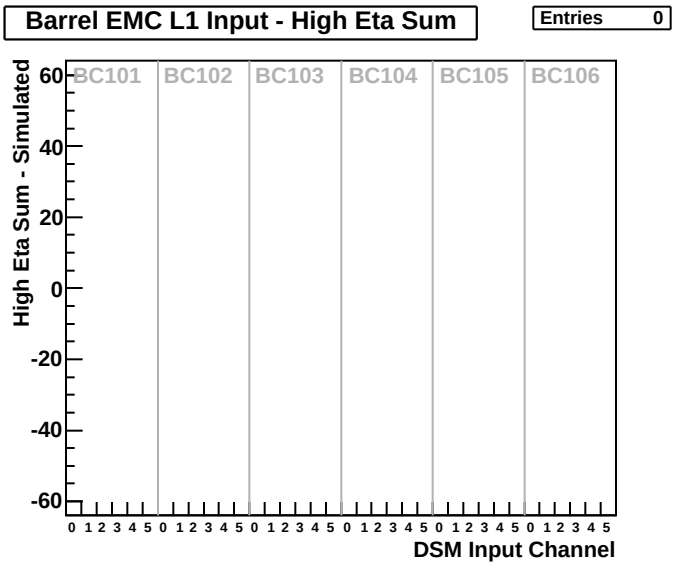
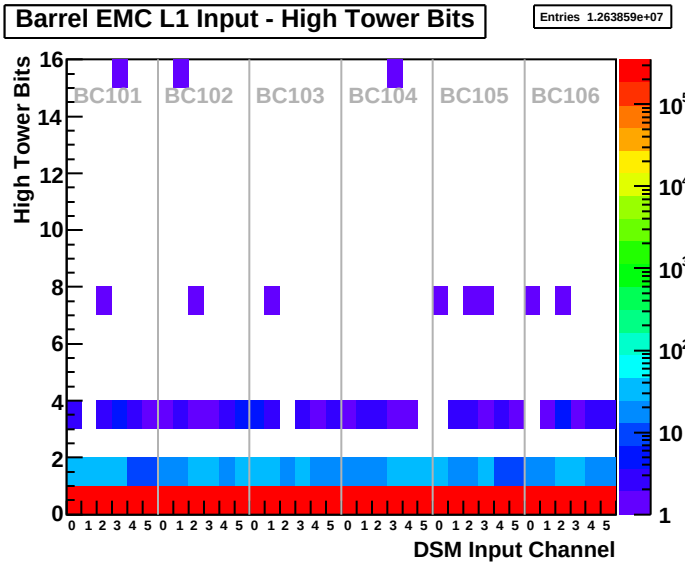
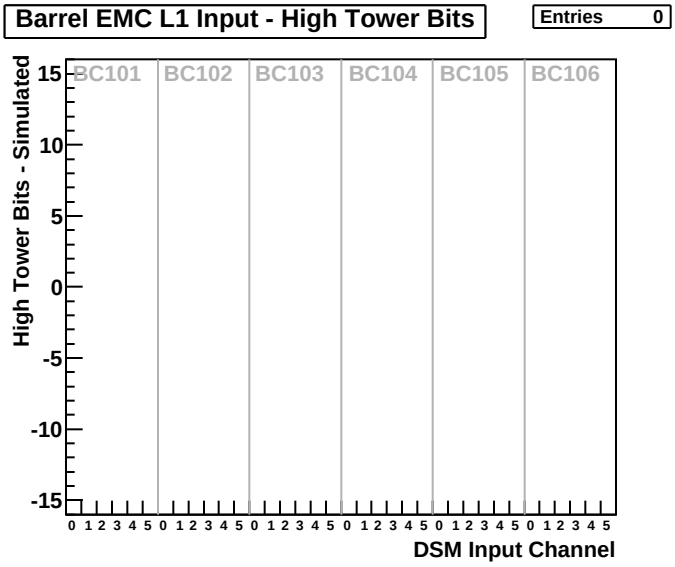

Barrel EMC L1 Input - Low Eta Sum
Entries 1.263859e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

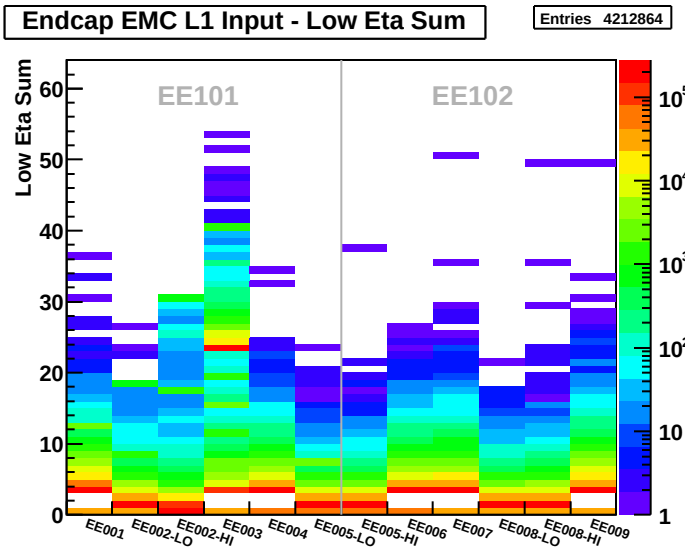

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

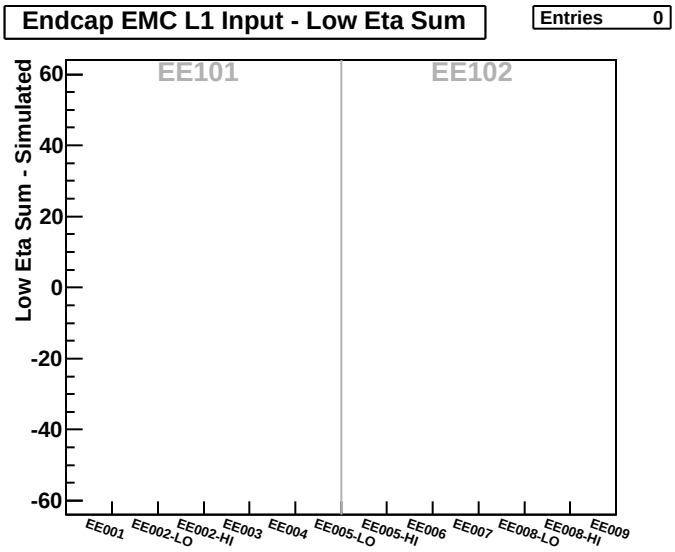

Barrel EMC L1 Input - High Eta Sum
Entries 1.263859e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

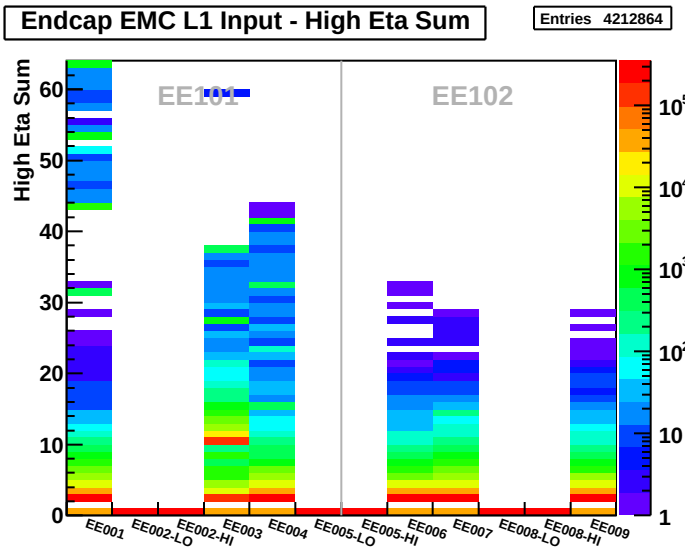

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

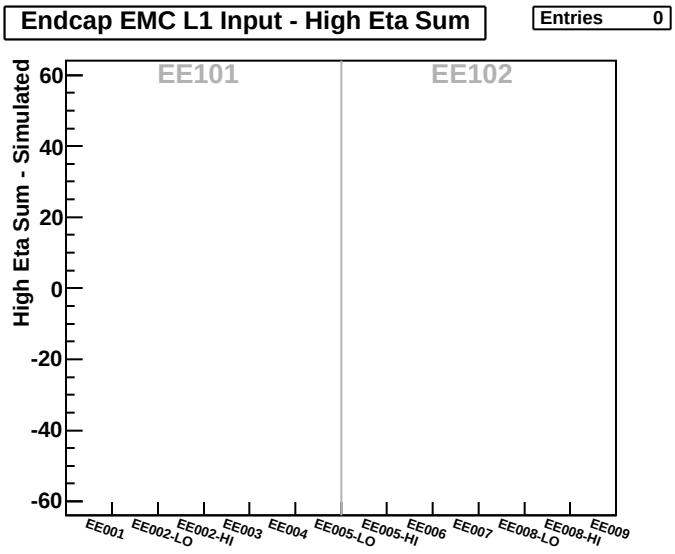

Barrel EMC L1 Input - High Tower Bits
Entries 1.263859e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

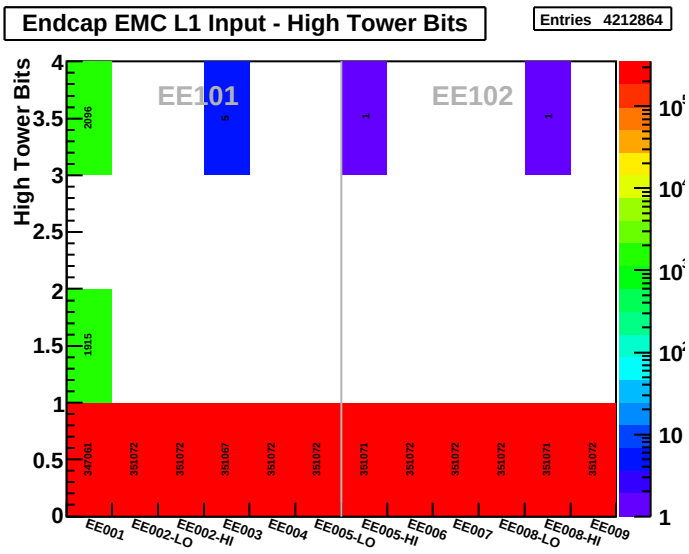

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

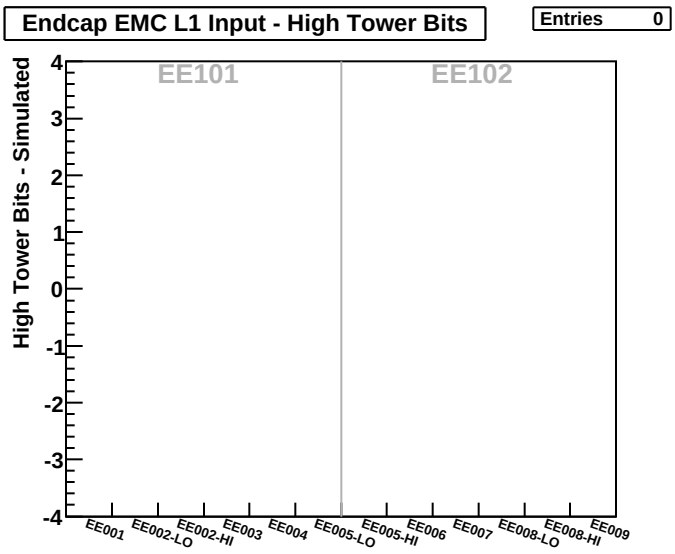

Endcap EMC L1 Input - Low Eta Sum
Entries 4212864
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

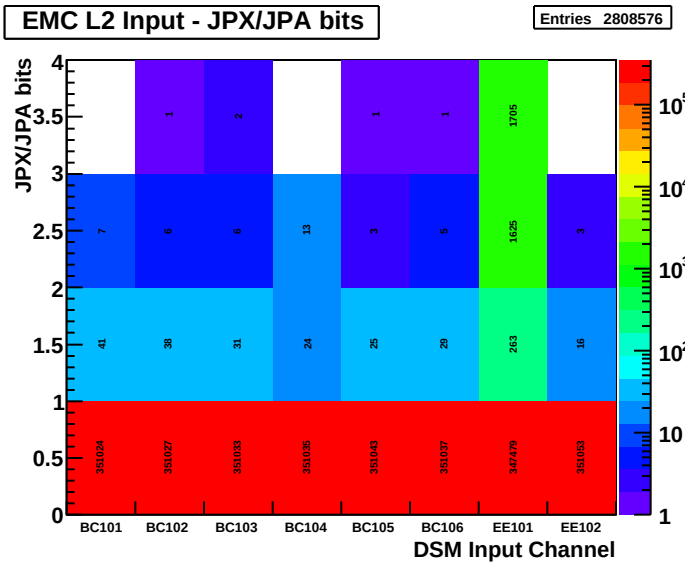

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

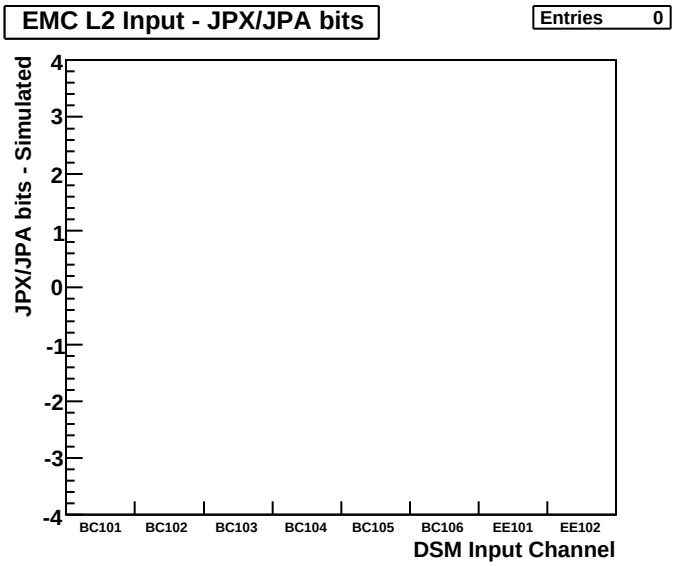

Endcap EMC L1 Input - High Eta Sum
Entries 4212864
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

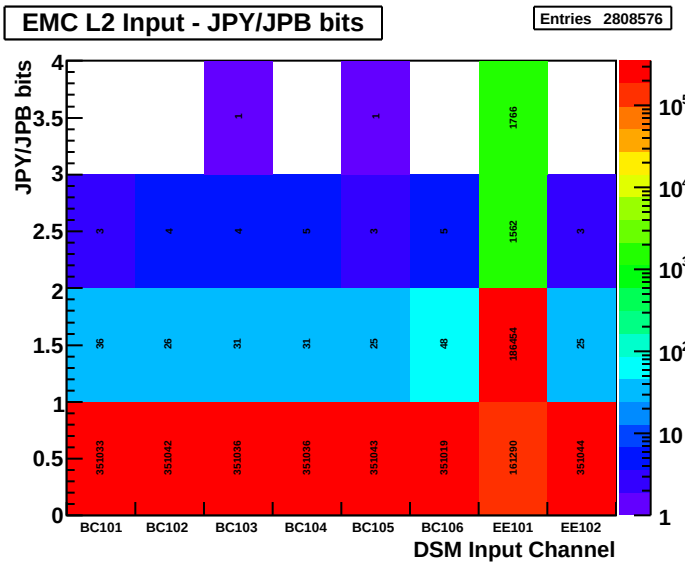

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

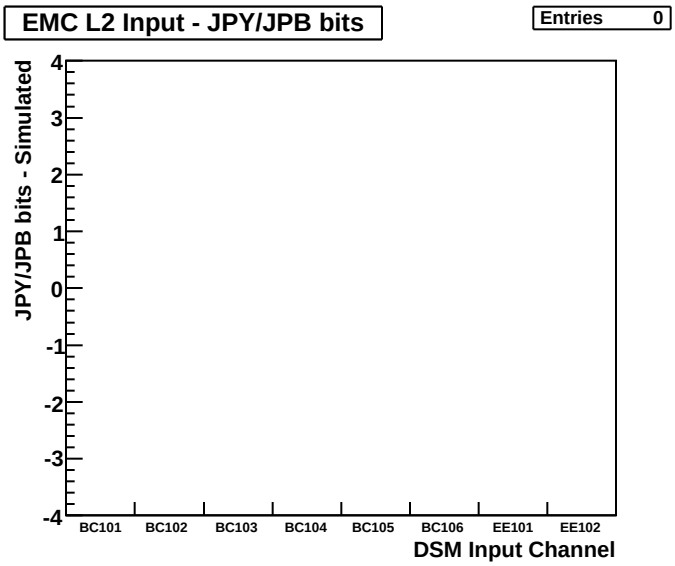

Endcap EMC L1 Input - High Tower Bits
Entries 4212864
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

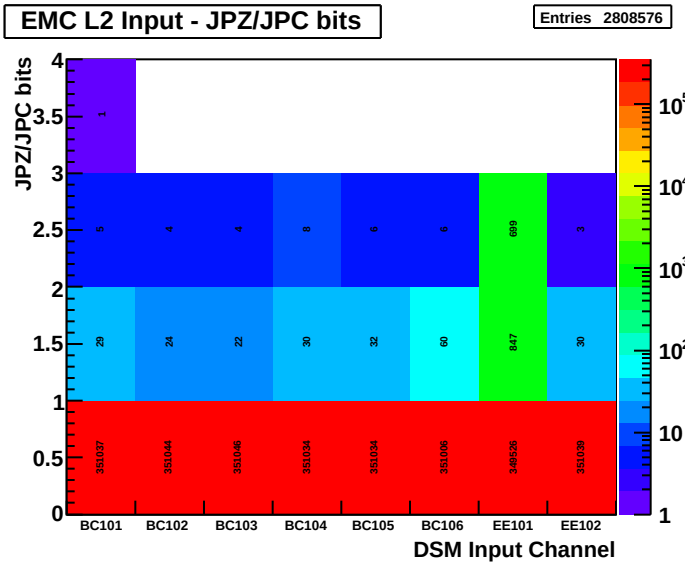

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

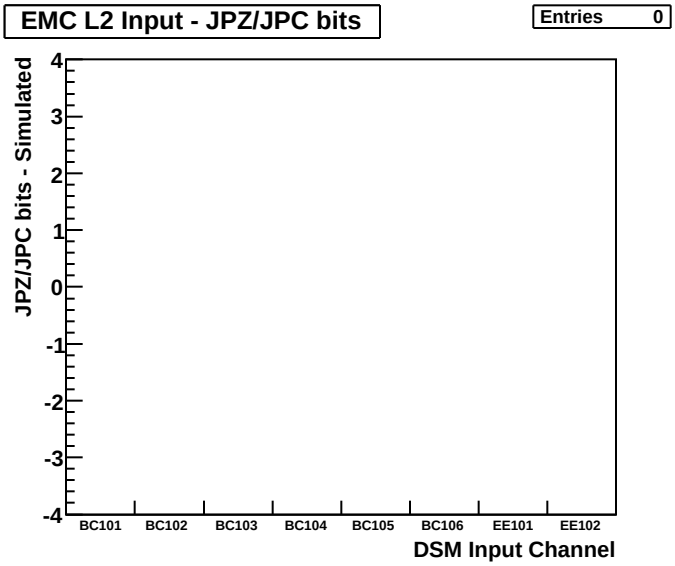

EMC L2 Input - JPX/JPA bits
Entries 2808576
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

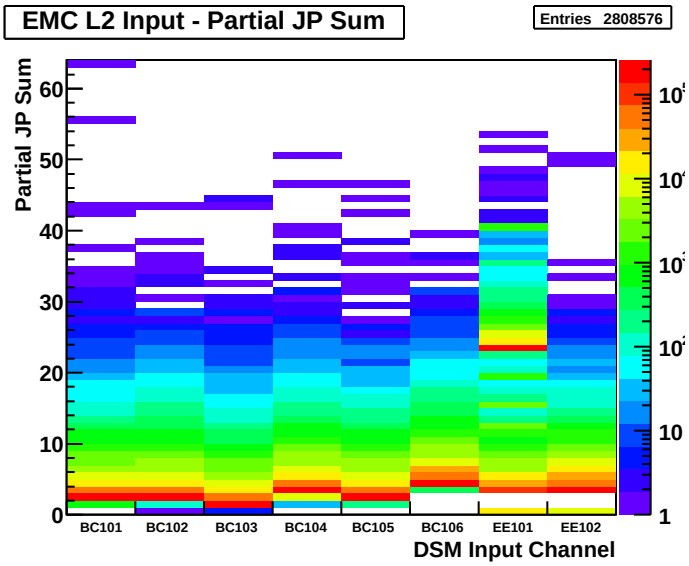

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

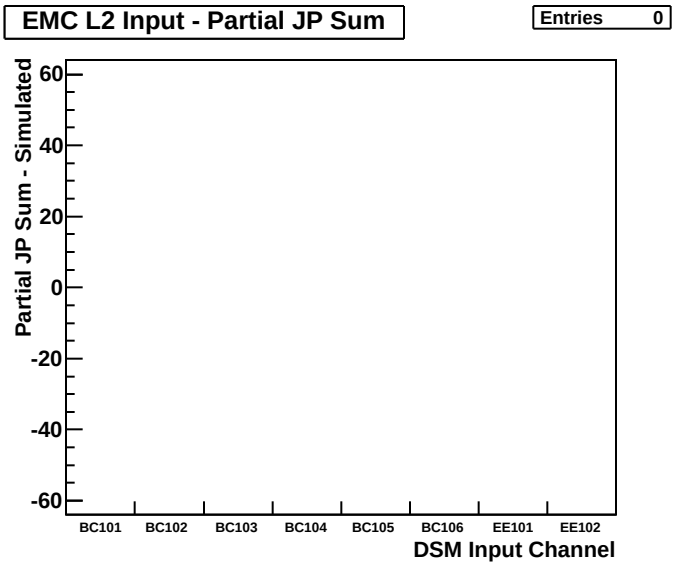

EMC L2 Input - JPY/JPB bits
Entries 2808576
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

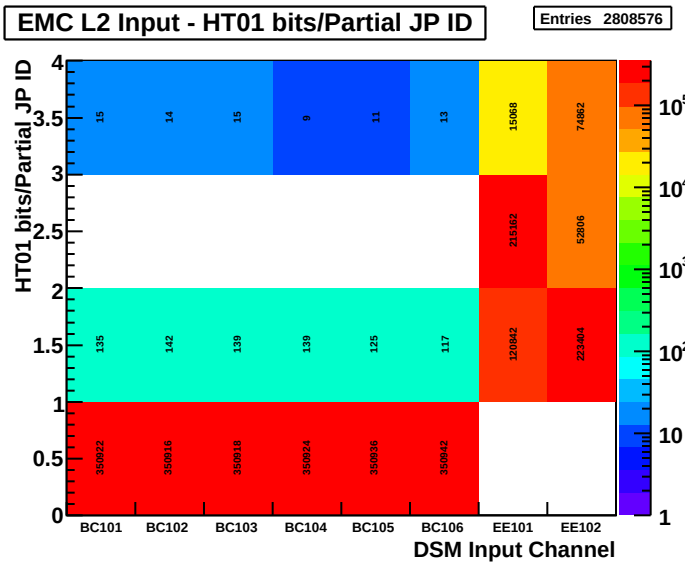

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

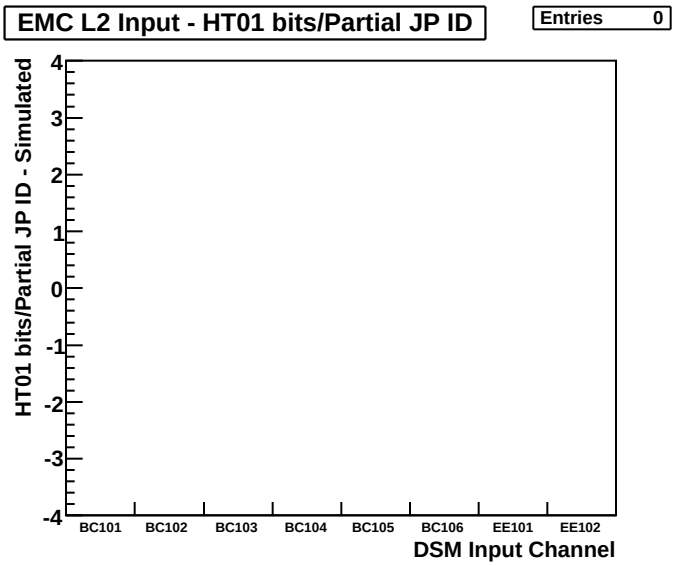

EMC L2 Input - JPZ/JPC bits
Entries 2808576
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

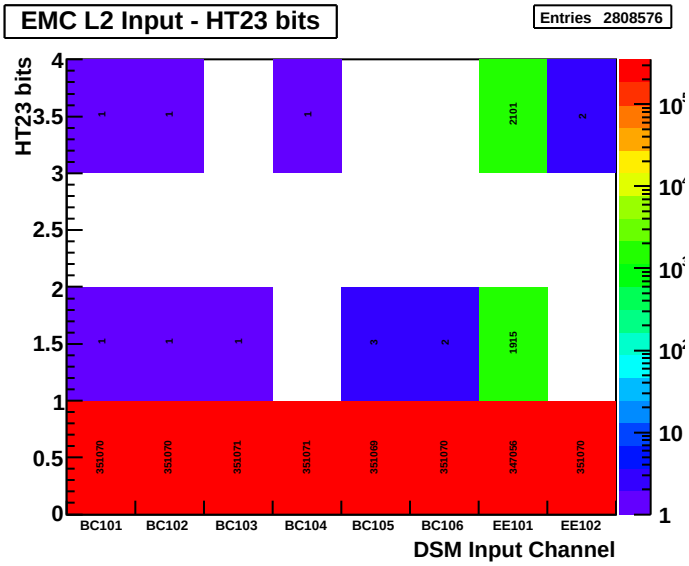

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

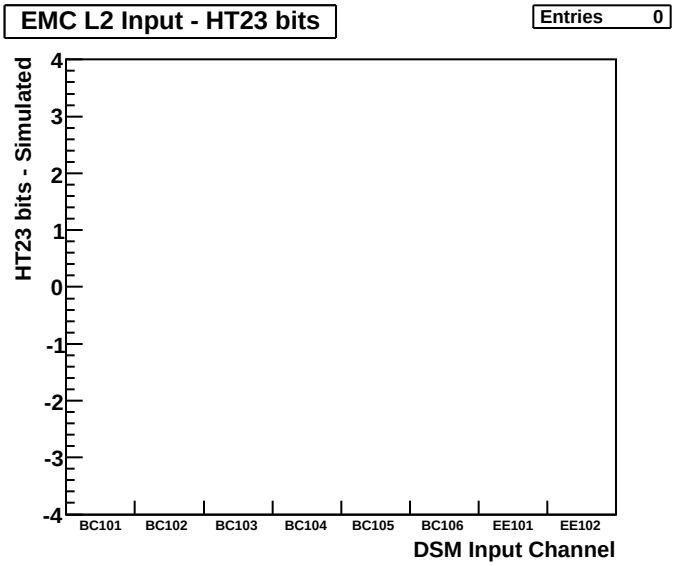

EMC L2 Input - Partial JP Sum
Entries 2808576
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

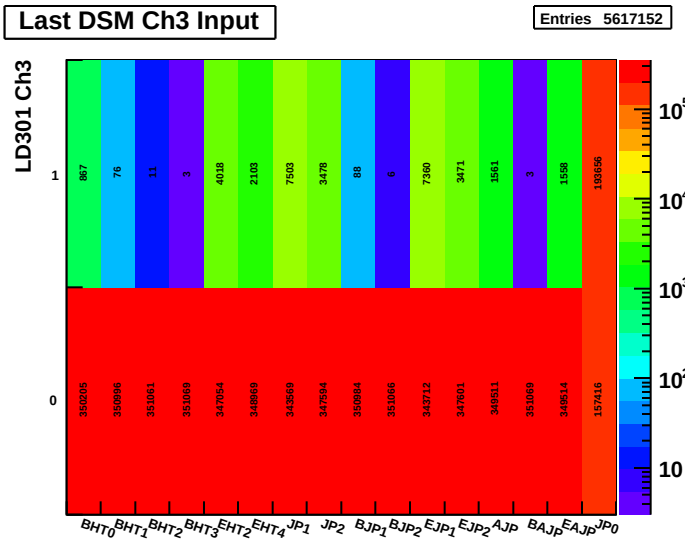

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

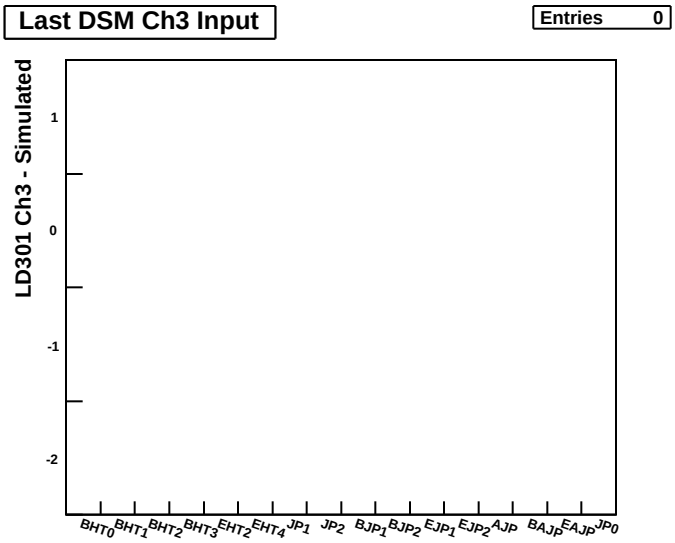

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2808576
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

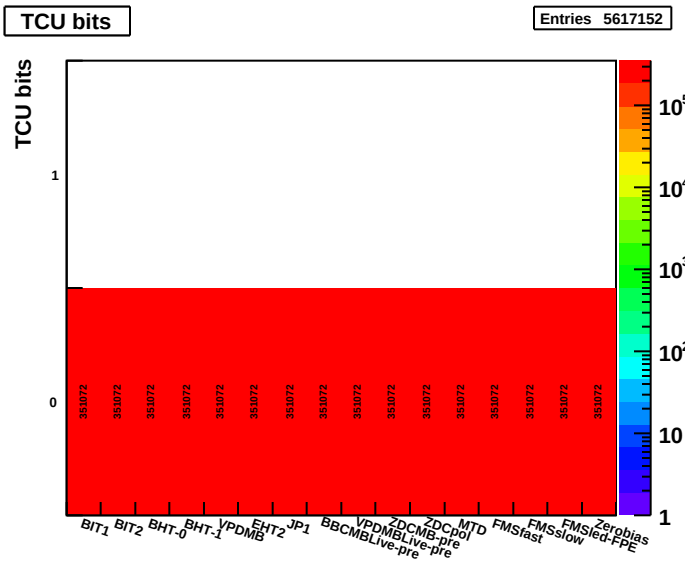

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

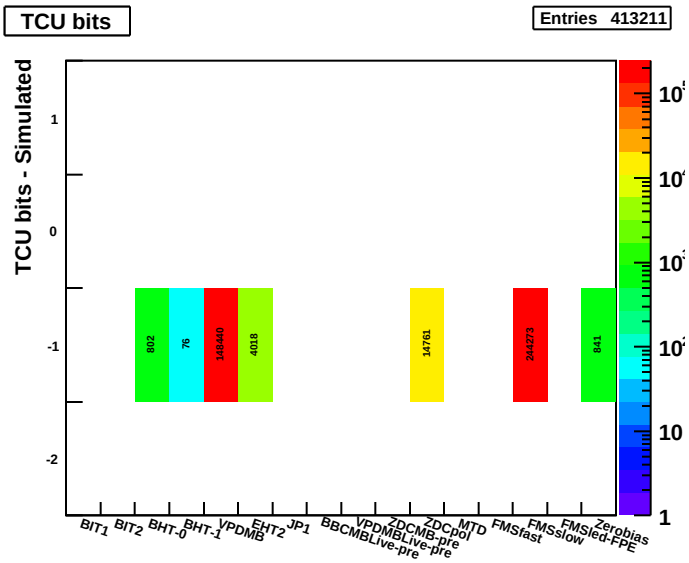

EMC L2 Input - HT23 bits
Entries 2808576
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

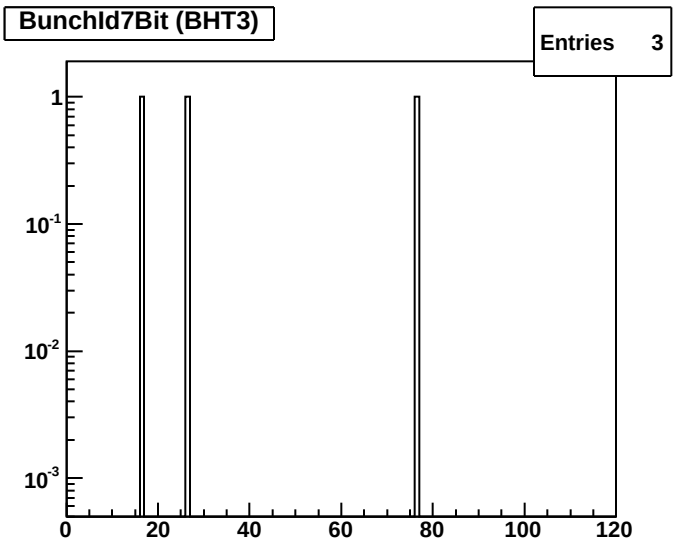

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

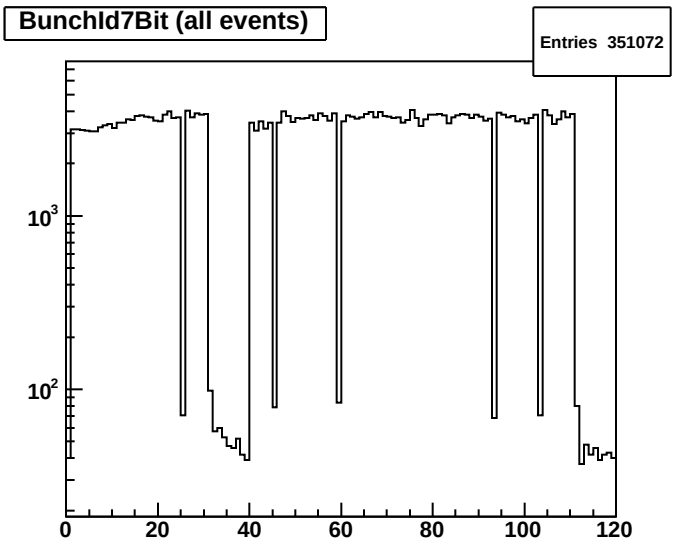

Last DSM Ch3 Input
Entries 5617152
LD301 Ch3
867 76 11 3 4018 2103 7503 3478 88 6 7360 3471 1561 3 1558 193656
350205 350996 351061 351069 347054 348969 343569 347594 350984 351066 343712 347601 349511 351069 349514 157416
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

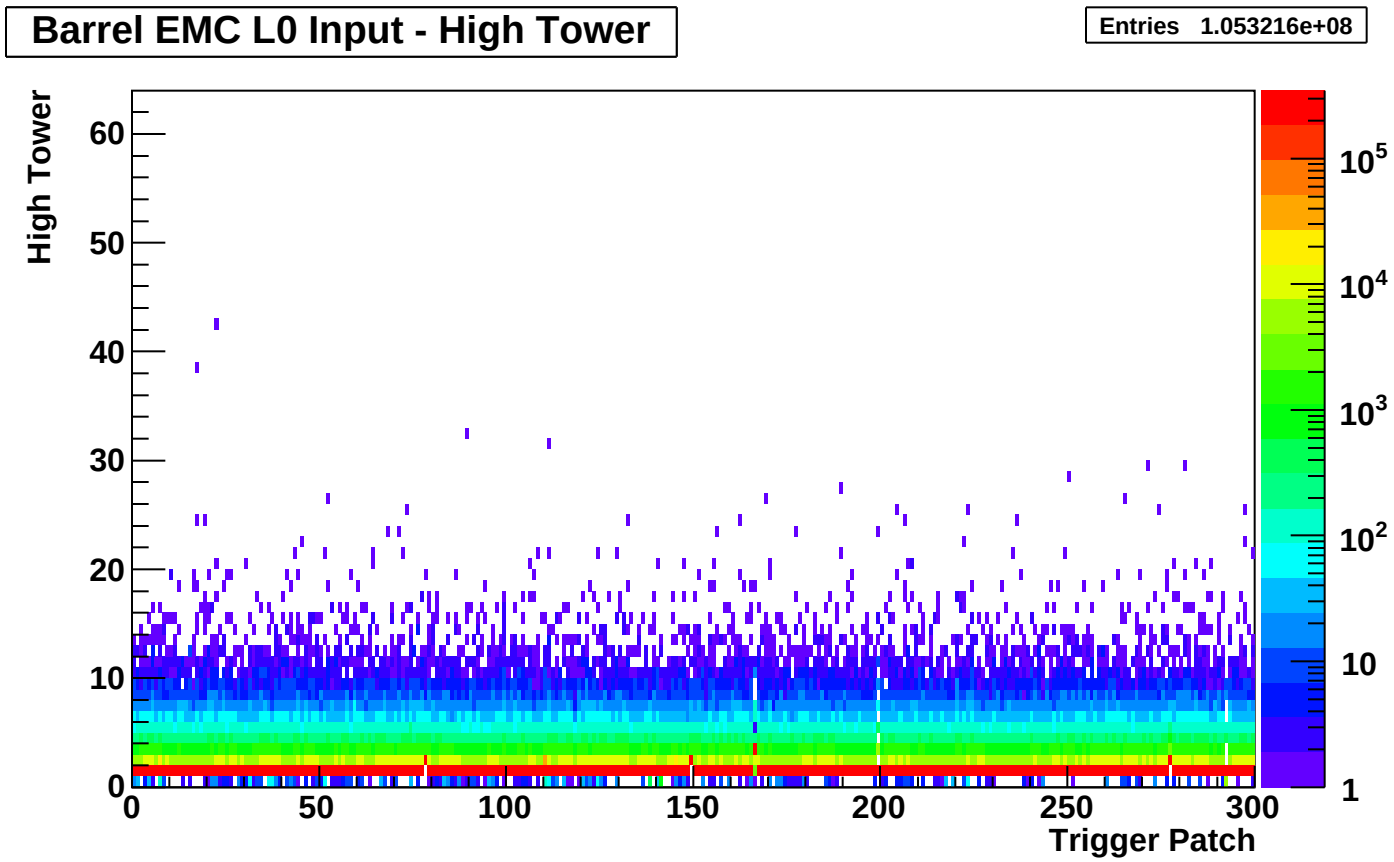

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

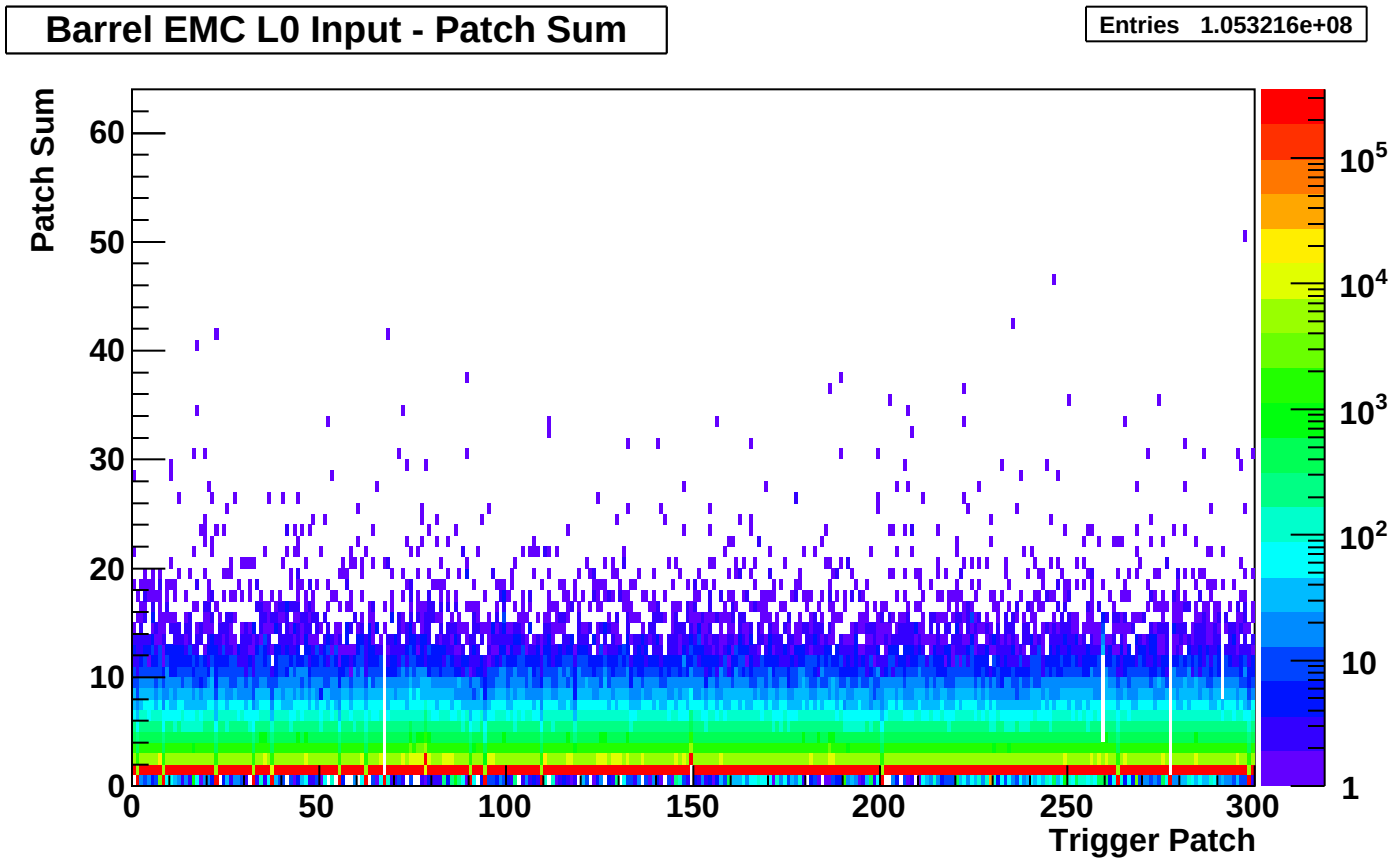

TCU bits
Entries 5617152
TCU bits
351072 351072 351072 351072 351072 351072 351072 351072 351072 351072 351072 351072 351072 351072 351072 351072
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

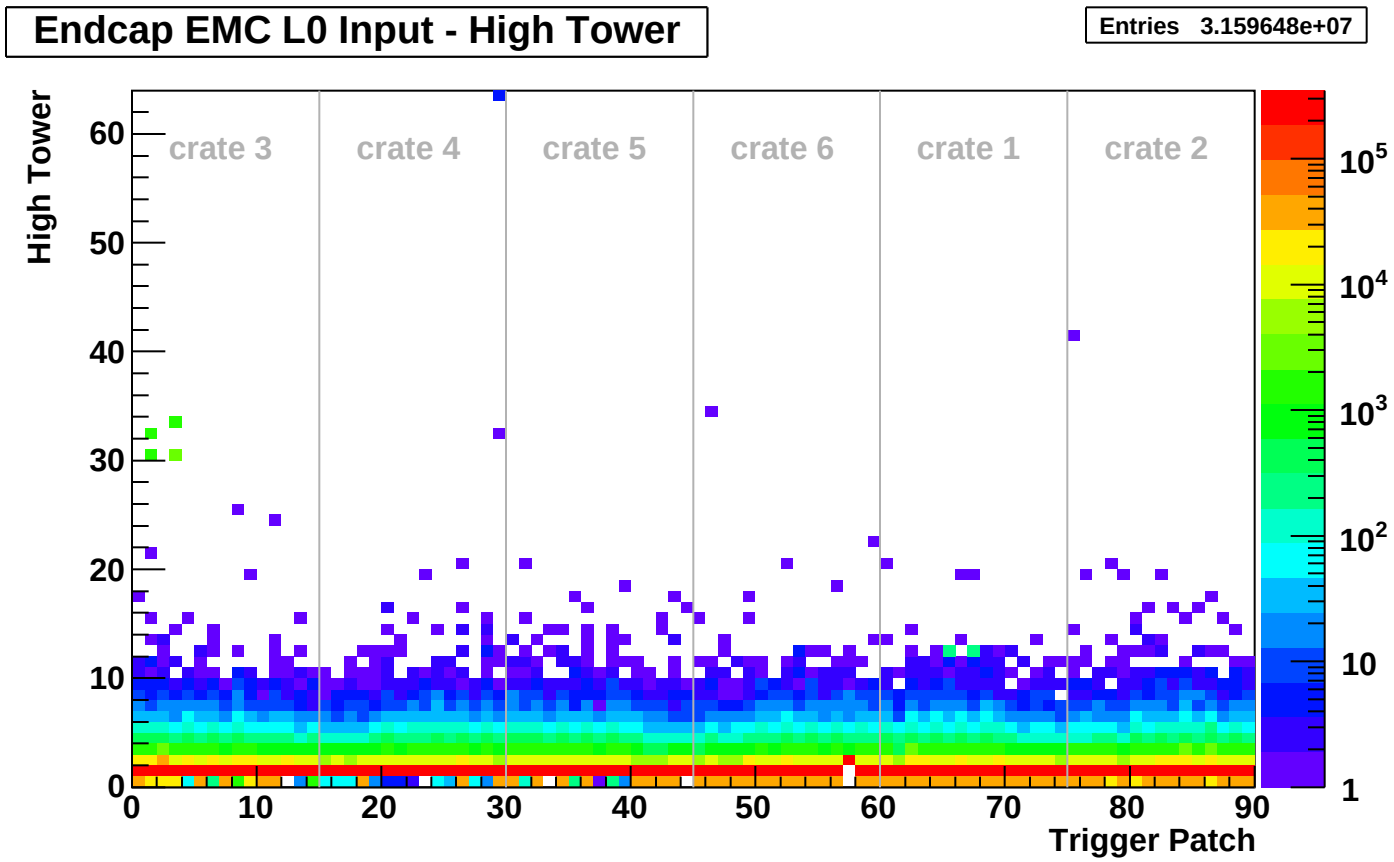

TCU bits
Entries 413211
TCU bits - Simulated
802 76 140440 4018 14761 244273 841
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

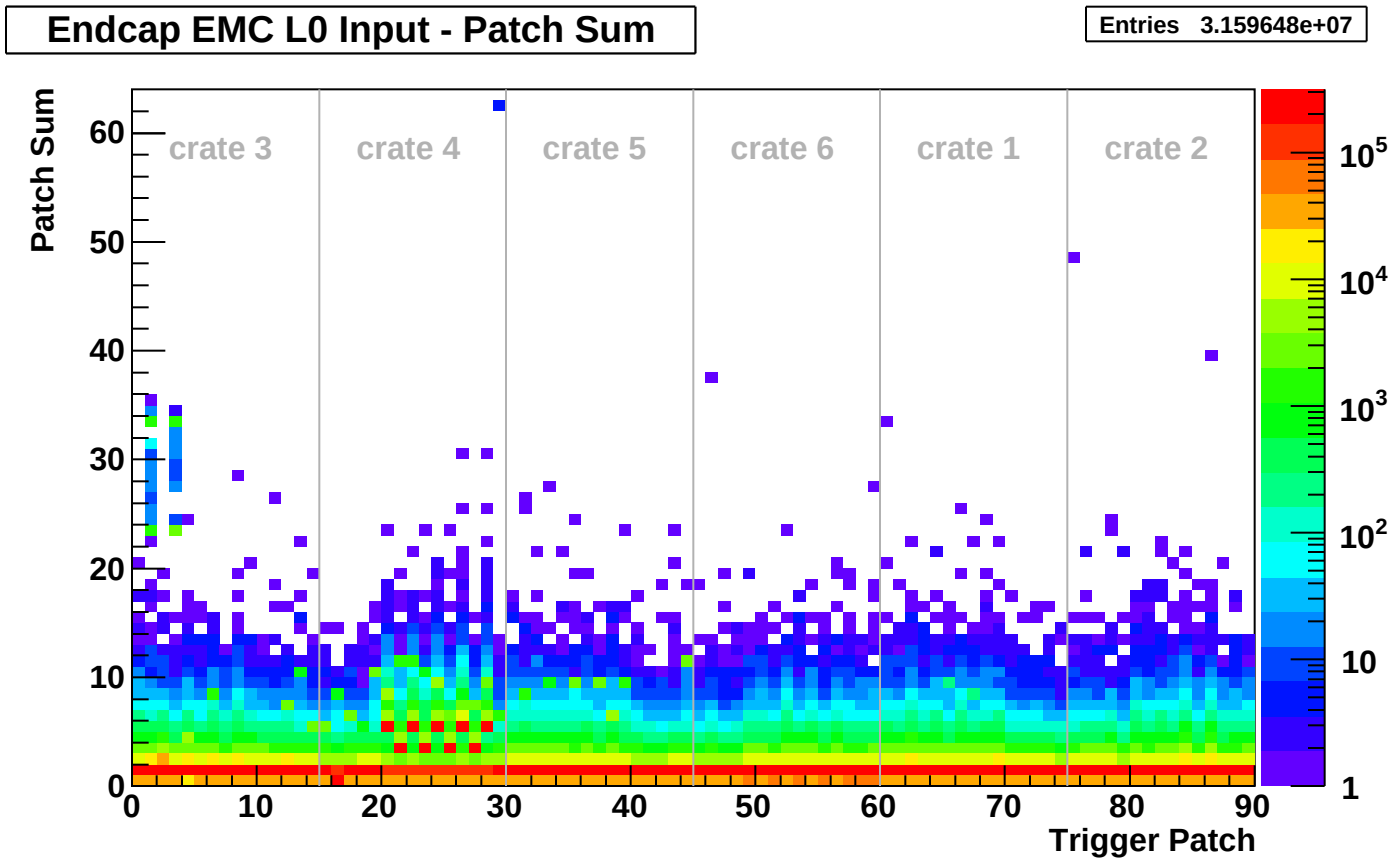

BunchId7Bit (BHT3)
Entries 3

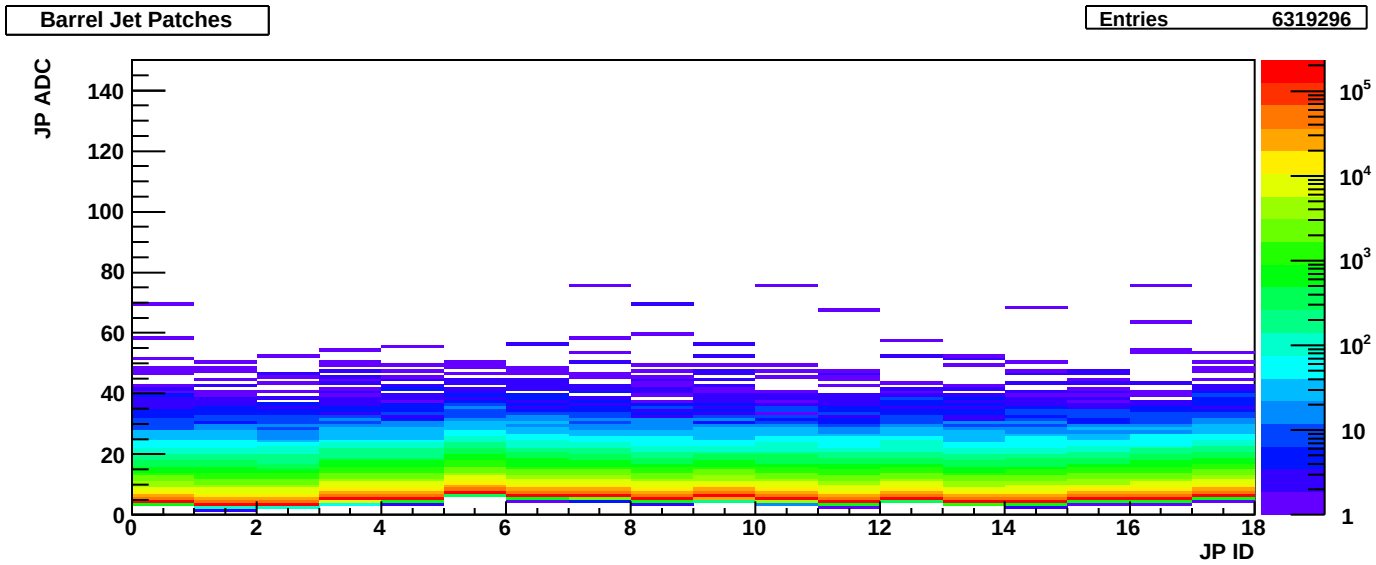

BunchId7Bit (all events)
Entries 351072

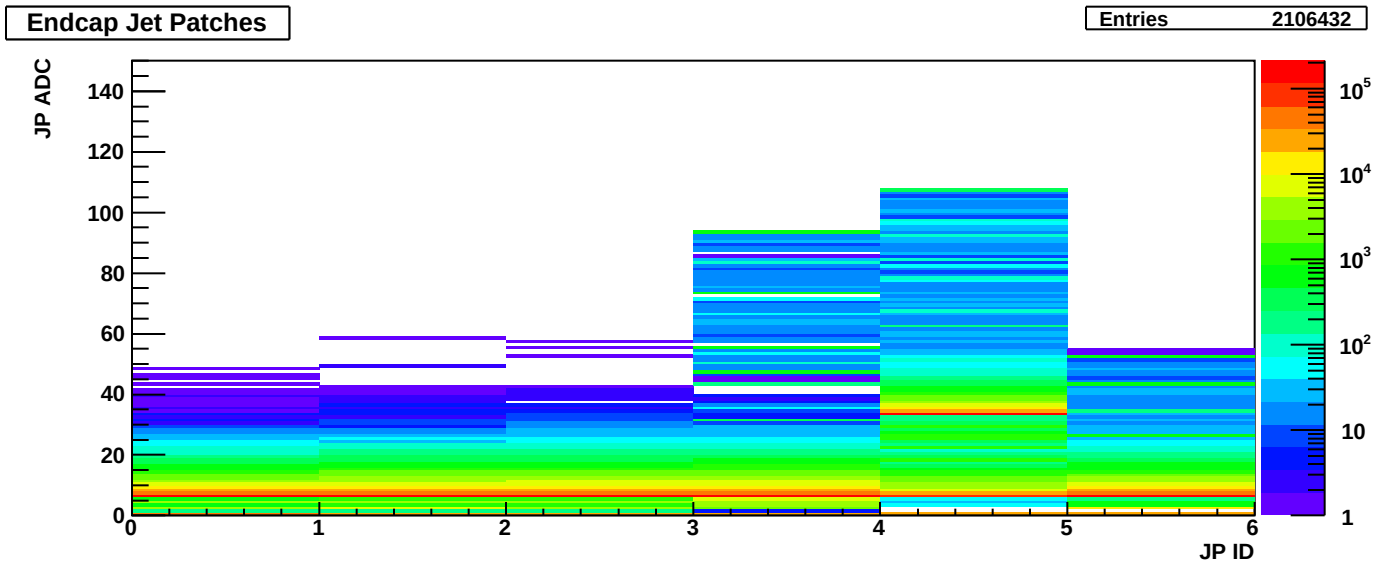

Barrel EMC L0 Input - High Tower
Entries 1.053216e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

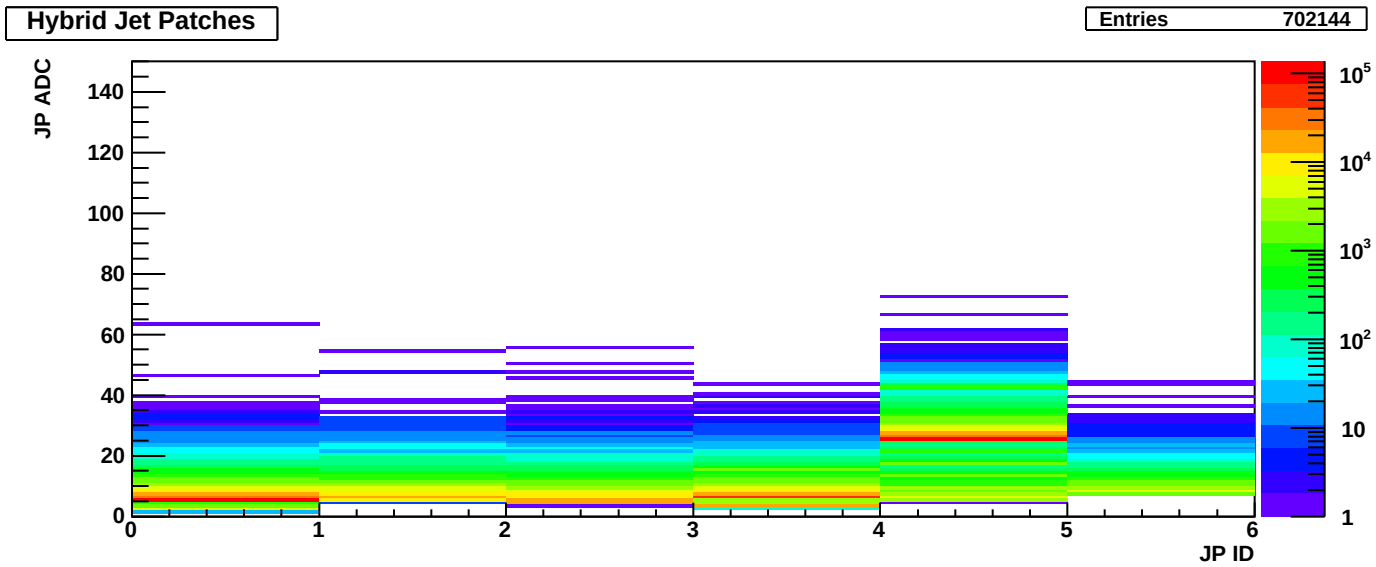

Barrel EMC L0 Input - Patch Sum
Entries 1.053216e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 3.159648e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

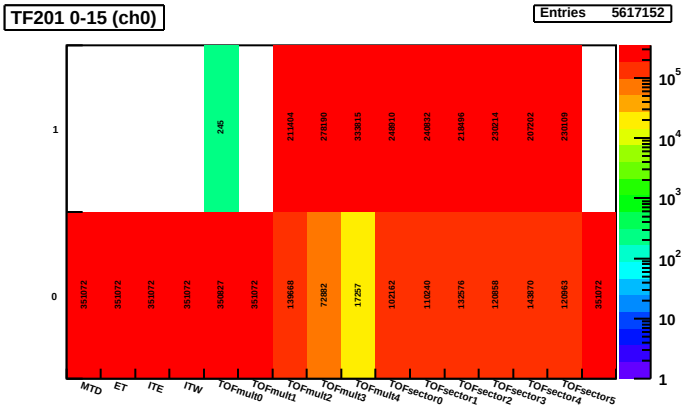

Endcap EMC L0 Input - Patch Sum
Entries 3.159648e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch


Barrel Jet Patches
Entries 6319296
JP ADC
JP ID

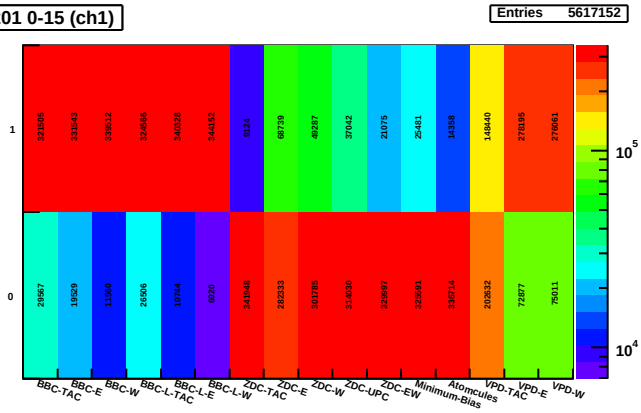

Endcap Jet Patches
Entries 2106432
JP ADC
JP ID


Hybrid Jet Patches
Entries 702144
JP ADC
JP ID

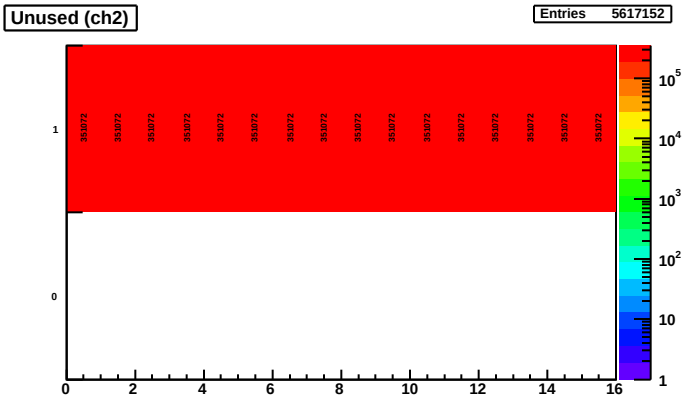
TF201 0-15 (ch0)



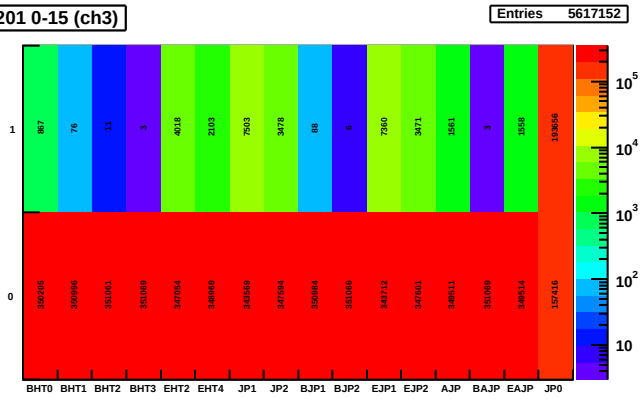
VT201 0-15 (ch1)



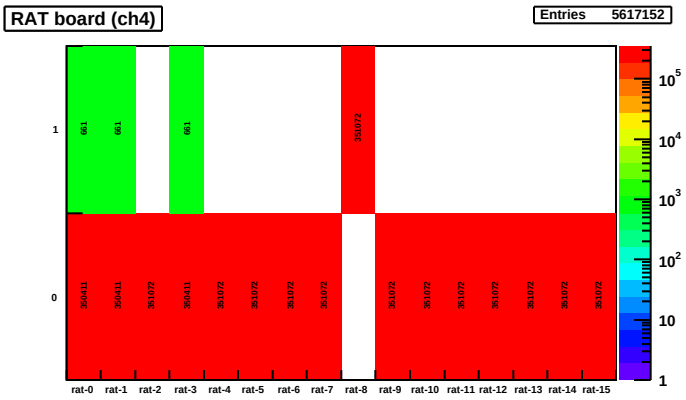
Unused (ch2)



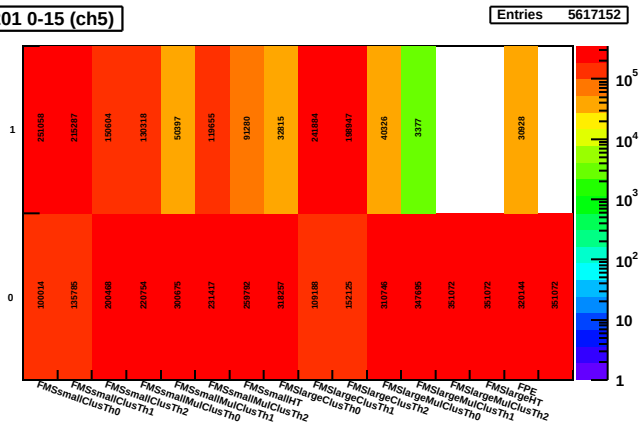
EM201 0-15 (ch3)



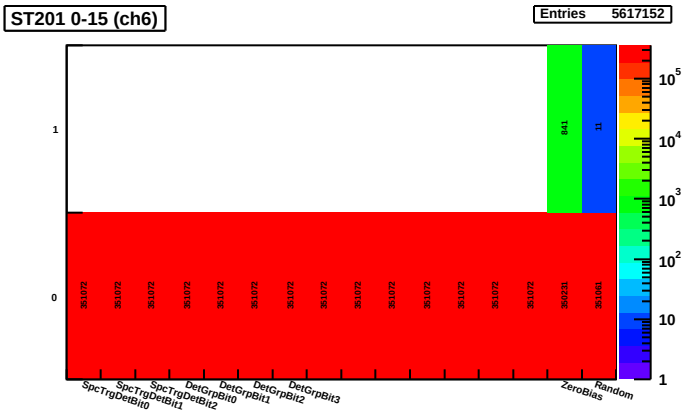
RAT board (ch4)



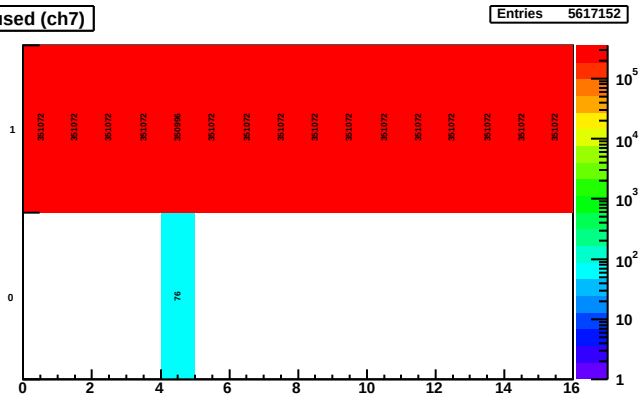
FP201 0-15 (ch5)

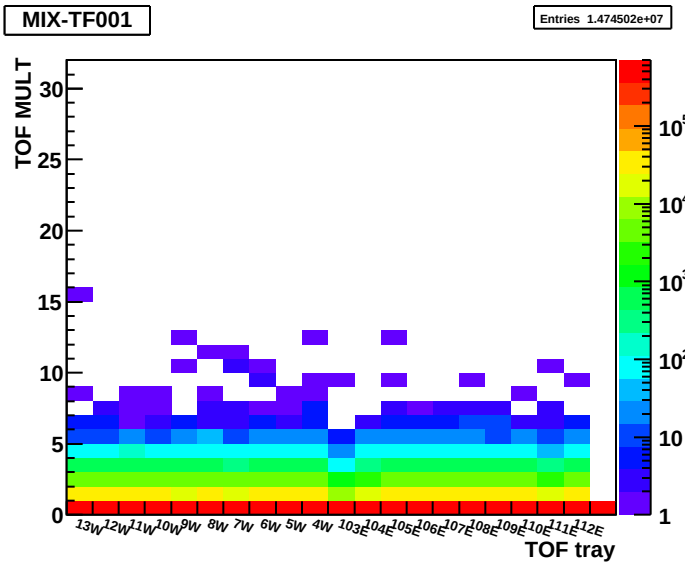


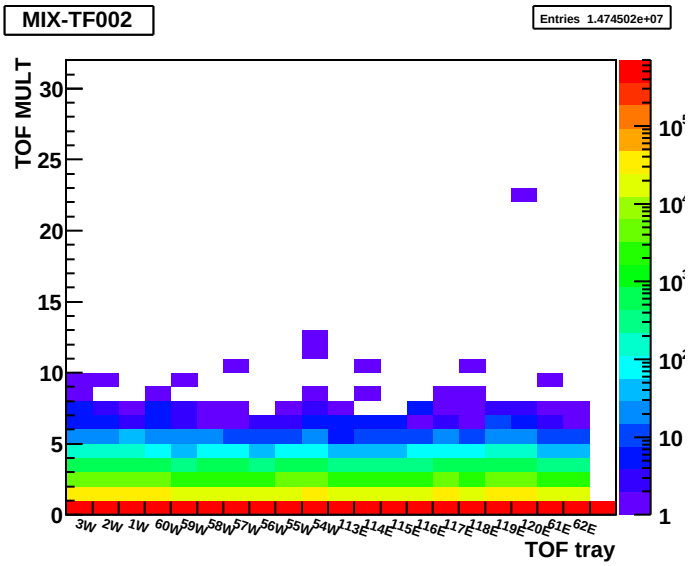
ST201 0-15 (ch6)

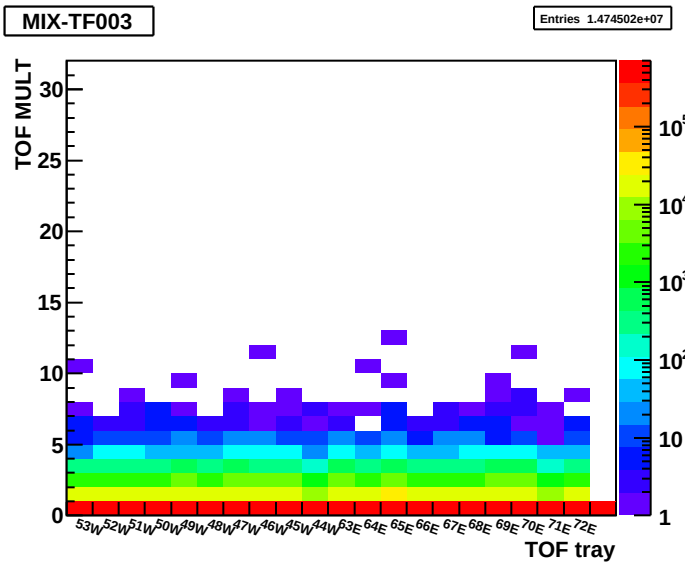


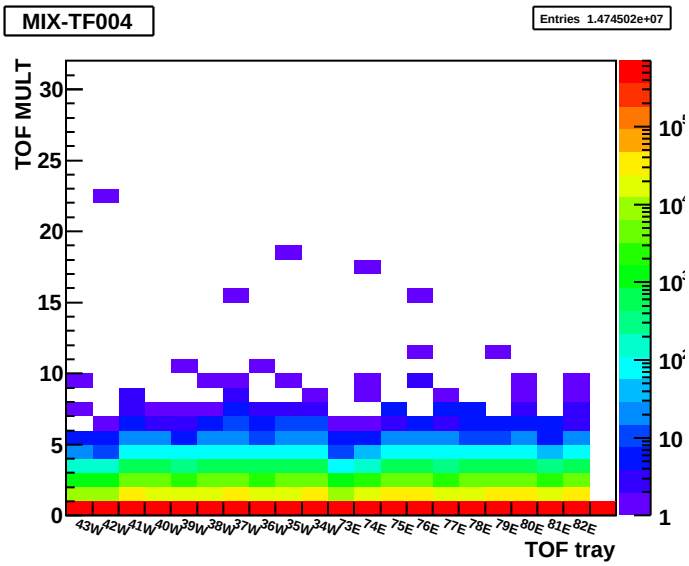
Unused (ch7)

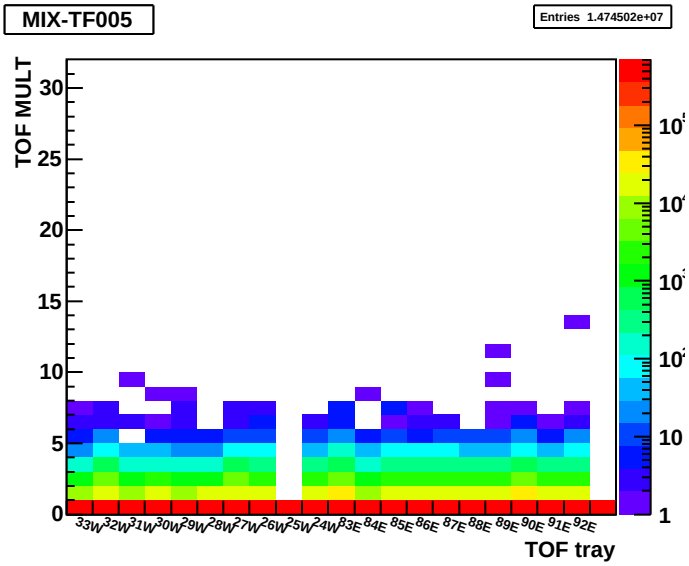


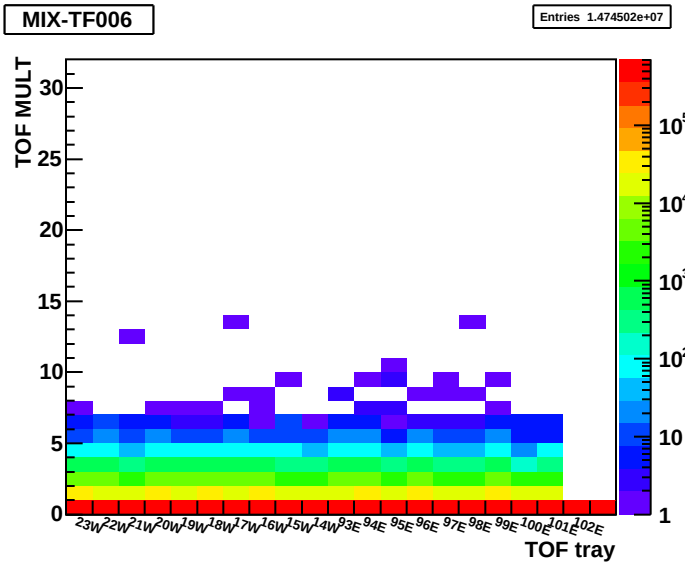

MIX-TF001
Entries 1.474502e+07
TOF MULT
TOF tray

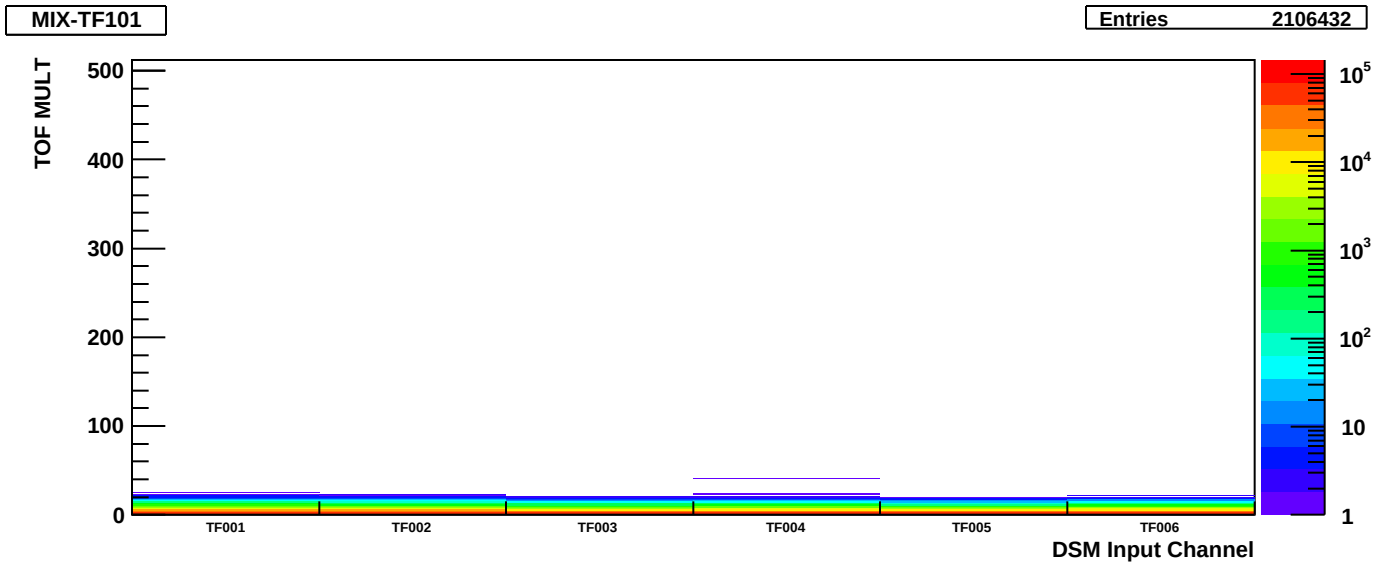

MIX-TF002
Entries 1.474502e+07
TOF MULT
TOF tray

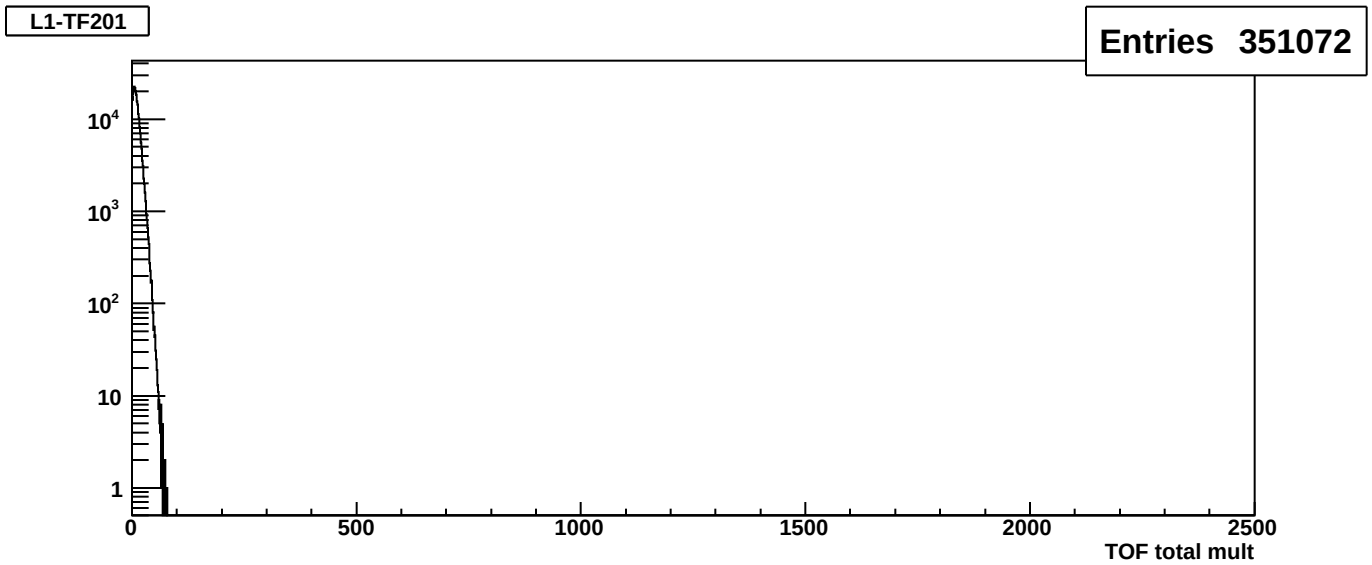

MIX-TF003
Entries 1.474502e+07
TOF MULT
TOF tray

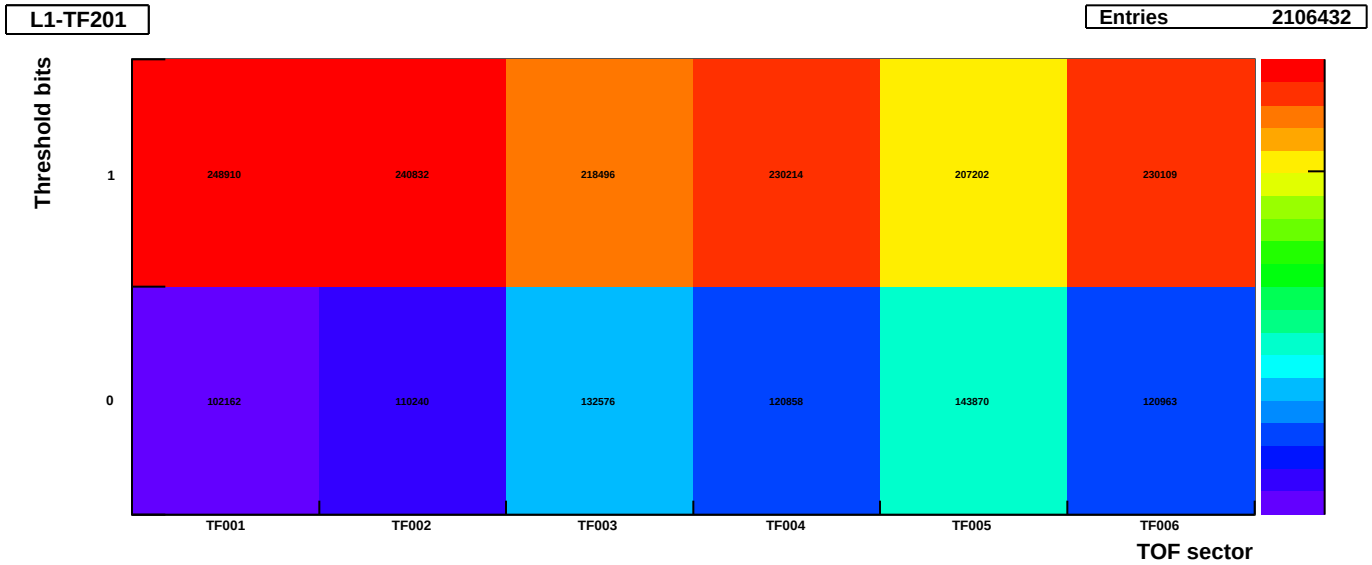

MIX-TF004
Entries 1.474502e+07
TOF MULT
TOF tray

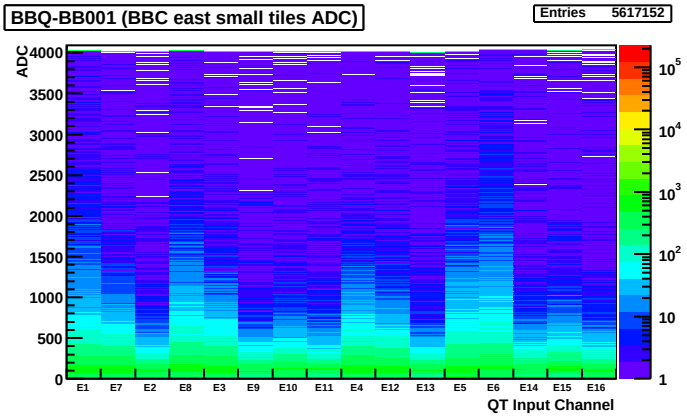

MIX-TF005
Entries 1.474502e+07
TOF MULT
TOF tray

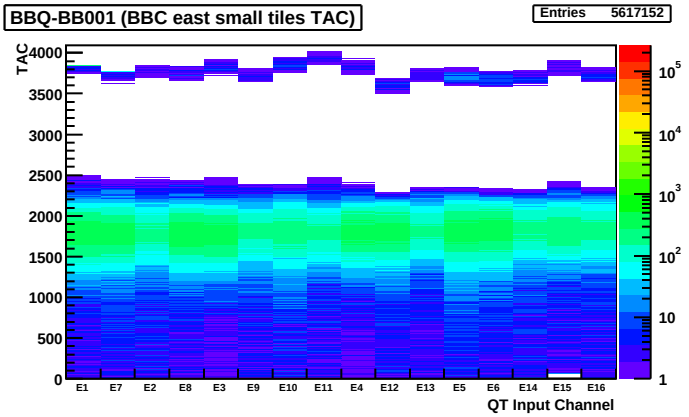

MIX-TF006
Entries 1.474502e+07
TOF MULT
TOF tray

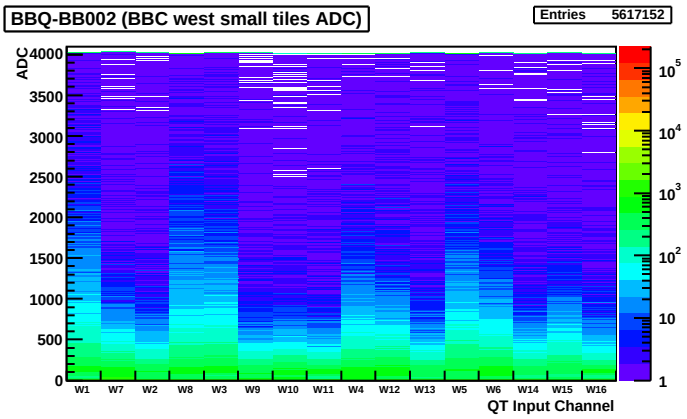

MIX-TF101
Entries 2106432
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

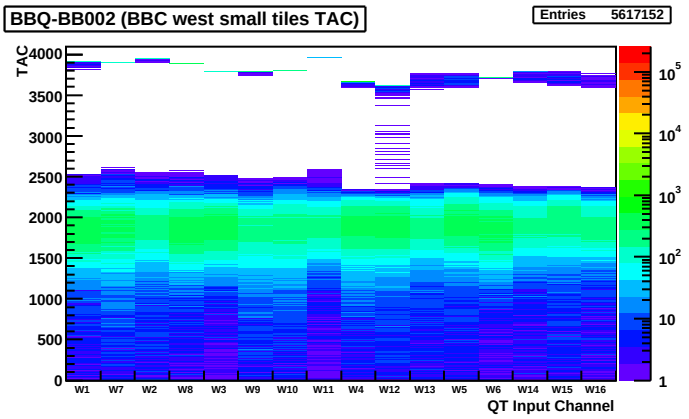

L1-TF201
Entries 351072
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

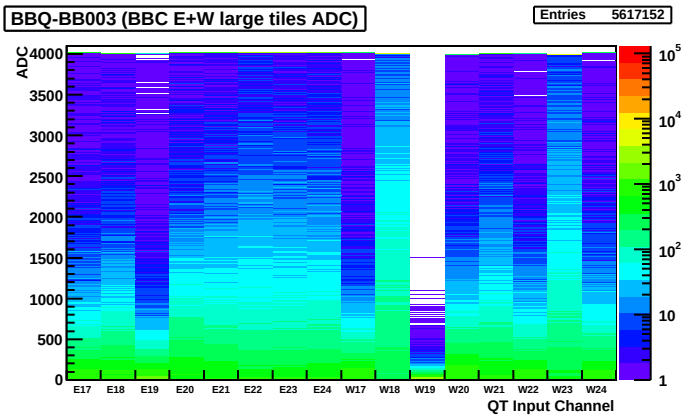

L1-TF201
Entries 2106432
Threshold bits
1
0
248910
240832
218496
230214
207202
230109
102162
110240
132576
120858
143870
120963
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

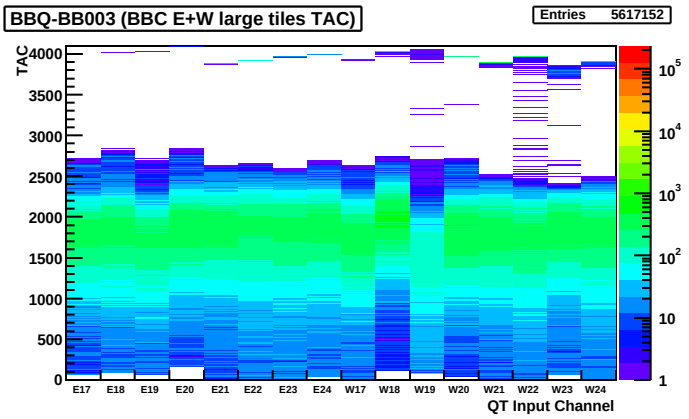

BBQ-BB001 (BBC east small tiles ADC)
Entries 5617152
ADC
QT Input Channel

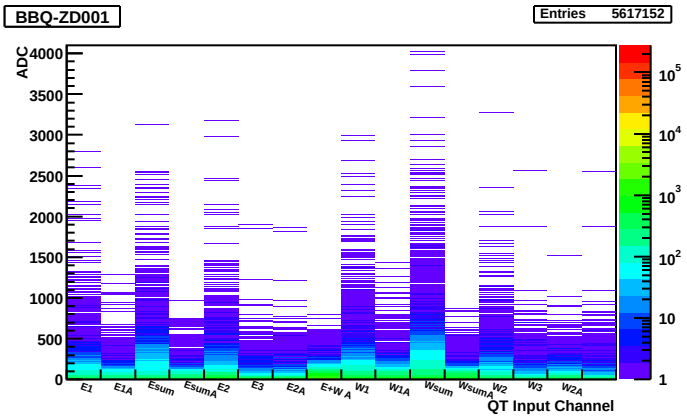

BBQ-BB001 (BBC east small tiles TAC)
Entries 5617152
TAC
QT Input Channel

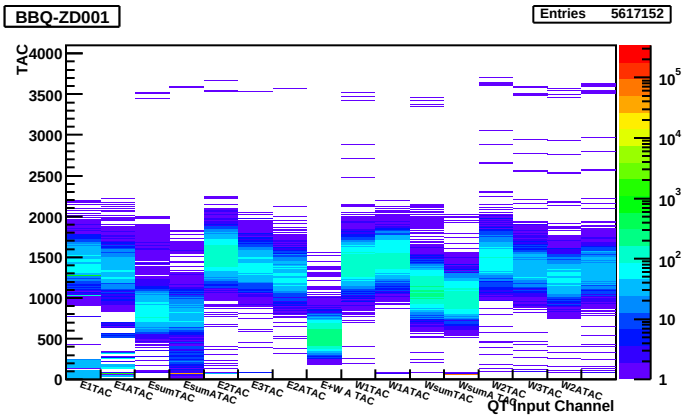

BBQ-BB002 (BBC west small tiles ADC)
Entries 5617152
ADC
QT Input Channel

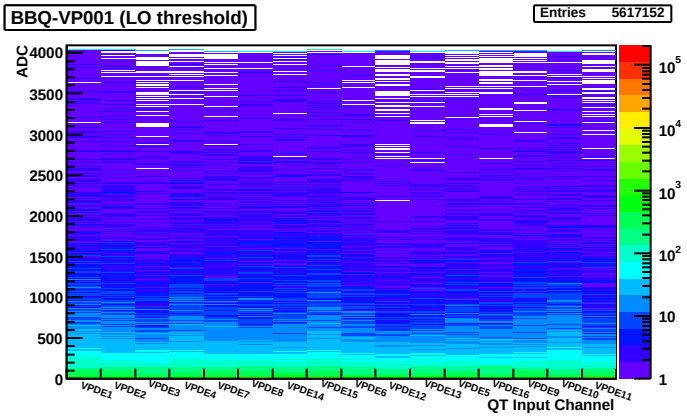

BBQ-BB002 (BBC west small tiles TAC)
Entries 5617152
TAC
QT Input Channel

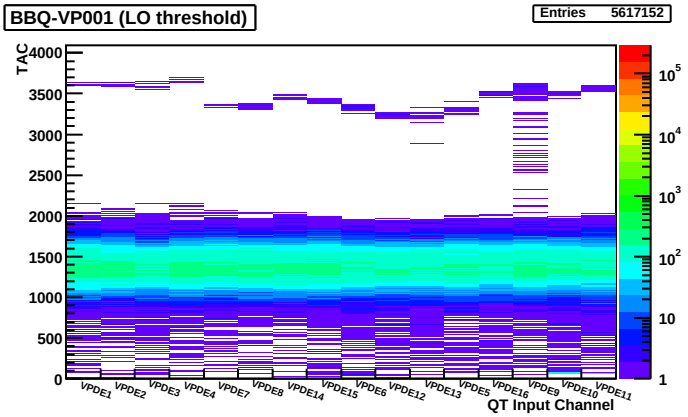

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5617152
ADC
QT Input Channel

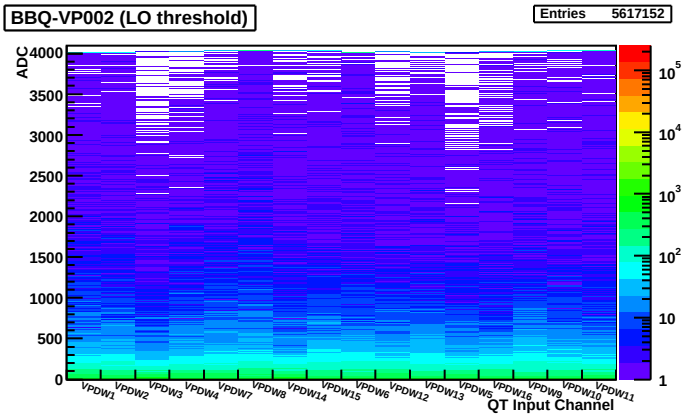

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5617152
TAC
QT Input Channel

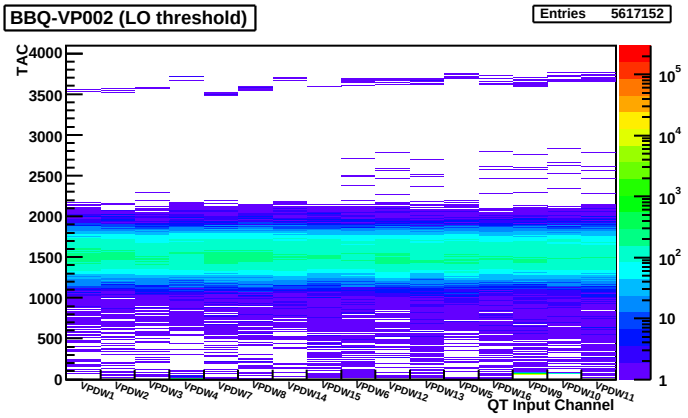

BBQ-ZD001
Entries 5617152
ADC
QT Input Channel

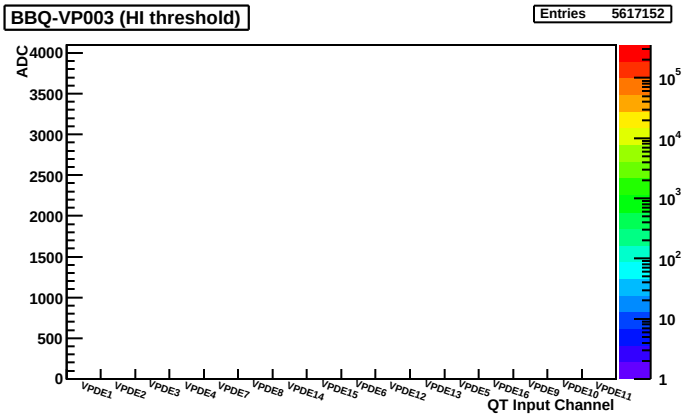

BBQ-ZD001
Entries 5617152
TAC
QT Input Channel

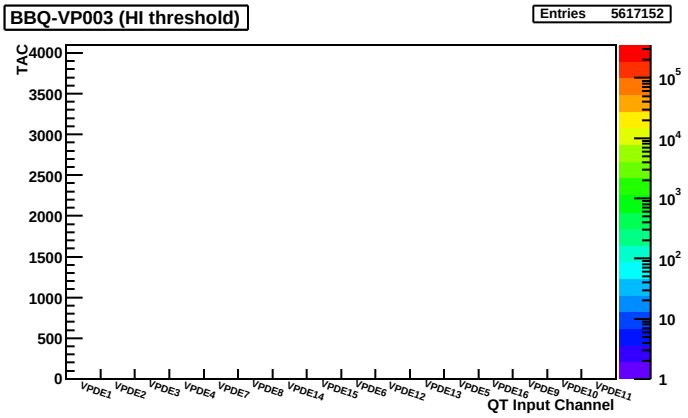

BBQ-VP001 (LO threshold)
Entries 5617152
ADC
QT Input Channel

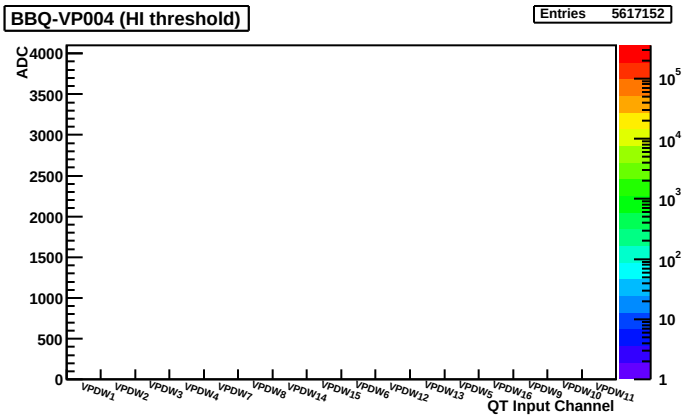

BBQ-VP001 (LO threshold)
Entries 5617152
TAC
QT Input Channel

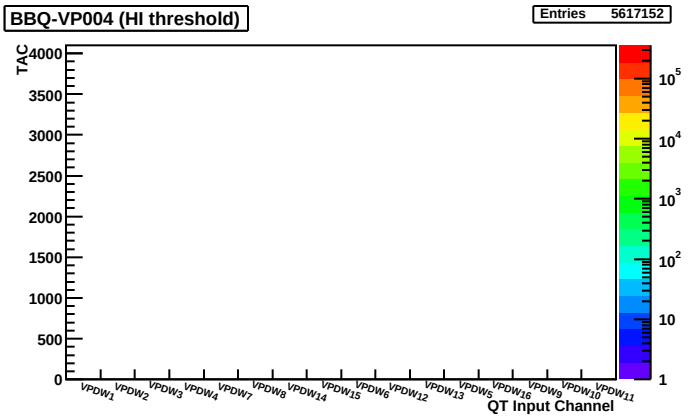

BBQ-VP002 (LO threshold)
Entries 5617152
ADC
QT Input Channel

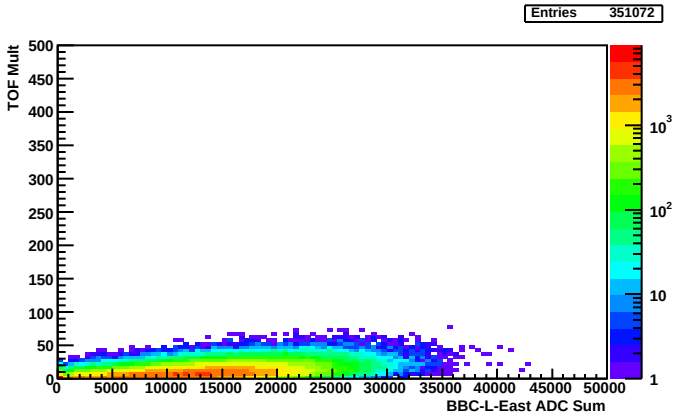

BBQ-VP002 (LO threshold)
Entries 5617152
TAC
QT Input Channel

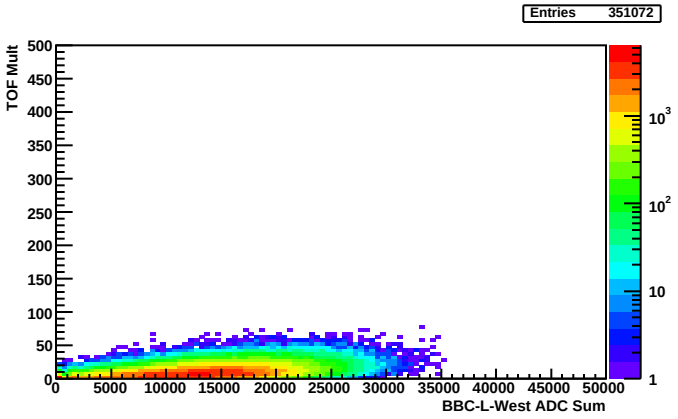

BBQ-VP003 (HI threshold)
Entries 5617152
ADC
QT Input Channel

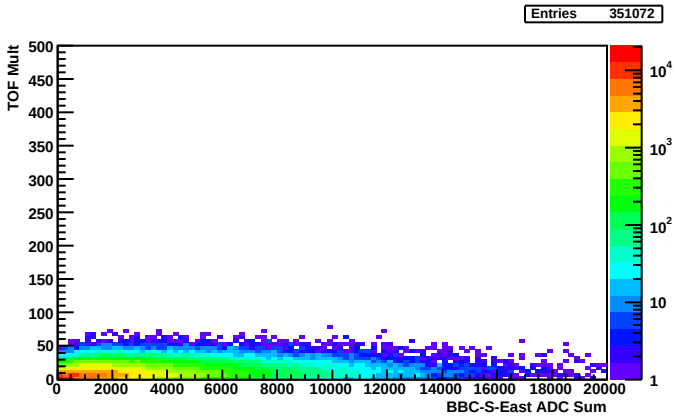

BBQ-VP003 (HI threshold)
Entries 5617152
TAC
QT Input Channel

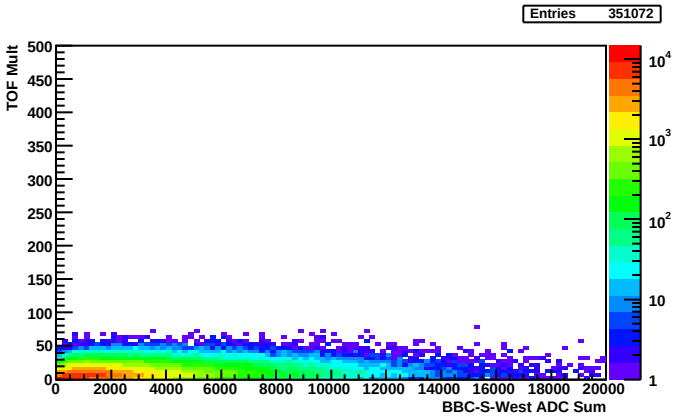

BBQ-VP004 (HI threshold)
Entries 5617152
ADC
QT Input Channel

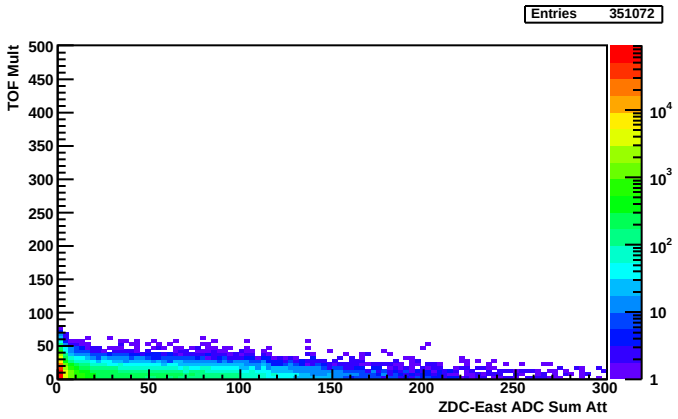

BBQ-VP004 (HI threshold)
Entries 5617152
TAC
QT Input Channel

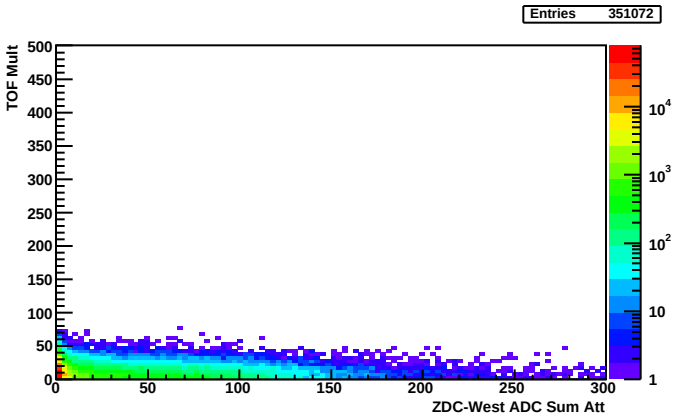

Entries 351072
TOF Mult
BBC-L-East ADC Sum

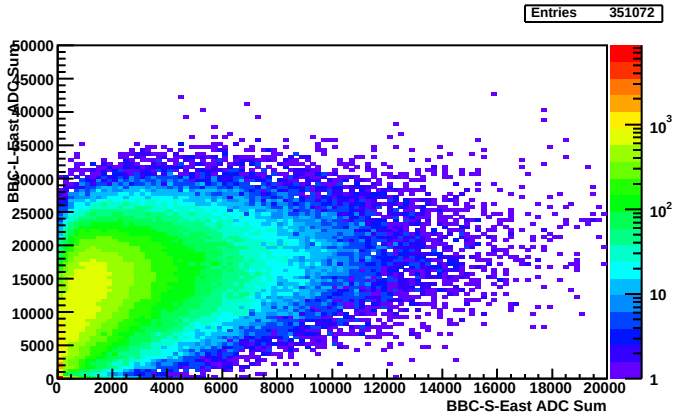

Entries 351072
TOF Mult
BBC-L-West ADC Sum

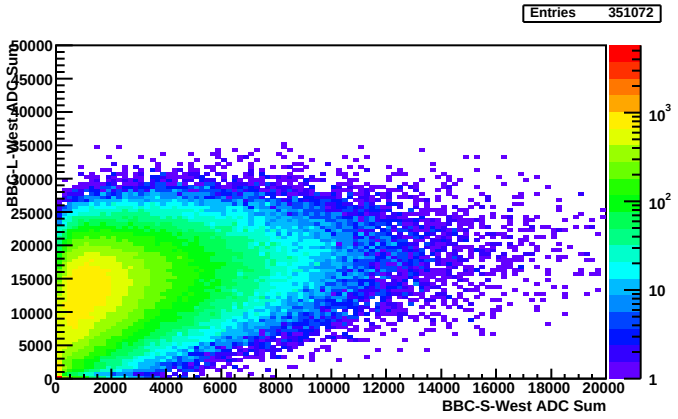

Entries 351072
TOF Mult
BBC-S-East ADC Sum

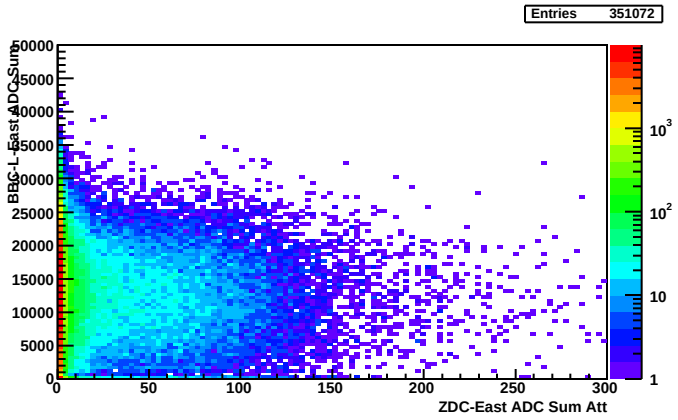

Entries 351072
TOF Mult
BBC-S-West ADC Sum

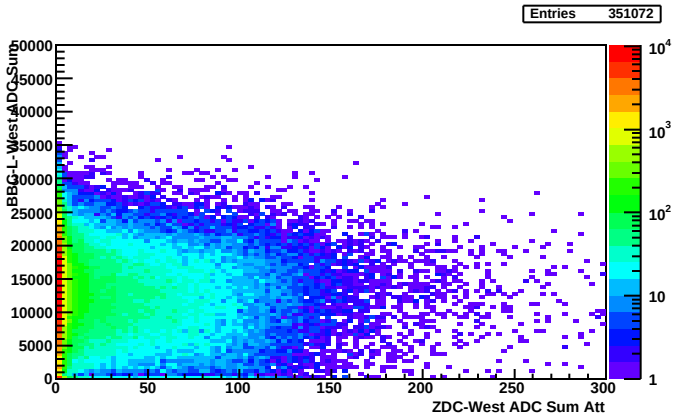

Entries 351072
TOF Mult
ZDC-East ADC Sum Att

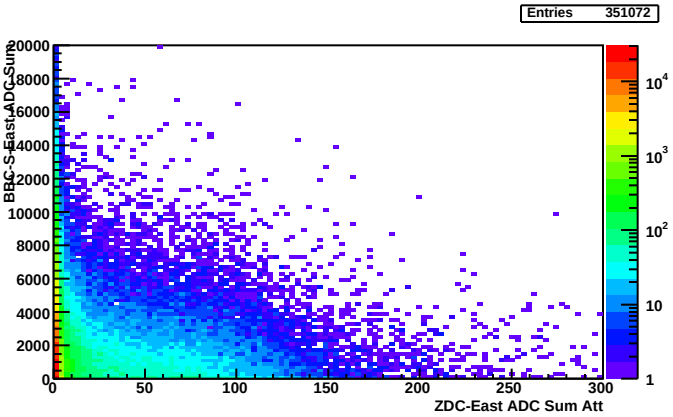

Entries 351072
TOF Mult
ZDC-West ADC Sum Att

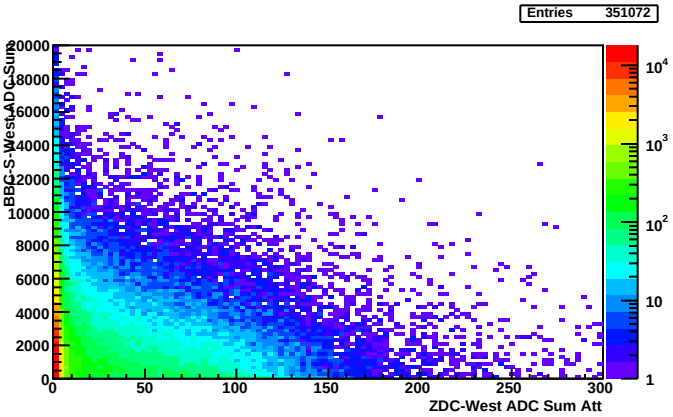

Entries 351072
BBC-L-East ADC Sum
BBC-S-East ADC Sum

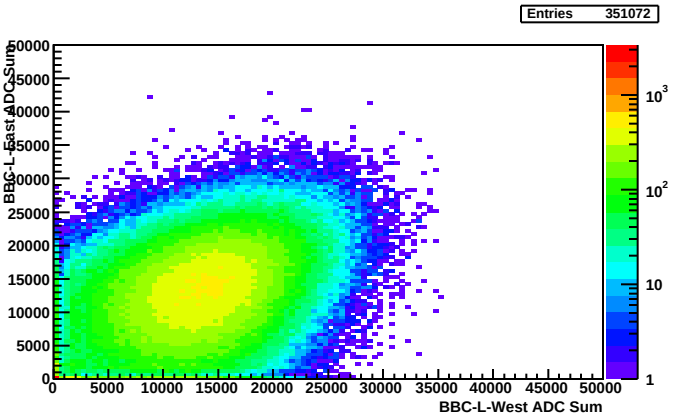

Entries 351072
BBC-L-West ADC Sum
BBC-S-West ADC Sum

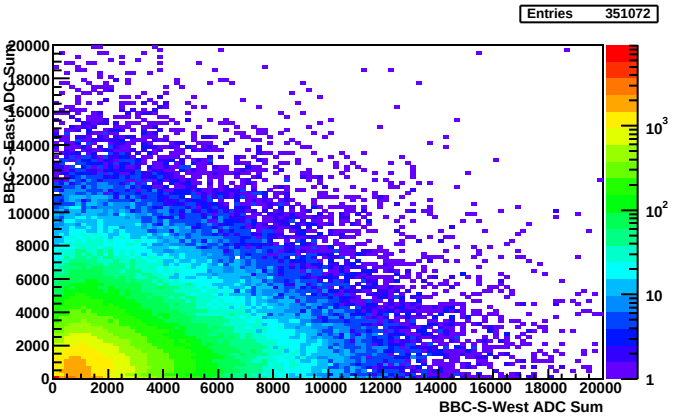

Entries 351072
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

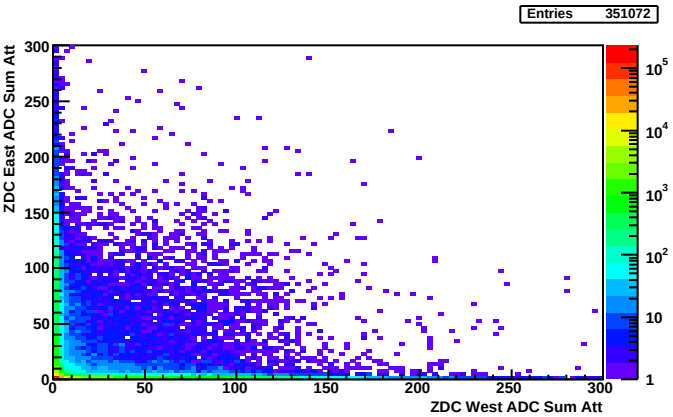

Entries 351072
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

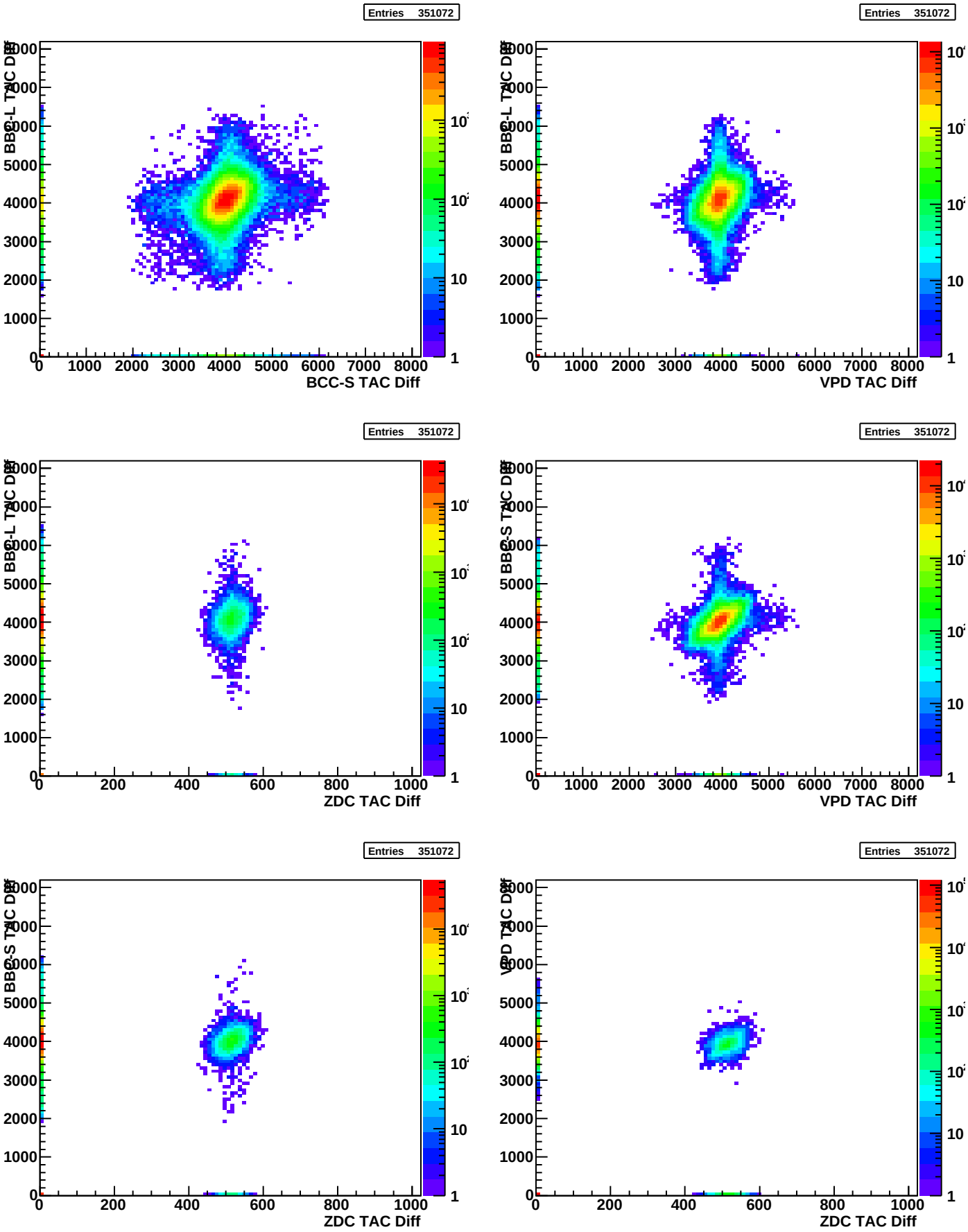

Entries 351072
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

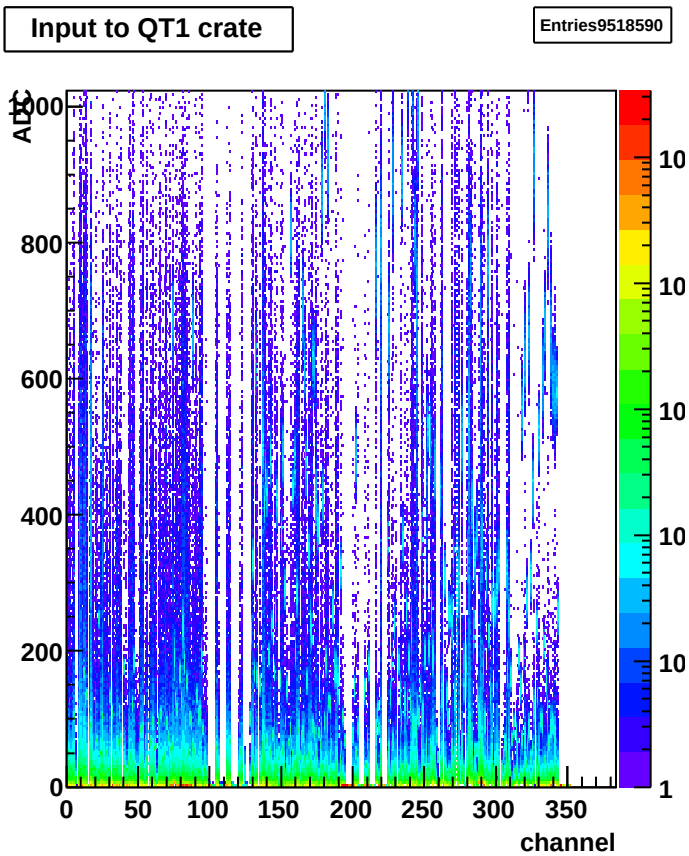

Entries 351072
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

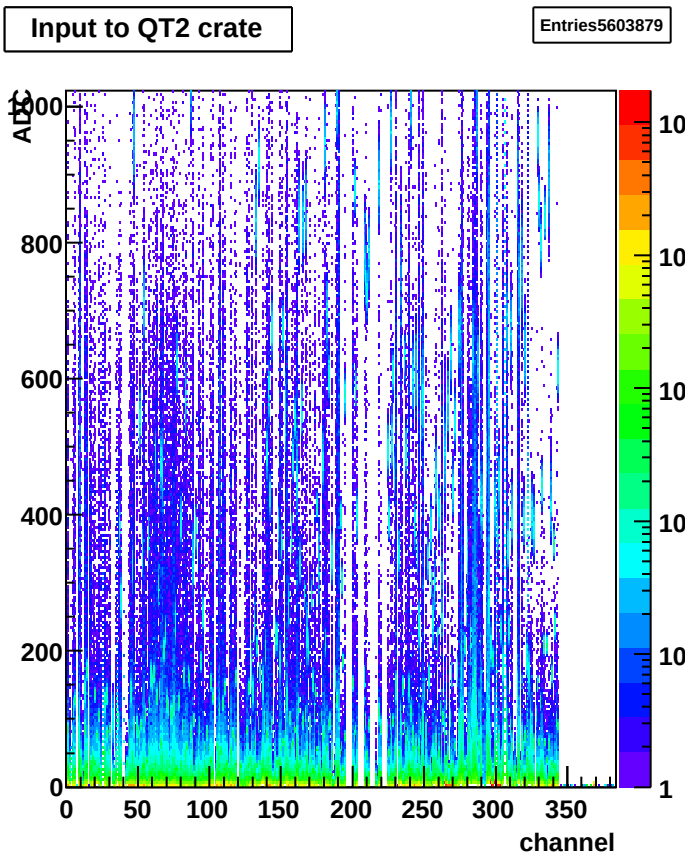

Entries 351072
BBC-L-East ADC Sum
BBC-L-West ADC Sum

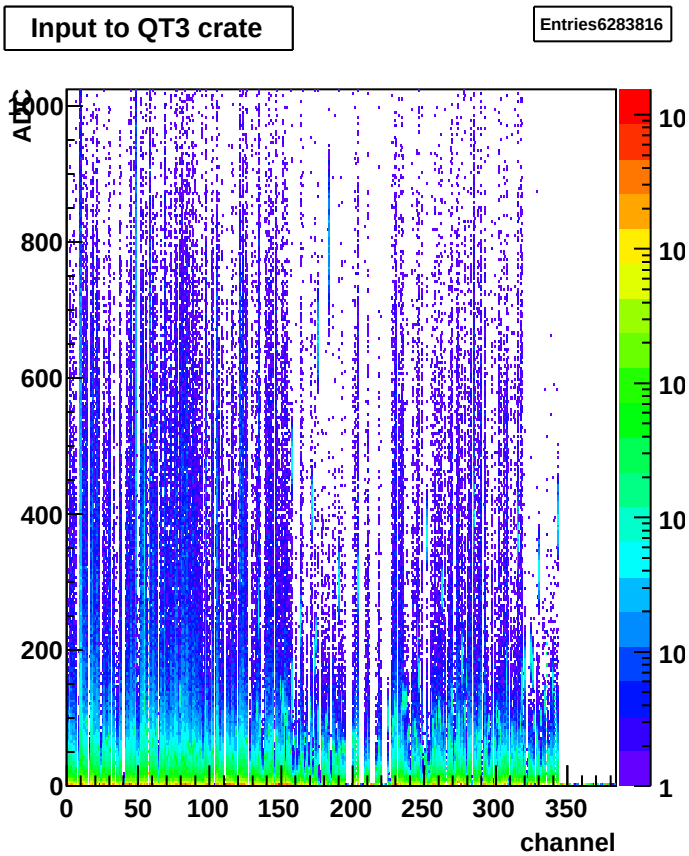

Entries 351072
BBC-S-East ADC Sum
BBC-S-West ADC Sum

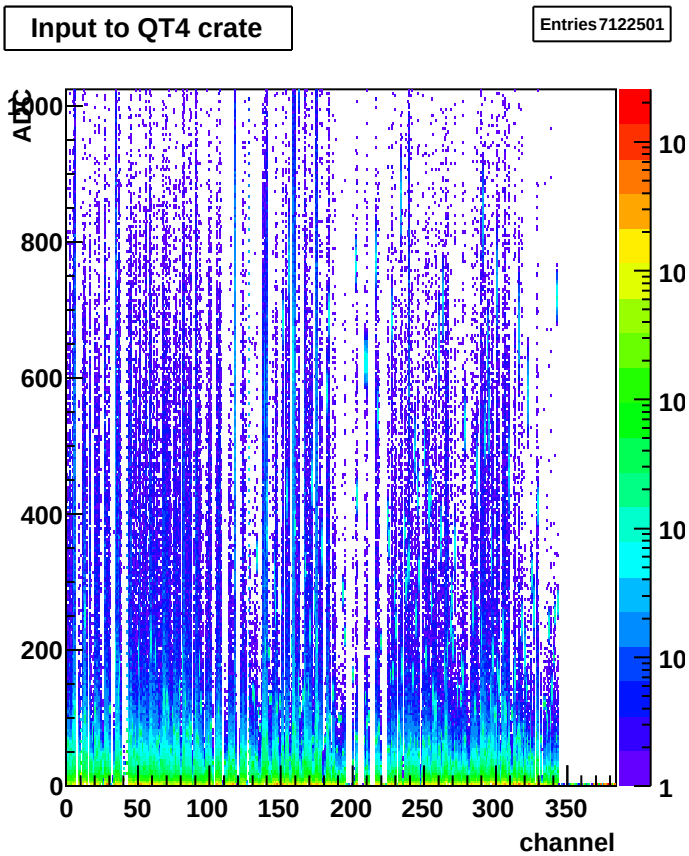

Entries 351072
ZDC East ADC Sum Att
ZDC West ADC Sum Att

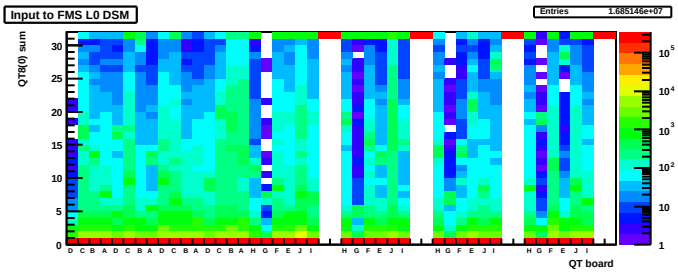

Entries 351072
BBC-L TAC Diff
BCC-S TAC Diff
Entries 351072
BBC-L TAC Diff
VPD TAC Diff
Entries 351072
BBC-L TAC Diff
ZDC TAC Diff
Entries 351072
BBC-S TAC Diff
VPD TAC Diff
Entries 351072
BBC-S TAC Diff
ZDC TAC Diff
Entries 351072
VPD TAC Diff
ZDC TAC Diff

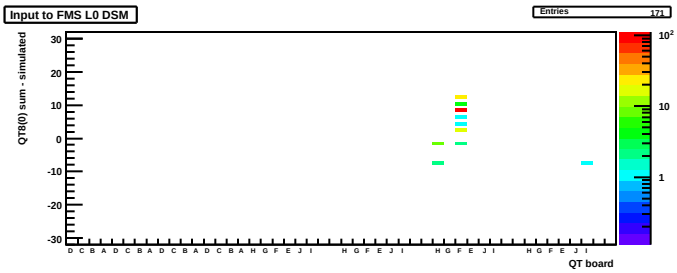

Input to QT1 crate
Entries9518590
ADC
channel

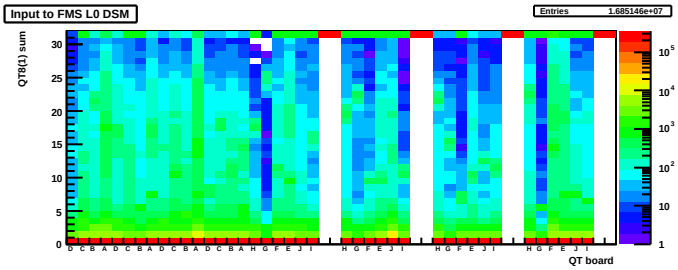

Input to QT2 crate
Entries5603879
ADC
channel

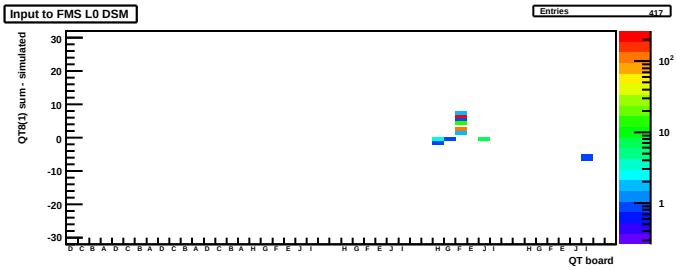

Input to QT3 crate
Entries6283816
ADC
channel

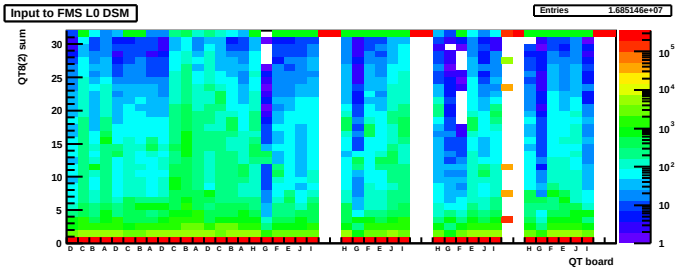

Input to QT4 crate
Entries7122501
ADC
channel

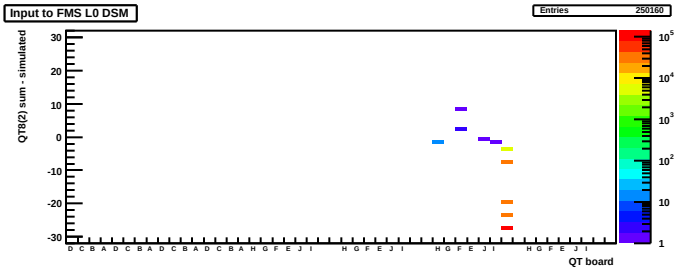

Input to FMS L0 DSM
Entries 1.685146e+07
QT8(0) sum
QT board

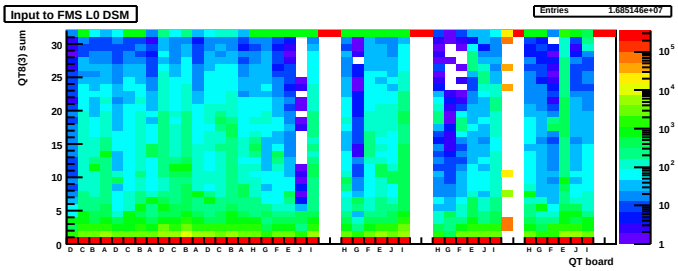

Input to FMS L0 DSM
Entries 171
QT8(0) sum - simulated
QT board

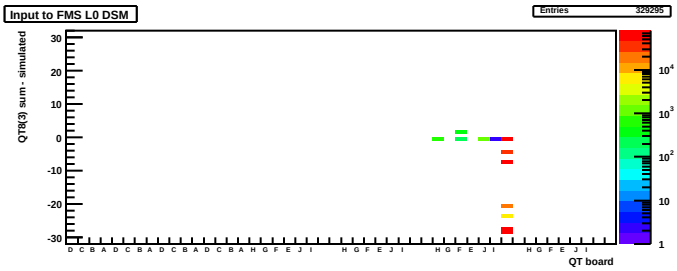

Input to FMS L0 DSM
Entries 1.685146e+07
QT8(1) sum
QT board

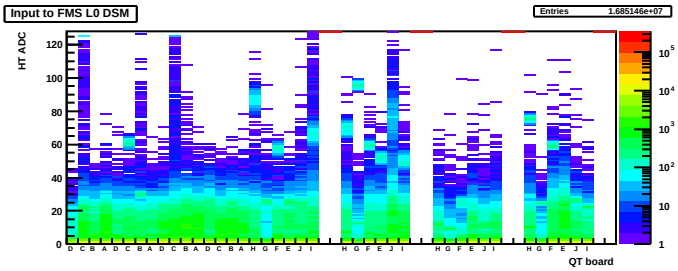

Input to FMS L0 DSM
Entries 417
QT8(1) sum - simulated
QT board

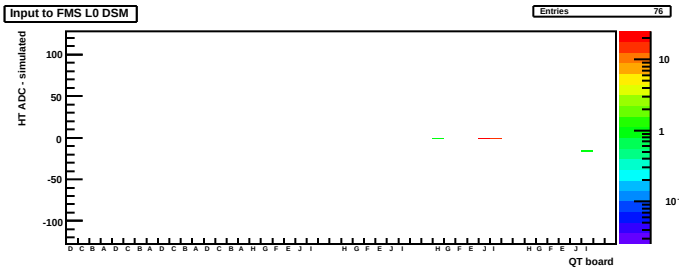

Input to FMS L0 DSM
Entries 1.685146e+07
QT8(2) sum
QT board

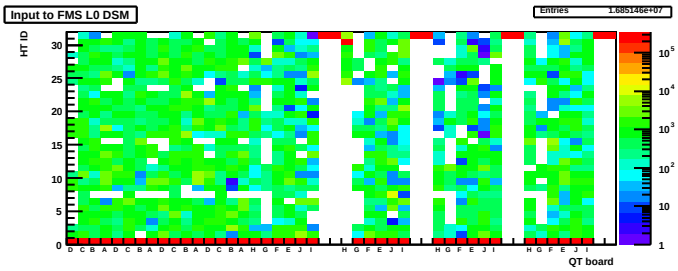

Input to FMS L0 DSM
Entries 250160
QT8(2) sum - simulated
QT board

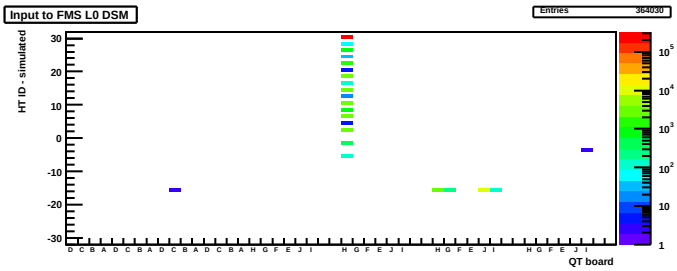

Input to FMS L0 DSM
Entries 1.685146e+07
QT8(3) sum
QT board

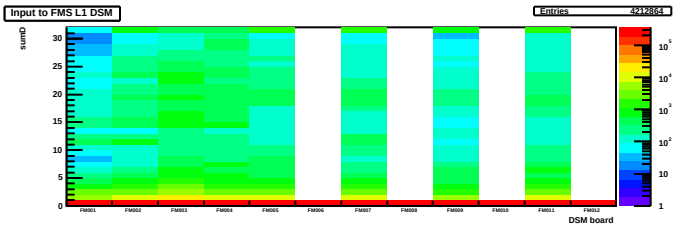

Input to FMS L0 DSM
Entries 329295
QT8(3) sum - simulated
QT board

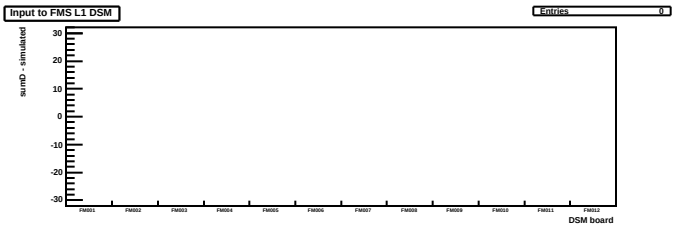

Input to FMS L0 DSM
Entries 1.685146e+07
HT ADC
QT board

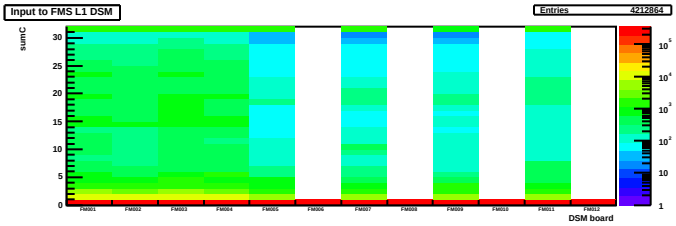

Input to FMS L0 DSM
Entries 76
HT ADC - simulated
QT board

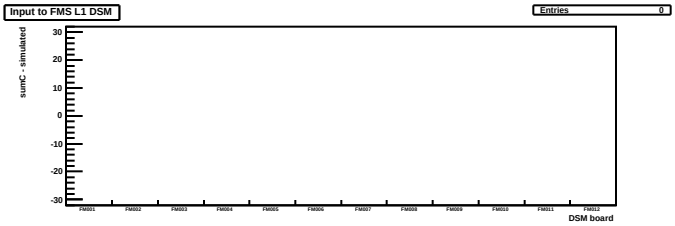

Input to FMS L0 DSM
Entries 1.685146e+07
HT ID
QT board

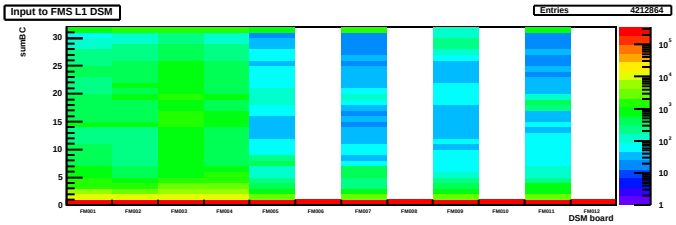

Input to FMS L0 DSM
Entries 364030
HT ID - simulated
QT board

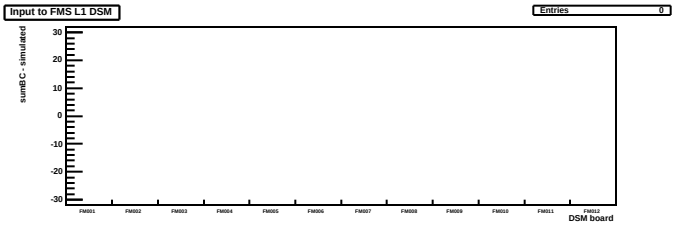

Input to FMS L1 DSM
Entries 4212864
sumD
DSM board

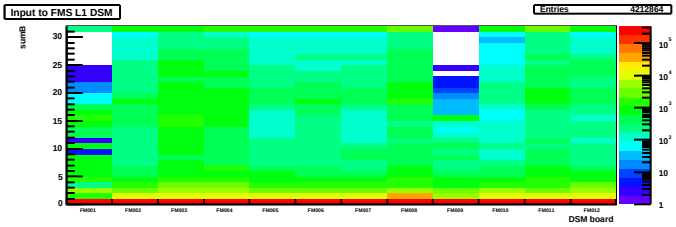

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

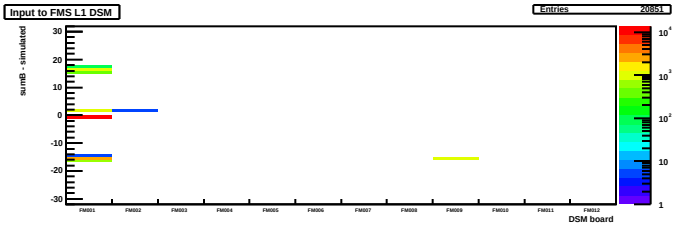

Input to FMS L1 DSM
Entries 4212864
sumC
DSM board

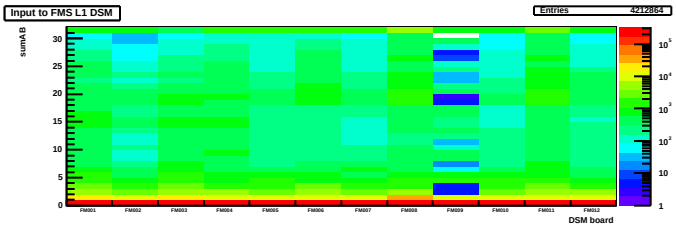

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

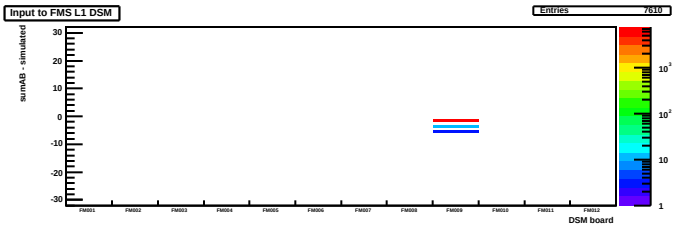

Input to FMS L1 DSM
Entries 4212864
sumBC
DSM board

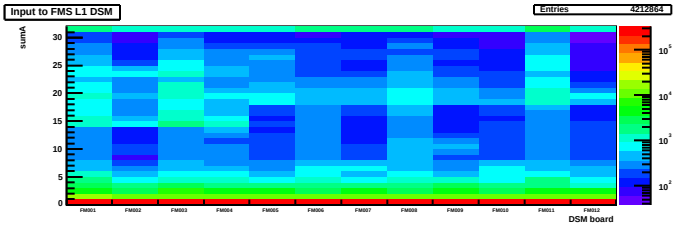

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

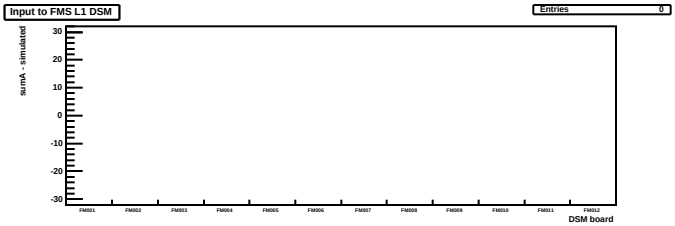

Input to FMS L1 DSM
Entries 4212864
sumB
DSM board

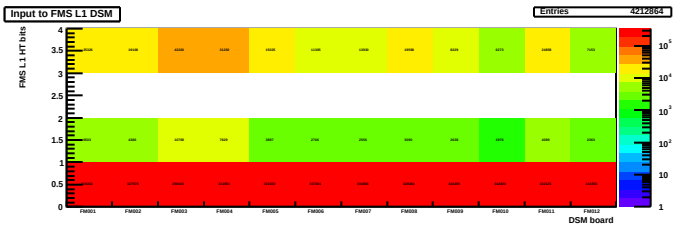

Input to FMS L1 DSM
Entries 20851
sumB - simulated
DSM board

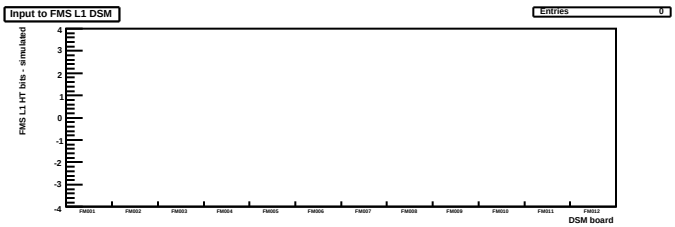

Input to FMS L1 DSM
Entries 4212864
sumAB
DSM board

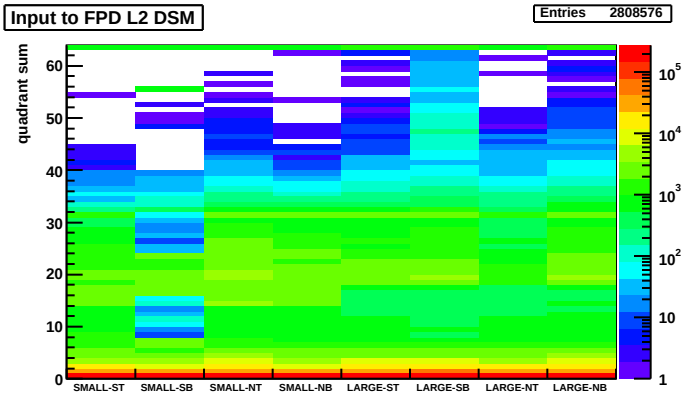

Input to FMS L1 DSM
Entries 7610
sumAB - simulated
DSM board

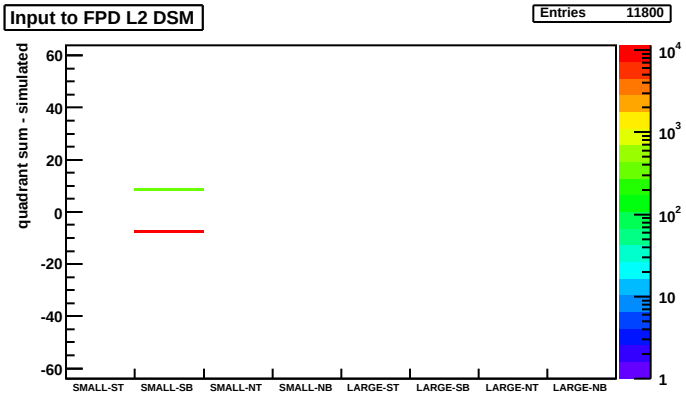

Input to FMS L1 DSM
Entries 4212864
sumA
DSM board

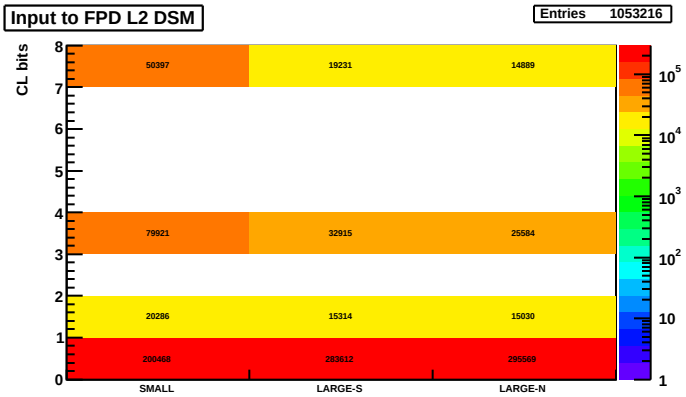

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

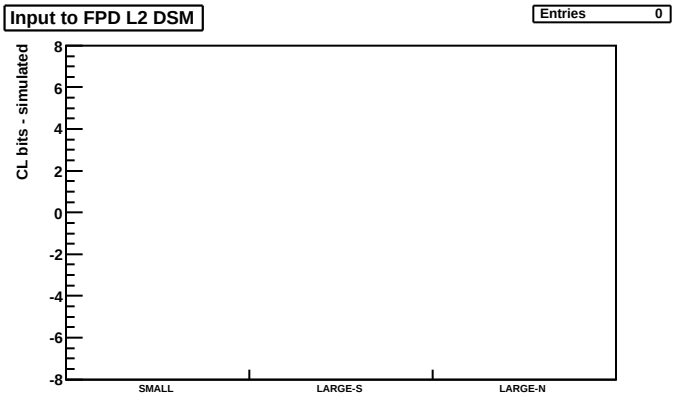

Input to FMS L1 DSM
Entries 4212864
FMS L1 HT bits
DSM board

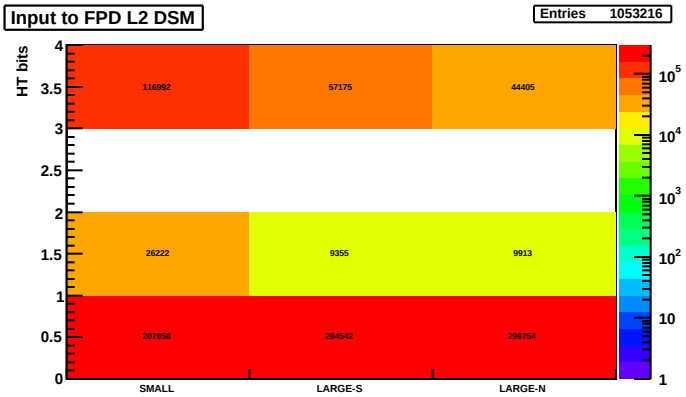

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

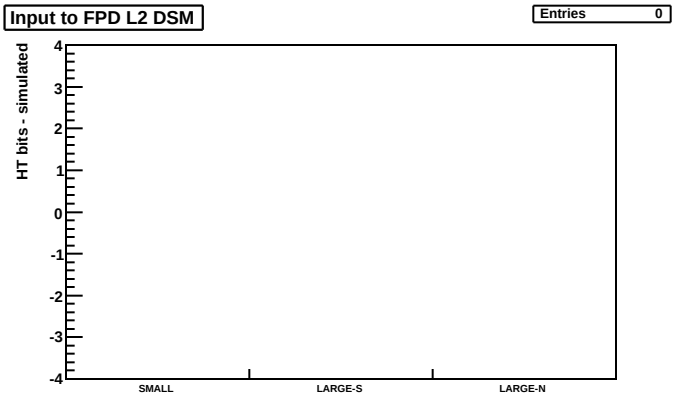

Input to FPD L2 DSM
Entries 2808576
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

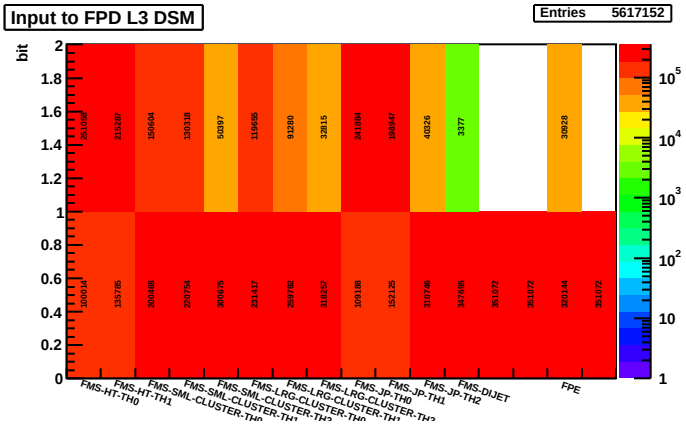

Input to FPD L2 DSM
Entries 11800
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

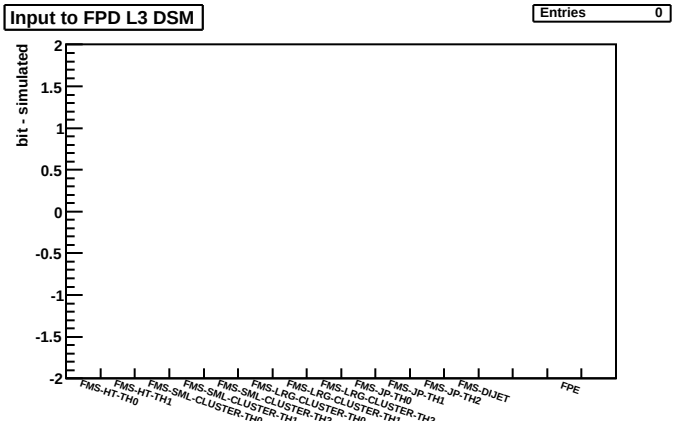

Input to FPD L2 DSM
Entries 1053216
CL bits
50397
19231
14889
79921
32915
25584
20286
15314
15030
200468
283612
295569
SMALL
LARGE-S
LARGE-N

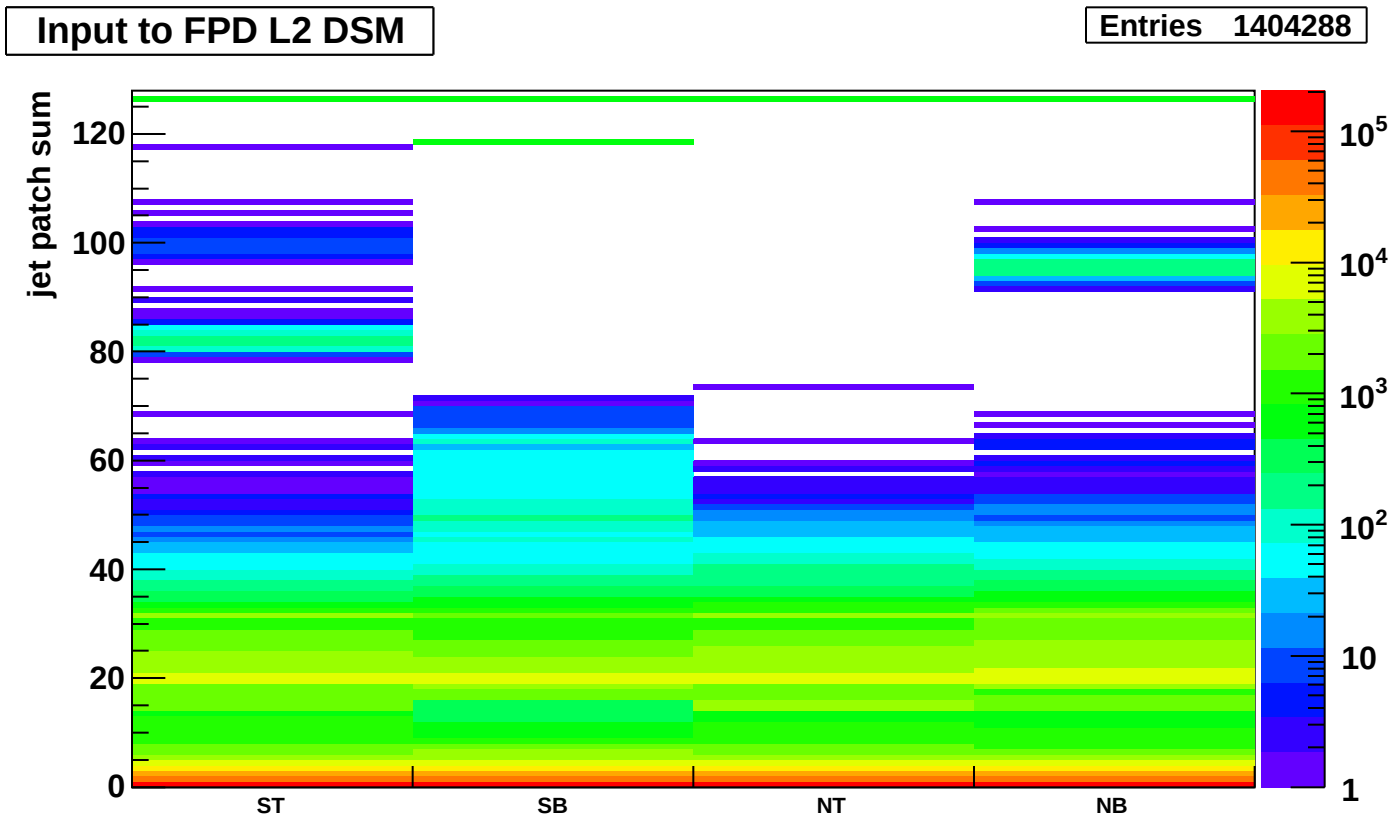

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

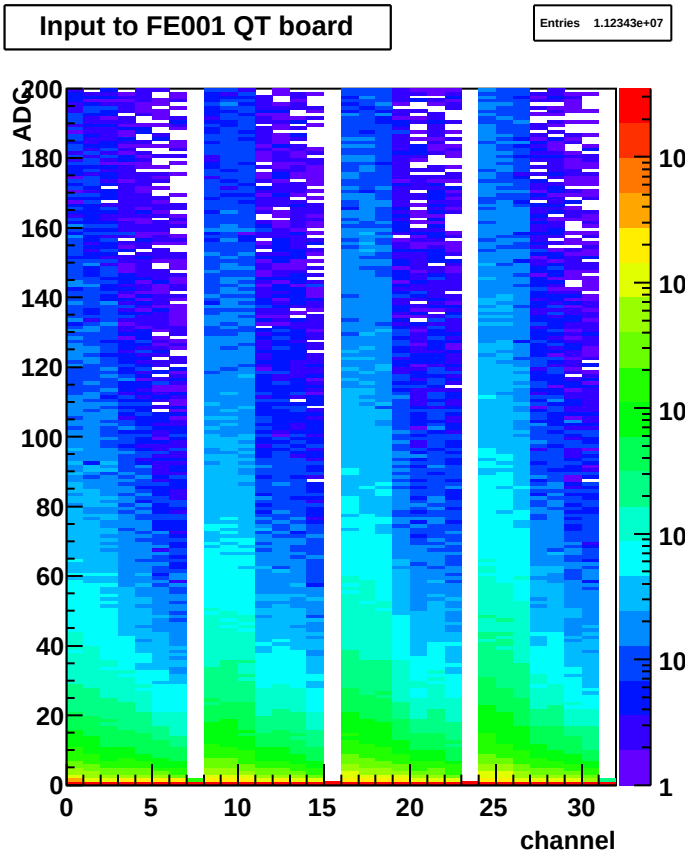

Input to FPD L2 DSM
Entries 1053216
HT bits
116992
57175
44405
26222
9355
9913
207858
284542
296754
SMALL
LARGE-S
LARGE-N

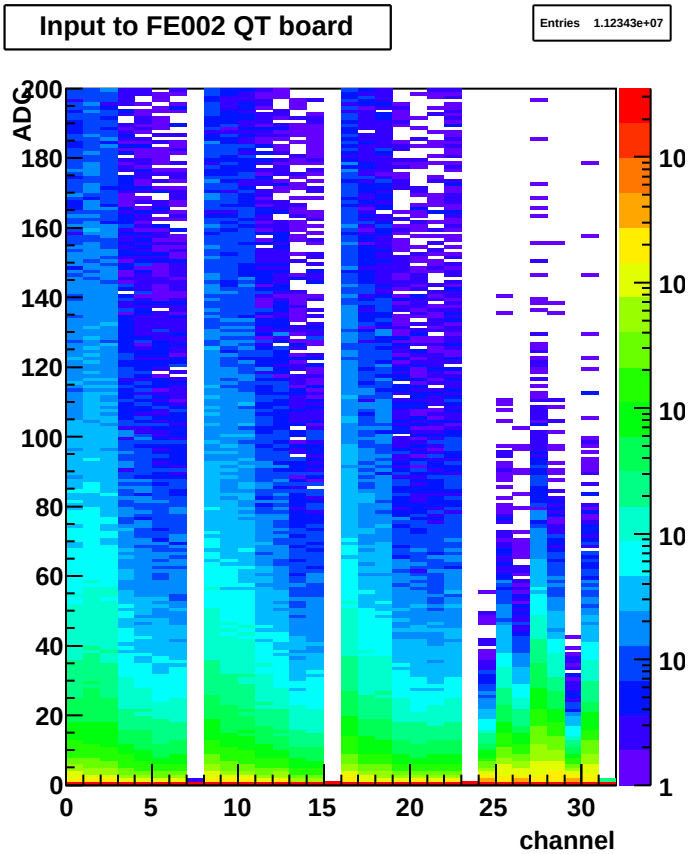

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

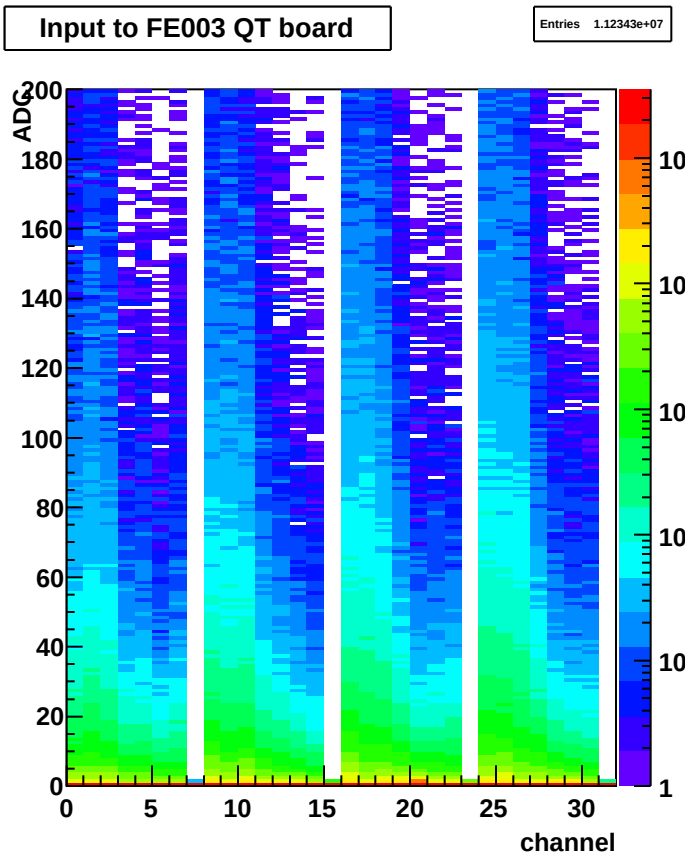

Input to FPD L3 DSM
Entries 5617152
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

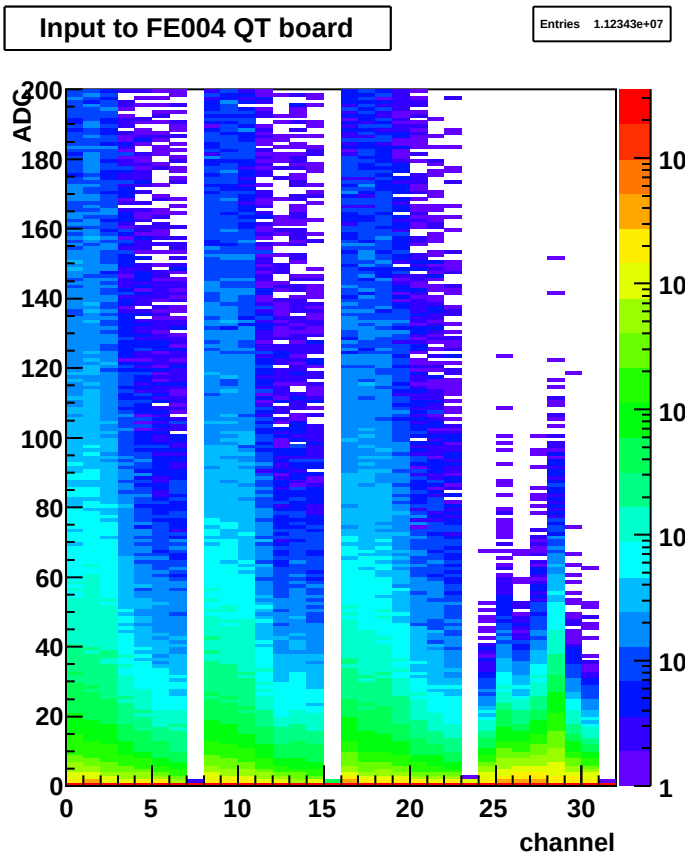

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

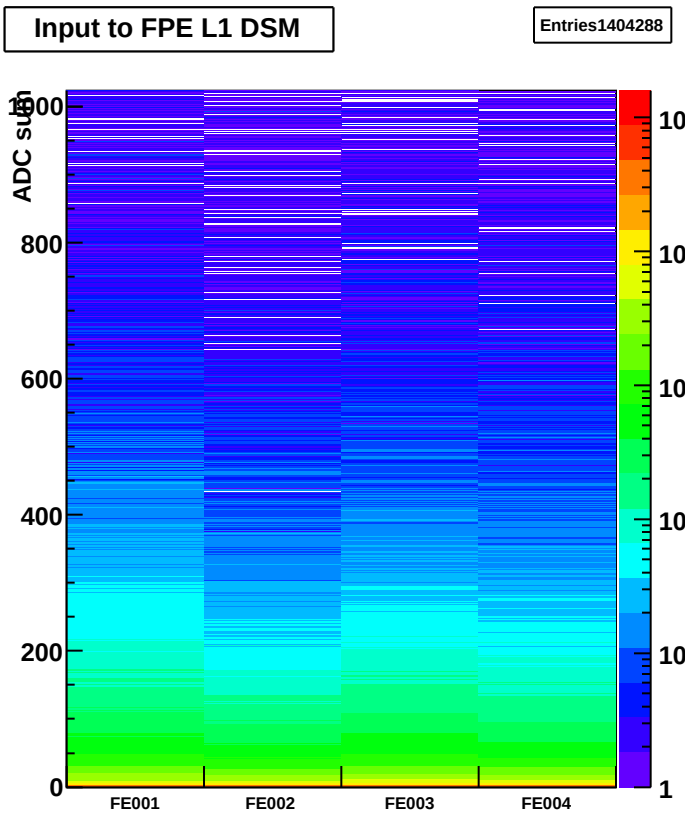

Input to FPD L2 DSM
Entries 1404288
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

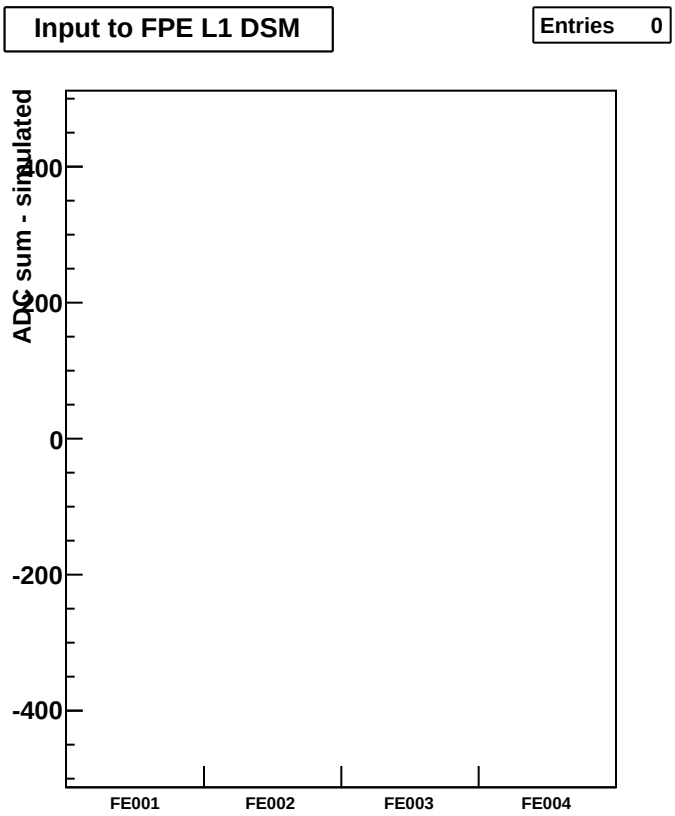

Input to FE001 QT board
Entries 1.12343e+07
ADC
channel

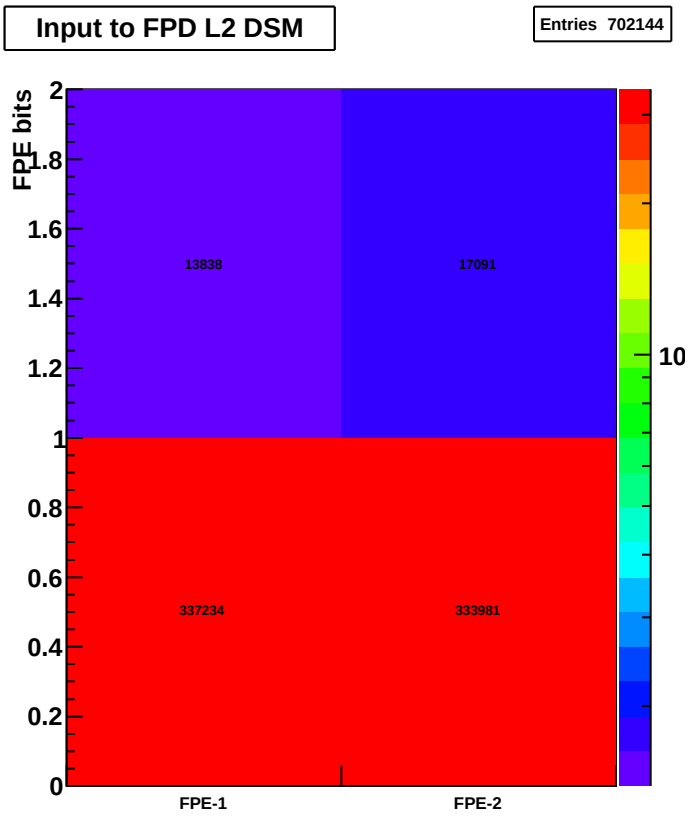

Input to FE002 QT board
Entries 1.12343e+07
ADC
channel

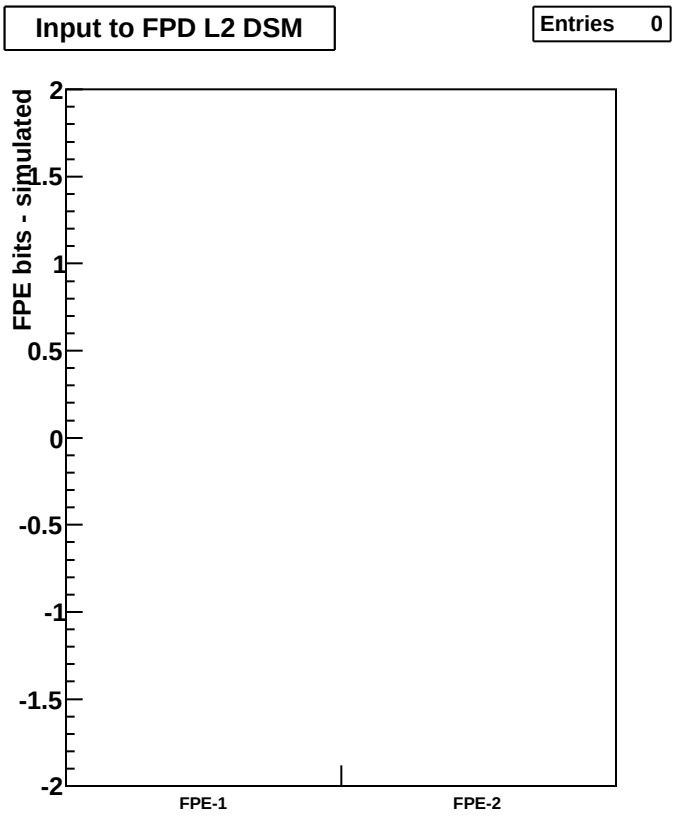

Input to FE003 QT board
Entries 1.12343e+07
ADC
channel


Input to FE004 QT board
Entries 1.12343e+07
ADC
channel


Input to FPE L1 DSM
Entries1404288
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 702144
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
13838
17091
337234
333981
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2