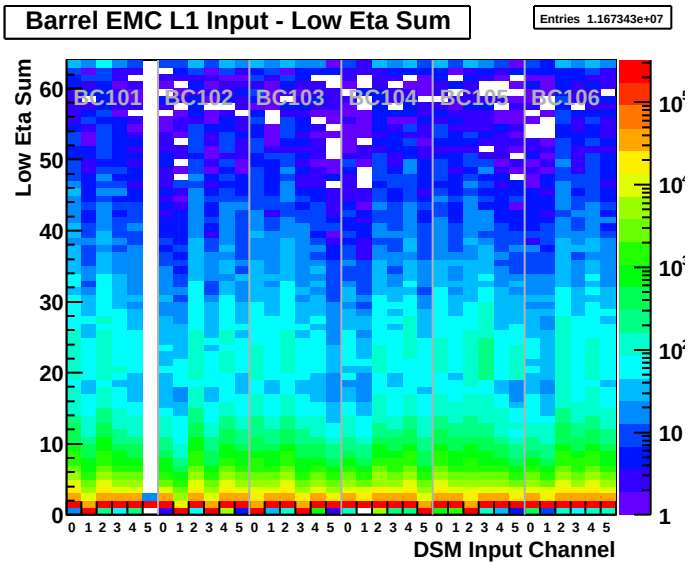
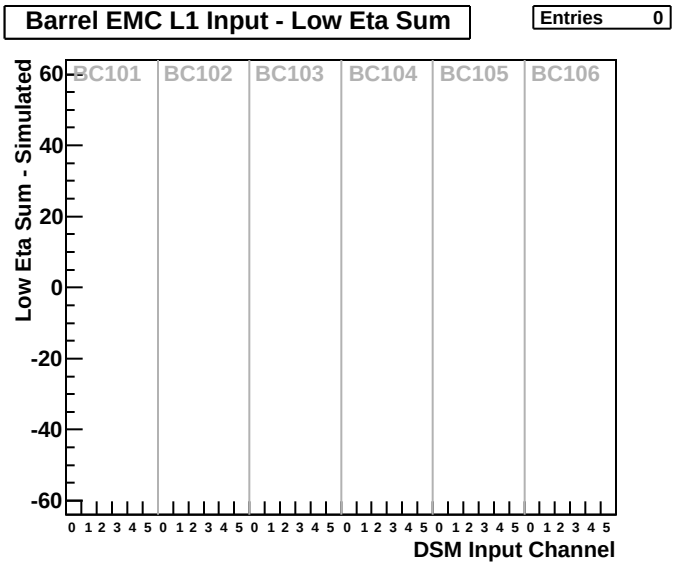
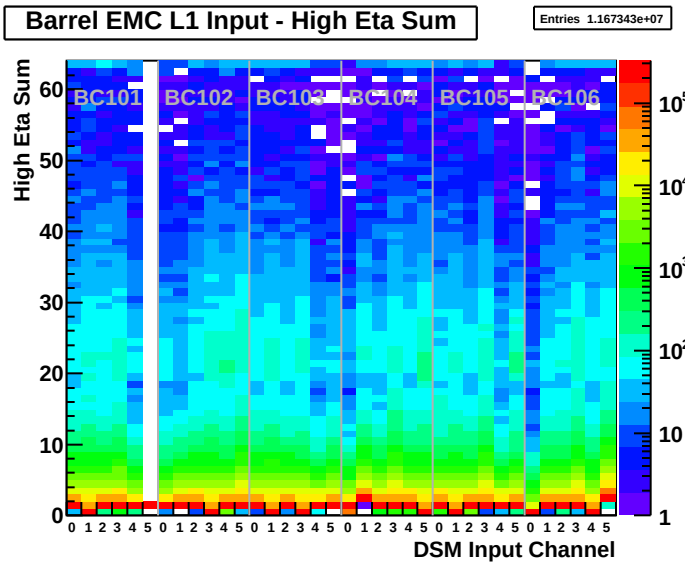
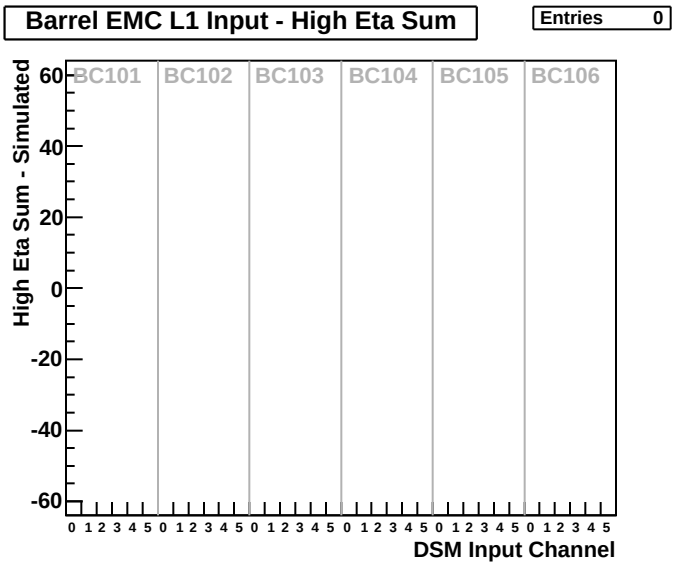
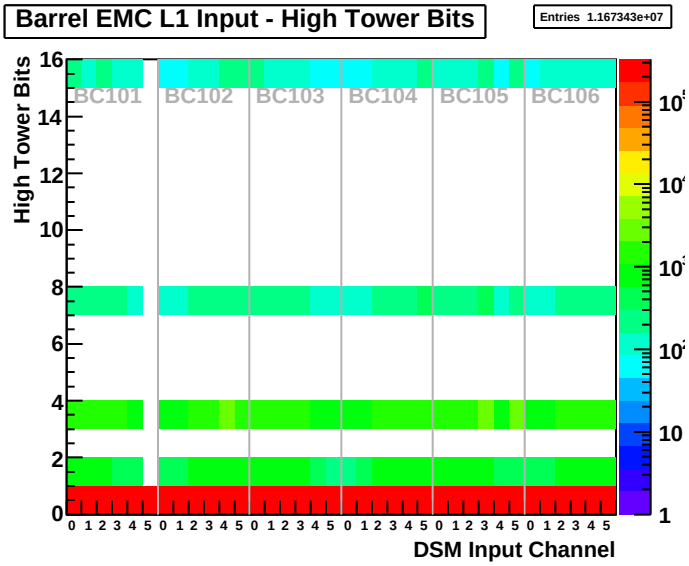
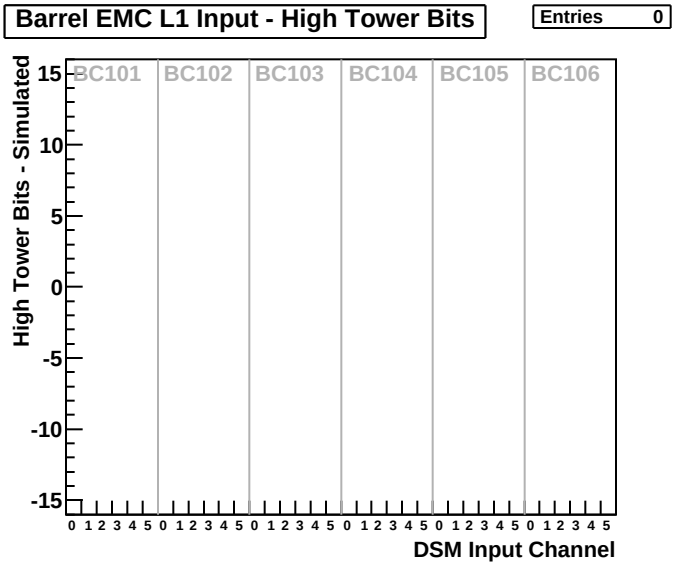

Barrel EMC L1 Input - Low Eta Sum
Entries 1.167343e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

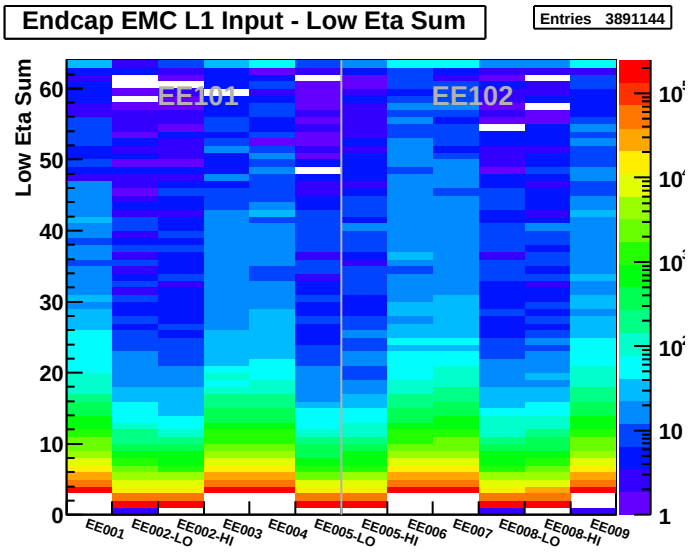

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

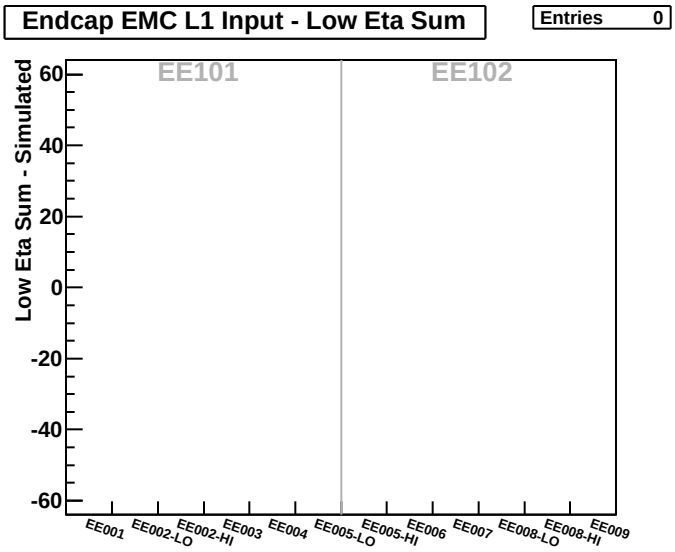

Barrel EMC L1 Input - High Eta Sum
Entries 1.167343e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

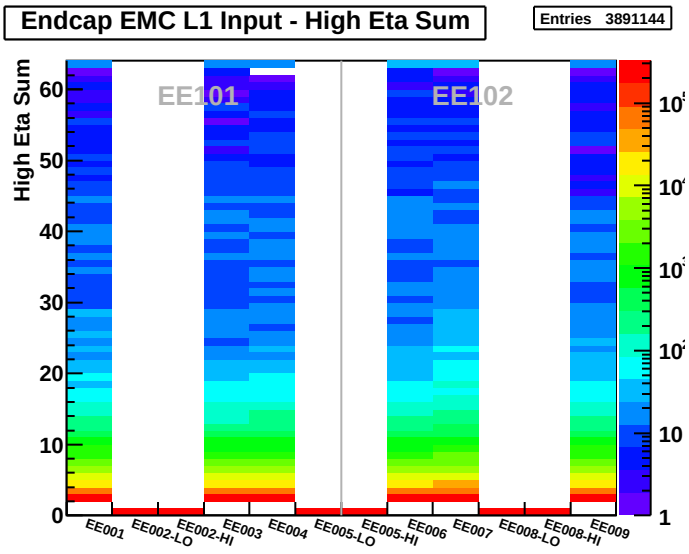

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

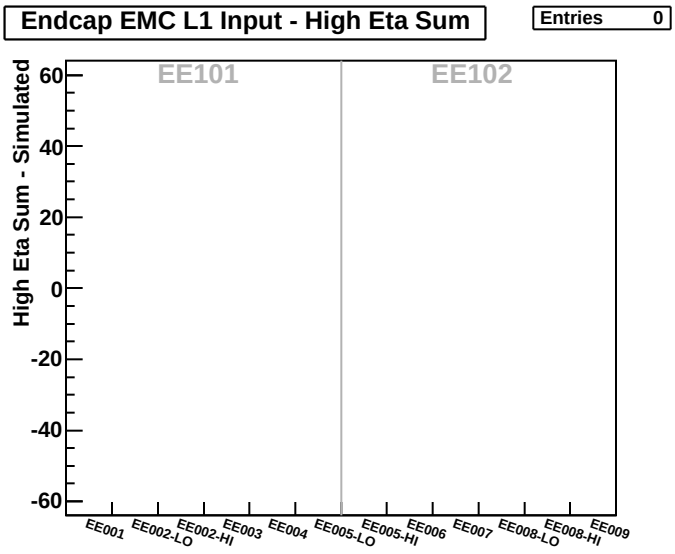

Barrel EMC L1 Input - High Tower Bits
Entries 1.167343e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

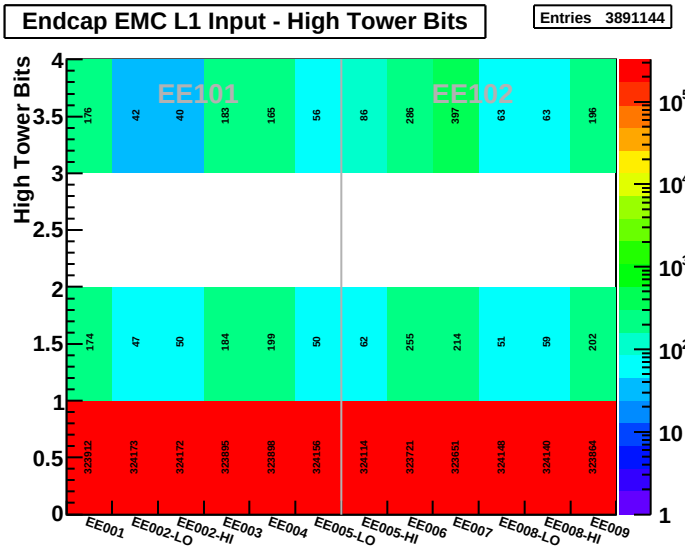

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

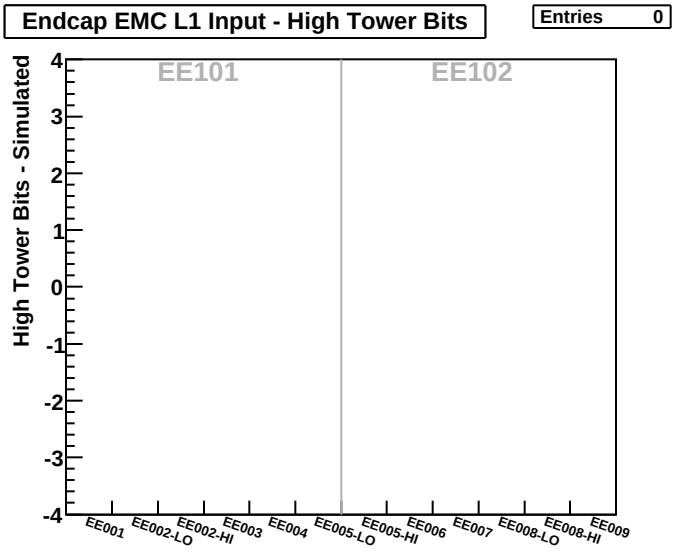

Endcap EMC L1 Input - Low Eta Sum
Entries 3891144
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

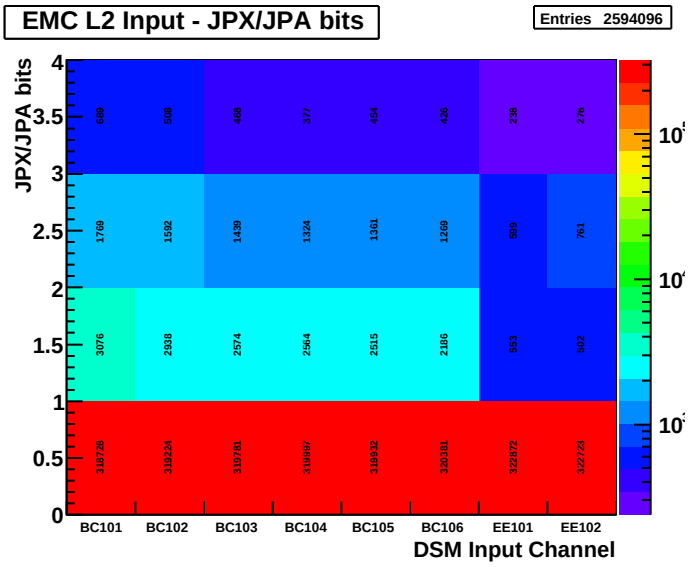

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

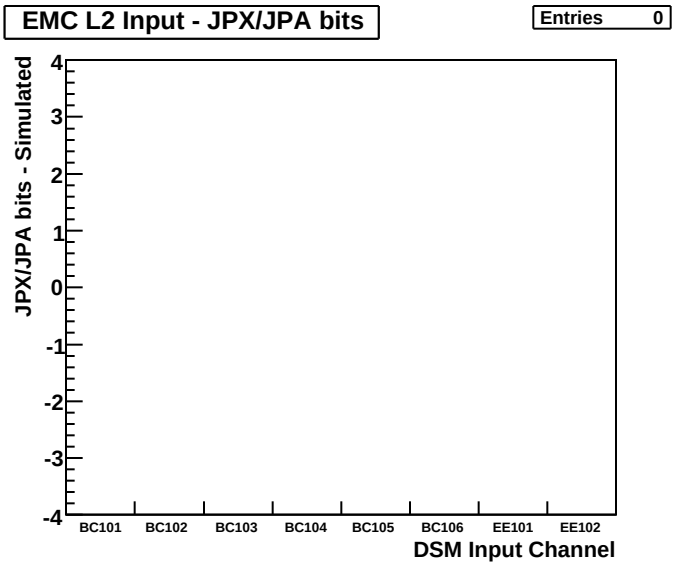

Endcap EMC L1 Input - High Eta Sum
Entries 3891144
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

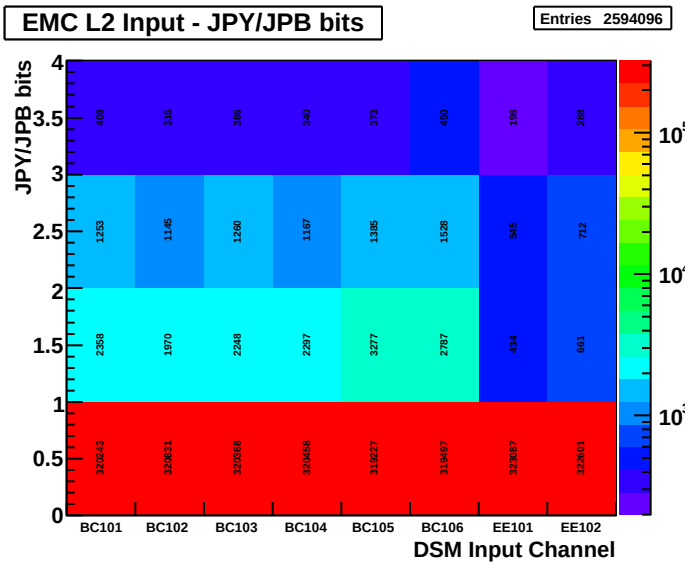

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

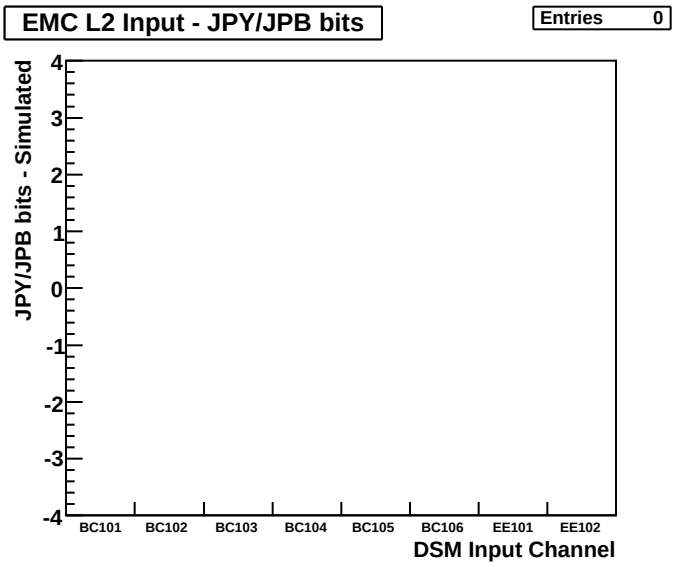

Endcap EMC L1 Input - High Tower Bits
Entries 3891144
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

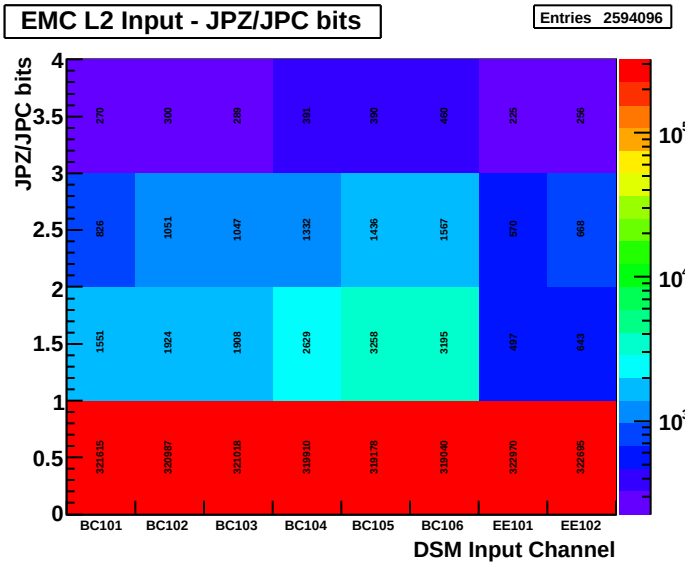

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

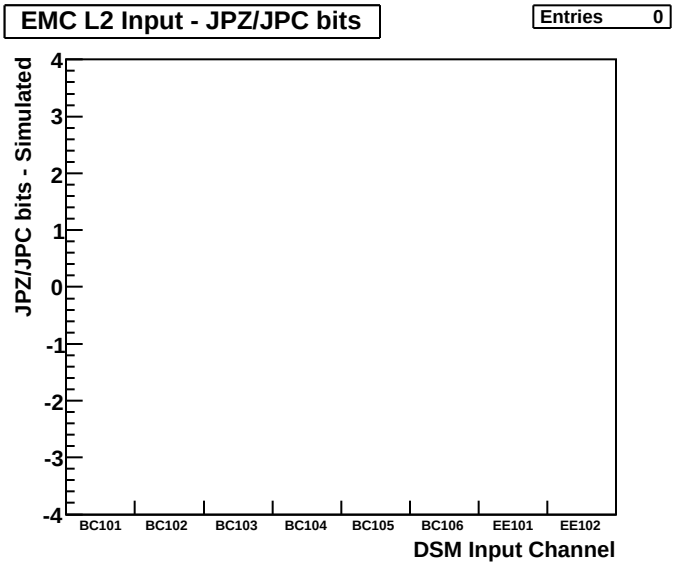

EMC L2 Input - JPX/JPA bits
Entries 2594096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
689 508 468 377 454 426 238 276
1769 1592 1439 1324 1361 1269 599 761
3076 2938 2574 2564 2515 2186 553 502
318728 319224 319781 319997 319932 320381 322872 322723

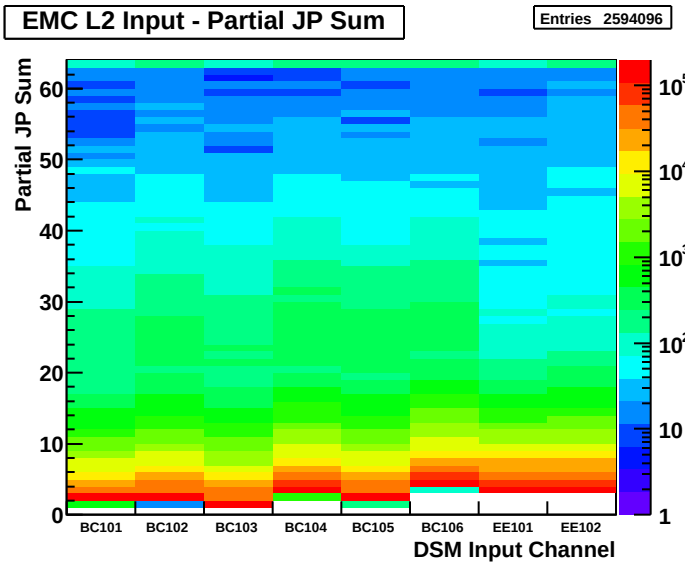

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

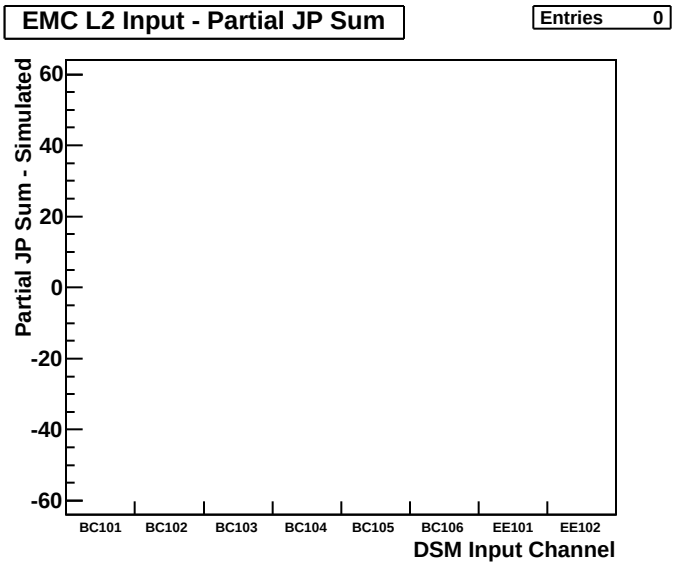

EMC L2 Input - JPY/JPB bits
Entries 2594096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
406 316 386 340 373 450 196 288
1253 1145 1260 1167 1385 1528 545 712
2358 1970 2248 2297 3277 2787 434 661
320243 320831 320368 320458 319227 319497 323087 322601

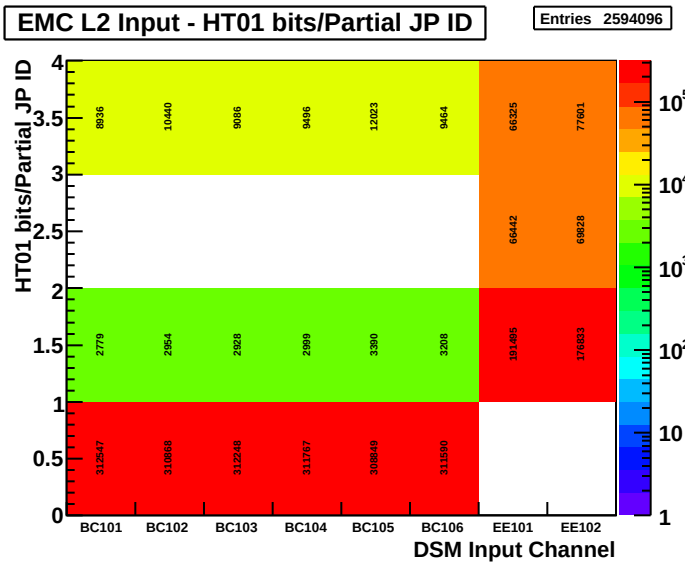

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

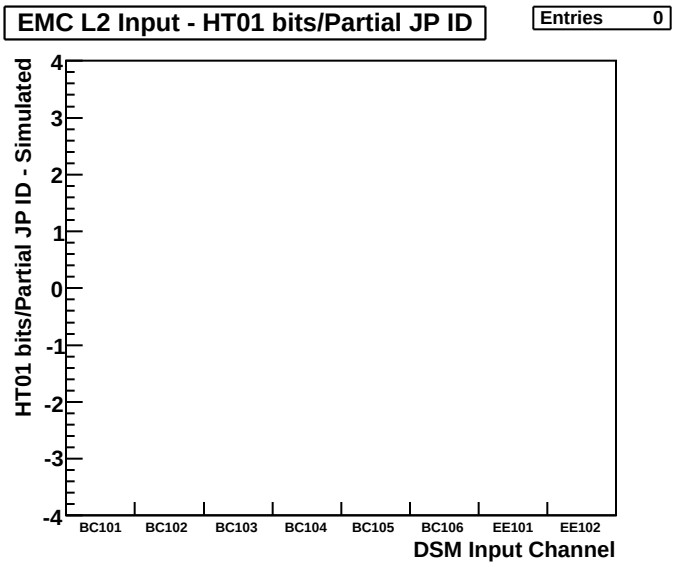

EMC L2 Input - JPZ/JPC bits
Entries 2594096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
270 300 289 391 390 460 225 256
826 1051 1047 1332 1436 1567 570 668
1551 1924 1908 2629 3258 3195 497 643
321515 320987 321018 319910 319178 319040 322970 322695

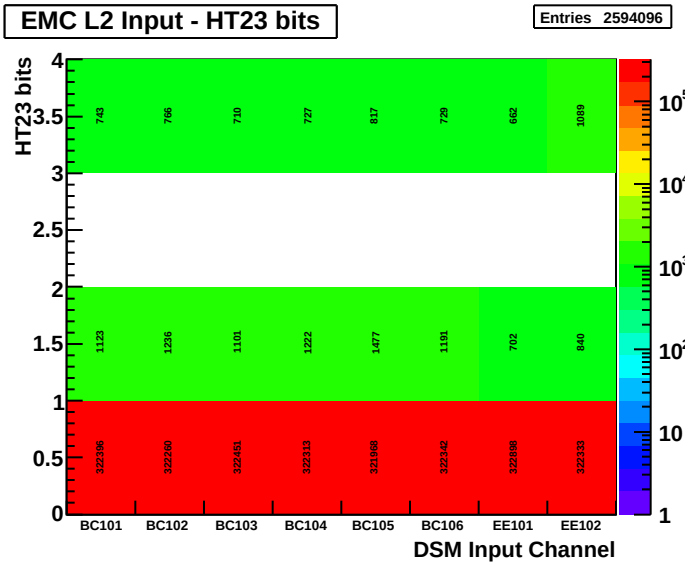

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

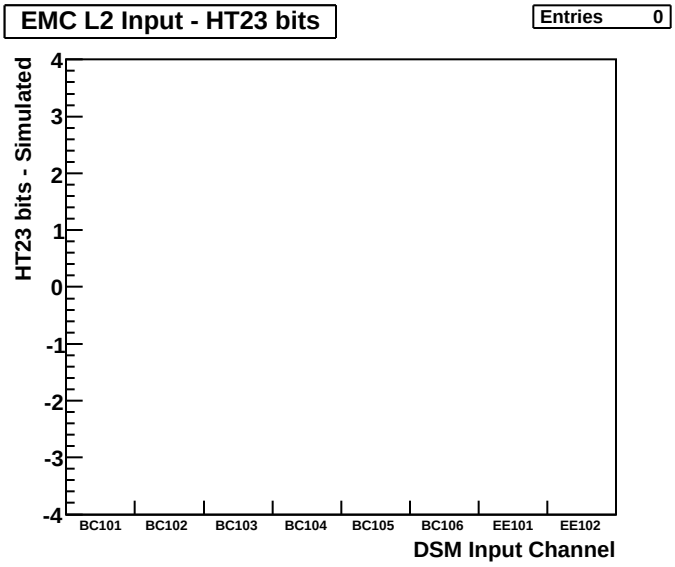

EMC L2 Input - Partial JP Sum
Entries 2594096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

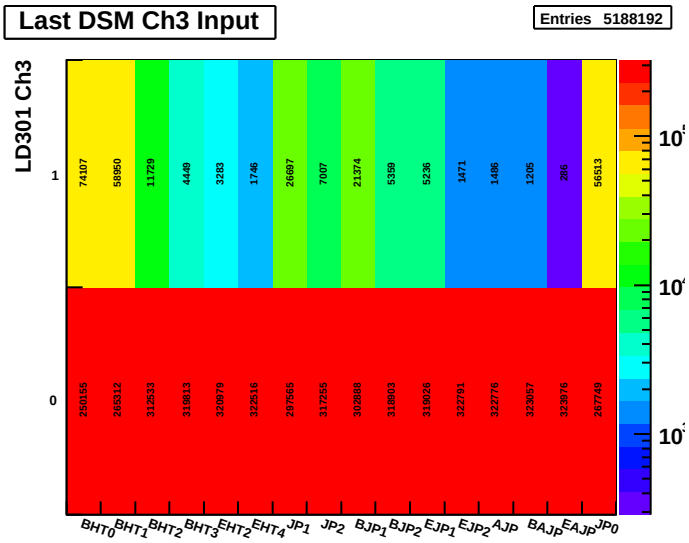

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

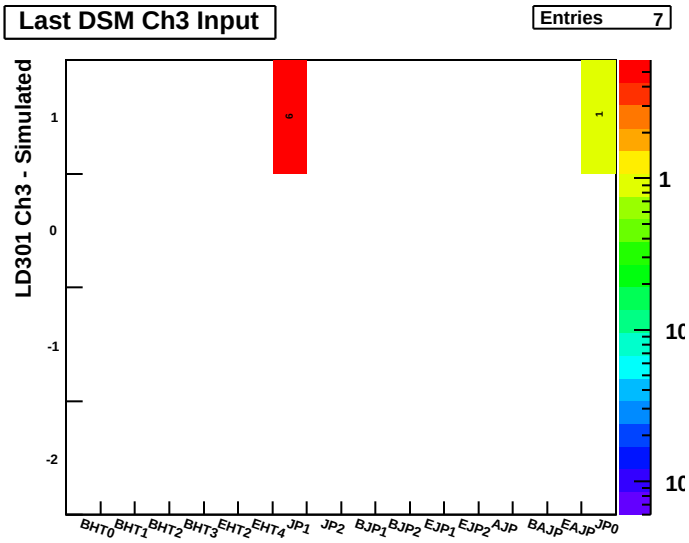

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2594096
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

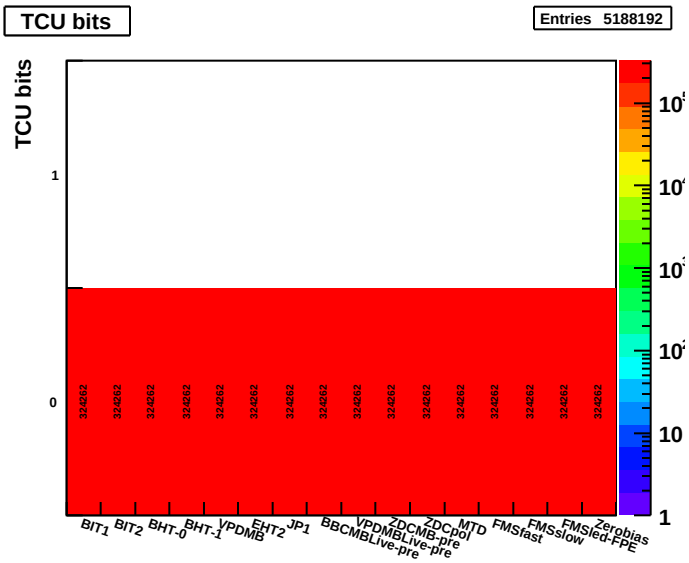

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

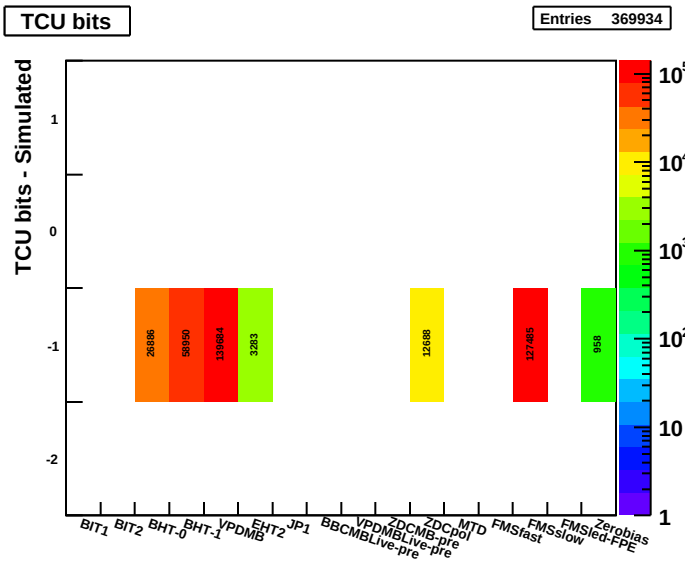

EMC L2 Input - HT23 bits
Entries 2594096
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

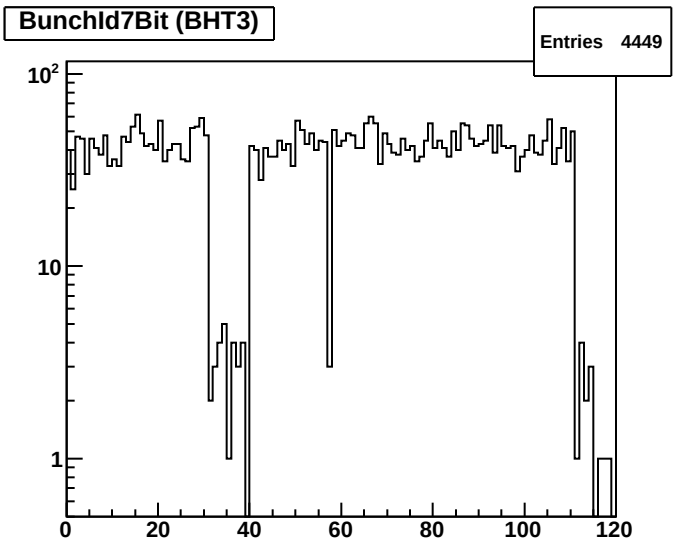

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

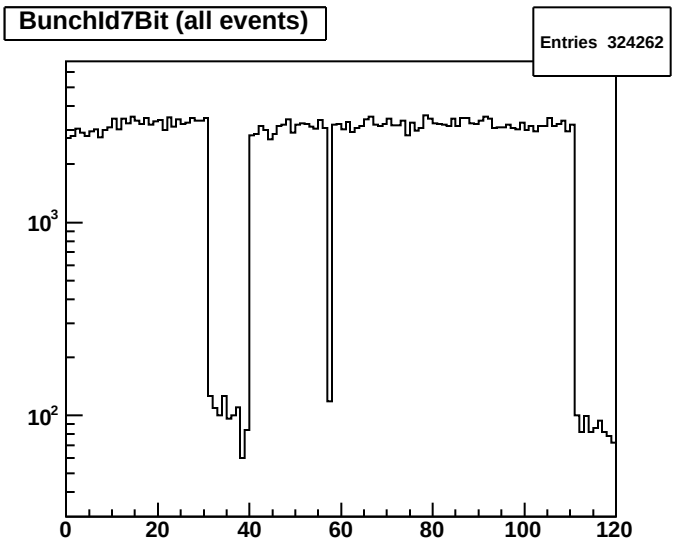

Last DSM Ch3 Input
Entries 5188192
LD301 Ch3
74107 58950 11729 4449 3283 1746 26697 7007 21374 5359 5236 1471 1486 1205 286 56513
250155 265312 312533 319813 320979 322516 297565 317255 302888 318903 319026 322791 322776 323057 323976 267749
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

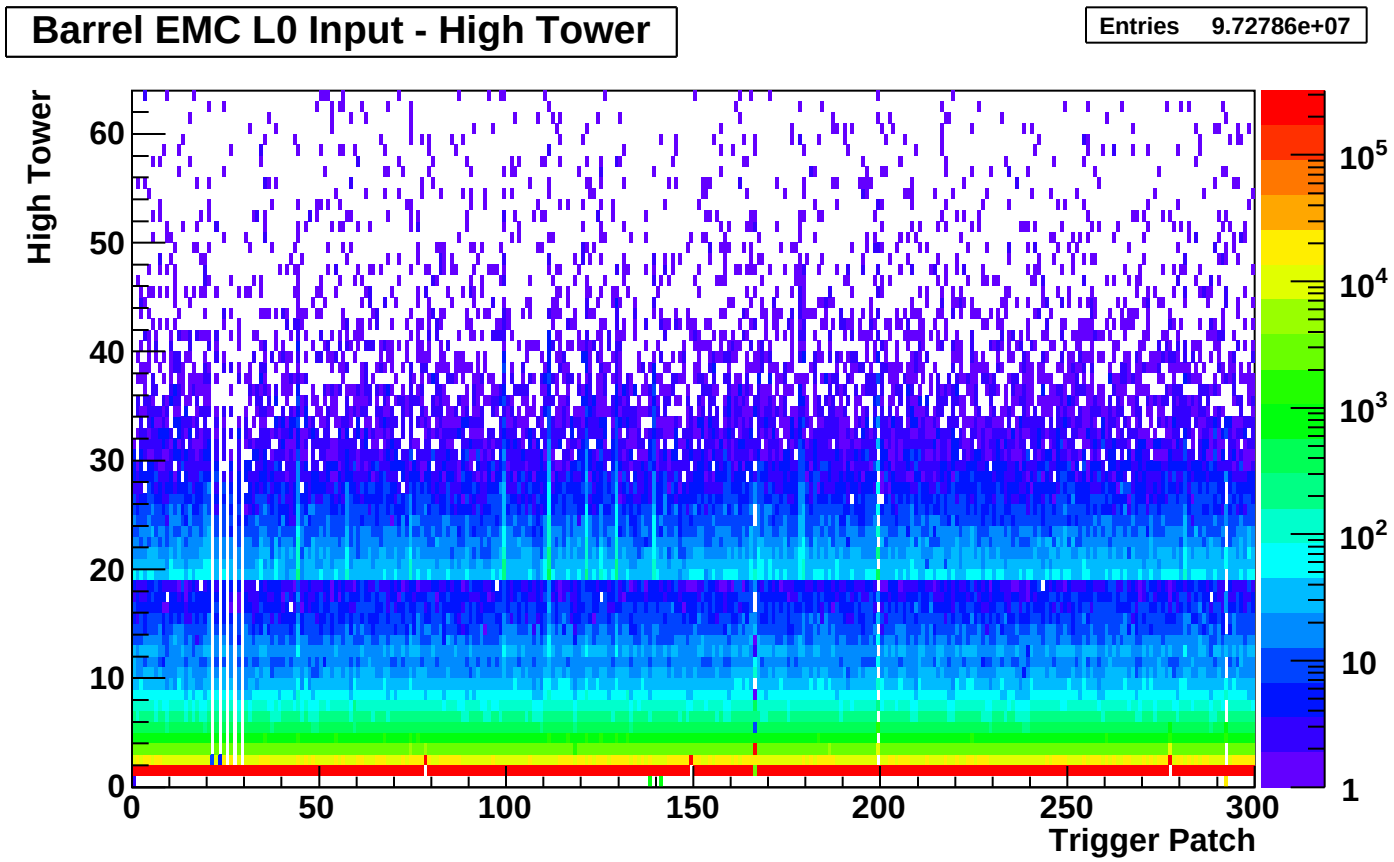

Last DSM Ch3 Input
Entries 7
LD301 Ch3 - Simulated
6
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

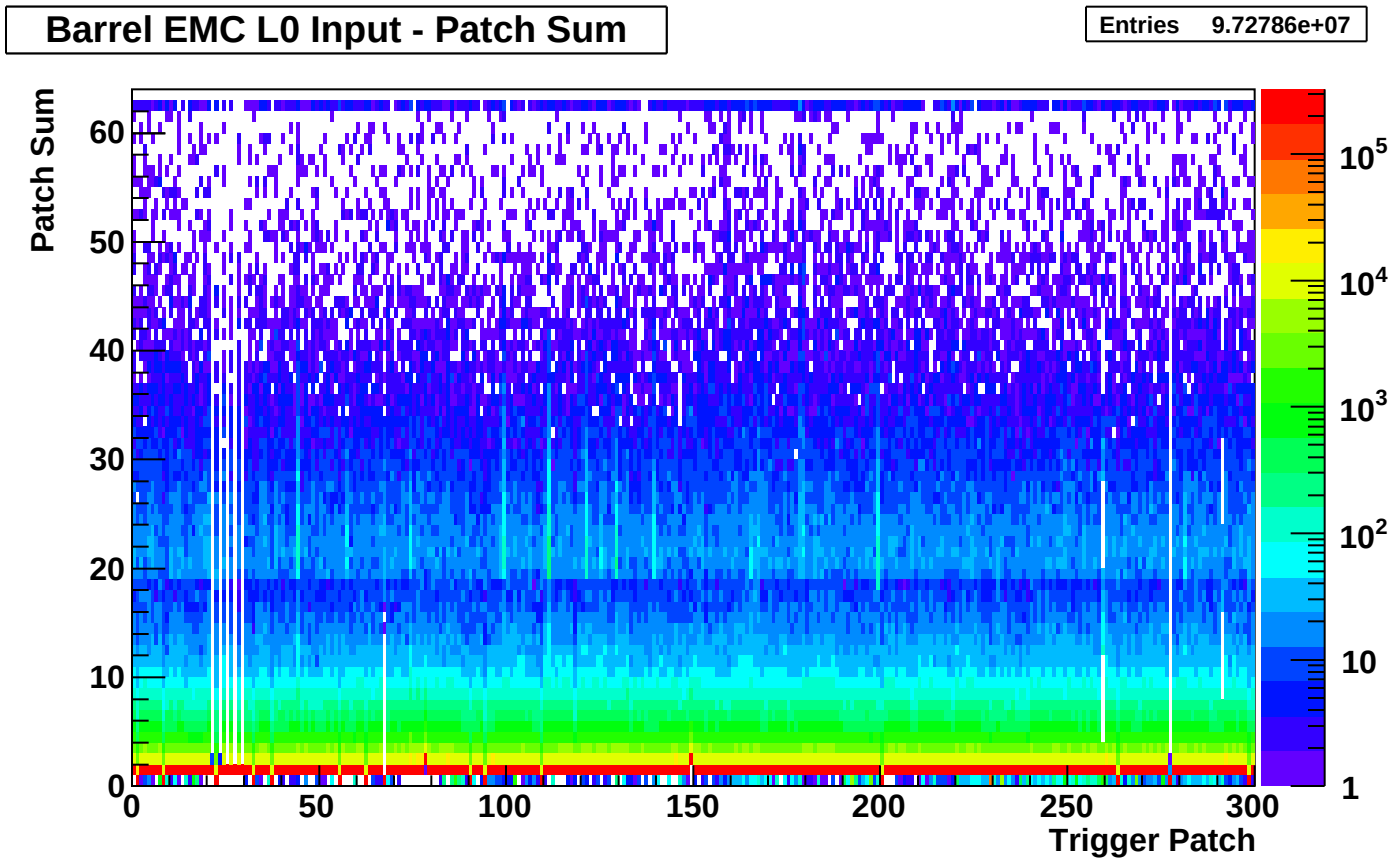

TCU bits
Entries 5188192
TCU bits
324262 324262 324262 324262 324262 324262 324262 324262 324262 324262 324262 324262 324262 324262 324262 324262
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

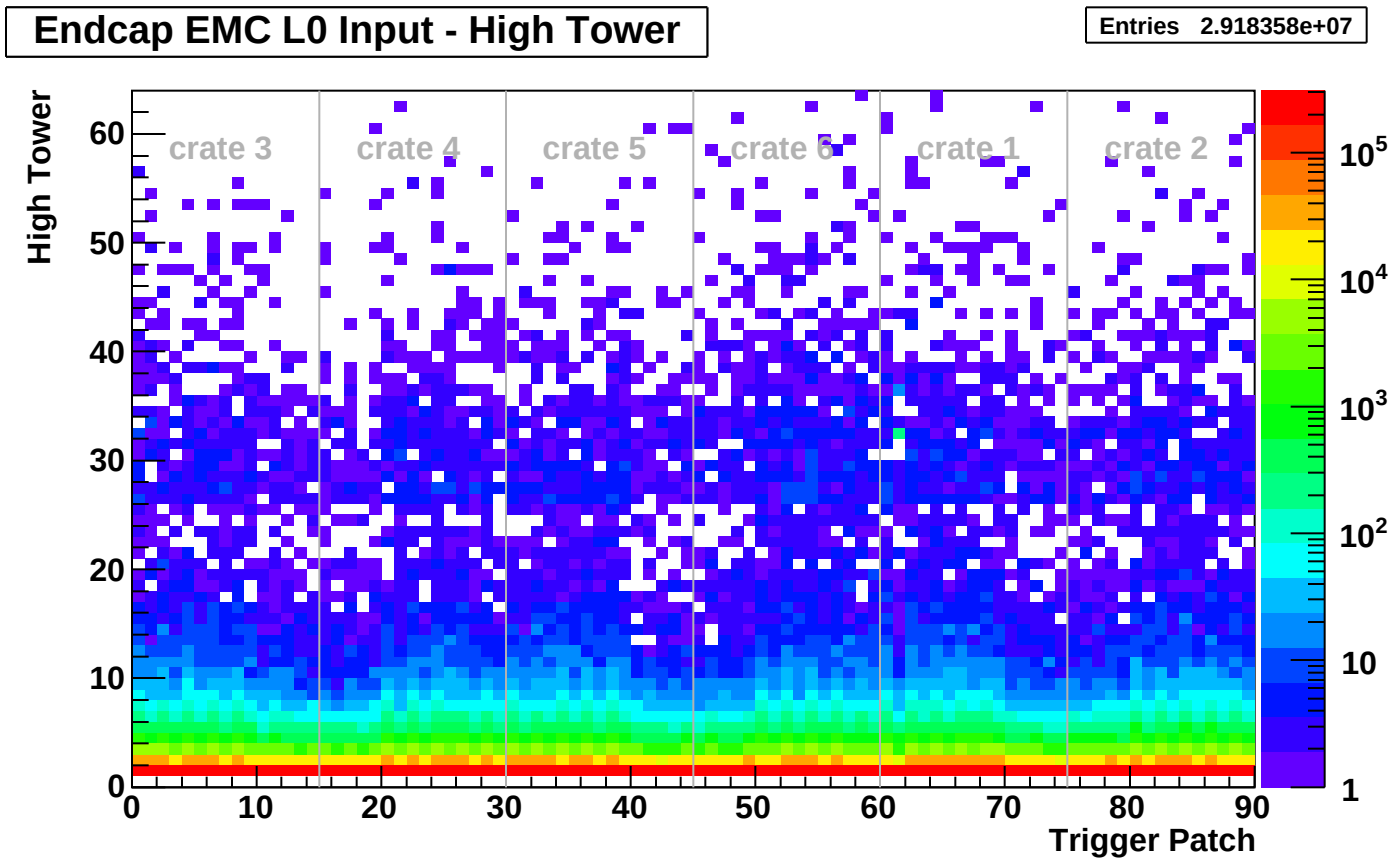

TCU bits
Entries 369934
TCU bits - Simulated
26886 58950 139684 3283 12688 127485 958
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

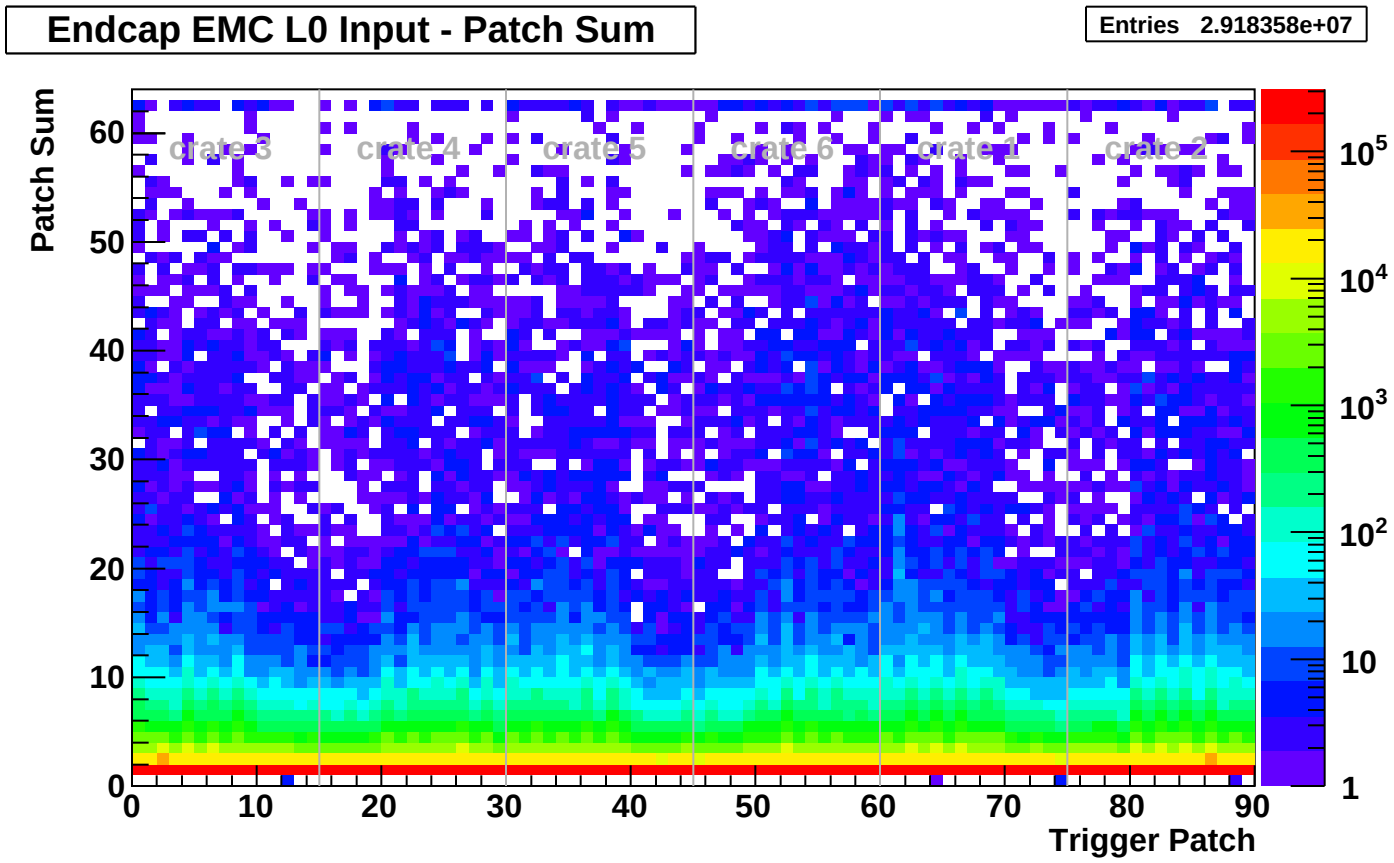

BunchId7Bit (BHT3)
Entries 4449

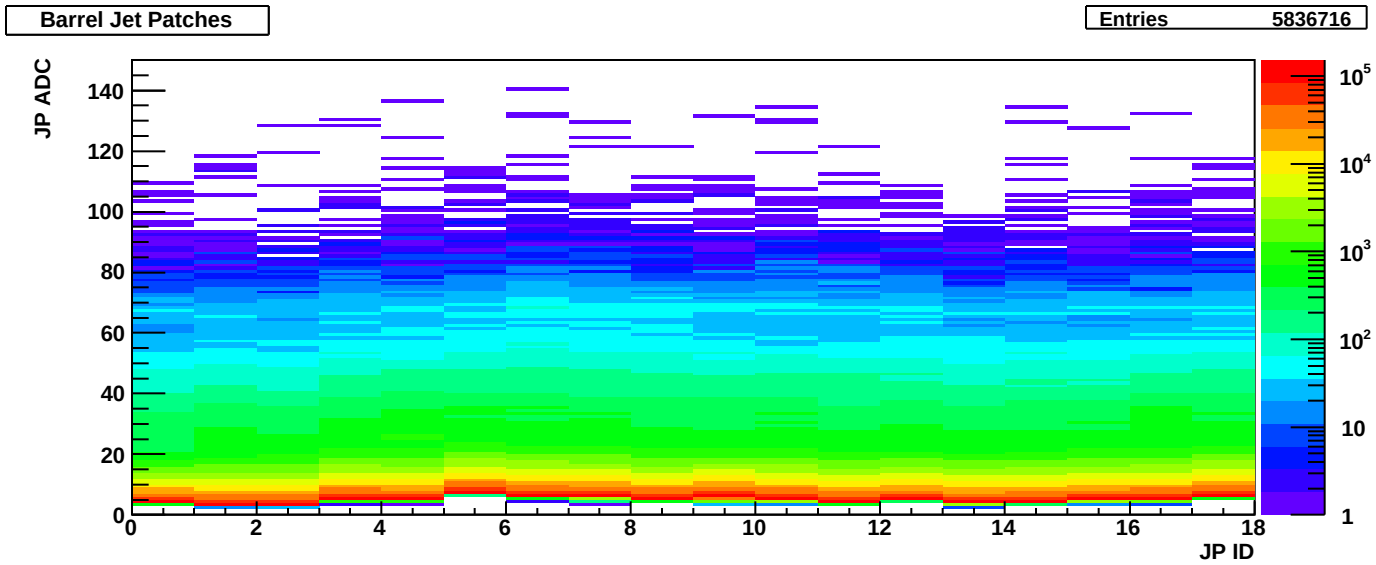

BunchId7Bit (all events)
Entries 324262

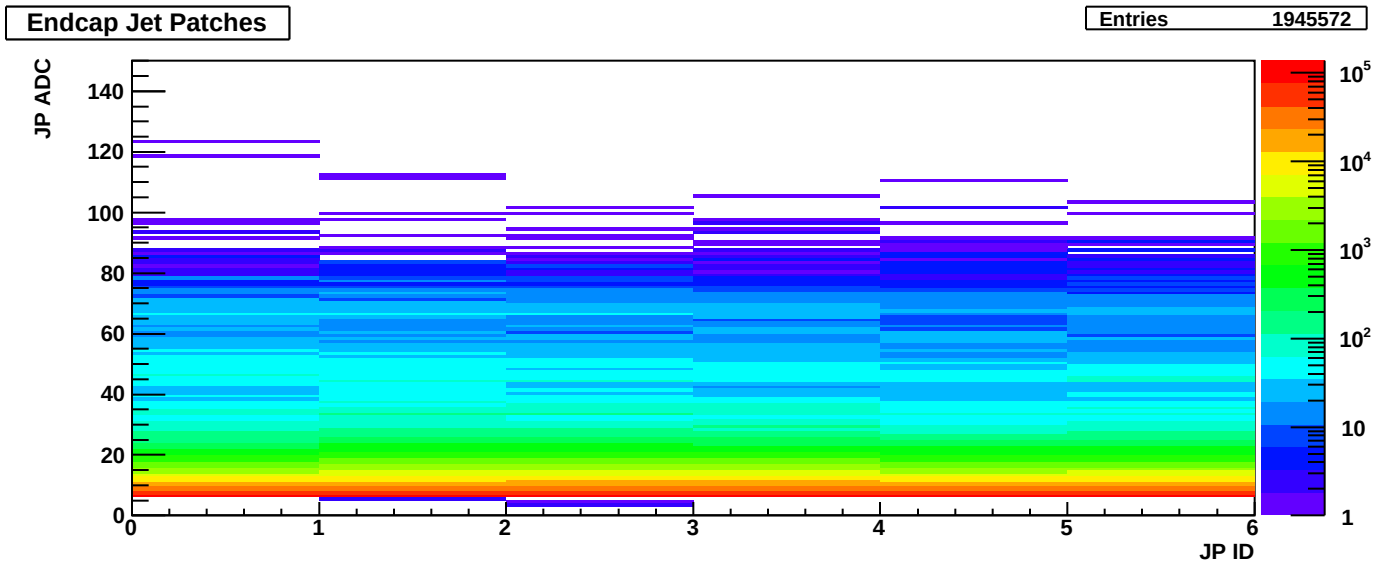

Barrel EMC L0 Input - High Tower
Entries 9.72786e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

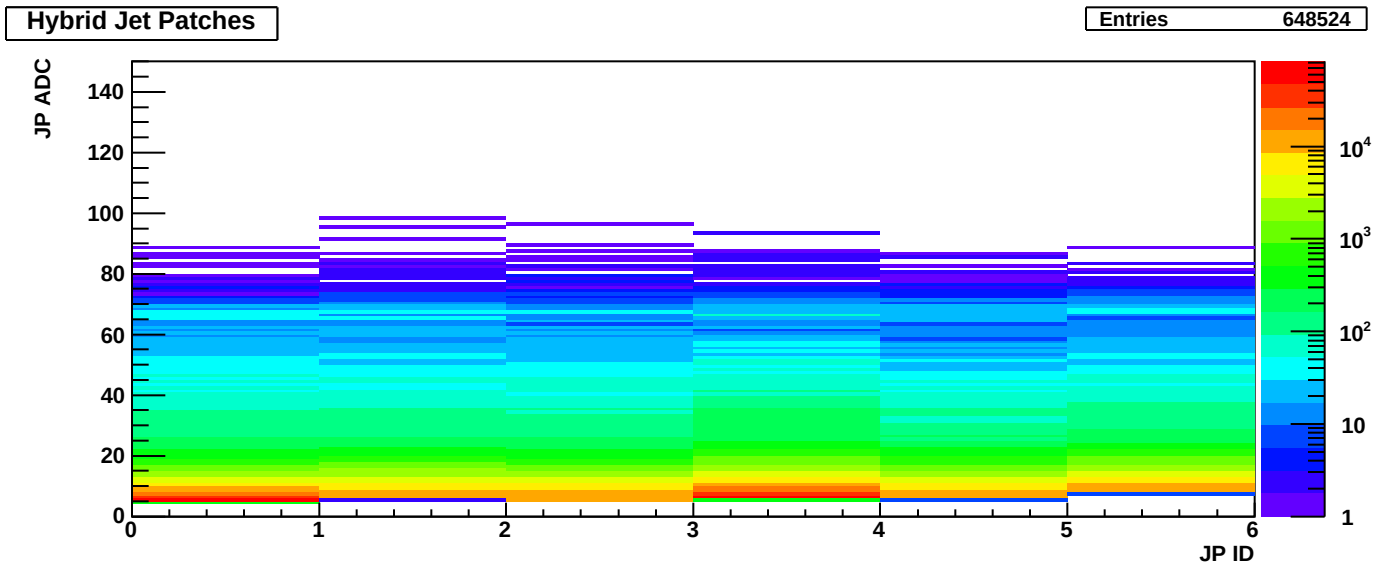

Barrel EMC L0 Input - Patch Sum
Entries 9.72786e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

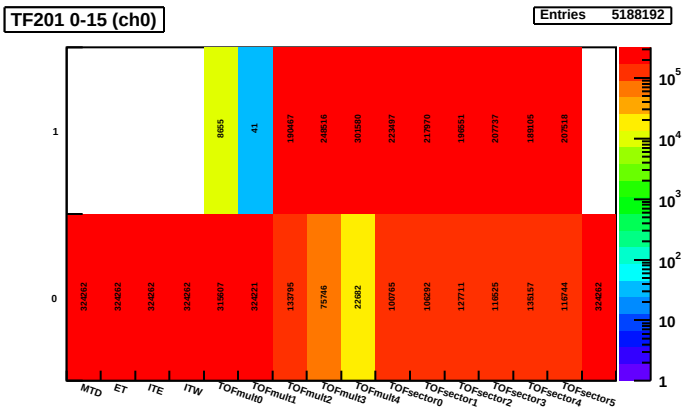

Endcap EMC L0 Input - High Tower
Entries 2.918358e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

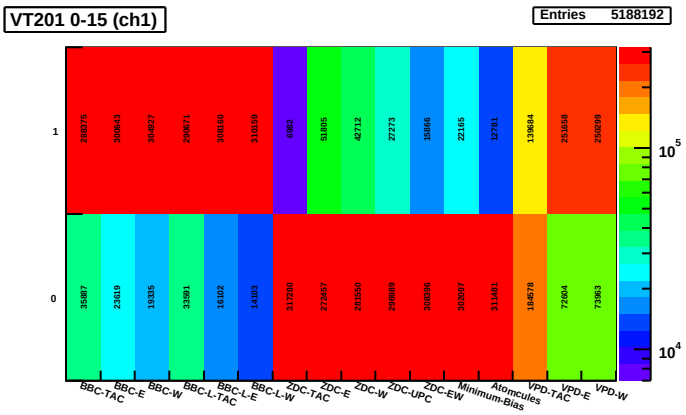

Endcap EMC L0 Input - Patch Sum
Entries 2.918358e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

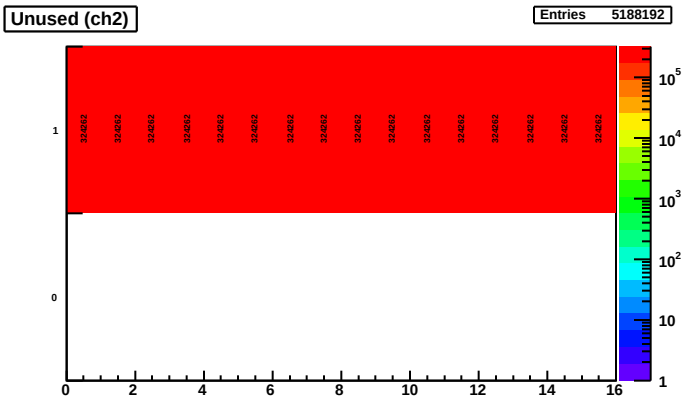

Barrel Jet Patches
Entries 5836716
JP ADC
JP ID

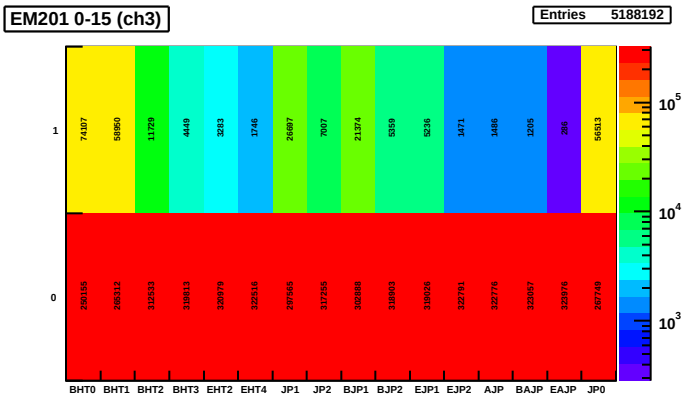

Endcap Jet Patches
Entries 1945572
JP ADC
JP ID

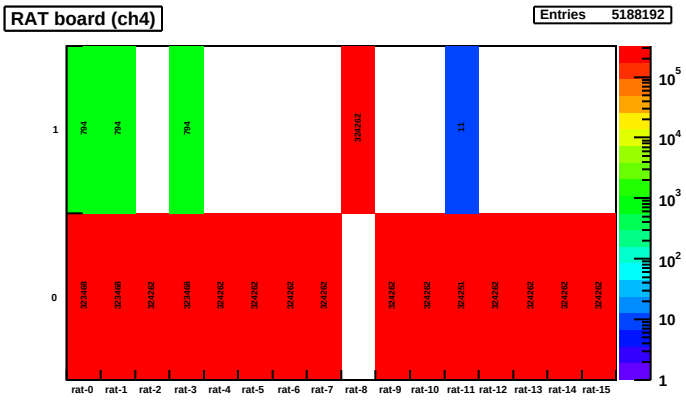

Hybrid Jet Patches
Entries 648524
JP ADC
JP ID

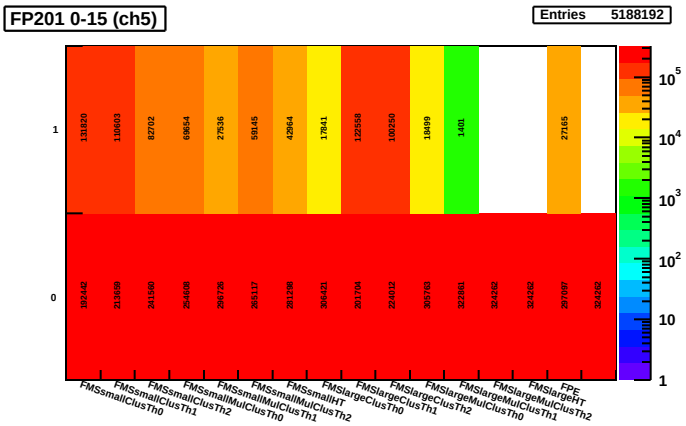

TF201 0-15 (ch0)
Entries 5188192
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

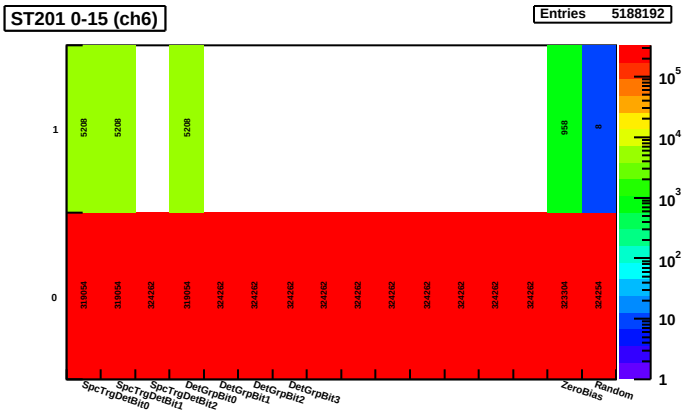

VT201 0-15 (ch1)
Entries 5188192
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

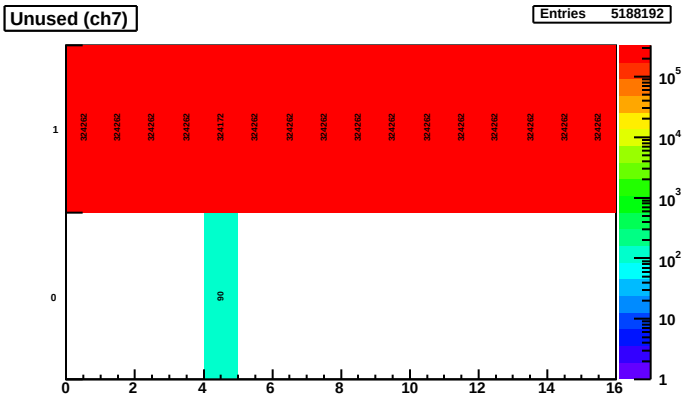

Unused (ch2)
Entries 5188192

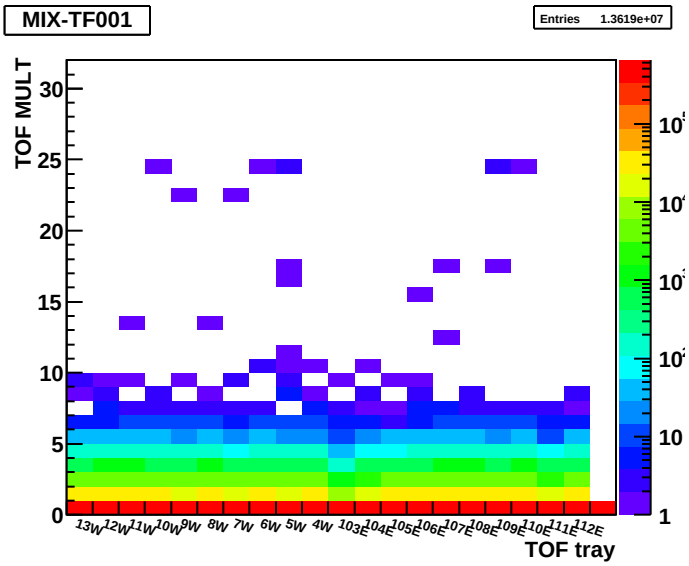

EM201 0-15 (ch3)
Entries 5188192
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

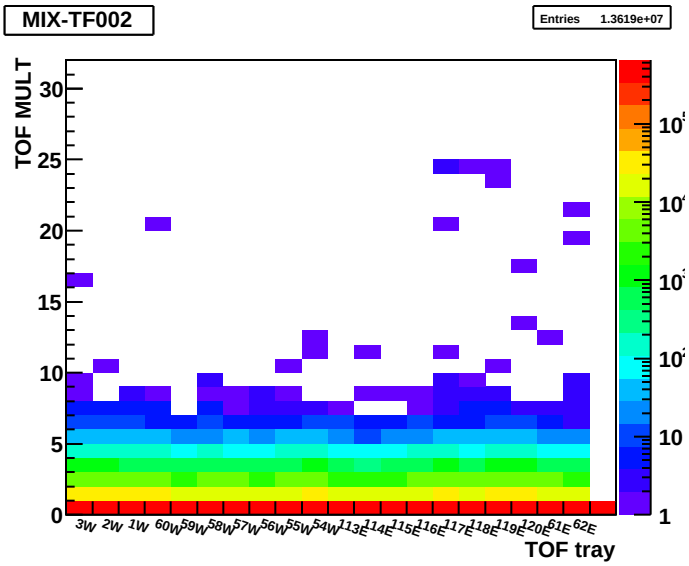

RAT board (ch4)
Entries 5188192
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

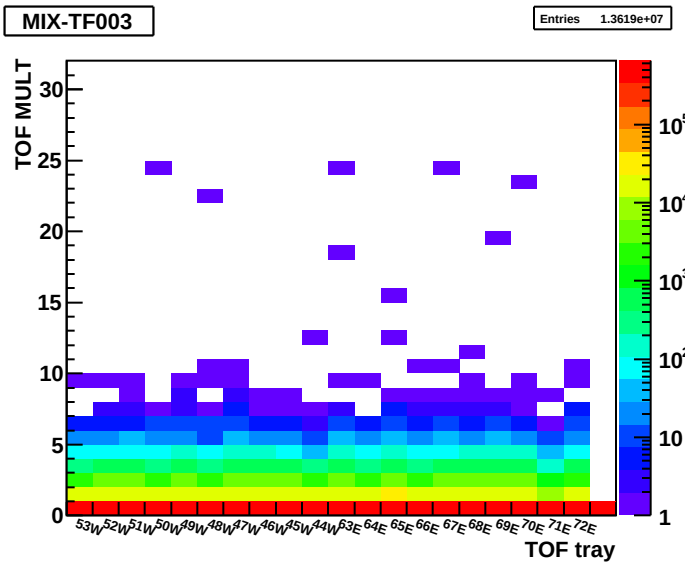

FP201 0-15 (ch5)
Entries 5188192
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

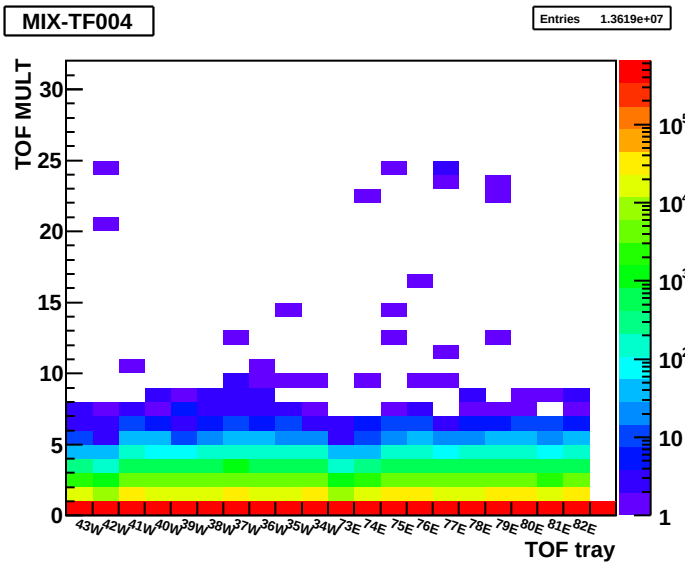

ST201 0-15 (ch6)
Entries 5188192
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

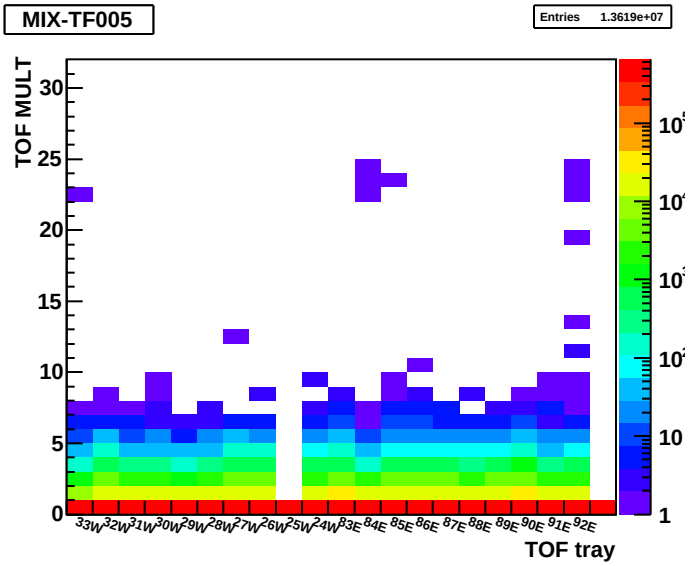

Unused (ch7)
Entries 5188192

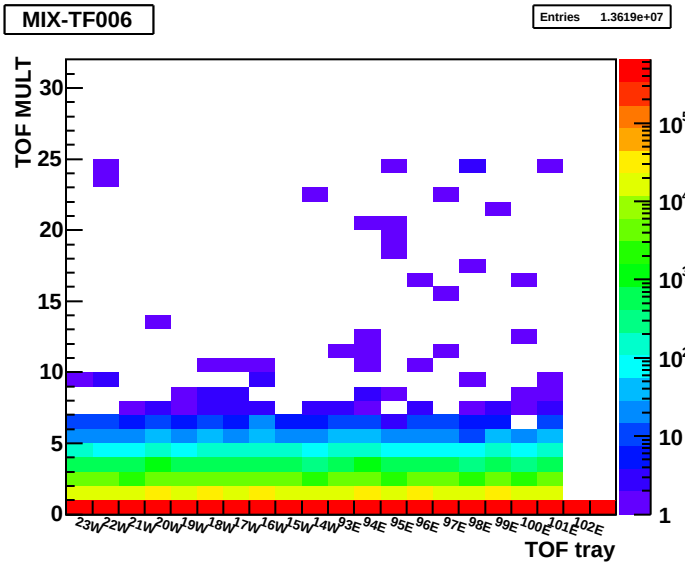

MIX-TF001
Entries 1.3619e+07
TOF MULT
TOF tray

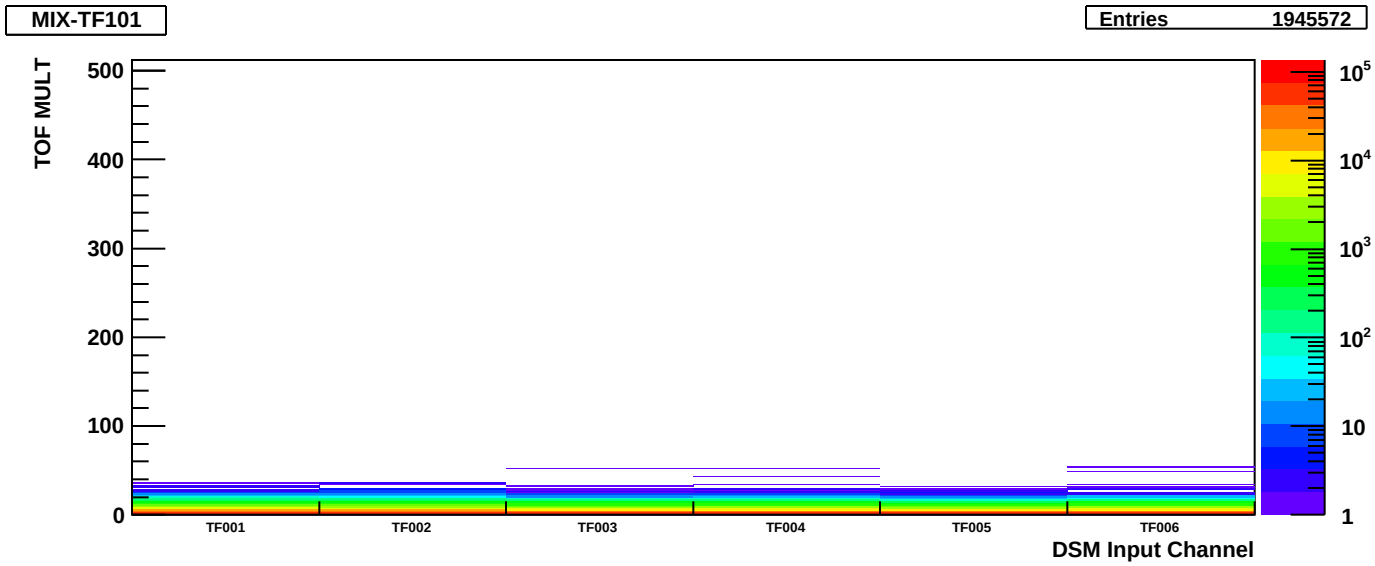

MIX-TF002
Entries 1.3619e+07
TOF MULT
TOF tray

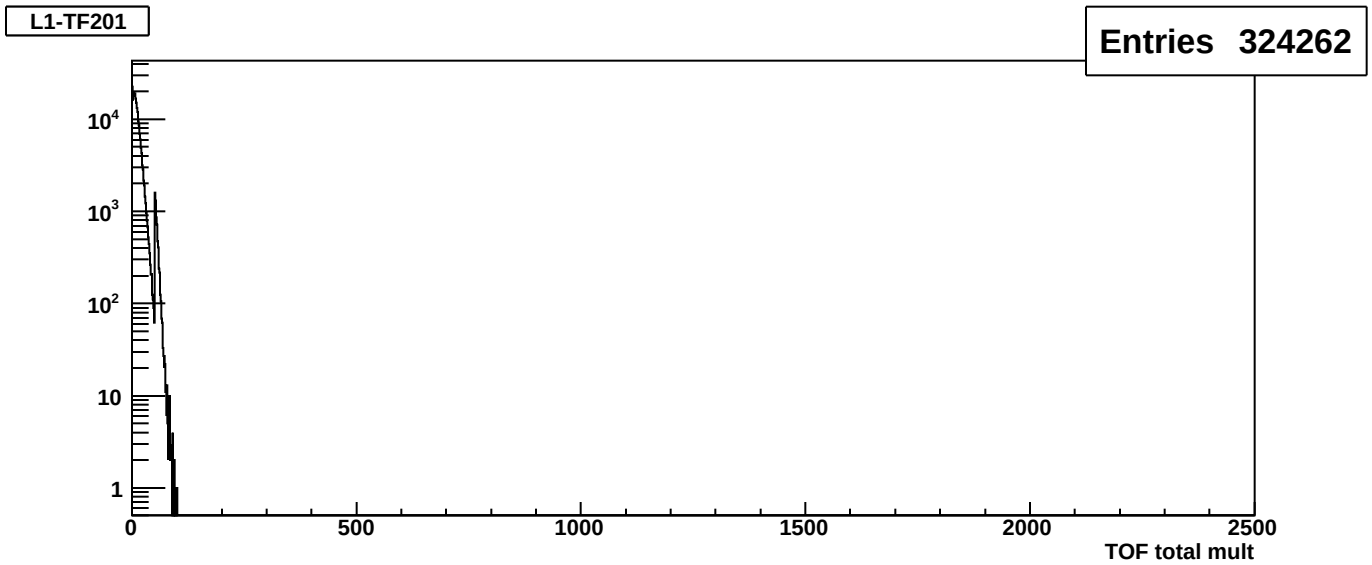

MIX-TF003
Entries 1.3619e+07
TOF MULT
TOF tray

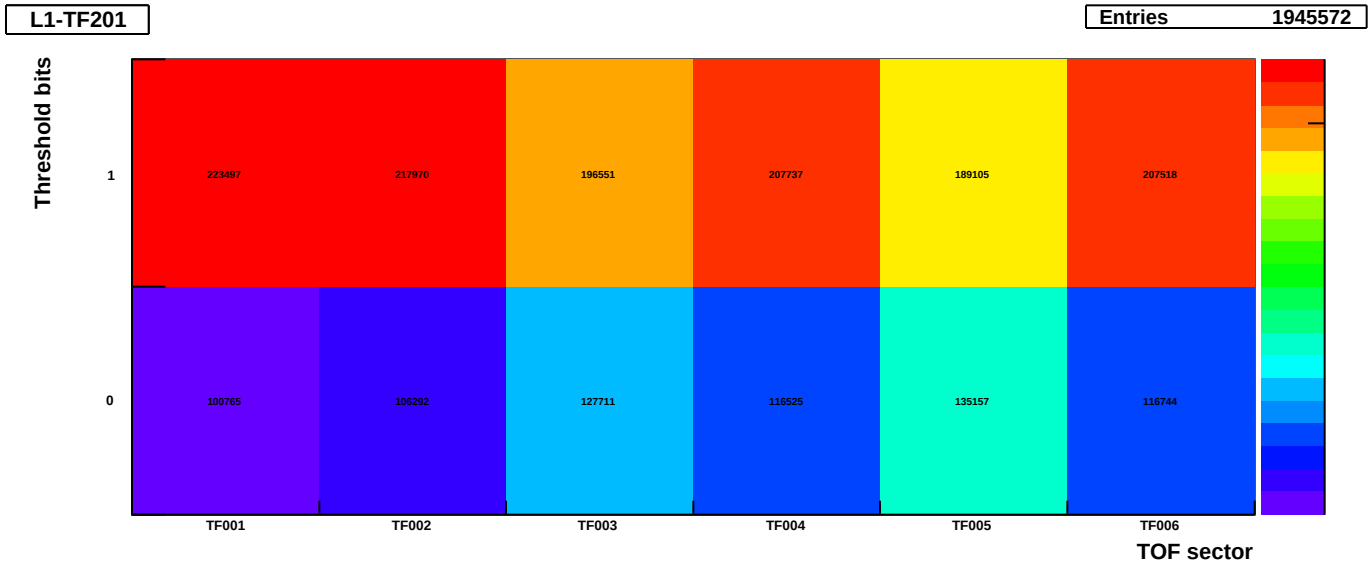

MIX-TF004
Entries 1.3619e+07
TOF MULT
TOF tray

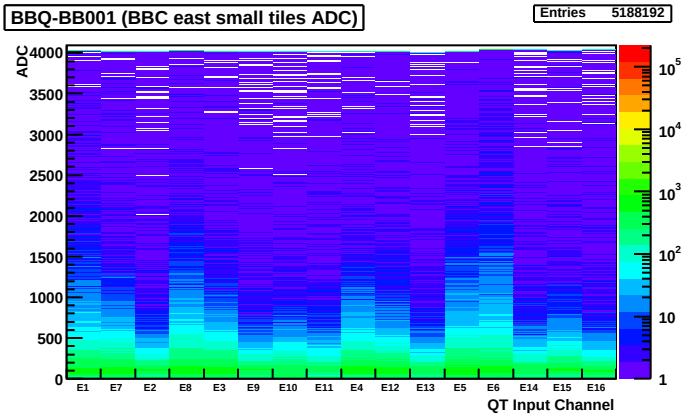

MIX-TF005
Entries 1.3619e+07
TOF MULT
TOF tray

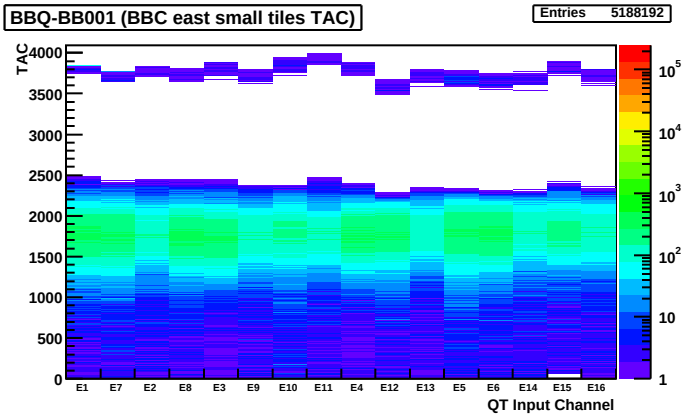

MIX-TF006
Entries 1.3619e+07
TOF MULT
TOF tray

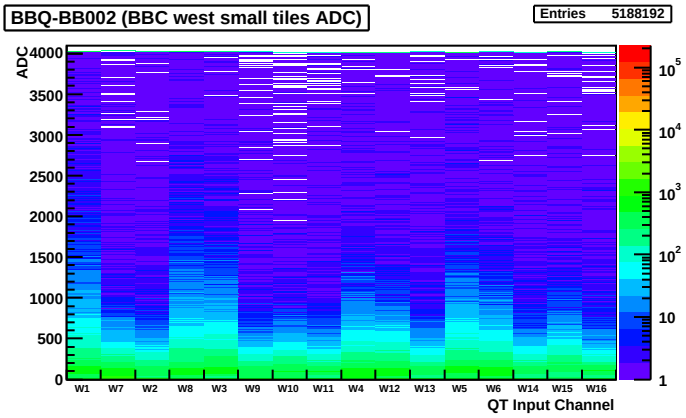

MIX-TF101
Entries 1945572
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

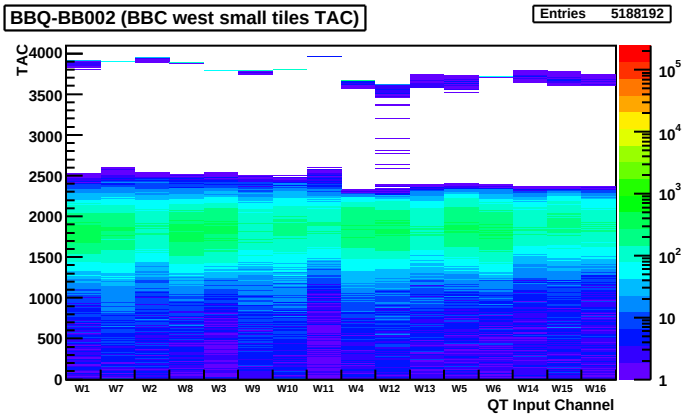

L1-TF201
Entries 324262
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

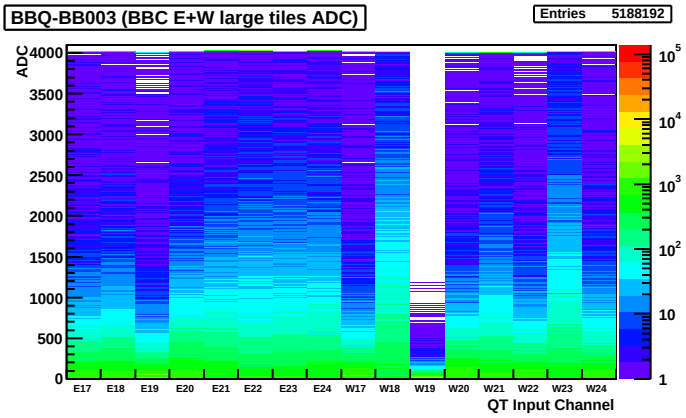

L1-TF201
Entries 1945572
Threshold bits
1
0
223497
217970
196551
207737
189105
207518
100765
106292
127711
116525
135157
116744
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

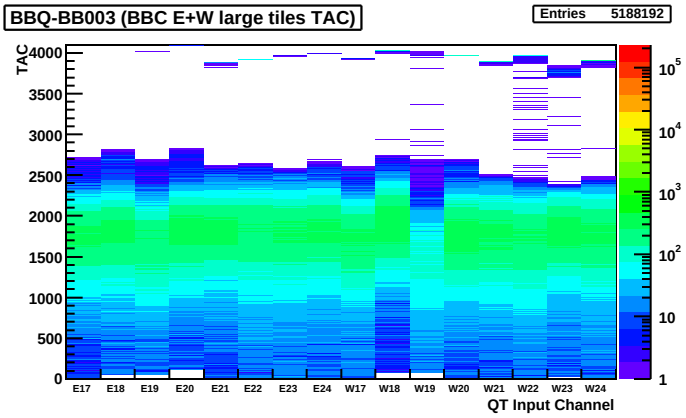

BBQ-BB001 (BBC east small tiles ADC)
Entries 5188192
ADC
QT Input Channel

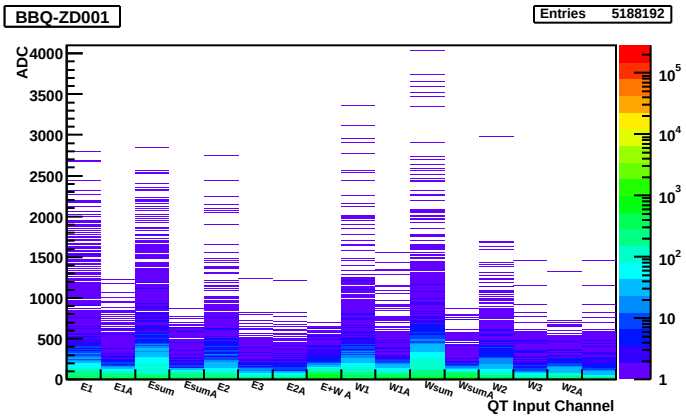

BBQ-BB001 (BBC east small tiles TAC)
Entries 5188192
TAC
QT Input Channel

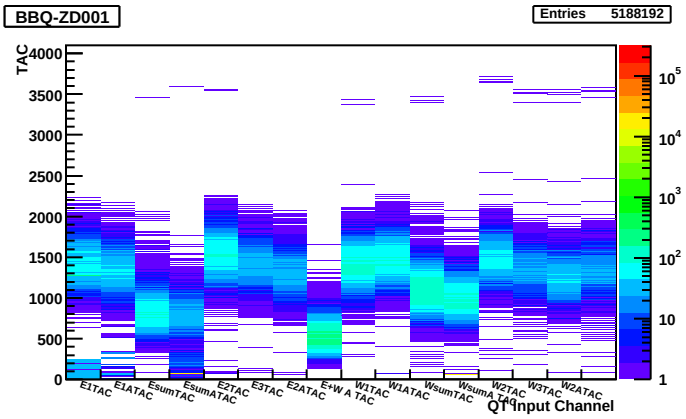

BBQ-BB002 (BBC west small tiles ADC)
Entries 5188192
ADC
QT Input Channel

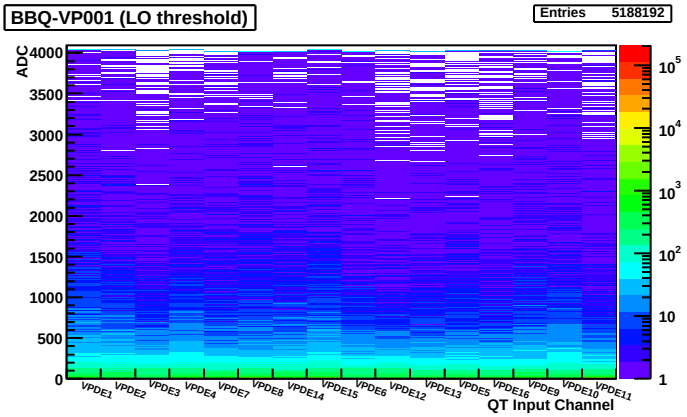

BBQ-BB002 (BBC west small tiles TAC)
Entries 5188192
TAC
QT Input Channel

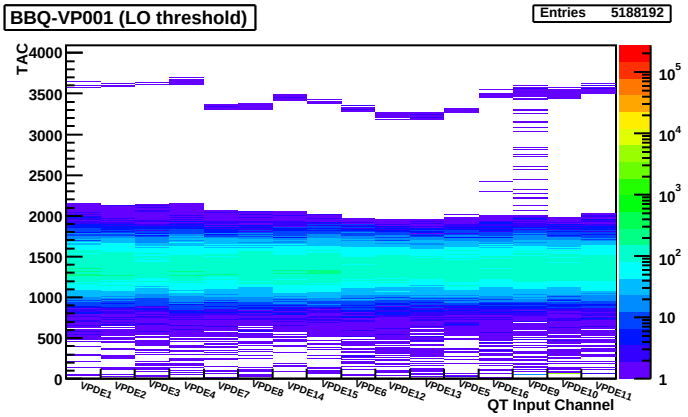

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5188192
ADC
QT Input Channel

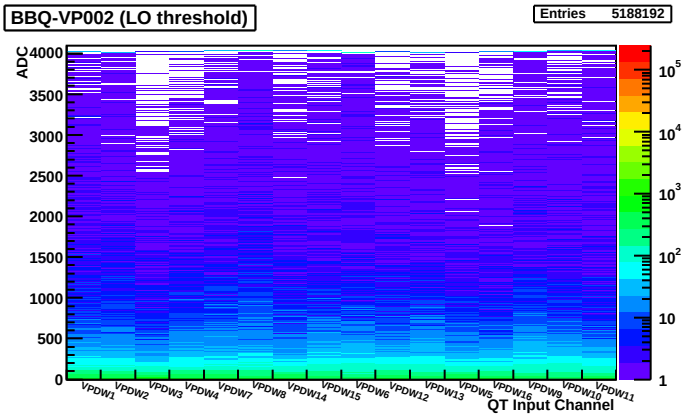

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5188192
TAC
QT Input Channel

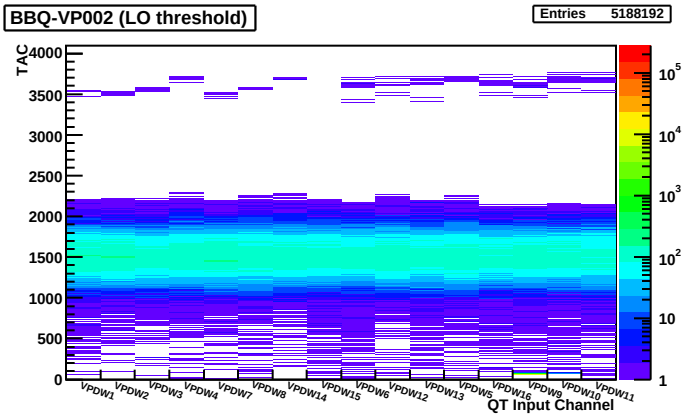

BBQ-ZD001
Entries 5188192
ADC
QT Input Channel

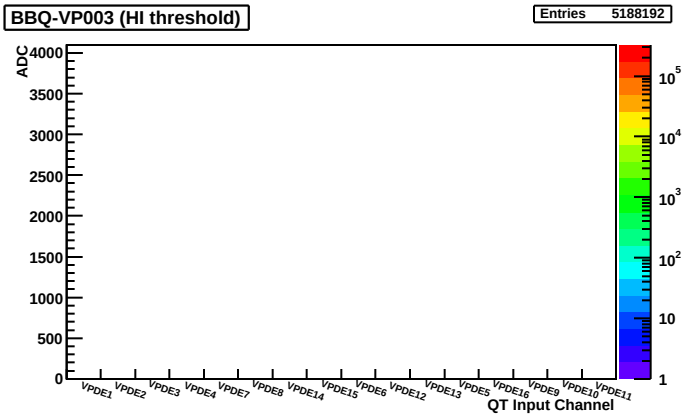

BBQ-ZD001
Entries 5188192
TAC
QT Input Channel

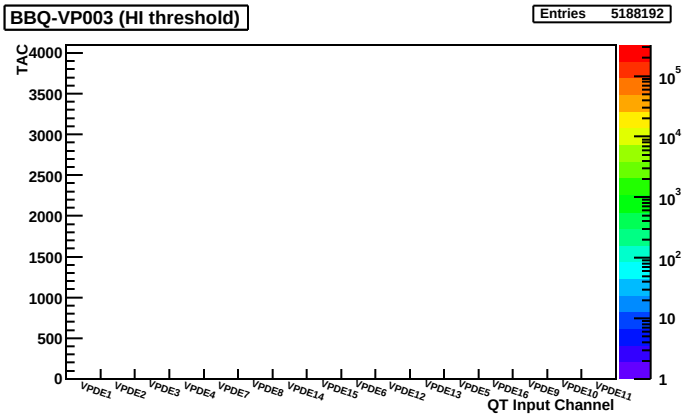

BBQ-VP001 (LO threshold)
Entries 5188192
ADC
QT Input Channel

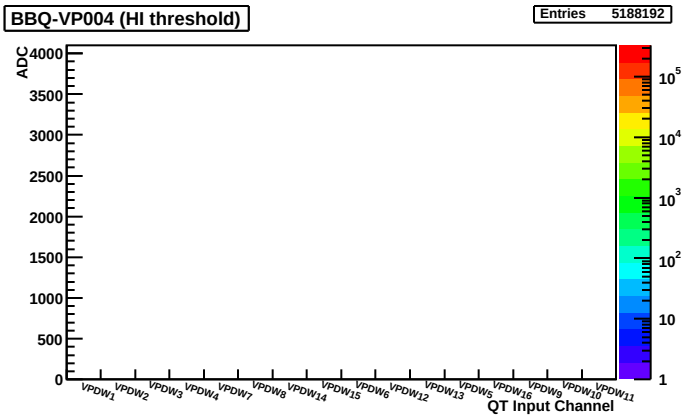

BBQ-VP001 (LO threshold)
Entries 5188192
TAC
QT Input Channel

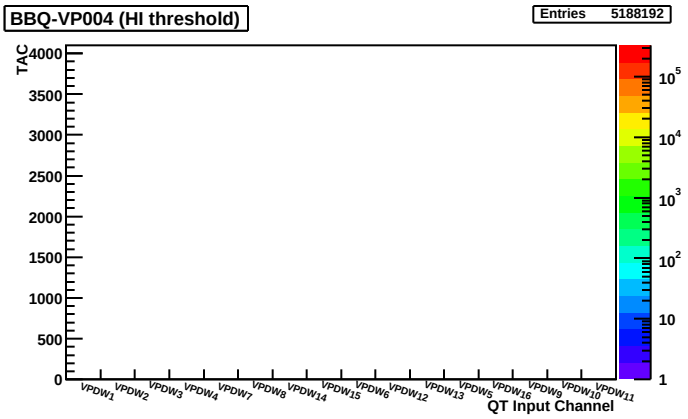

BBQ-VP002 (LO threshold)
Entries 5188192
ADC
QT Input Channel

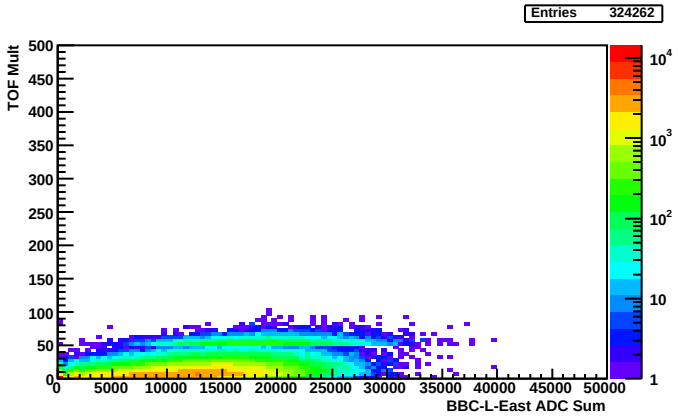

BBQ-VP002 (LO threshold)
Entries 5188192
TAC
QT Input Channel

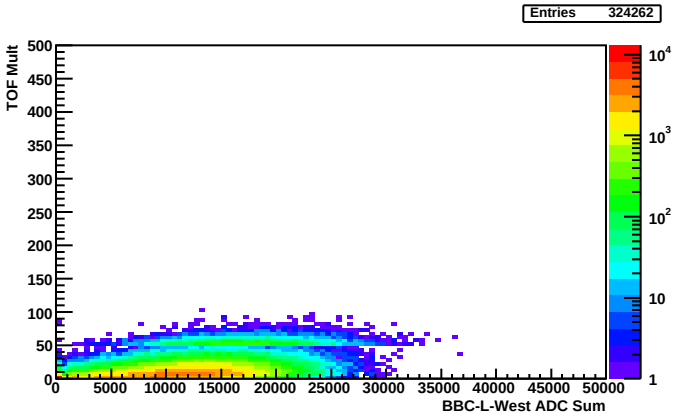

BBQ-VP003 (HI threshold)
Entries 5188192
ADC
QT Input Channel

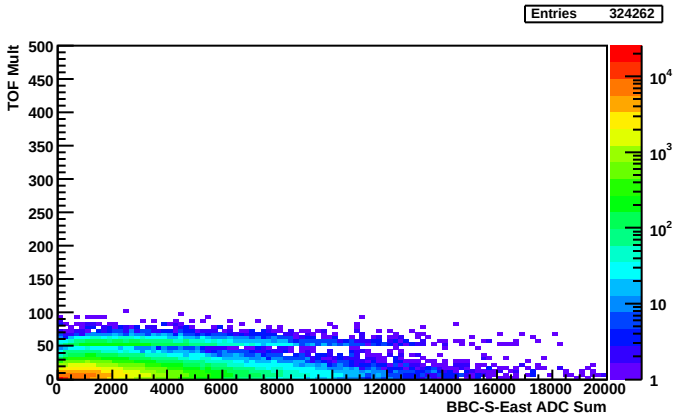

BBQ-VP003 (HI threshold)
Entries 5188192
TAC
QT Input Channel

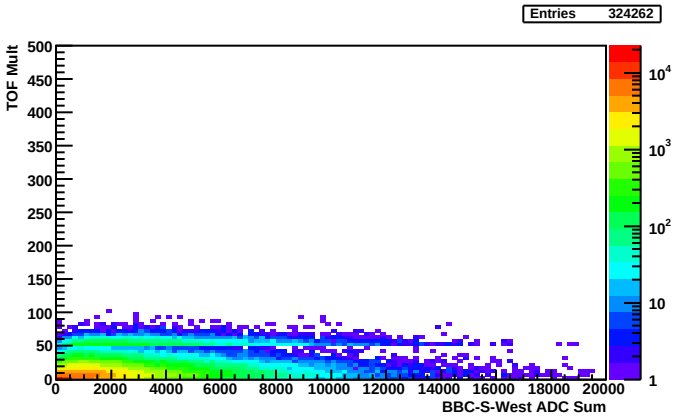

BBQ-VP004 (HI threshold)
Entries 5188192
ADC
QT Input Channel

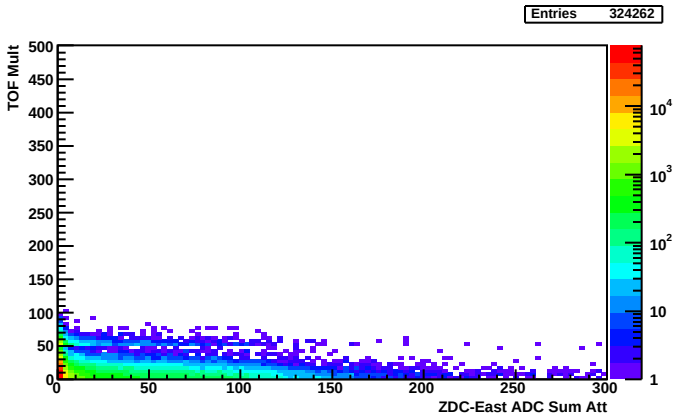

BBQ-VP004 (HI threshold)
Entries 5188192
TAC
QT Input Channel

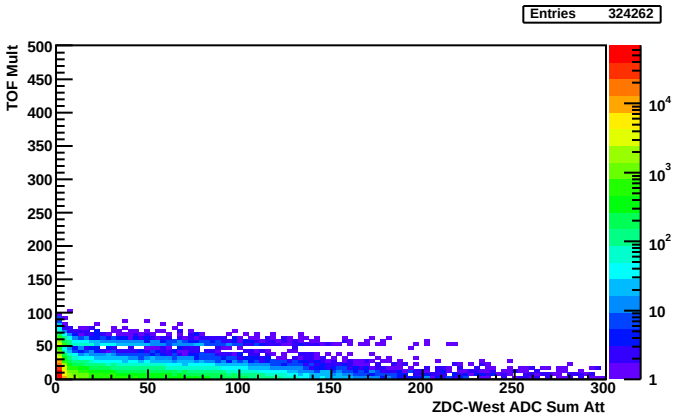

Entries 324262
TOF Mult
BBC-L-East ADC Sum

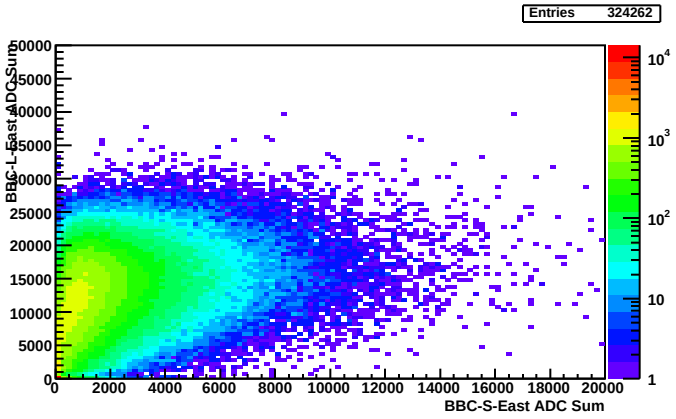

Entries 324262
TOF Mult
BBC-L-West ADC Sum

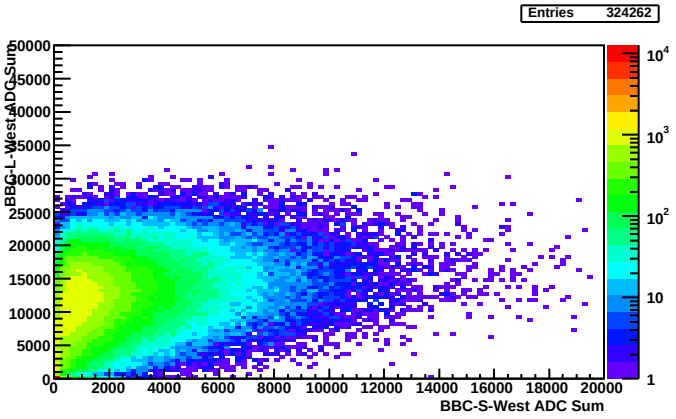

Entries 324262
TOF Mult
BBC-S-East ADC Sum

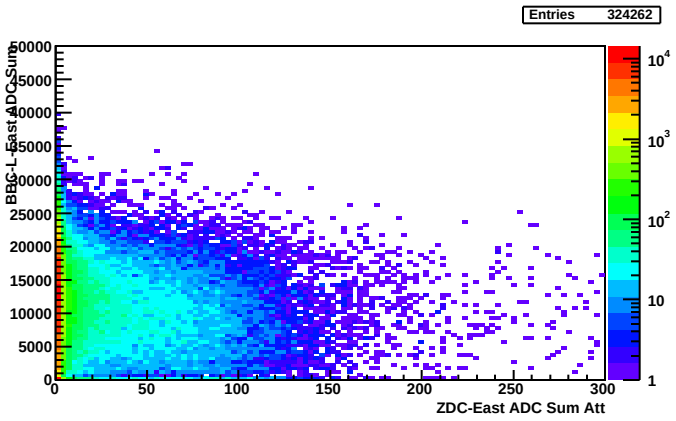

Entries 324262
TOF Mult
BBC-S-West ADC Sum

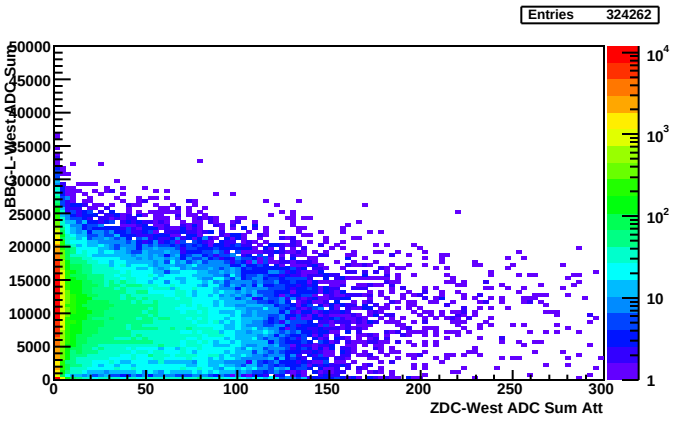

Entries 324262
TOF Mult
ZDC-East ADC Sum Att

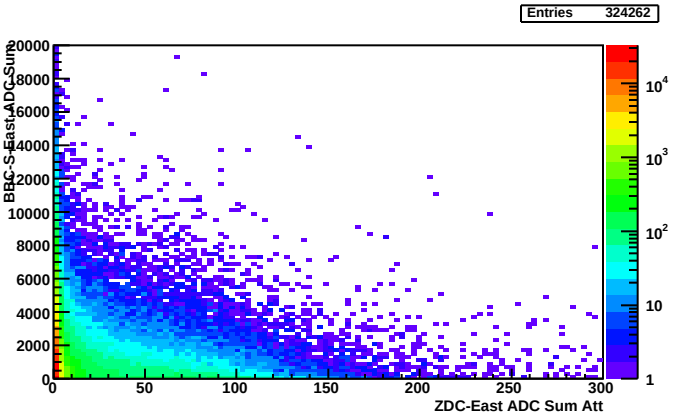

Entries 324262
TOF Mult
ZDC-West ADC Sum Att

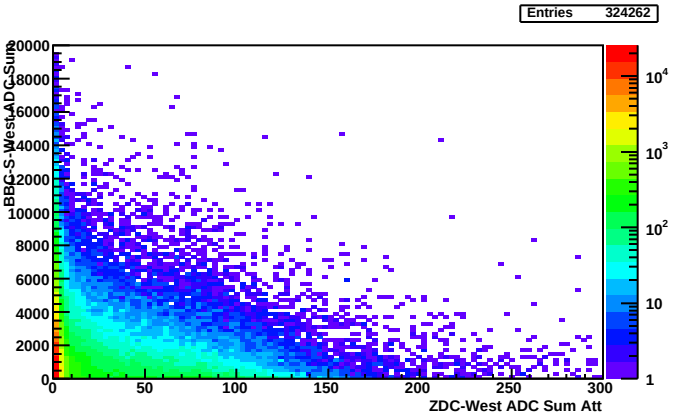

Entries 324262
BBC-L-East ADC Sum
BBC-S-East ADC Sum

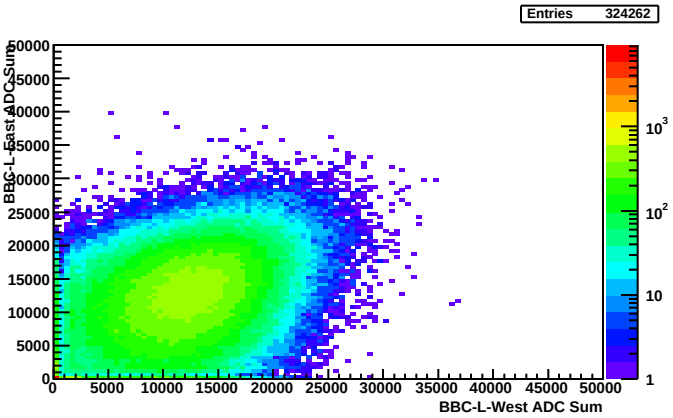

Entries 324262
BBC-L-West ADC Sum
BBC-S-West ADC Sum

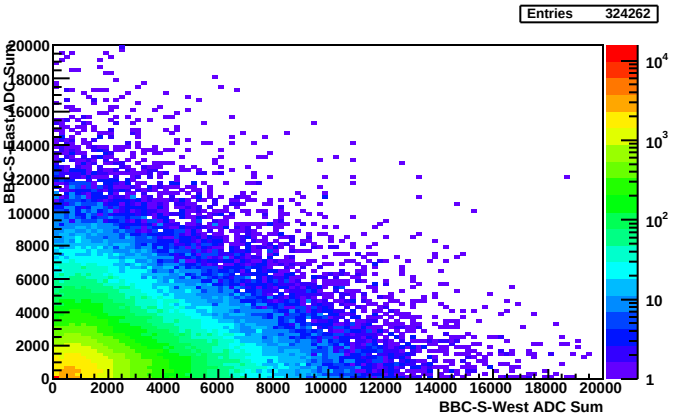

Entries 324262
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

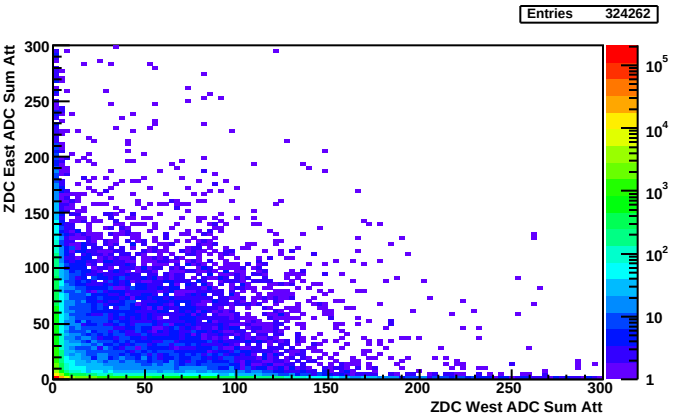

Entries 324262
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

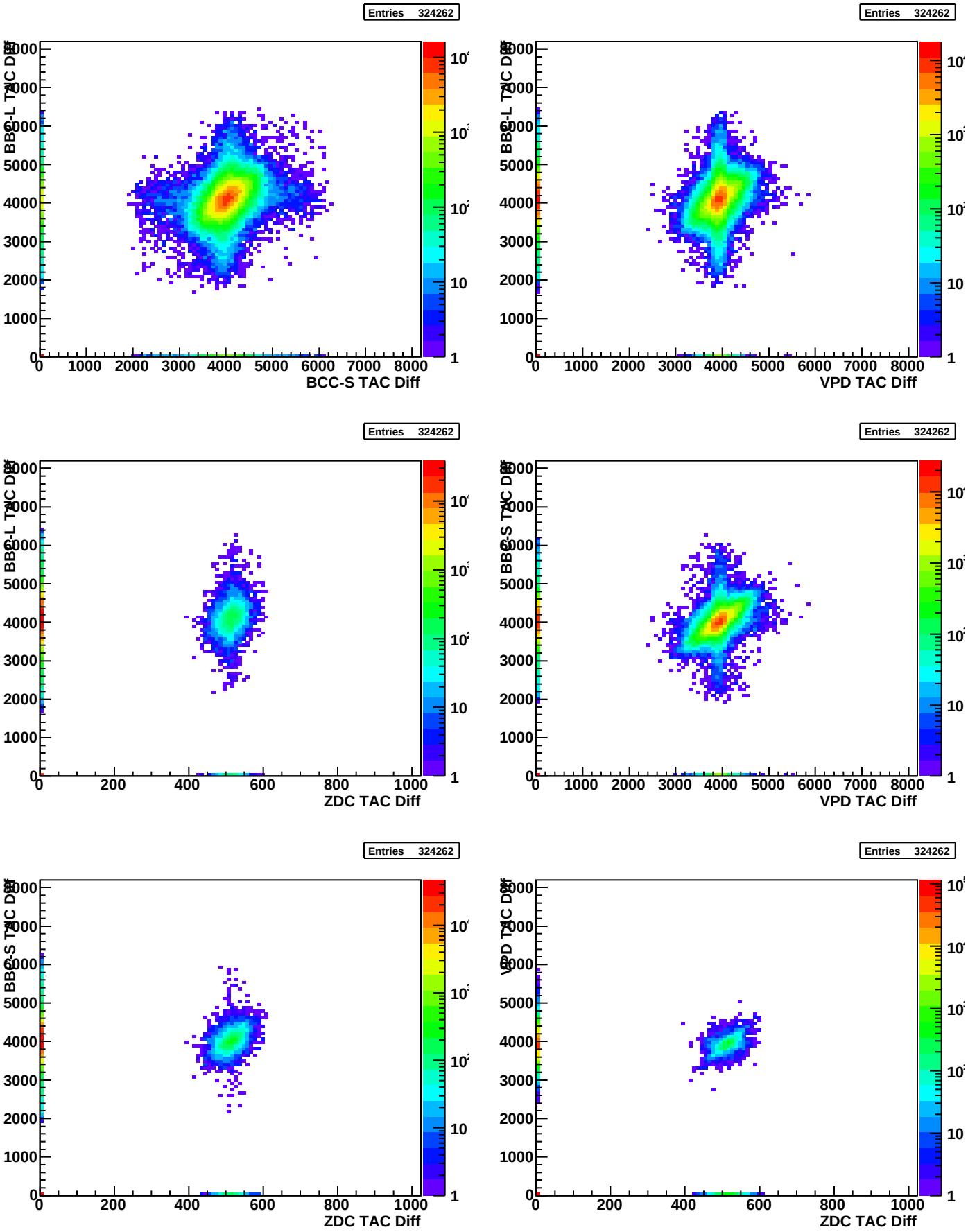

Entries 324262
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

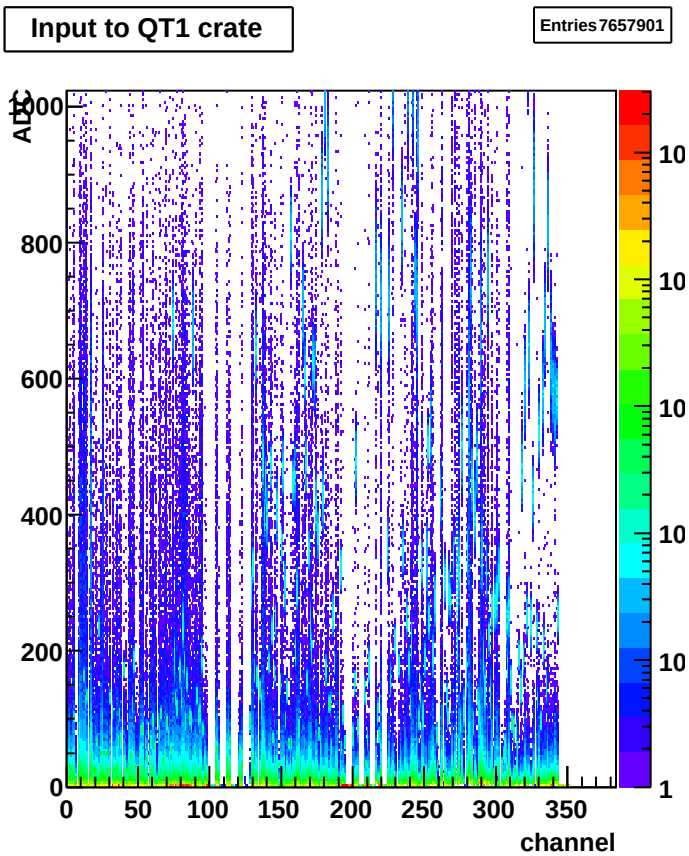

Entries 324262
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

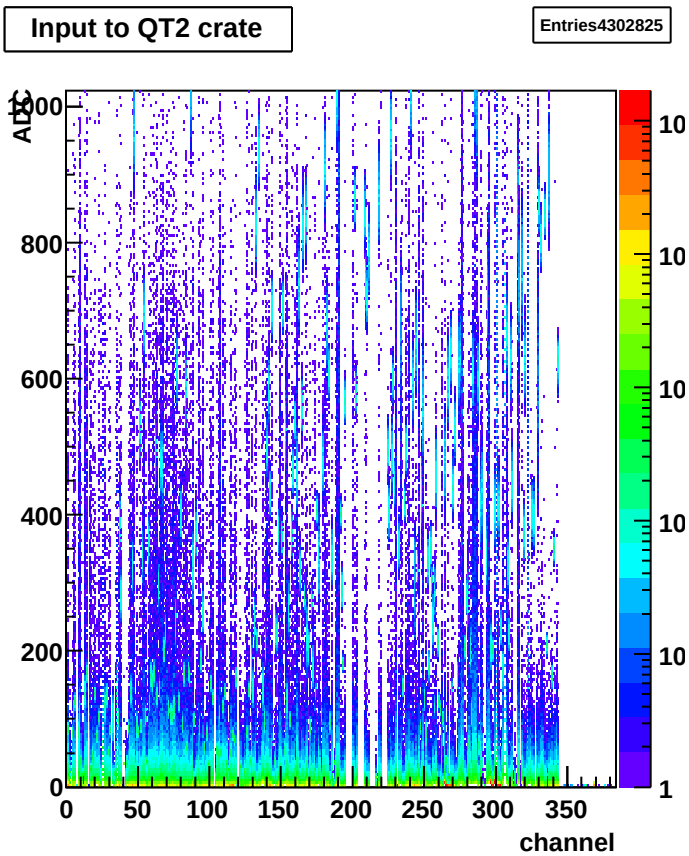

Entries 324262
BBC-L-East ADC Sum
BBC-L-West ADC Sum

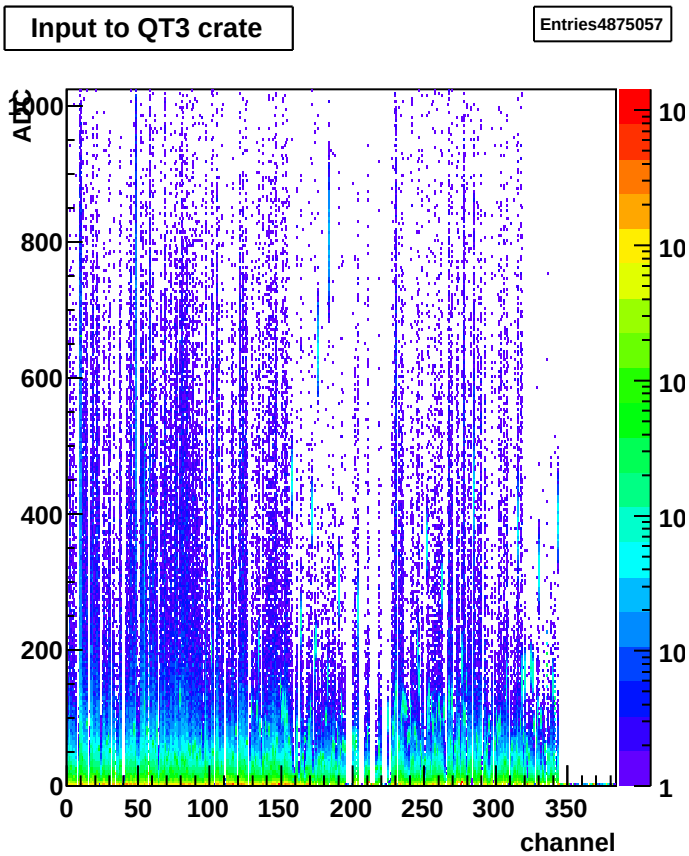

Entries 324262
BBC-S-East ADC Sum
BBC-S-West ADC Sum

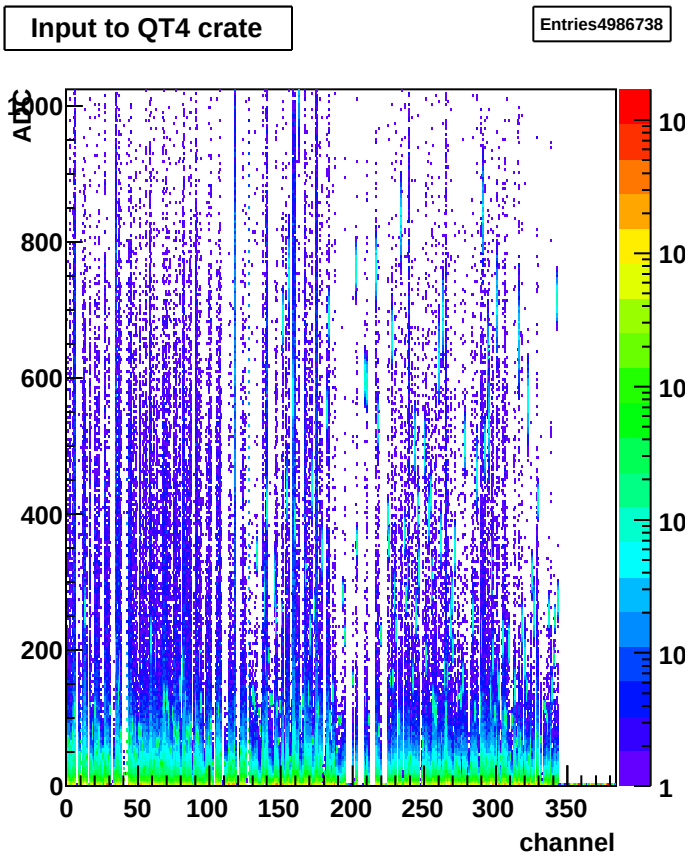

Entries 324262
ZDC East ADC Sum Att
ZDC West ADC Sum Att

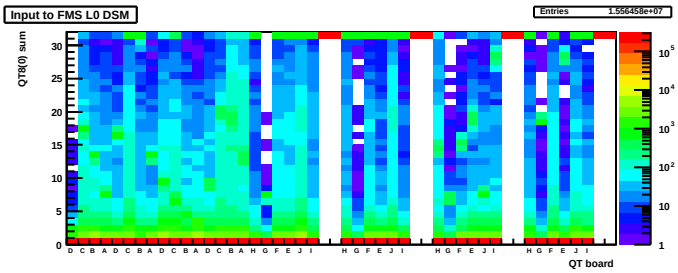

Entries 324262
BBC-L TAC Diff
BCC-S TAC Diff
Entries 324262
BBC-L TAC Diff
VPD TAC Diff
Entries 324262
BBC-L TAC Diff
ZDC TAC Diff
Entries 324262
BBC-S TAC Diff
VPD TAC Diff
Entries 324262
BBC-S TAC Diff
ZDC TAC Diff
Entries 324262
VPD TAC Diff
ZDC TAC Diff

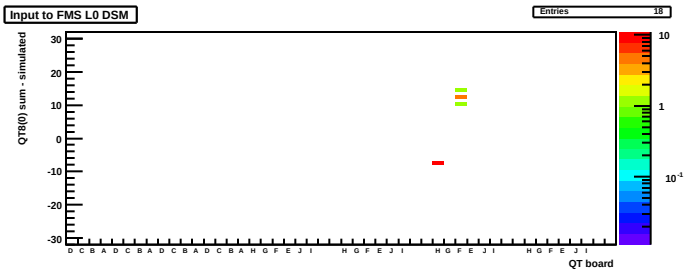

Input to QT1 crate
Entries7657901
ADC
channel

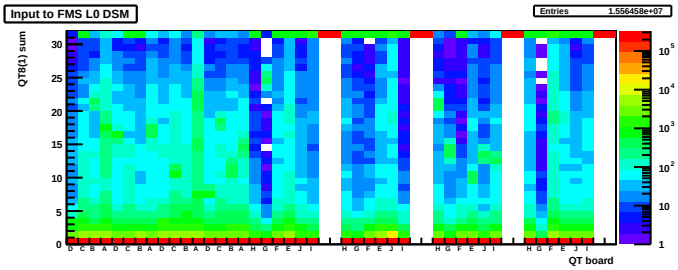

Input to QT2 crate
Entries4302825
ADC
channel

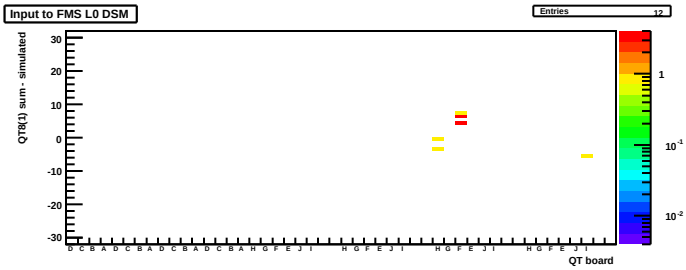

Input to QT3 crate
Entries4875057
ADC
channel

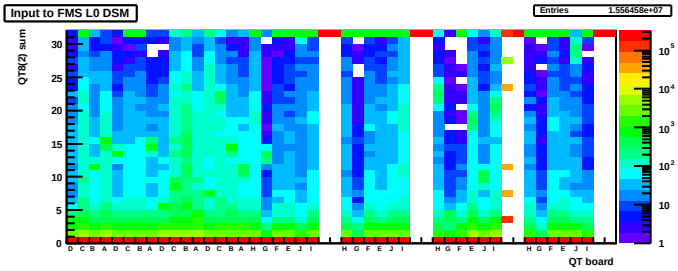

Input to QT4 crate
Entries4986738
ADC
channel

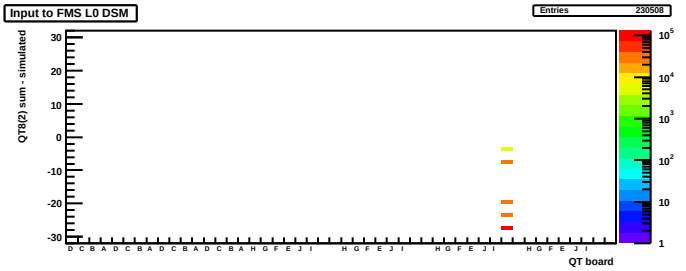

Input to FMS L0 DSM
Entries 1.556458e+07
QT8(0) sum
QT board

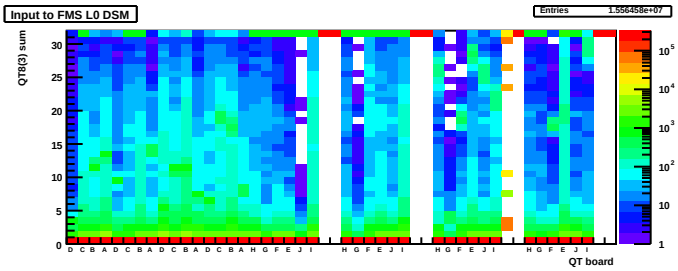

Input to FMS L0 DSM
Entries 18
QT8(0) sum - simulated
QT board

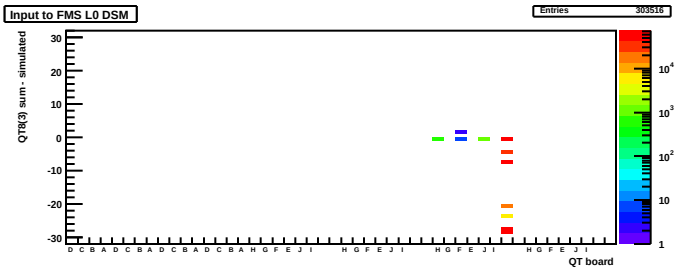

Input to FMS L0 DSM
Entries 1.556458e+07
QT8(1) sum
QT board

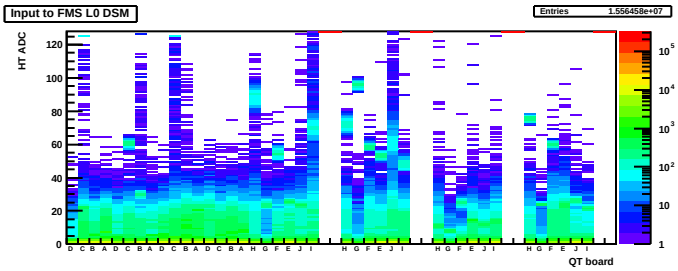

Input to FMS L0 DSM
Entries 12
QT8(1) sum - simulated
QT board

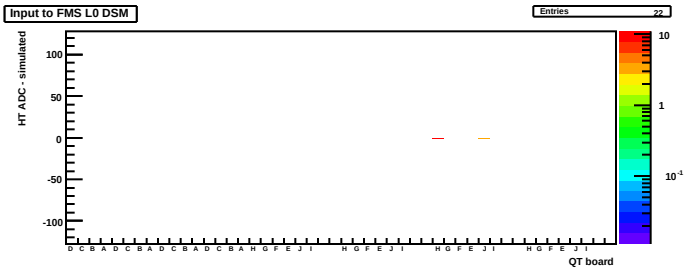

Input to FMS L0 DSM
Entries 1.556458e+07
QT8(2) sum
QT board

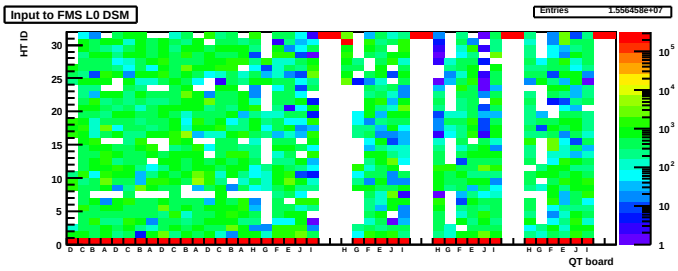

Input to FMS L0 DSM
Entries 230508
QT8(2) sum - simulated
QT board

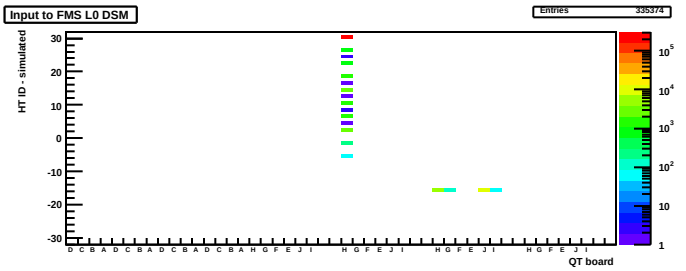

Input to FMS L0 DSM
Entries 1.556458e+07
QT8(3) sum
QT board

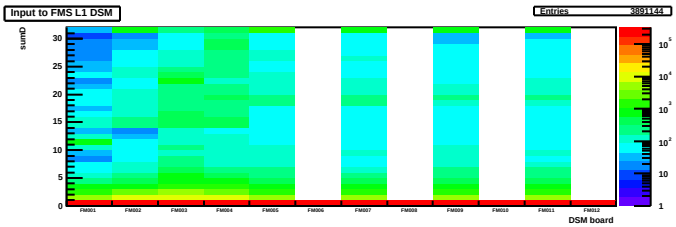

Input to FMS L0 DSM
Entries 303516
QT8(3) sum - simulated
QT board

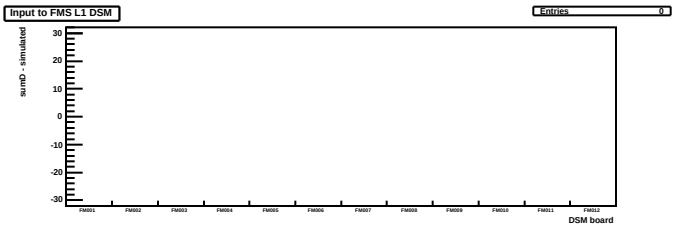

Input to FMS L0 DSM
Entries 1.556458e+07
HT ADC
QT board

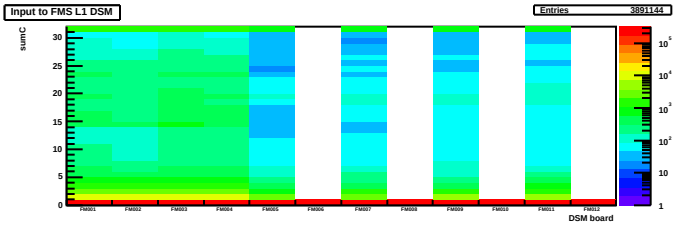

Input to FMS L0 DSM
Entries 22
HT ADC - simulated
QT board

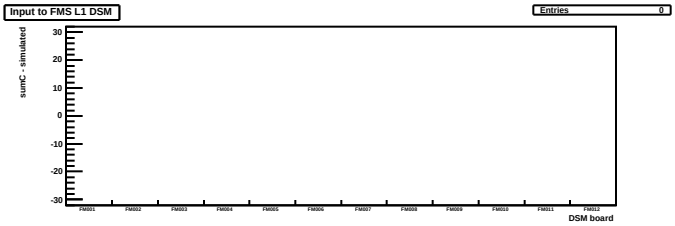

Input to FMS L0 DSM
Entries 1.556458e+07
HT ID
QT board

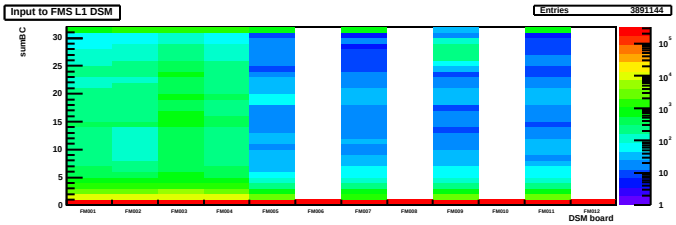

Input to FMS L0 DSM
Entries 335374
HT ID - simulated
QT board

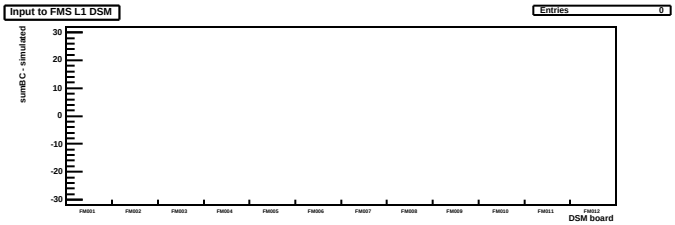

Input to FMS L1 DSM
Entries 3891144
sumD
DSM board

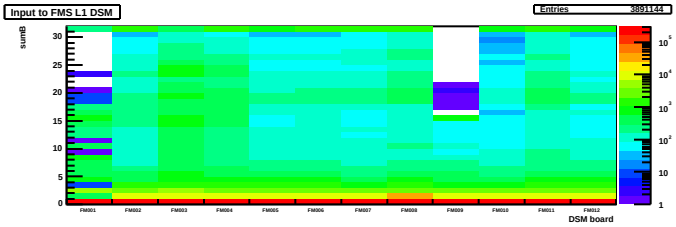

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

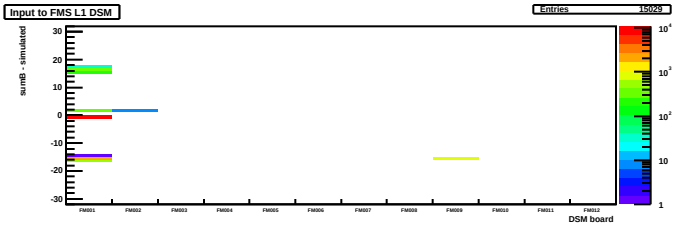

Input to FMS L1 DSM
Entries 3891144
sumC
DSM board

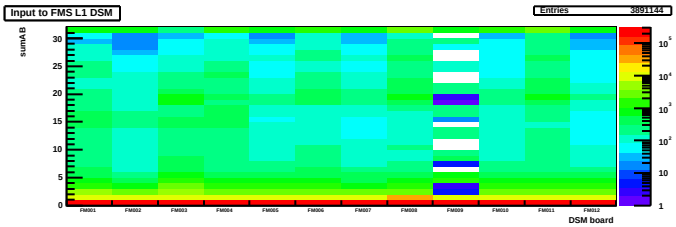

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

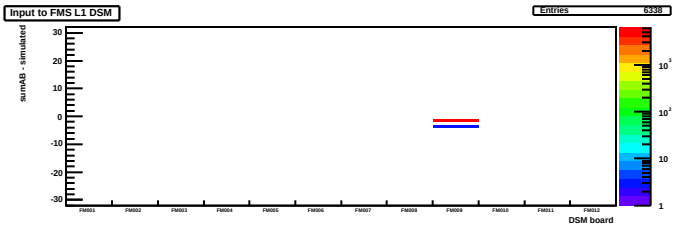

Input to FMS L1 DSM
Entries 3891144
sumBC
DSM board

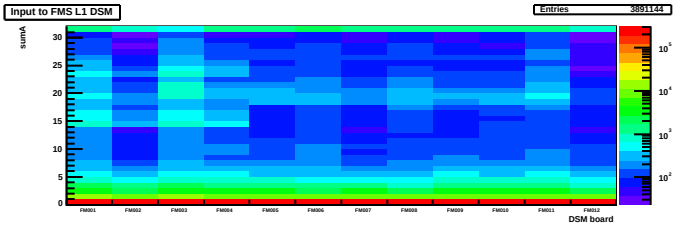

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

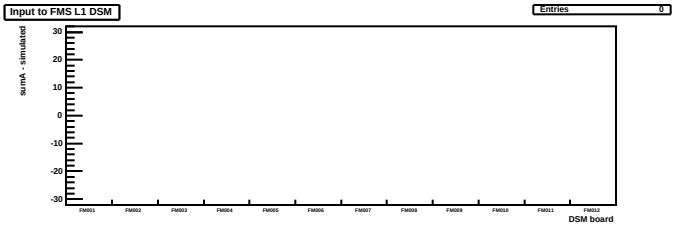

Input to FMS L1 DSM
Entries 3891144
sumB
DSM board

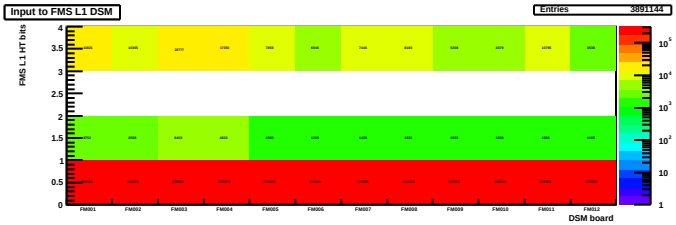

Input to FMS L1 DSM
Entries 15029
sumB - simulated
DSM board

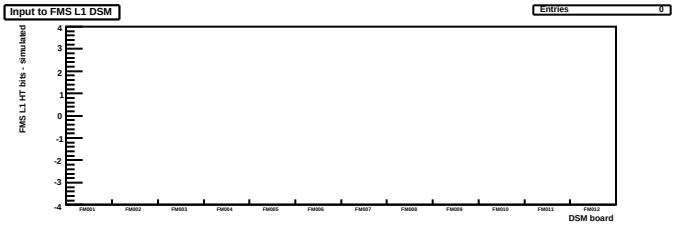

Input to FMS L1 DSM
Entries 3891144
sumAB
DSM board

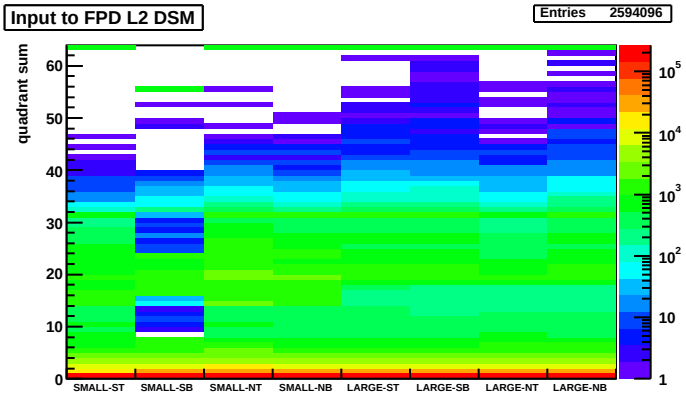

Input to FMS L1 DSM
Entries 6338
sumAB - simulated
DSM board

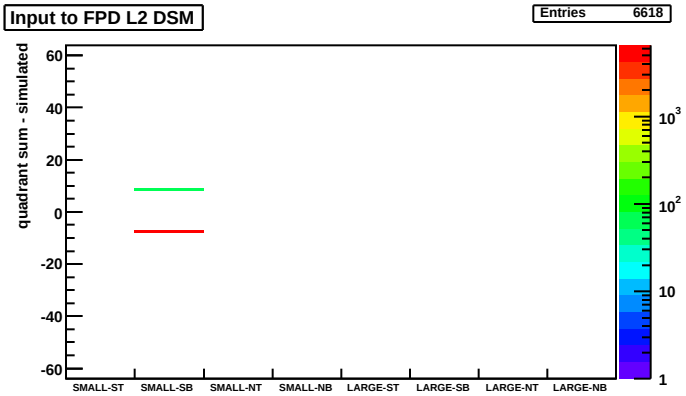

Input to FMS L1 DSM
Entries 3891144
sumA
DSM board

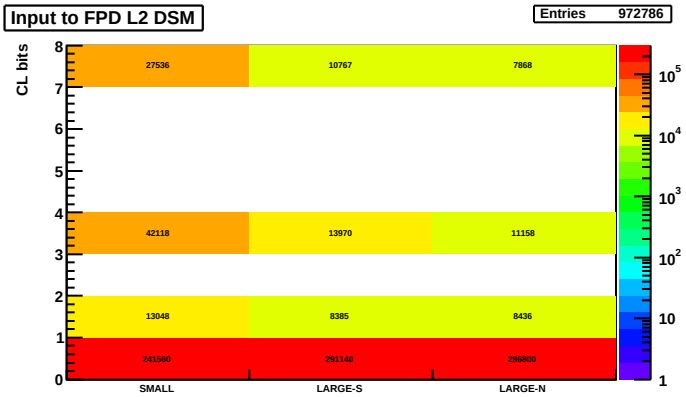

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

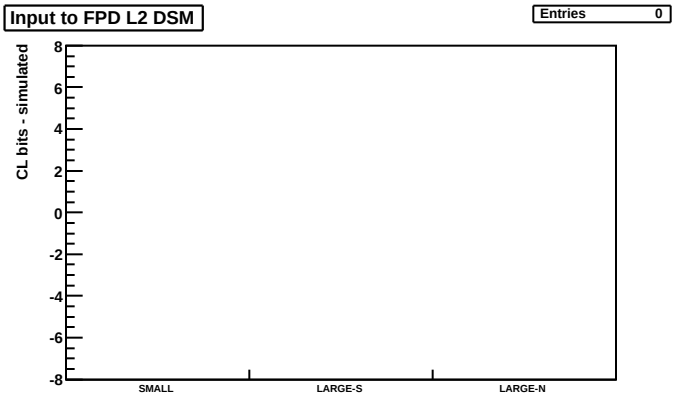

Input to FMS L1 DSM
Entries 3891144
FMS L1 HT bits
DSM board

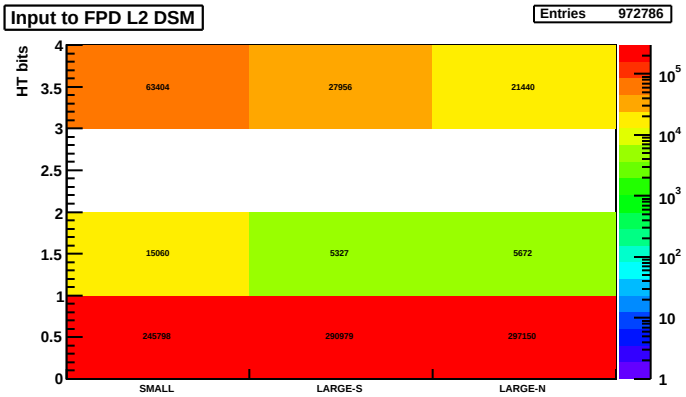

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

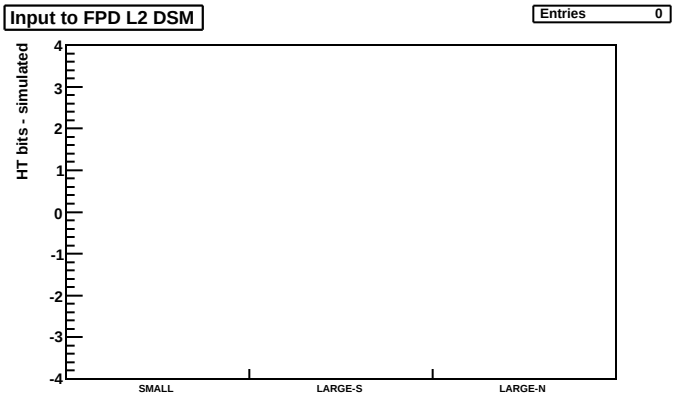

Input to FPD L2 DSM
Entries 2594096
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

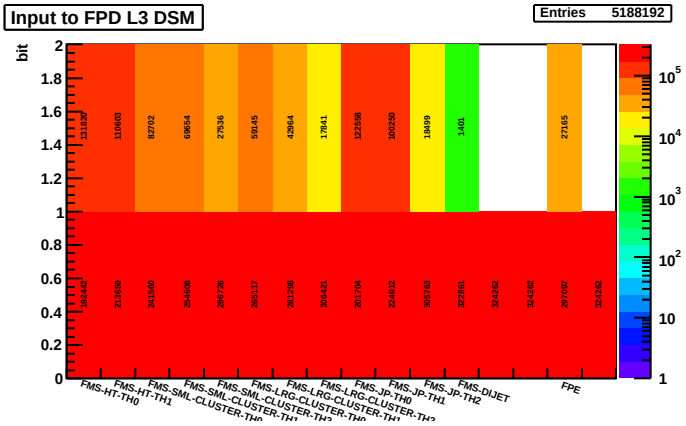

Input to FPD L2 DSM
Entries 6618
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

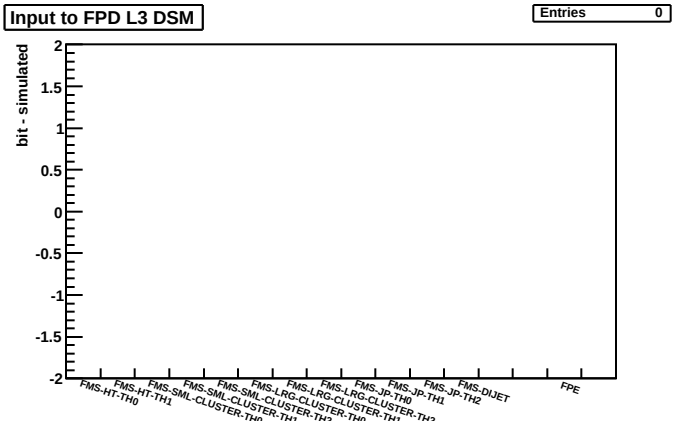

Input to FPD L2 DSM
Entries 972786
CL bits
27536
10767
7868
42118
13970
11158
13048
8385
8436
241560
291140
296800
SMALL
LARGE-S
LARGE-N

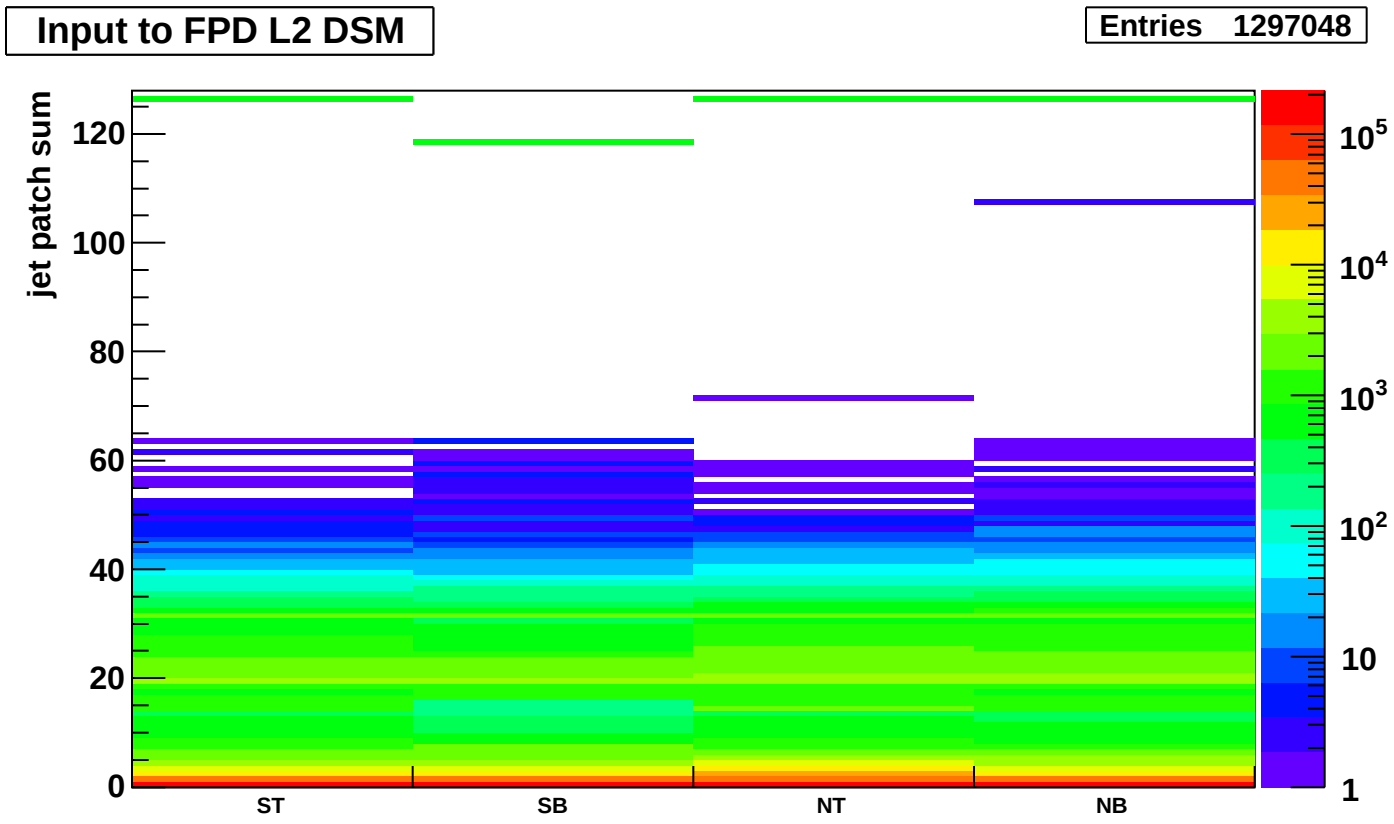

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

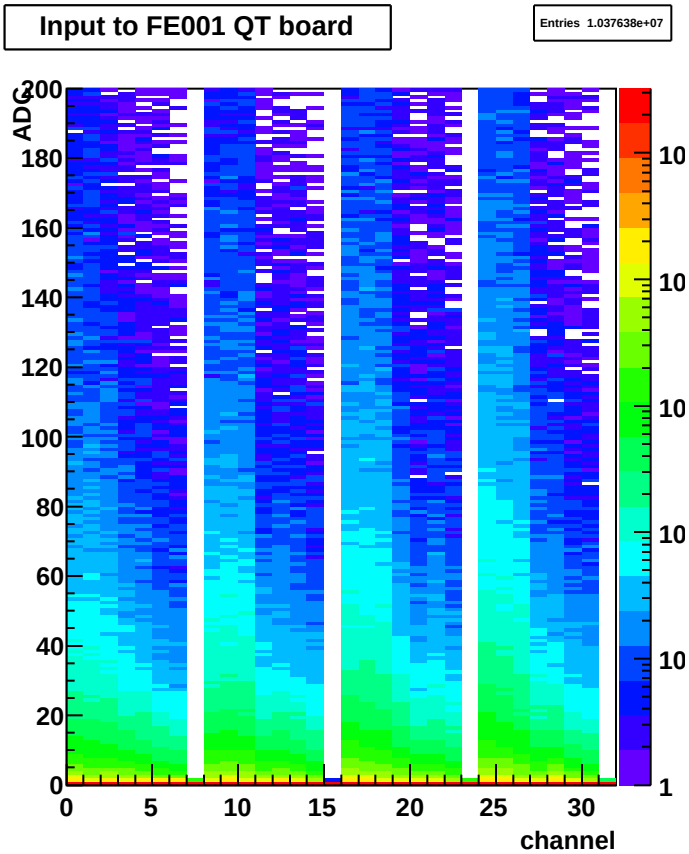

Input to FPD L2 DSM
Entries 972786
HT bits
63404
27956
21440
15060
5327
5672
245798
290979
297150
SMALL
LARGE-S
LARGE-N

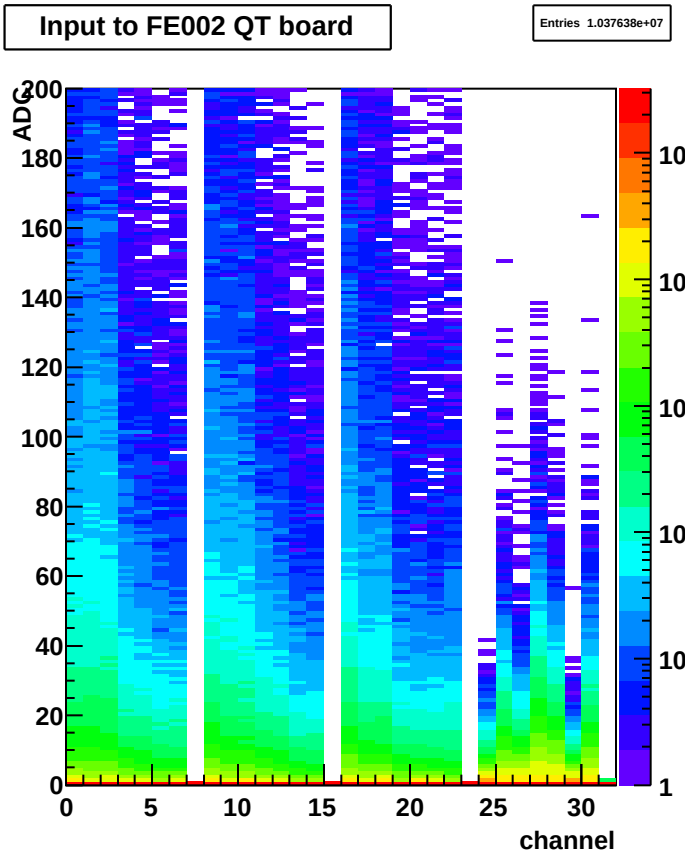

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

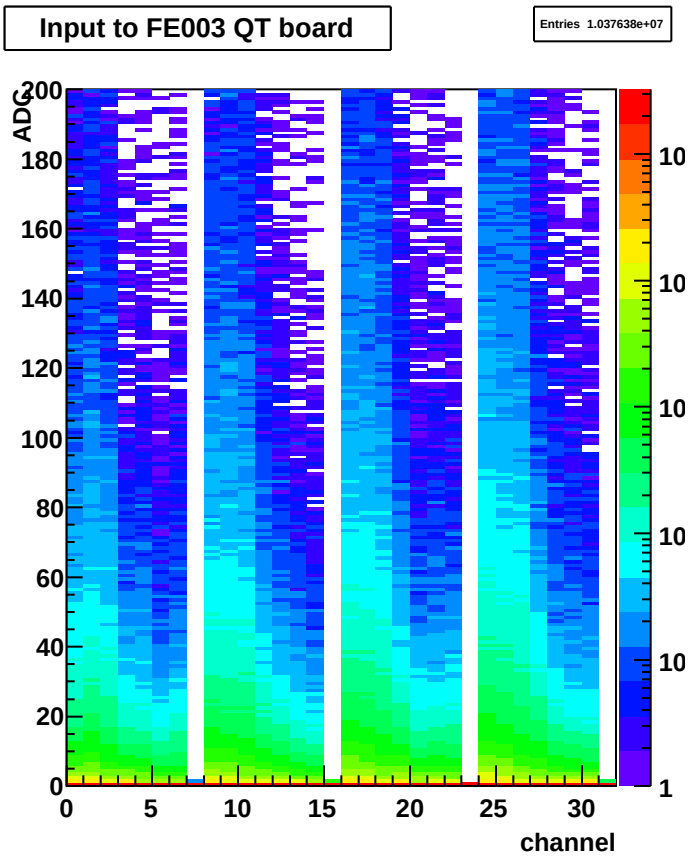

Input to FPD L3 DSM
Entries 5188192
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

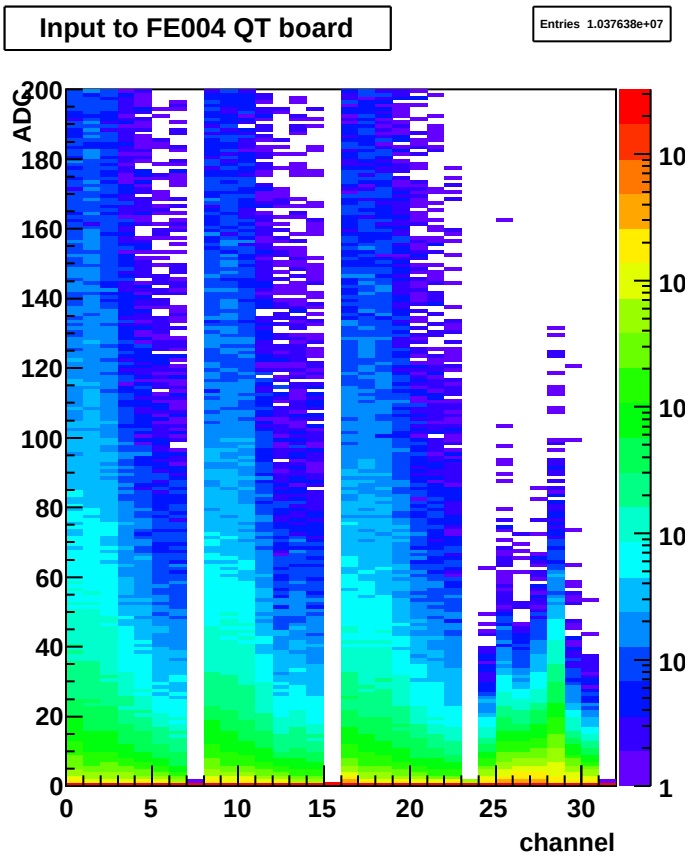

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

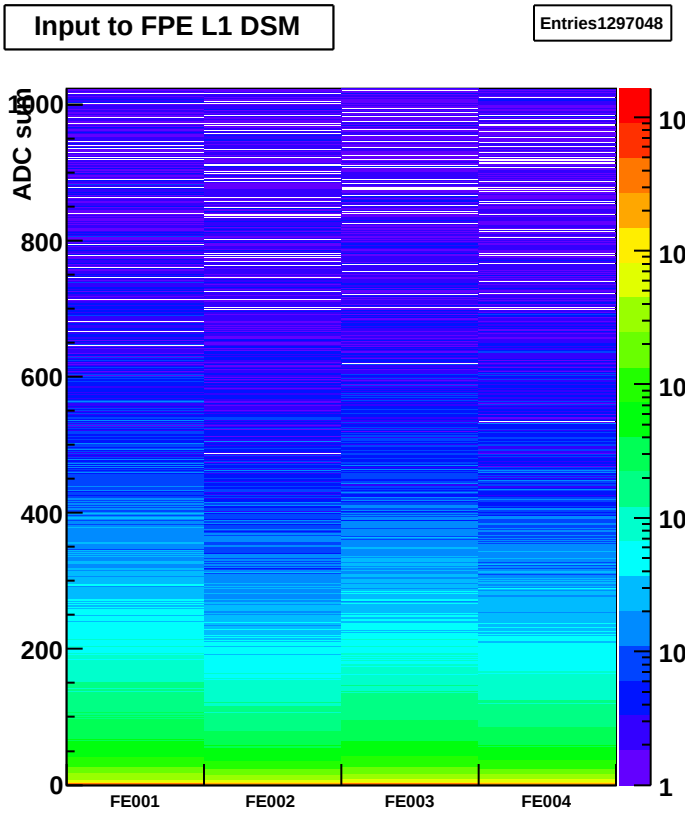

Input to FPD L2 DSM
Entries 1297048
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

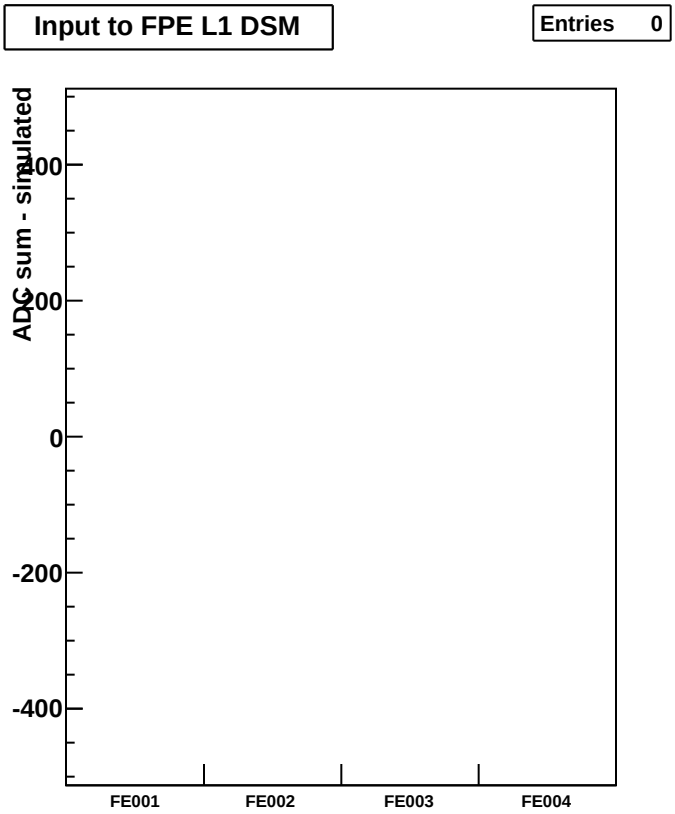

Input to FE001 QT board
Entries 1.037638e+07
ADC
channel

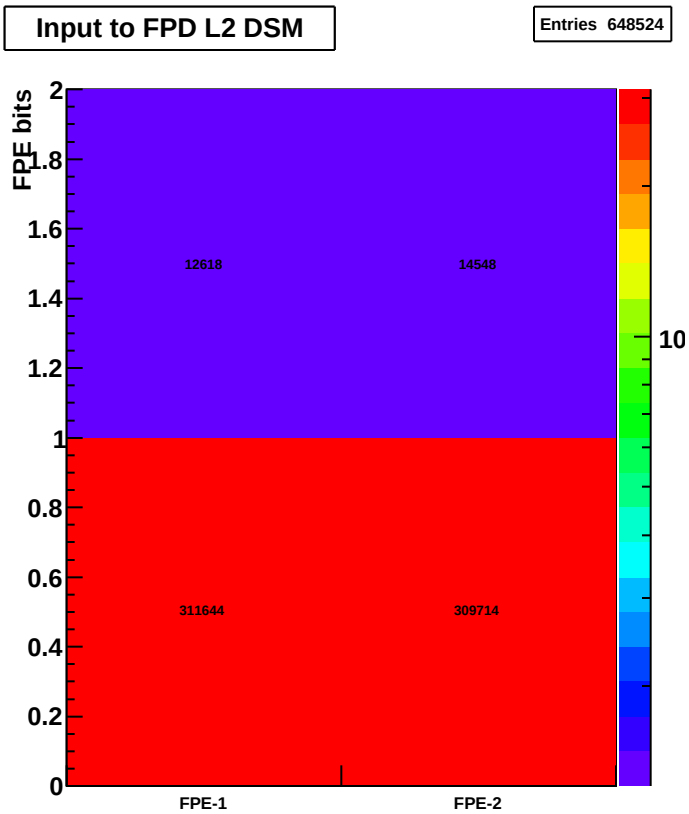

Input to FE002 QT board
Entries 1.037638e+07
ADC
channel

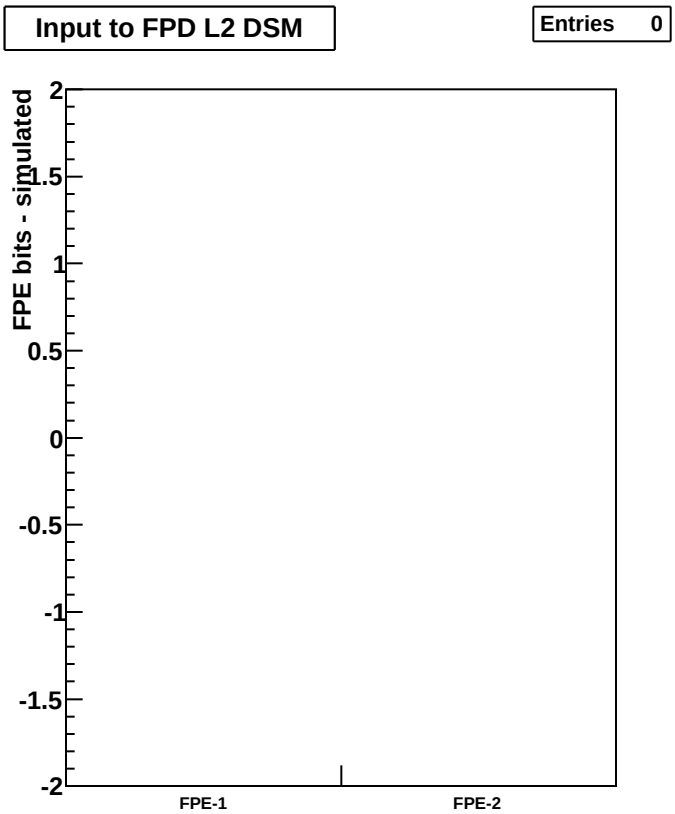

Input to FE003 QT board
Entries 1.037638e+07
ADC
channel


Input to FE004 QT board
Entries 1.037638e+07
ADC
channel


Input to FPE L1 DSM
Entries1297048
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 648524
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
12618
14548
311644
309714
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2