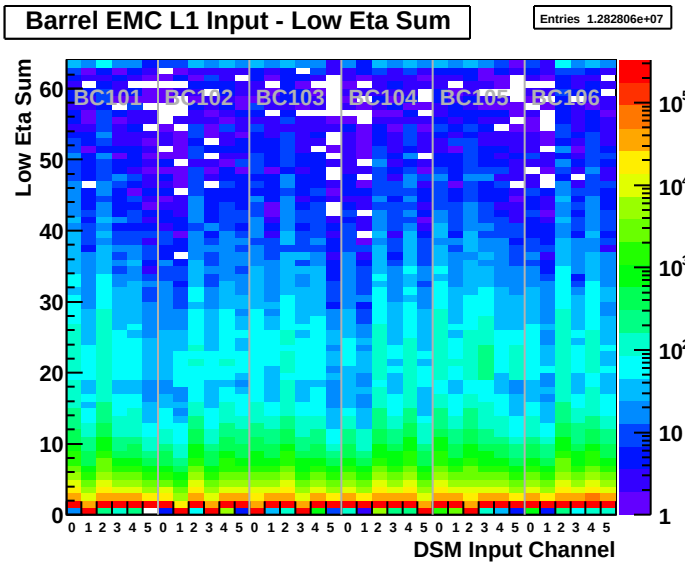
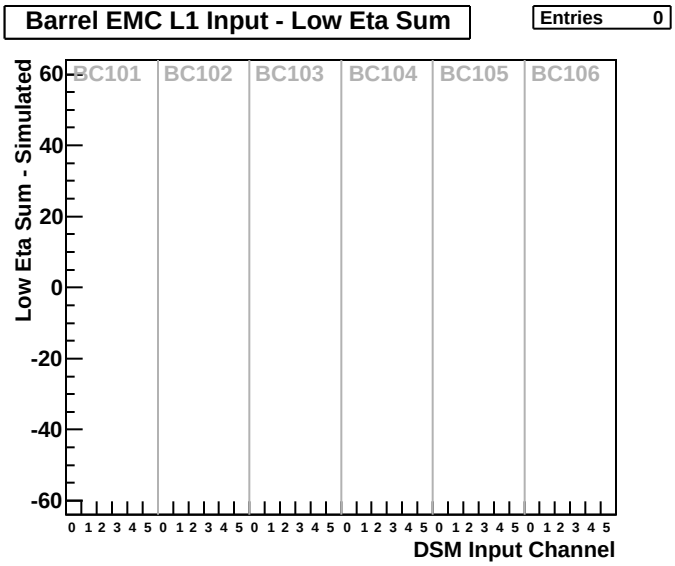
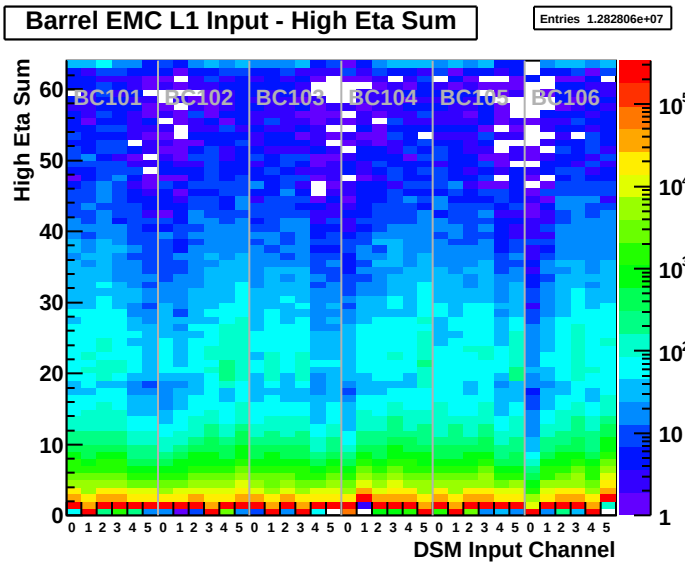
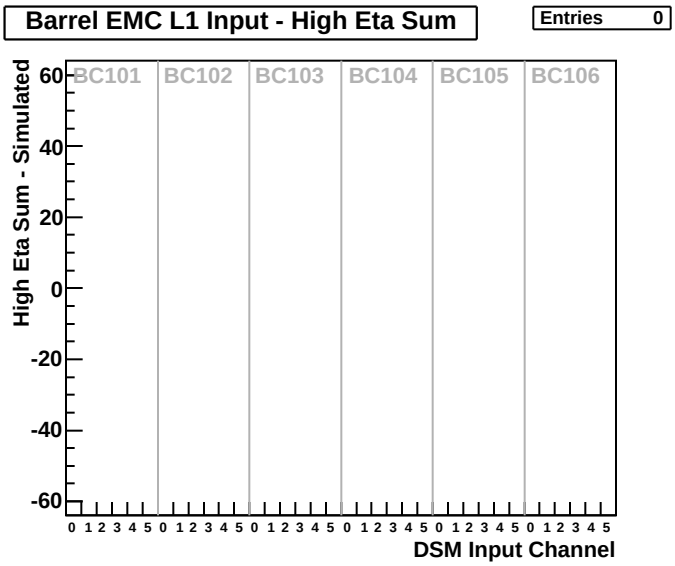
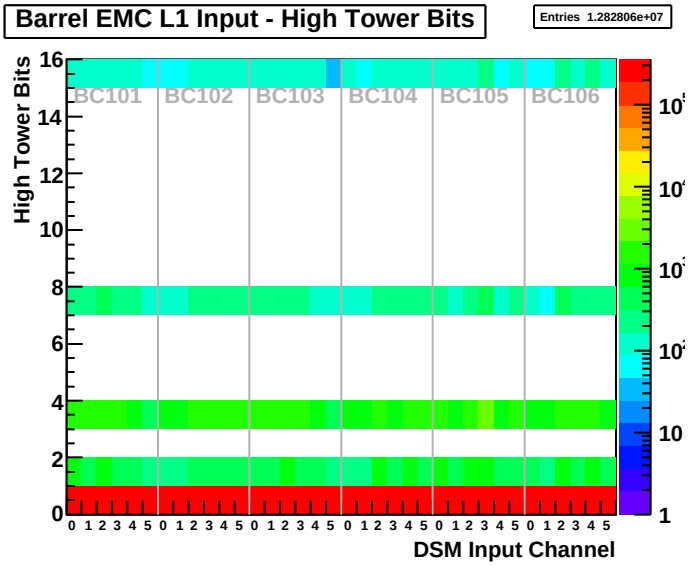
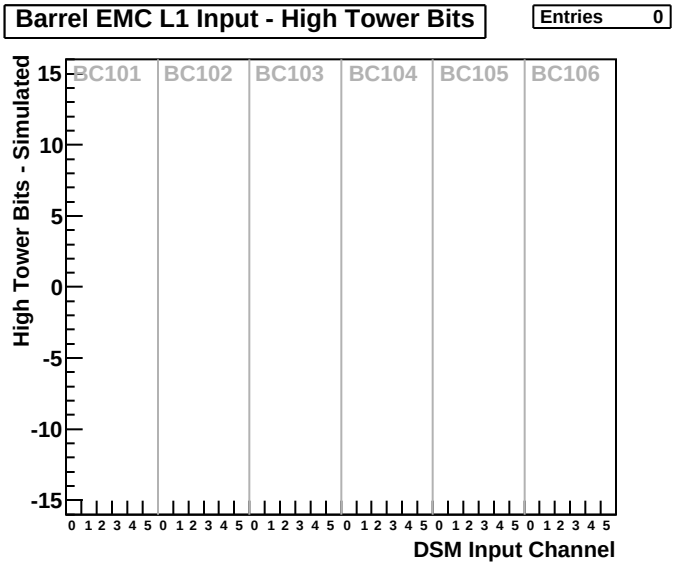

Barrel EMC L1 Input - Low Eta Sum
Entries 1.282806e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

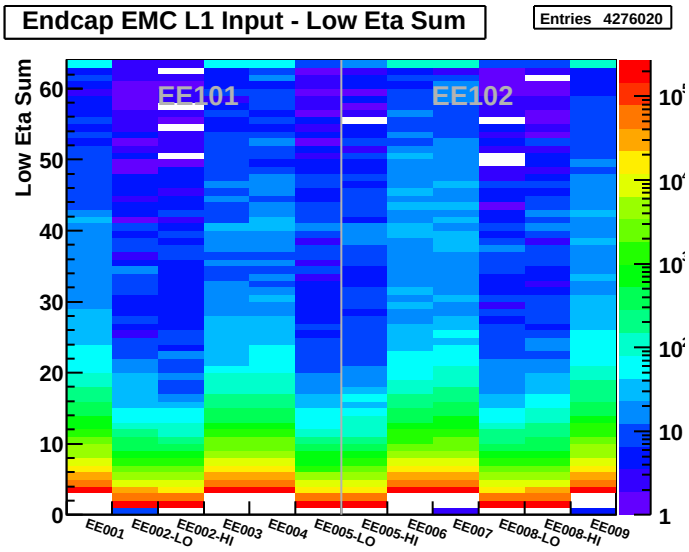

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

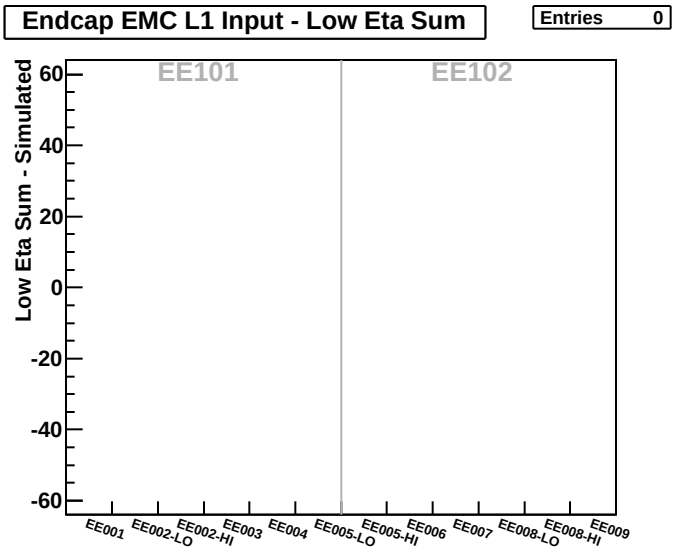

Barrel EMC L1 Input - High Eta Sum
Entries 1.282806e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

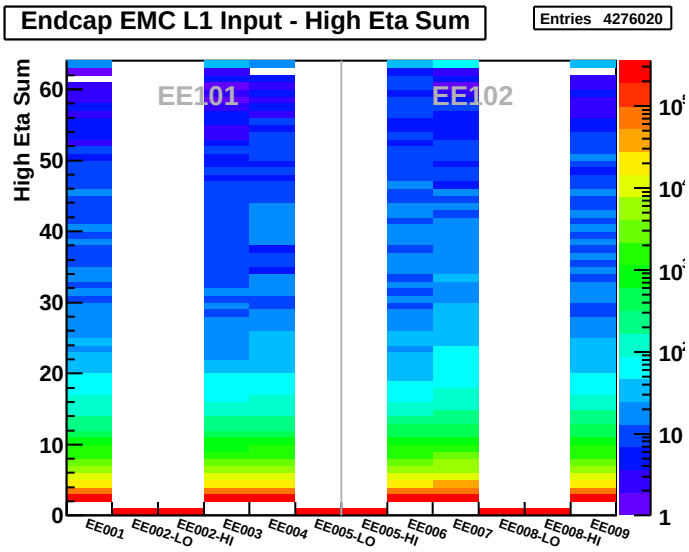

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

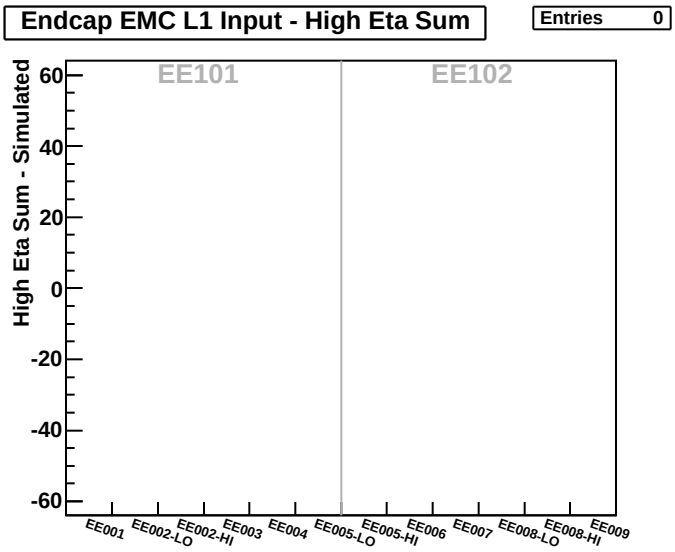

Barrel EMC L1 Input - High Tower Bits
Entries 1.282806e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

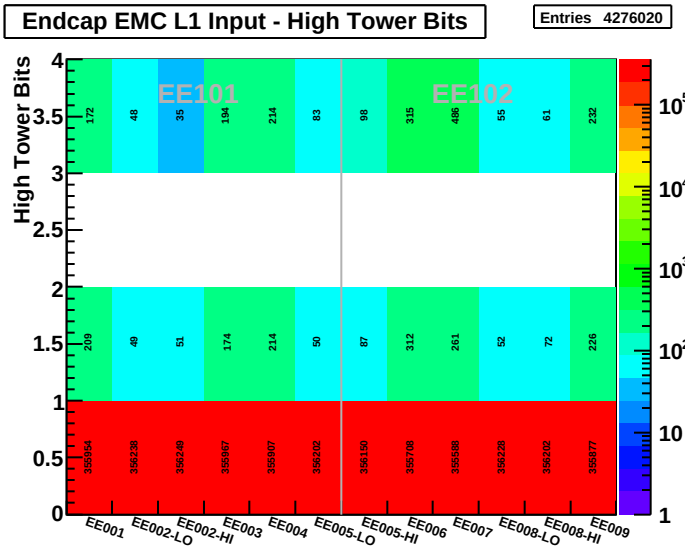

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

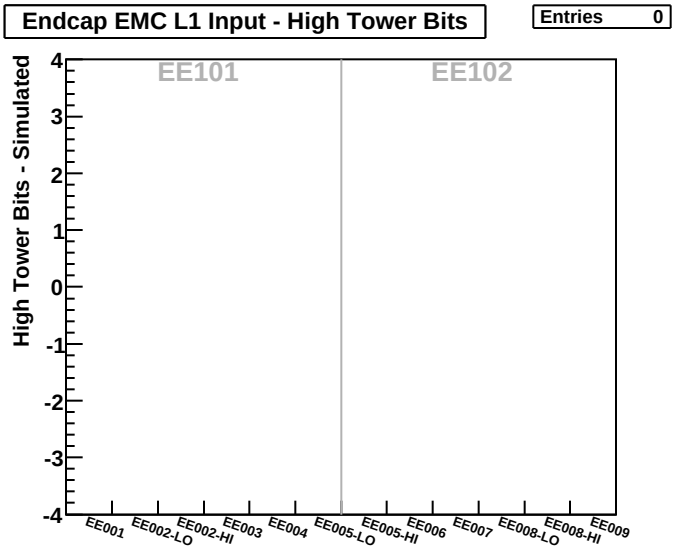

Endcap EMC L1 Input - Low Eta Sum
Entries 4276020
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

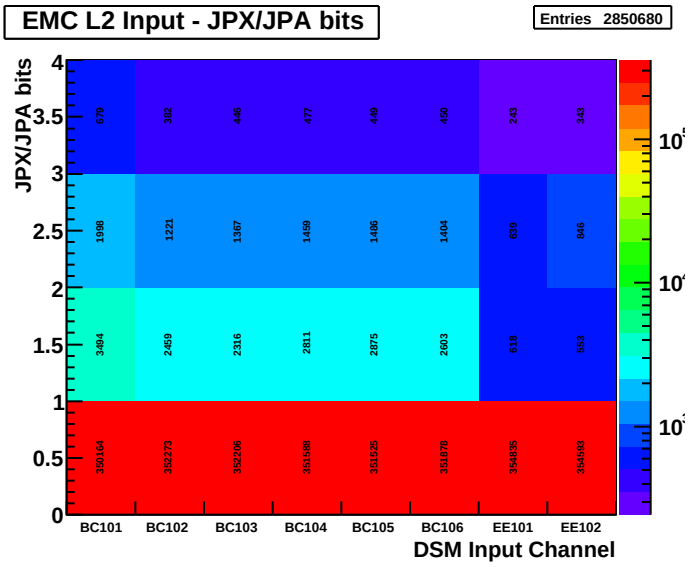

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

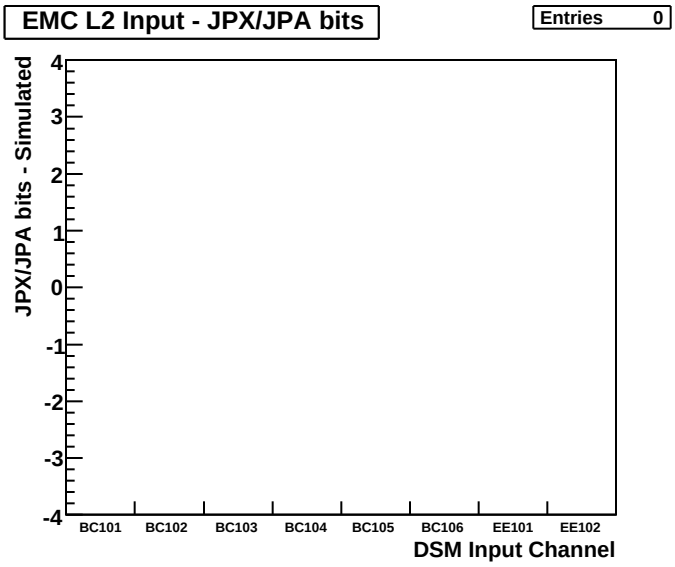

Endcap EMC L1 Input - High Eta Sum
Entries 4276020
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

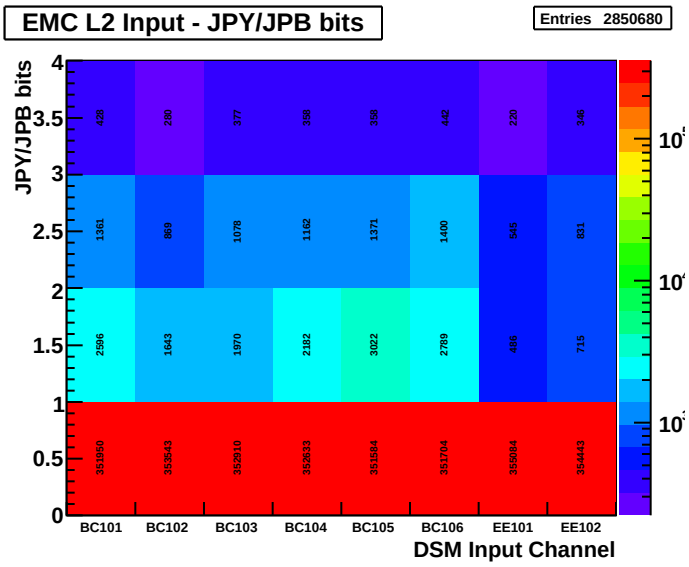

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

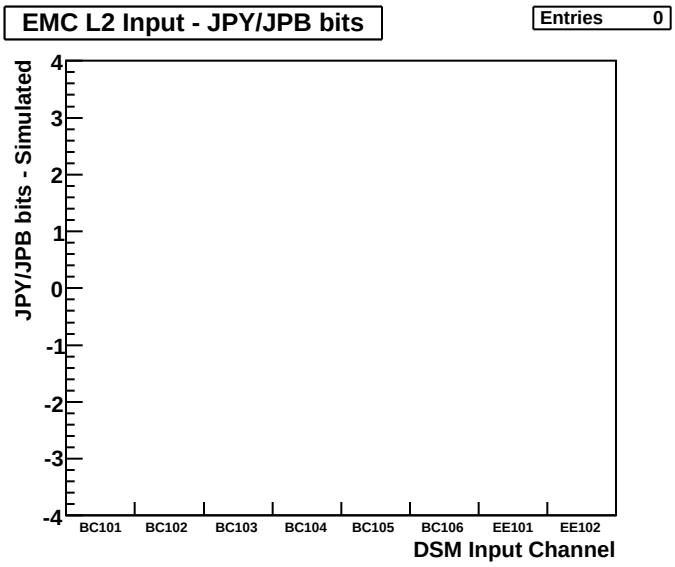

Endcap EMC L1 Input - High Tower Bits
Entries 4276020
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

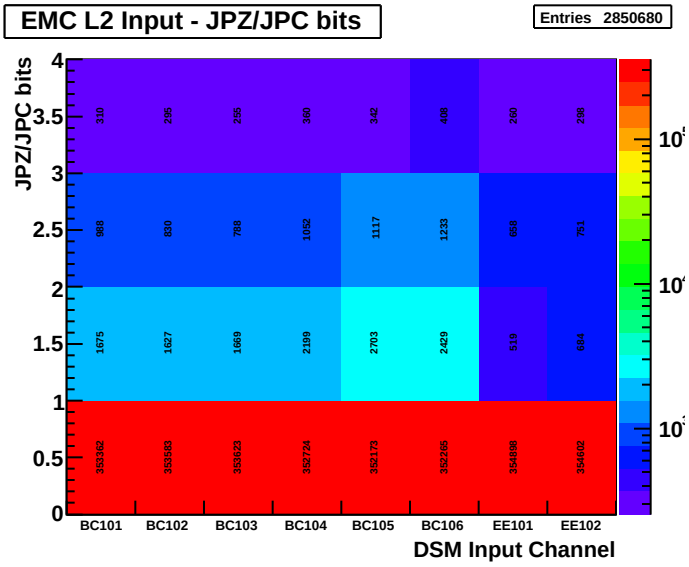

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

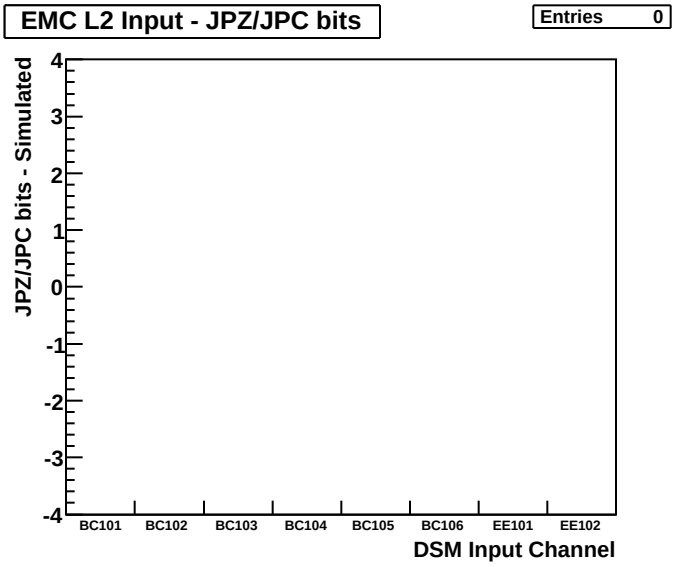

EMC L2 Input - JPX/JPA bits
Entries 2850680
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
679 382 446 477 449 450 243 343
1998 1221 1367 1459 1486 1404 639 846
3494 2459 2316 2811 2875 2603 618 553
350164 352273 352206 351588 351525 351878 354835 354503

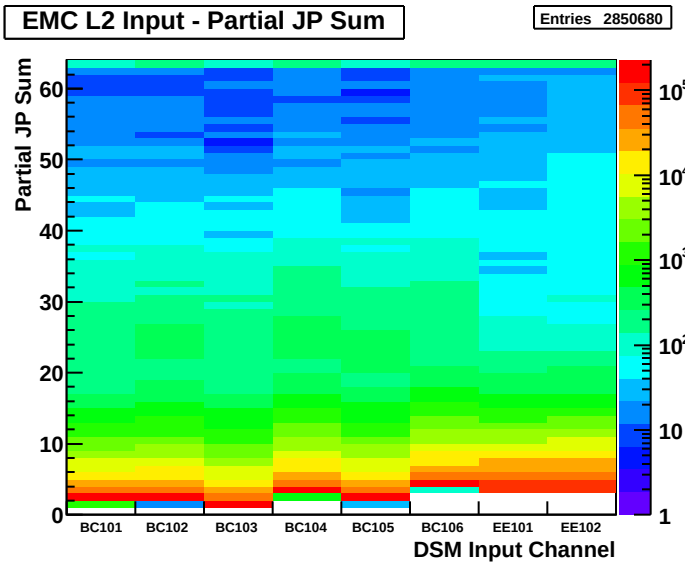

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

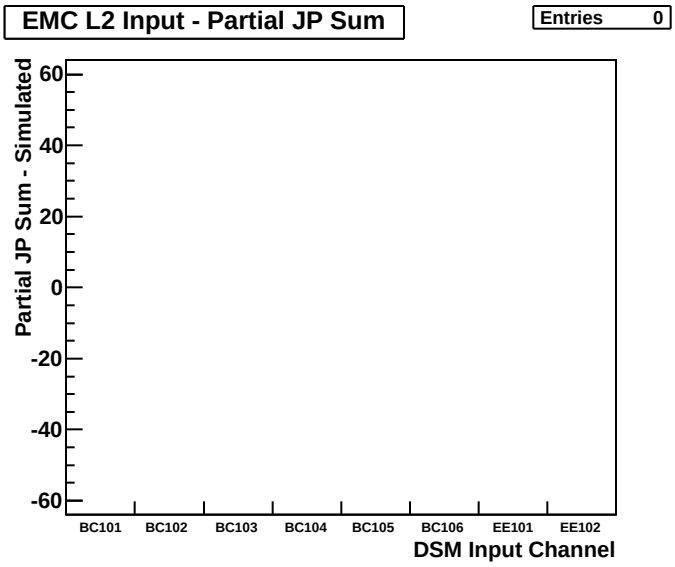

EMC L2 Input - JPY/JPB bits
Entries 2850680
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
426 280 377 358 358 442 220 346
1361 869 1078 1162 1371 1400 545 831
2596 1643 1970 2182 3022 2789 486 715
353950 353543 353910 353833 353584 353704 355084 354443

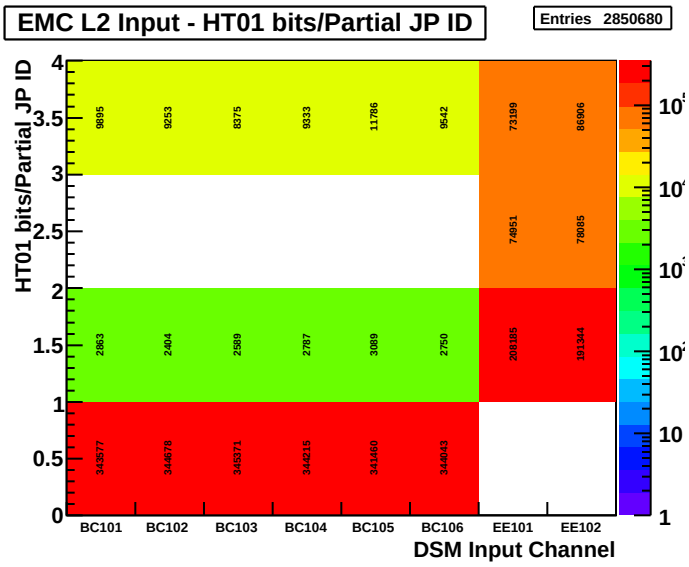

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

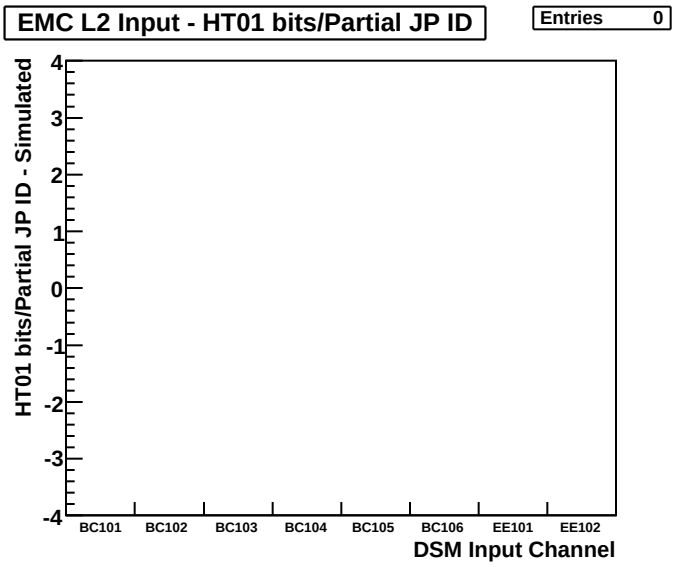

EMC L2 Input - JPZ/JPC bits
Entries 2850680
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
310 295 255 360 342 408 260 288
988 830 788 1052 1117 1233 658 751
1675 1627 1669 2199 2703 2429 519 634
353362 353583 353823 352724 352173 352265 354898 354602

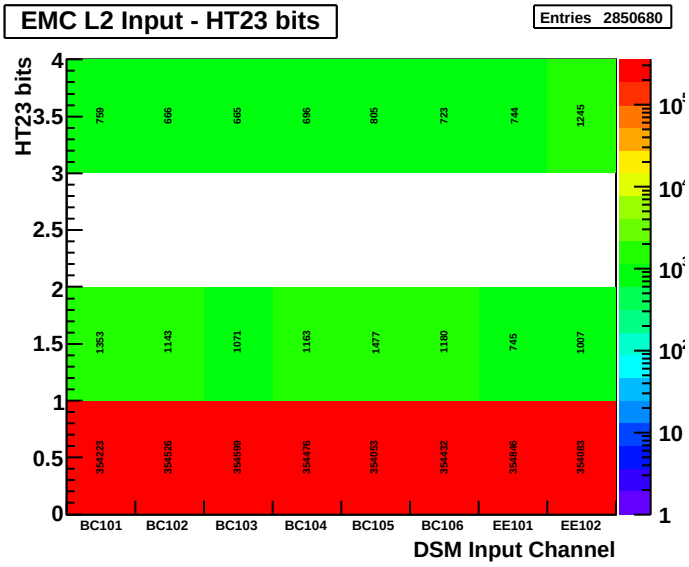

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

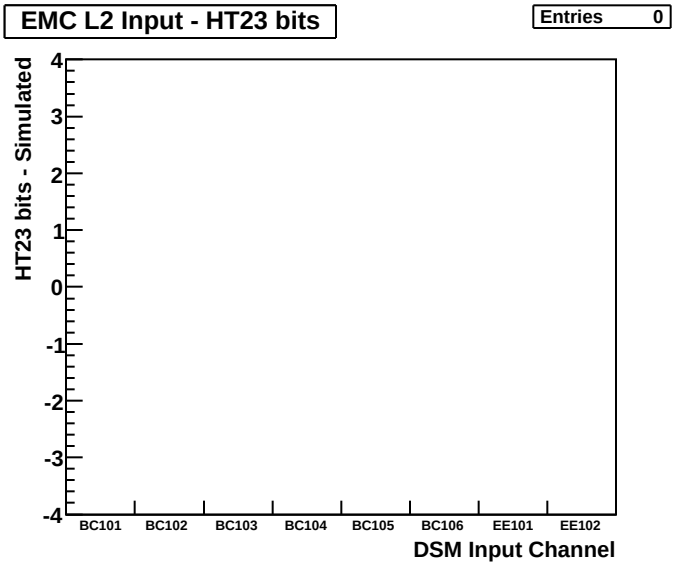

EMC L2 Input - Partial JP Sum
Entries 2850680
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

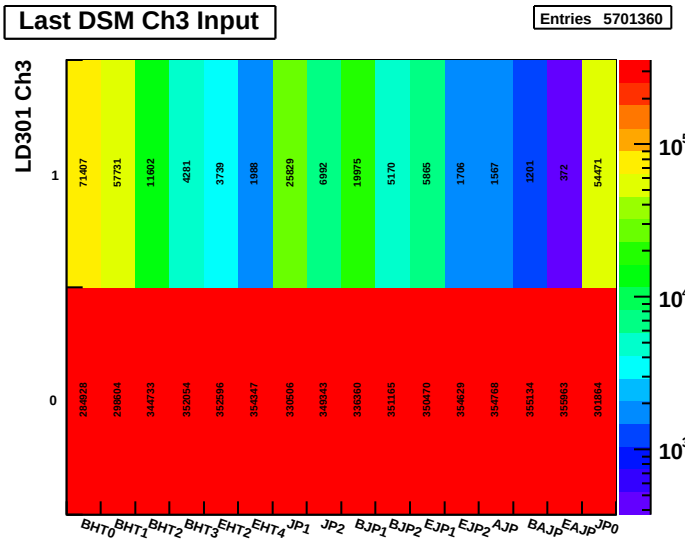

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

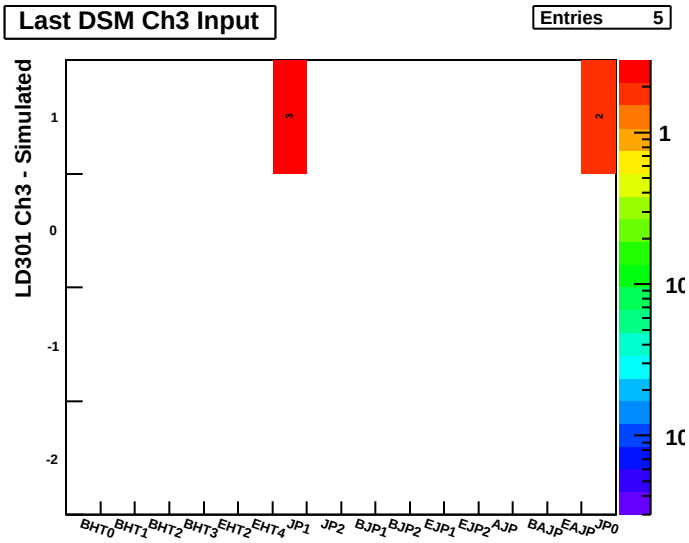

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2850680
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

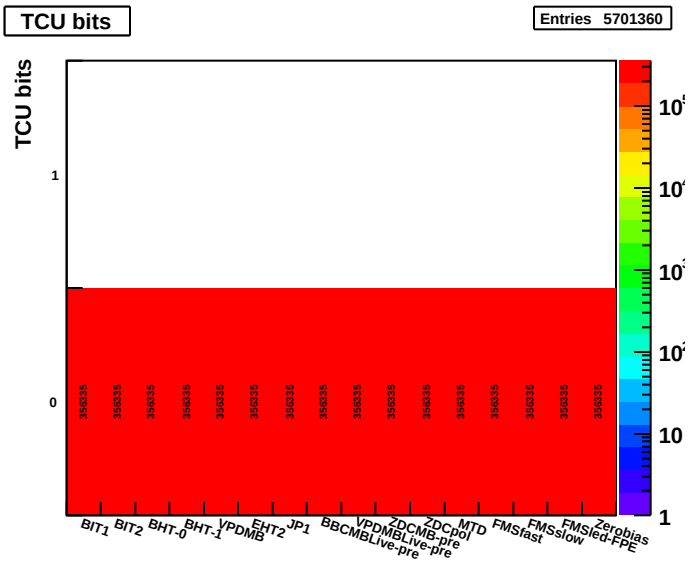

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

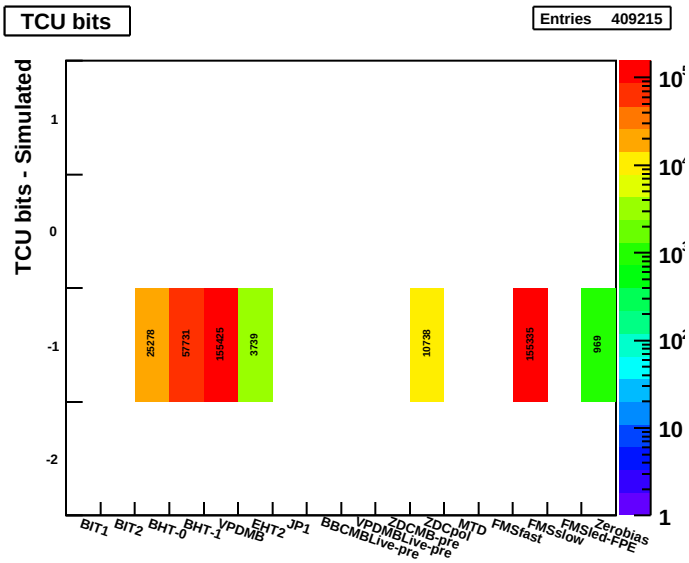

EMC L2 Input - HT23 bits
Entries 2850680
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

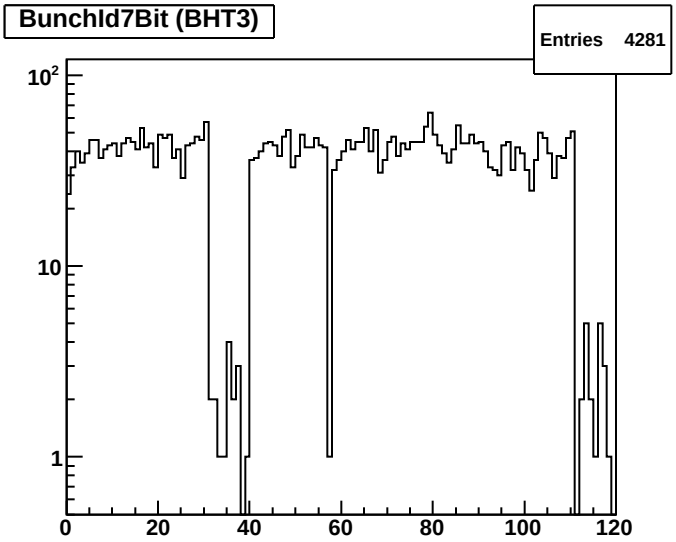

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

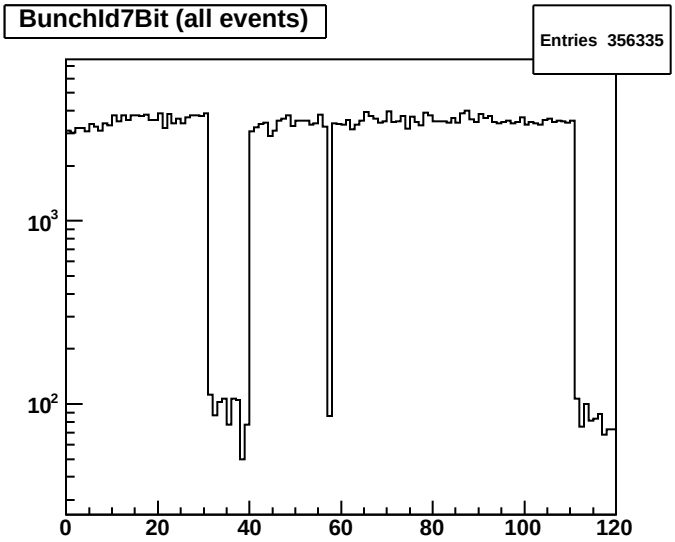

Last DSM Ch3 Input
Entries 5701360
LD301 Ch3
71407 57731 11602 4281 3739 1988 25829 6992 19975 5170 5865 1706 1567 1201 372 54471
284928 298604 344733 352054 352596 354347 330506 340943 336360 351165 350470 354629 354768 355134 355963 301864
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

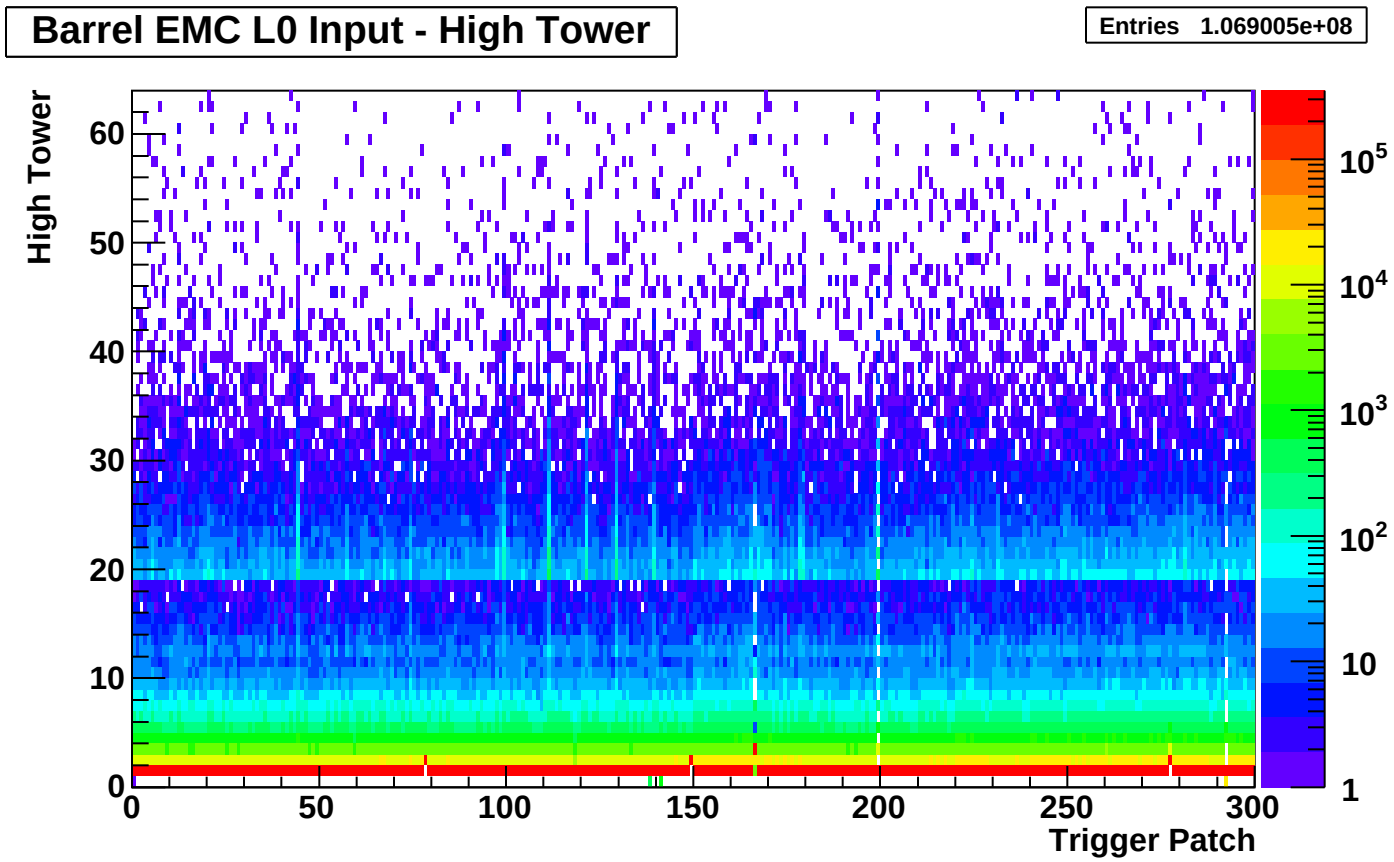

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
3
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

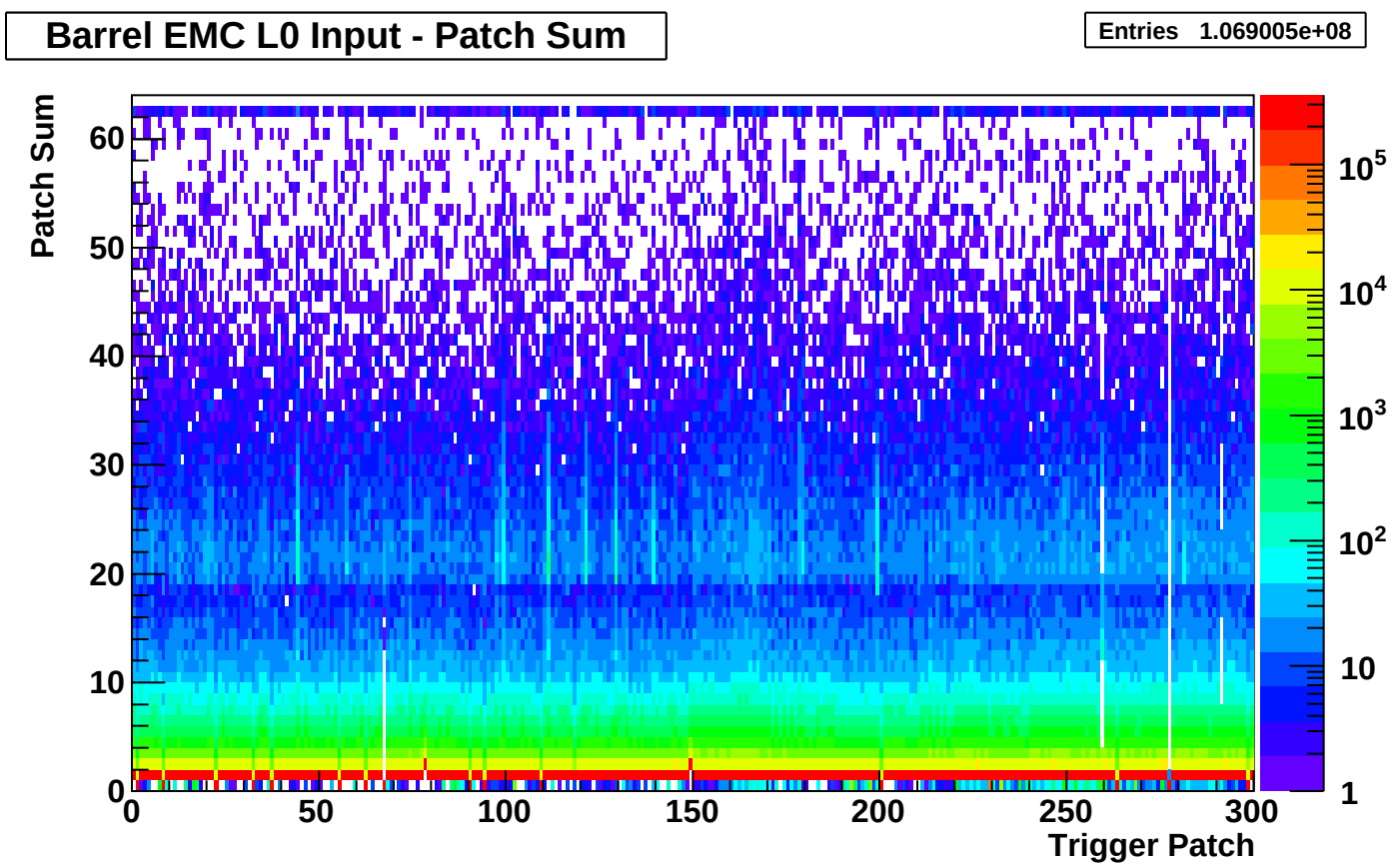

TCU bits
Entries 5701360
TCU bits
356335
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

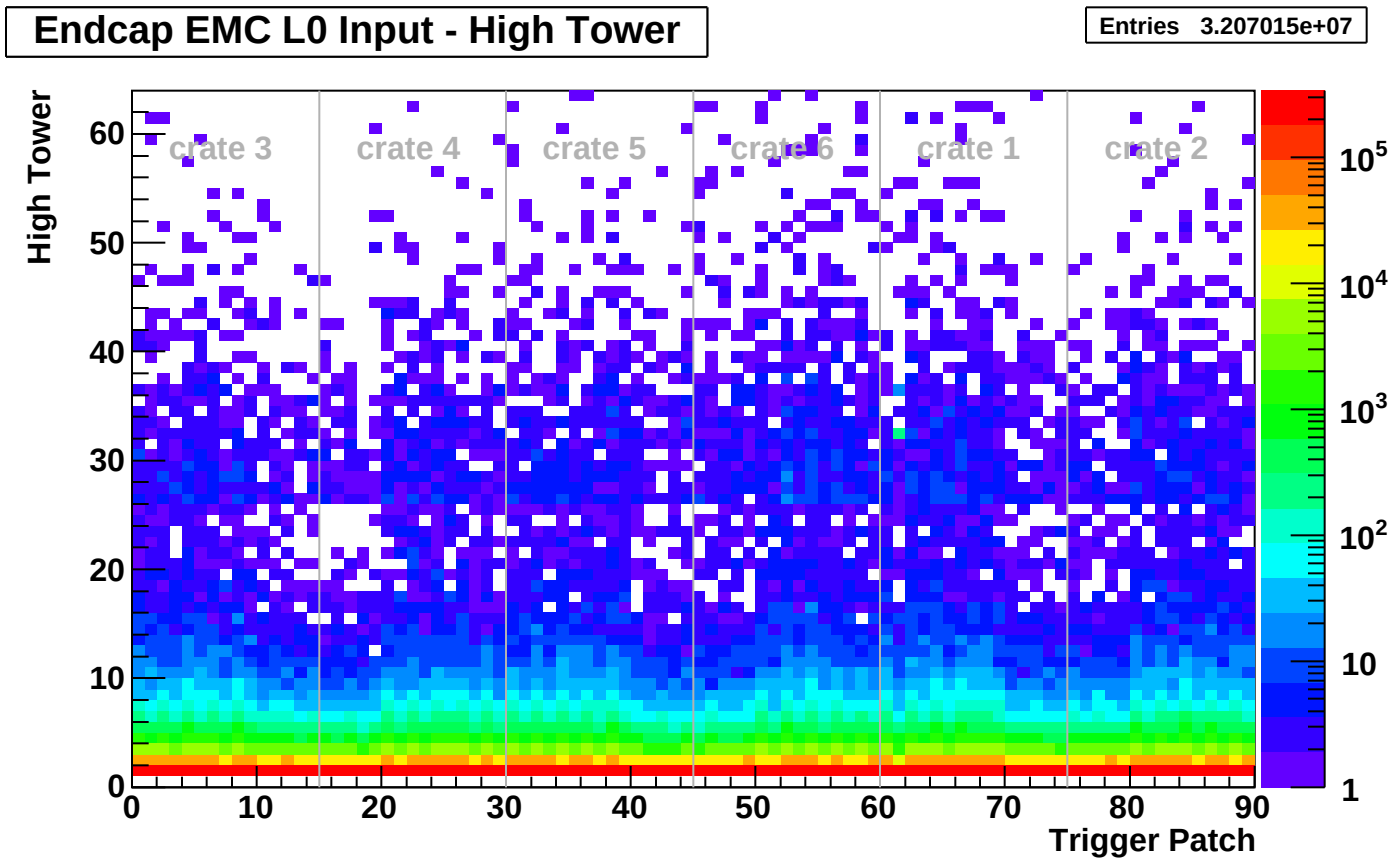

TCU bits
Entries 409215
TCU bits - Simulated
25278 5731 155425 3739 10738 155335 969
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

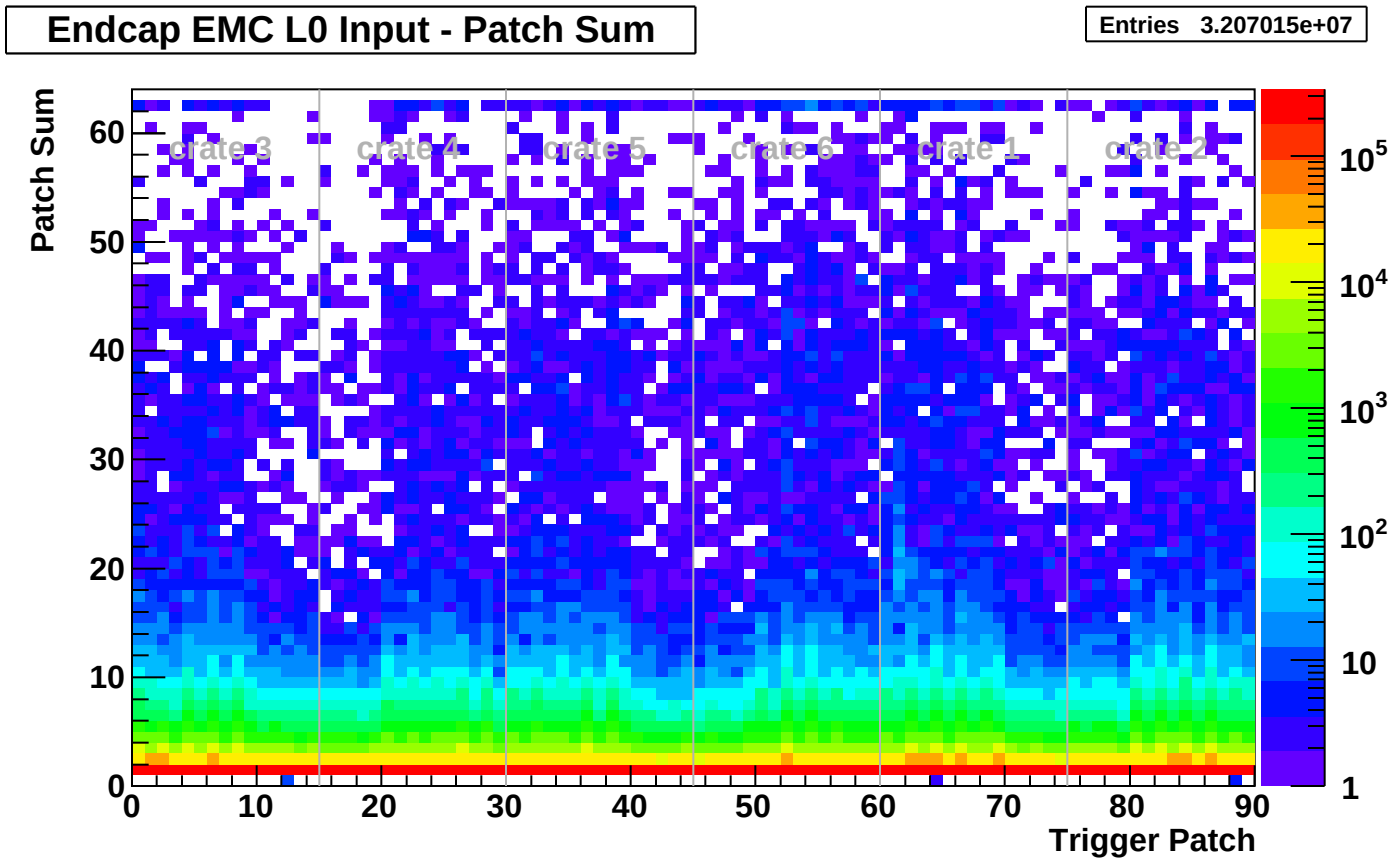

BunchId7Bit (BHT3)
Entries 4281

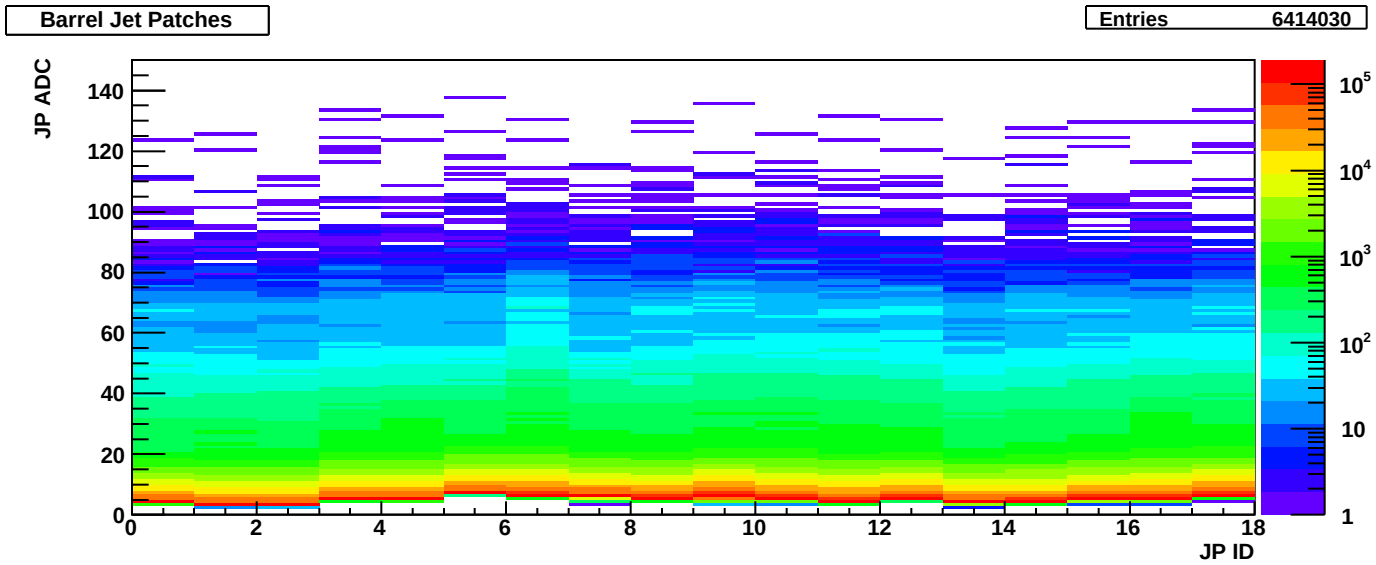

BunchId7Bit (all events)
Entries 356335

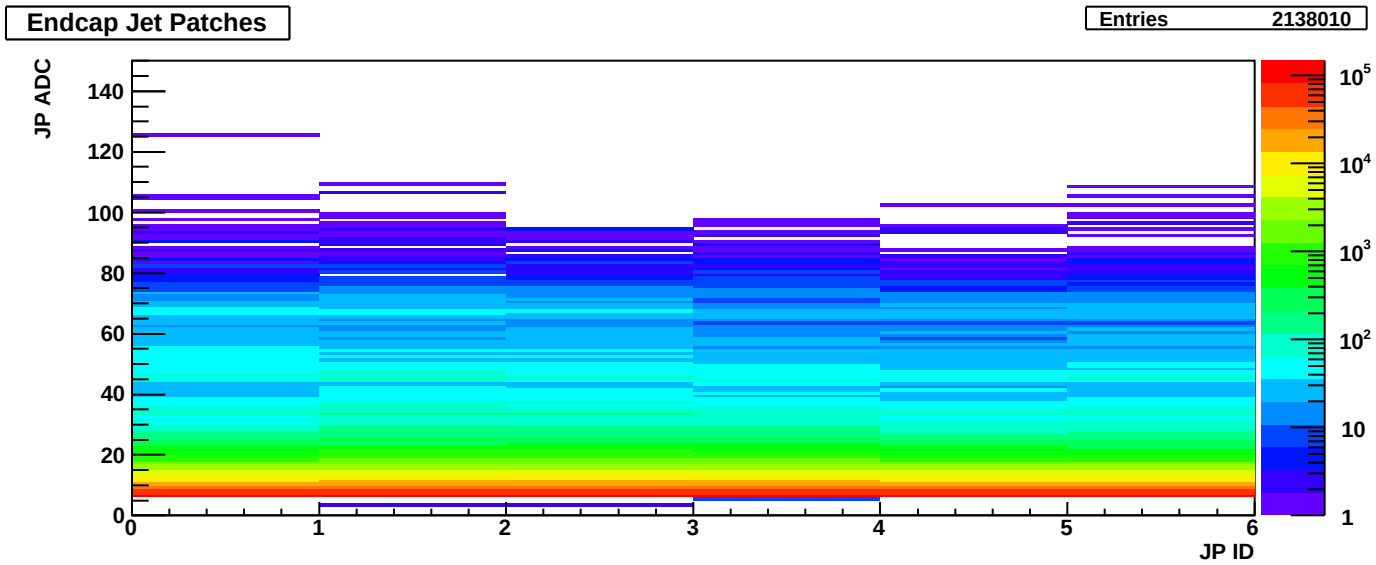

Barrel EMC L0 Input - High Tower
Entries 1.069005e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

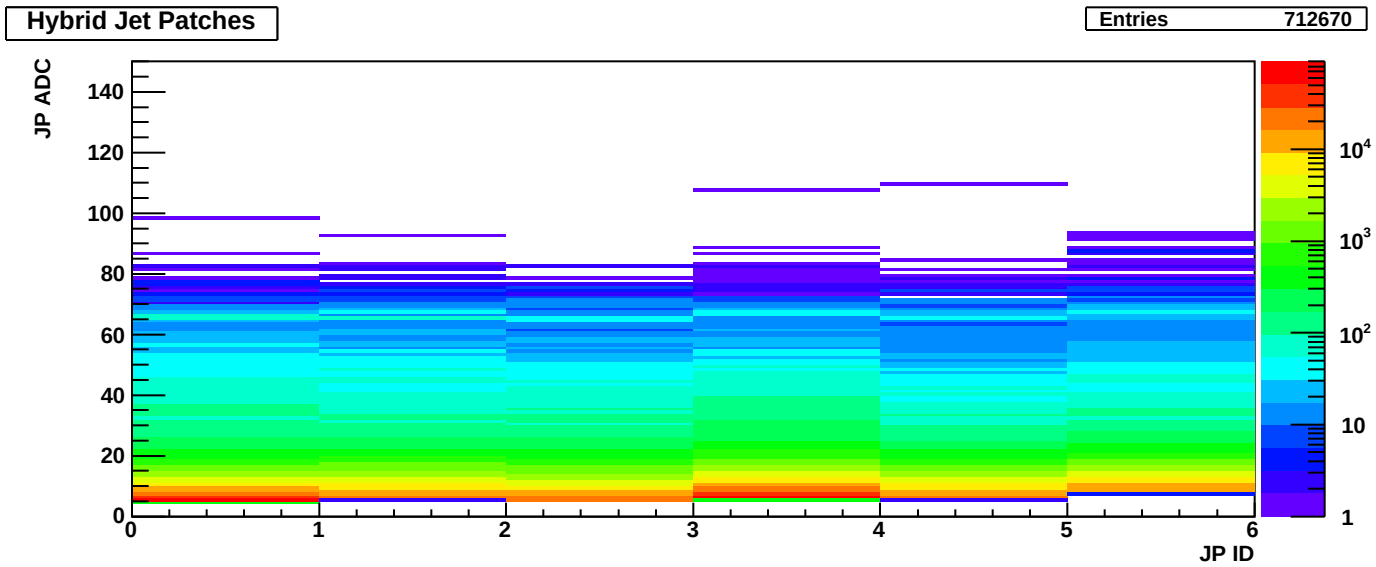

Barrel EMC L0 Input - Patch Sum
Entries 1.069005e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

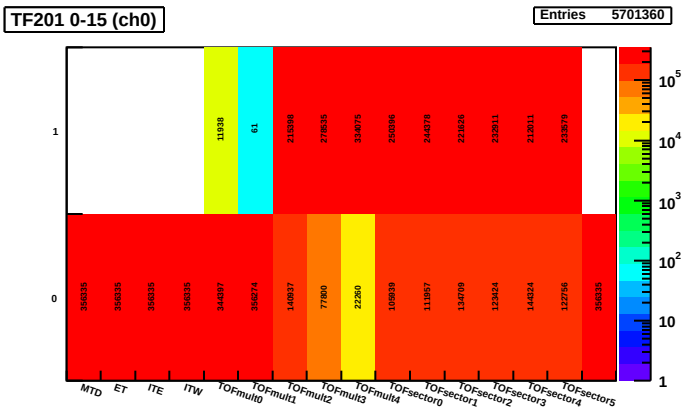

Endcap EMC L0 Input - High Tower
Entries 3.207015e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

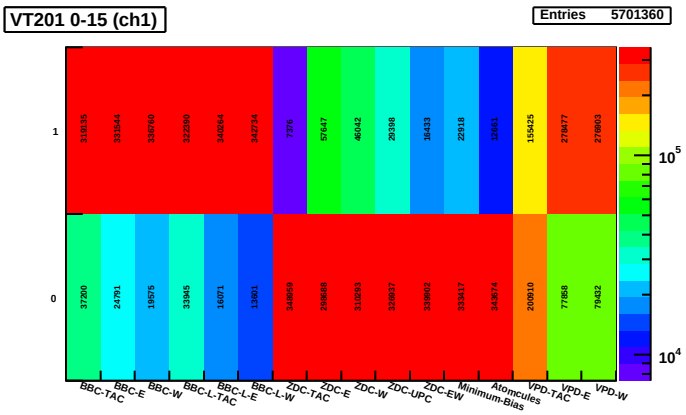

Endcap EMC L0 Input - Patch Sum
Entries 3.207015e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

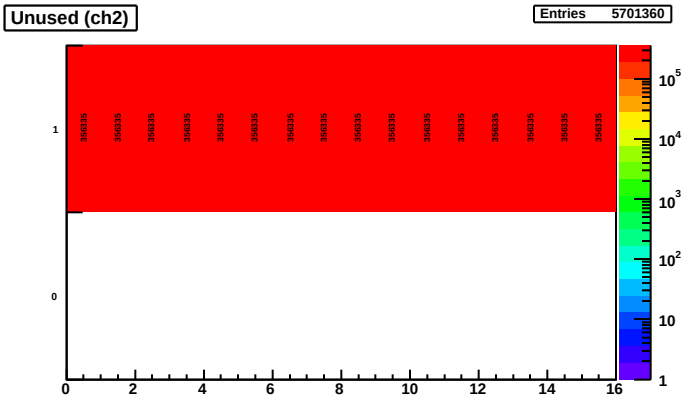

Barrel Jet Patches
Entries 6414030
JP ADC
JP ID

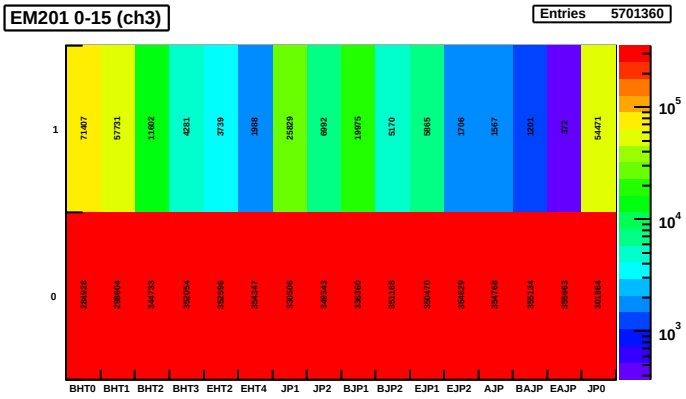

Endcap Jet Patches
Entries 2138010
JP ADC
JP ID

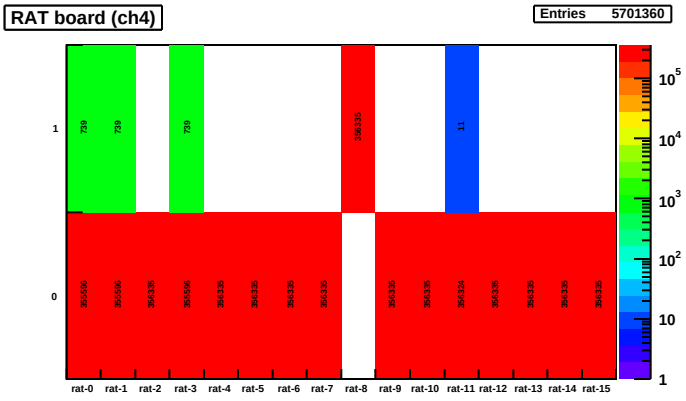

Hybrid Jet Patches
Entries 712670
JP ADC
JP ID

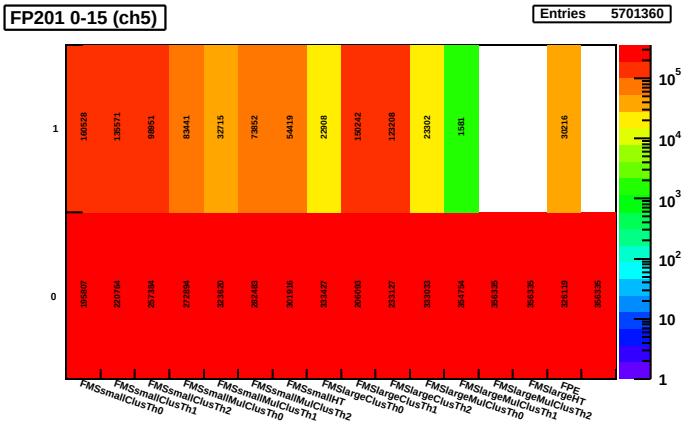

TF201 0-15 (ch0)
Entries 5701360
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

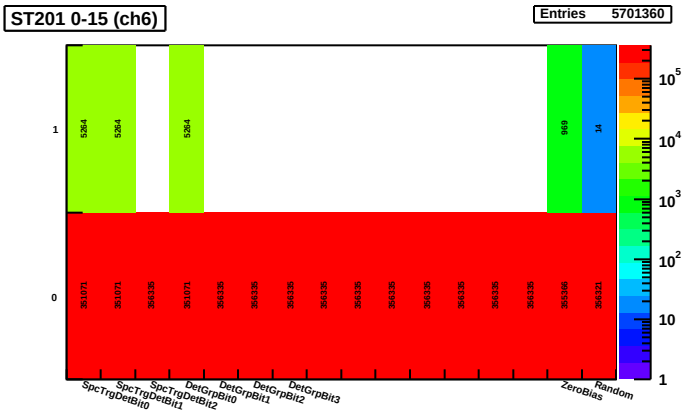

VT201 0-15 (ch1)
Entries 5701360
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

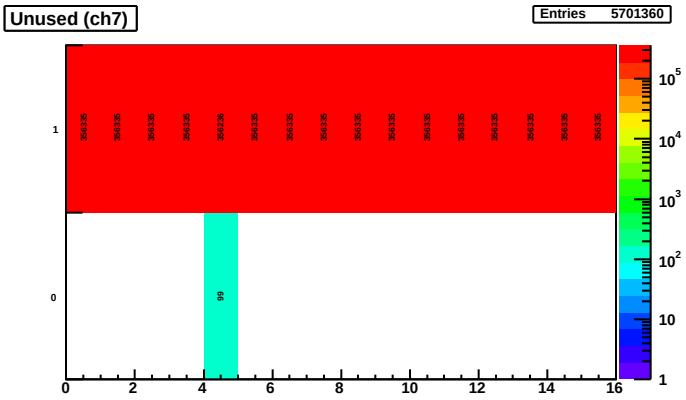

Unused (ch2)
Entries 5701360

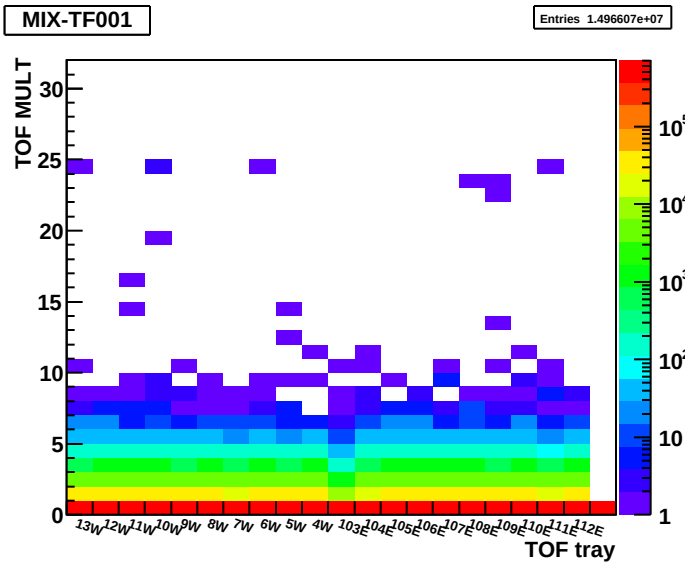

EM201 0-15 (ch3)
Entries 5701360
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

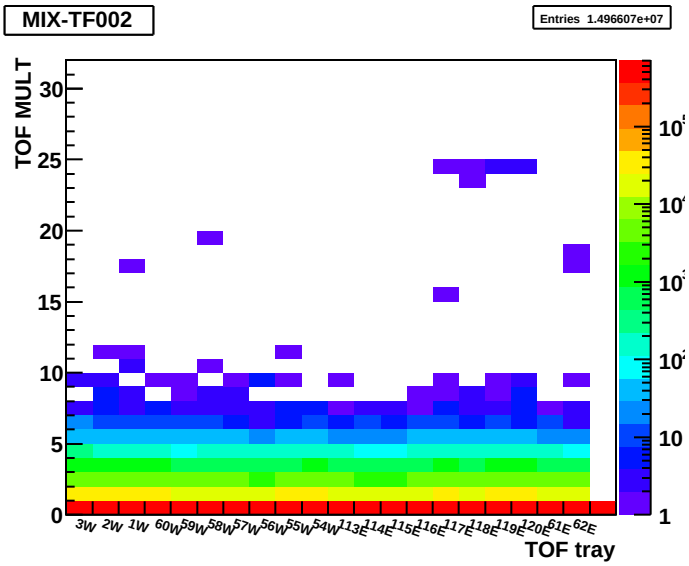

RAT board (ch4)
Entries 5701360
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

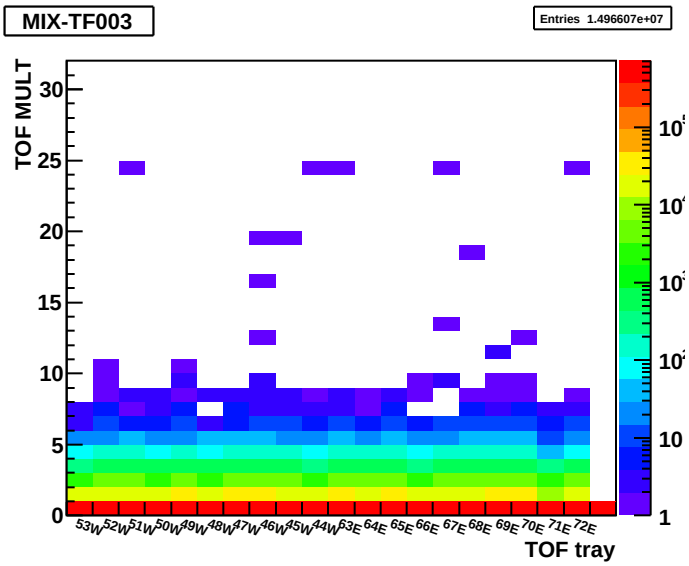

FP201 0-15 (ch5)
Entries 5701360
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

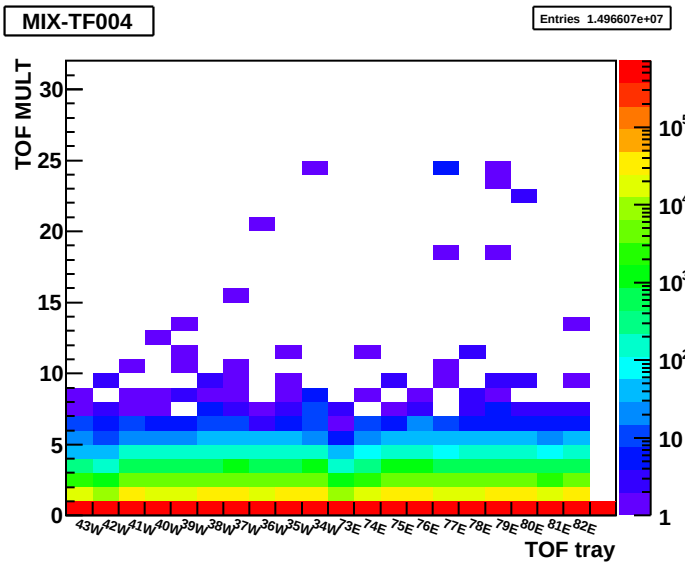

ST201 0-15 (ch6)
Entries 5701360
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

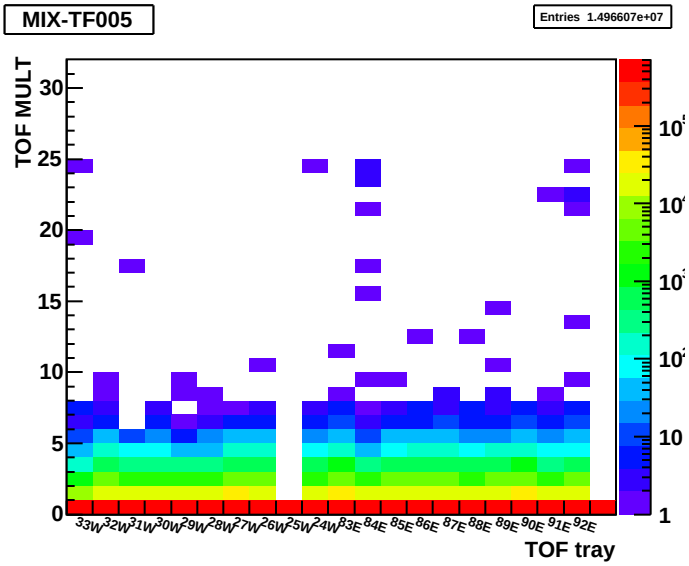

Unused (ch7)
Entries 5701360

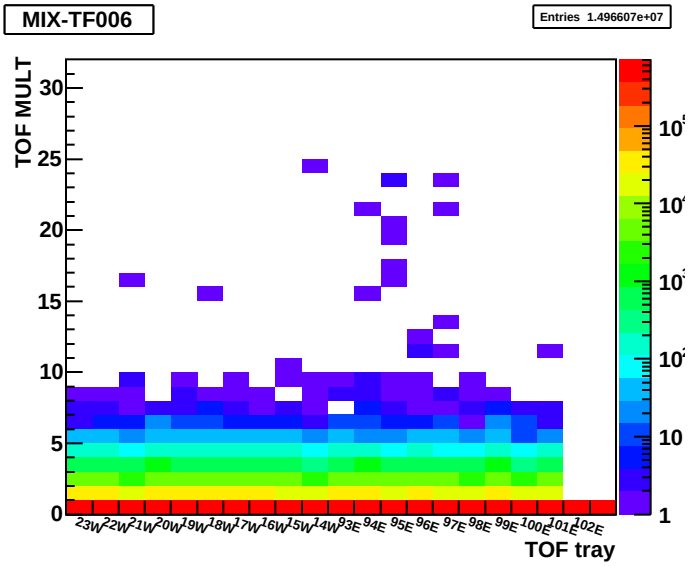

MIX-TF001
Entries 1.496607e+07
TOF MULT
TOF tray

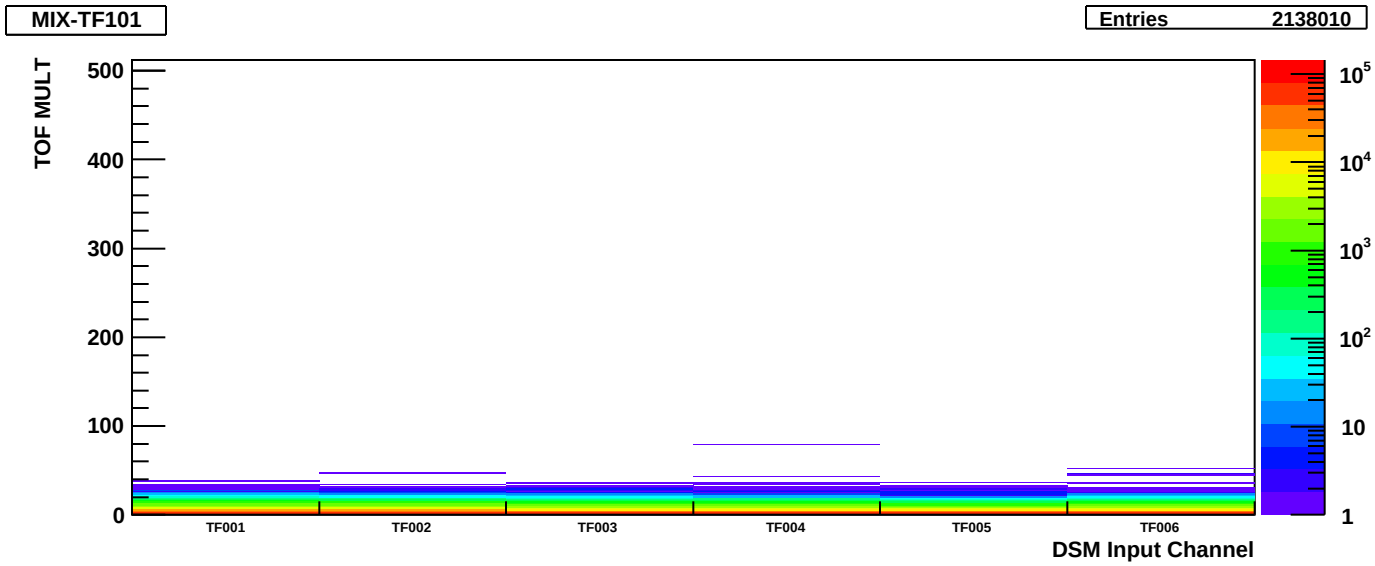

MIX-TF002
Entries 1.496607e+07
TOF MULT
TOF tray

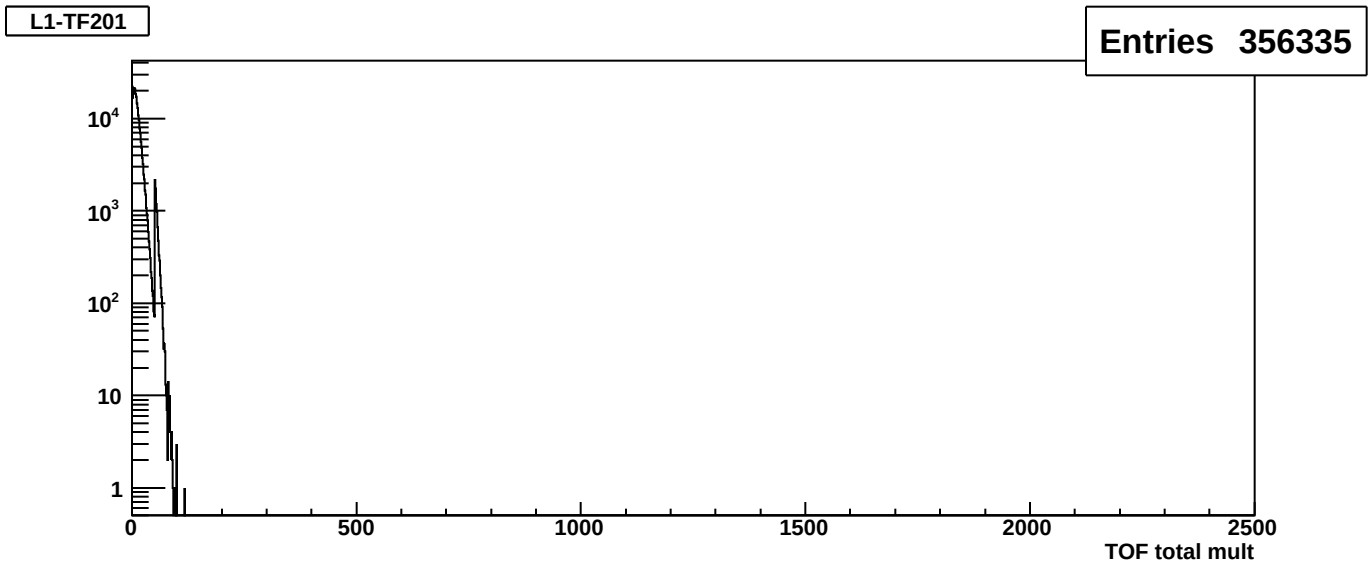

MIX-TF003
Entries 1.496607e+07
TOF MULT
TOF tray

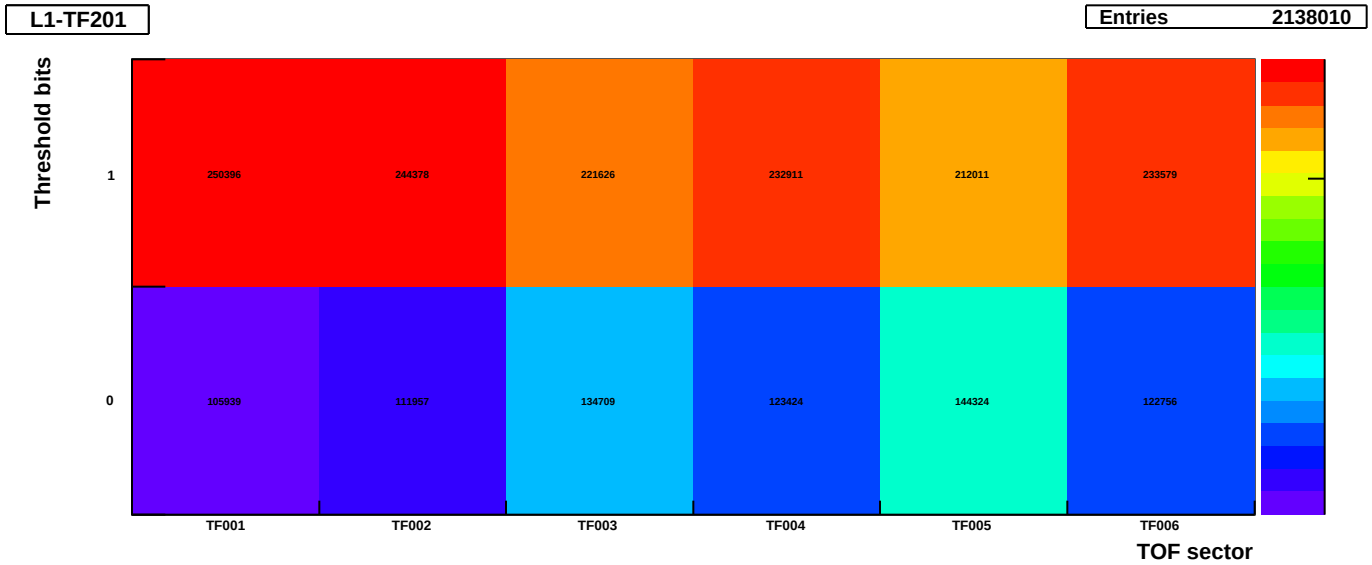

MIX-TF004
Entries 1.496607e+07
TOF MULT
TOF tray

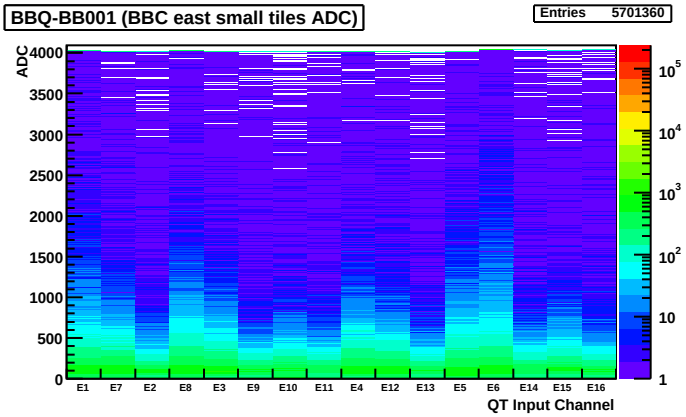

MIX-TF005
Entries 1.496607e+07
TOF MULT
TOF tray

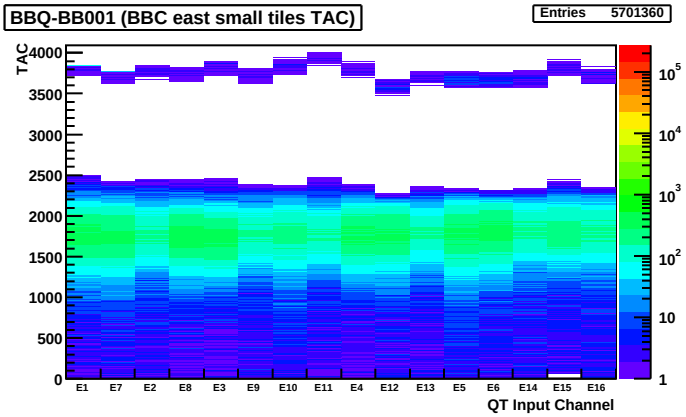

MIX-TF006
Entries 1.496607e+07
TOF MULT
TOF tray

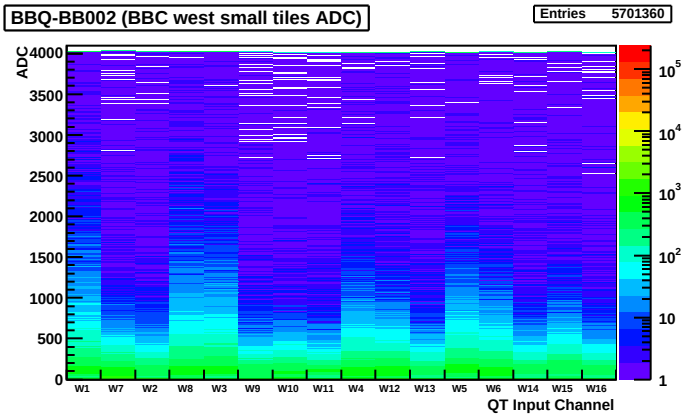

MIX-TF101
Entries 2138010
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

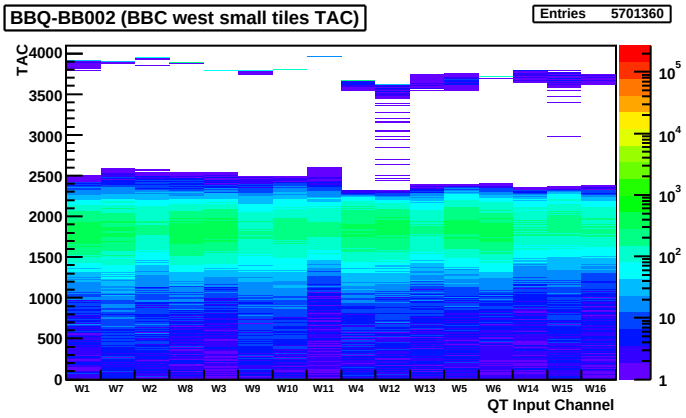

L1-TF201
Entries 356335
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

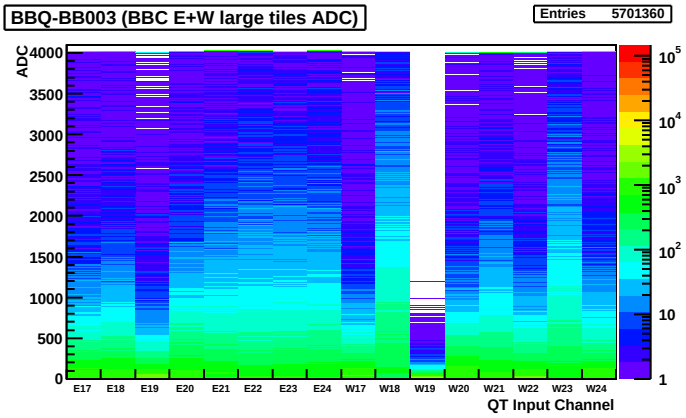

L1-TF201
Entries 2138010
Threshold bits
1
0
250396
244378
221626
232911
212011
233579
105939
111957
134709
123424
144324
122756
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

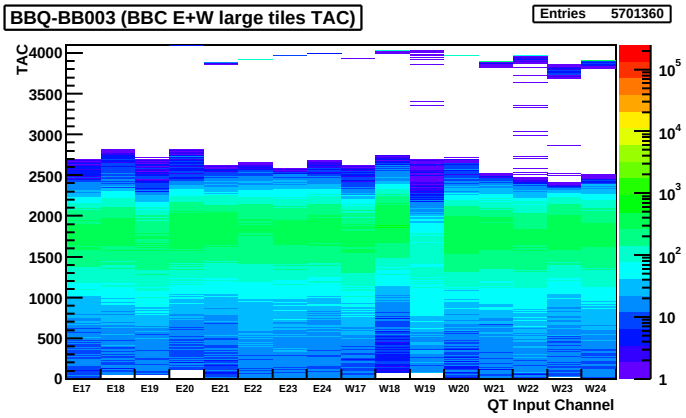

BBQ-BB001 (BBC east small tiles ADC)
Entries 5701360
ADC
QT Input Channel

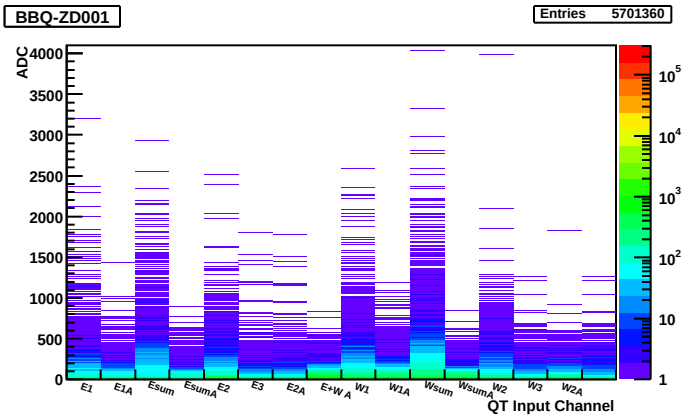

BBQ-BB001 (BBC east small tiles TAC)
Entries 5701360
TAC
QT Input Channel

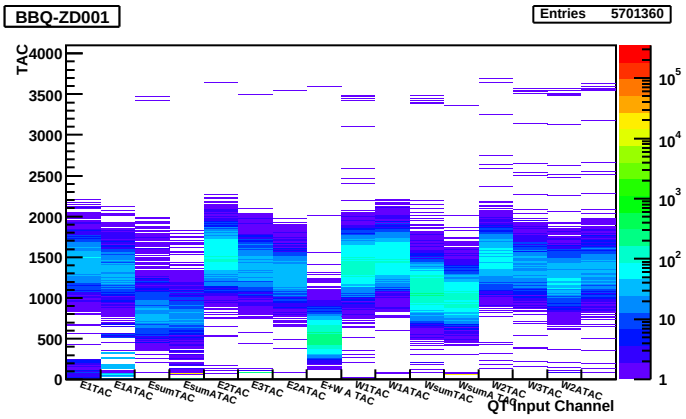

BBQ-BB002 (BBC west small tiles ADC)
Entries 5701360
ADC
QT Input Channel

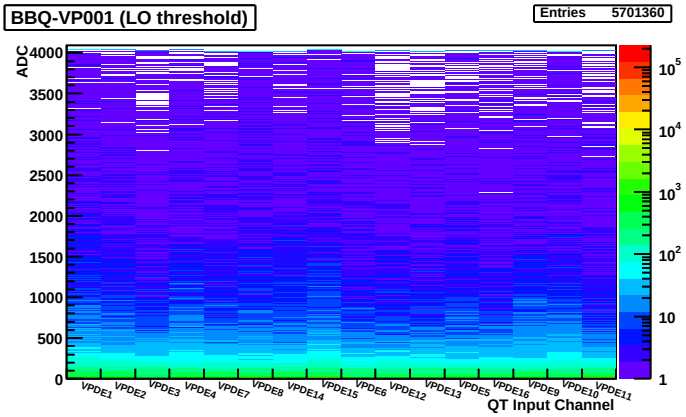

BBQ-BB002 (BBC west small tiles TAC)
Entries 5701360
TAC
QT Input Channel

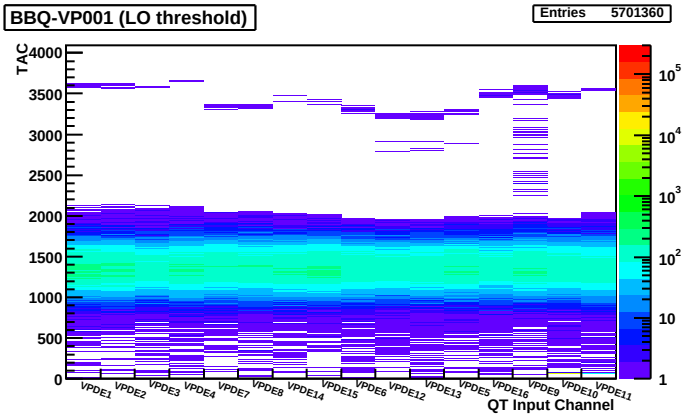

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5701360
ADC
QT Input Channel

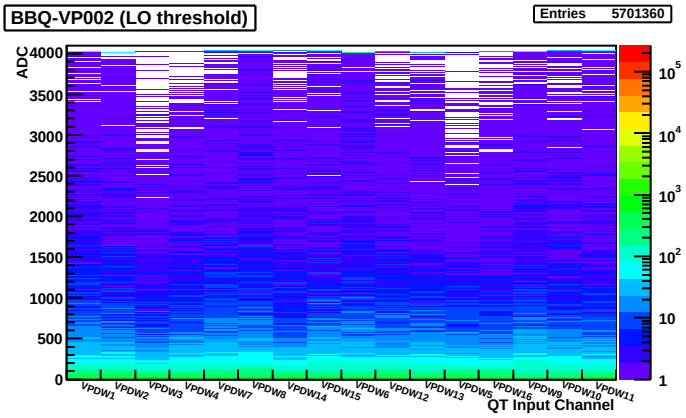

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5701360
TAC
QT Input Channel

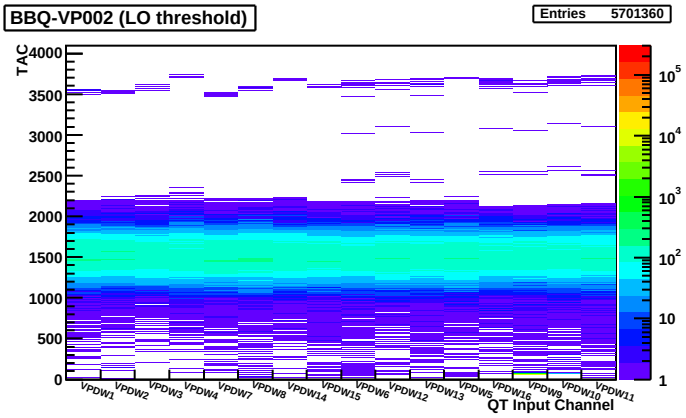

BBQ-ZD001
Entries 5701360
ADC
QT Input Channel

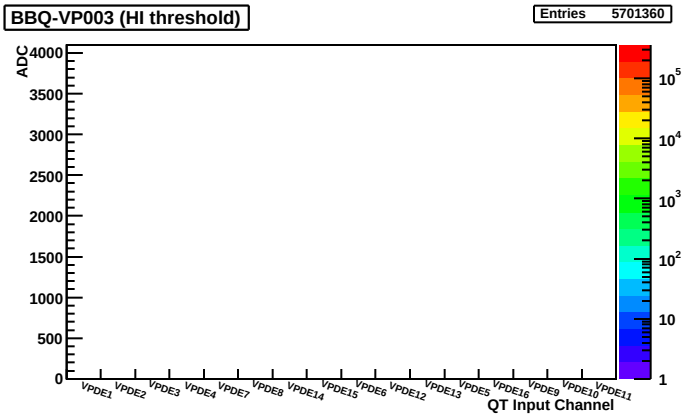

BBQ-ZD001
Entries 5701360
TAC
QT Input Channel

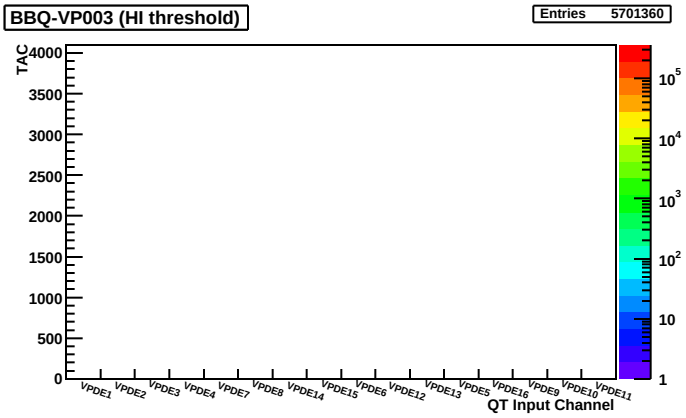

BBQ-VP001 (LO threshold)
Entries 5701360
ADC
QT Input Channel

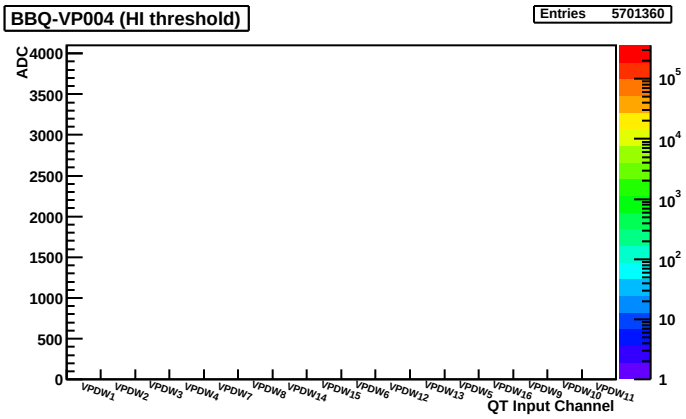

BBQ-VP001 (LO threshold)
Entries 5701360
TAC
QT Input Channel

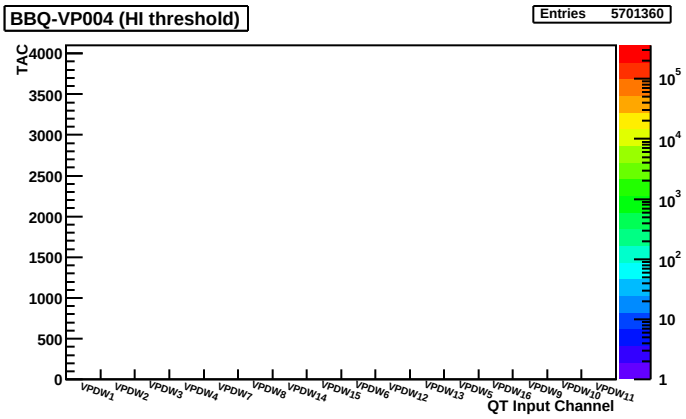

BBQ-VP002 (LO threshold)
Entries 5701360
ADC
QT Input Channel

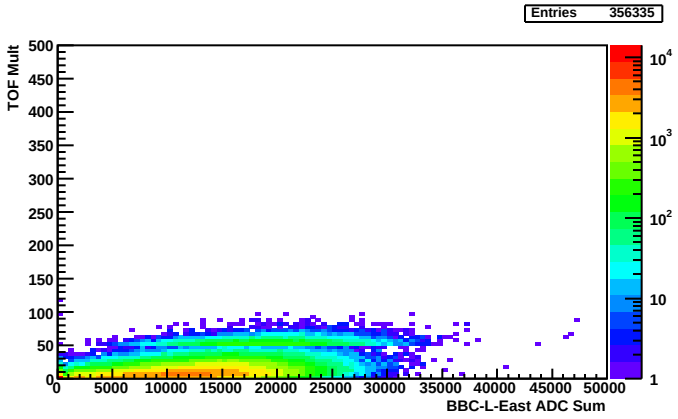

BBQ-VP002 (LO threshold)
Entries 5701360
TAC
QT Input Channel

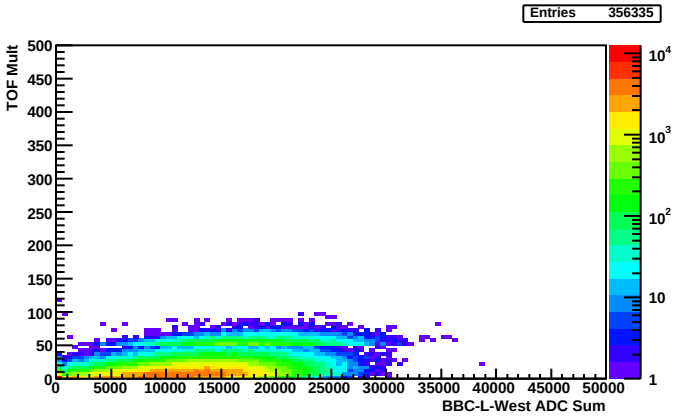

BBQ-VP003 (HI threshold)
Entries 5701360
ADC
QT Input Channel

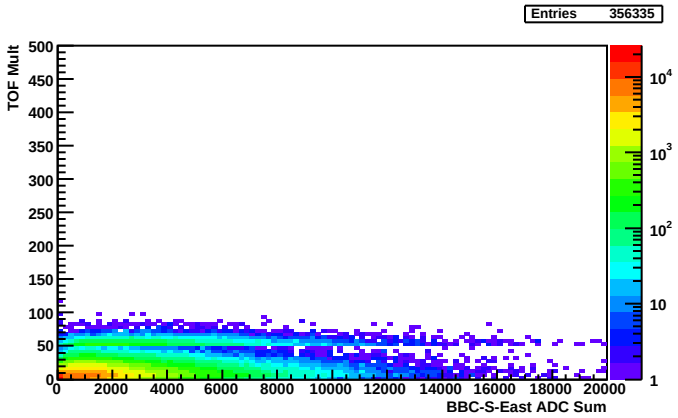

BBQ-VP003 (HI threshold)
Entries 5701360
TAC
QT Input Channel

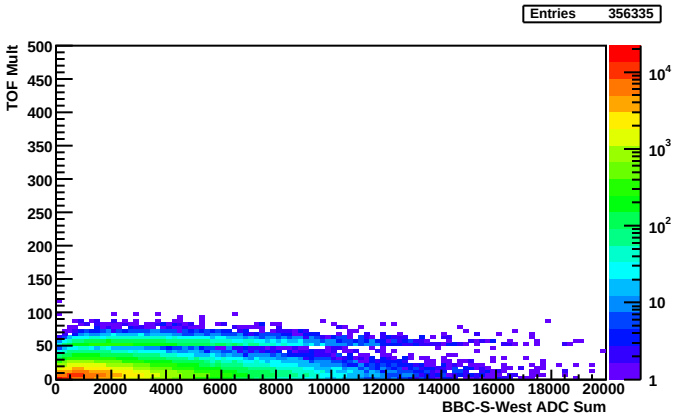

BBQ-VP004 (HI threshold)
Entries 5701360
ADC
QT Input Channel

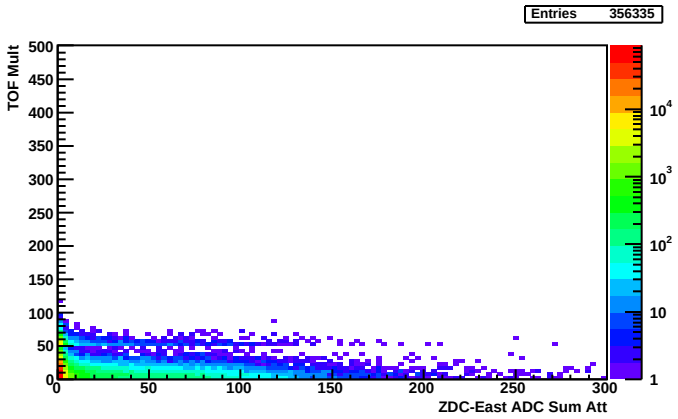

BBQ-VP004 (HI threshold)
Entries 5701360
TAC
QT Input Channel

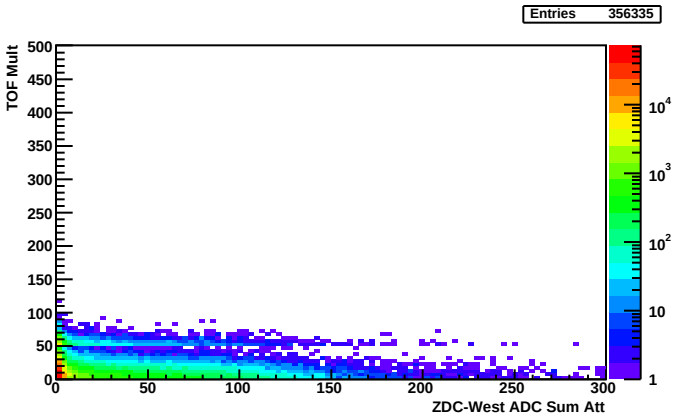

Entries 356335
TOF Mult
BBC-L-East ADC Sum

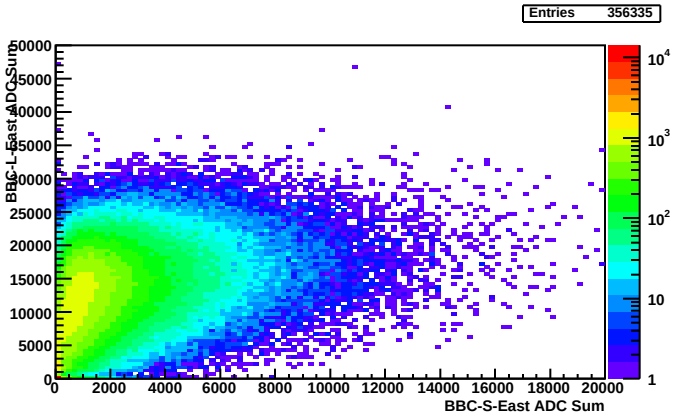

Entries 356335
TOF Mult
BBC-L-West ADC Sum

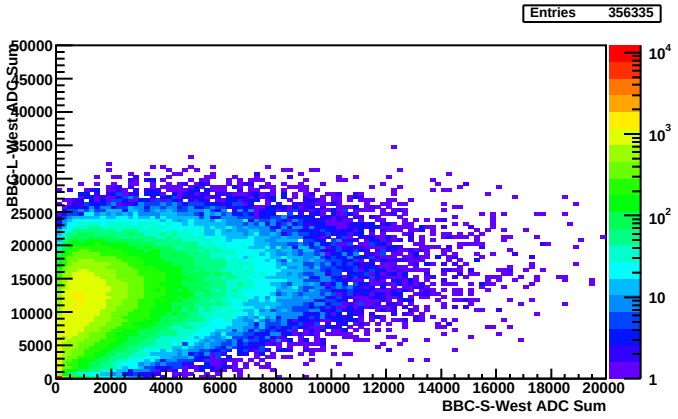

Entries 356335
TOF Mult
BBC-S-East ADC Sum

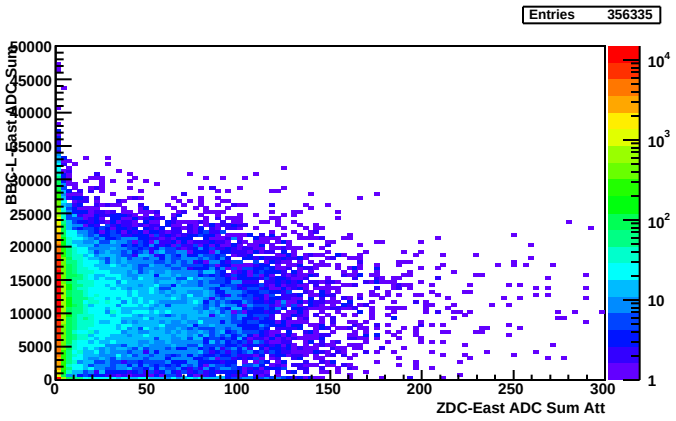

Entries 356335
TOF Mult
BBC-S-West ADC Sum

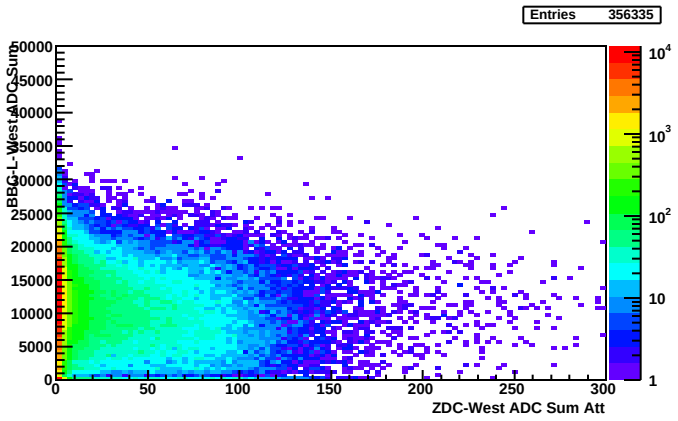

Entries 356335
TOF Mult
ZDC-East ADC Sum Att

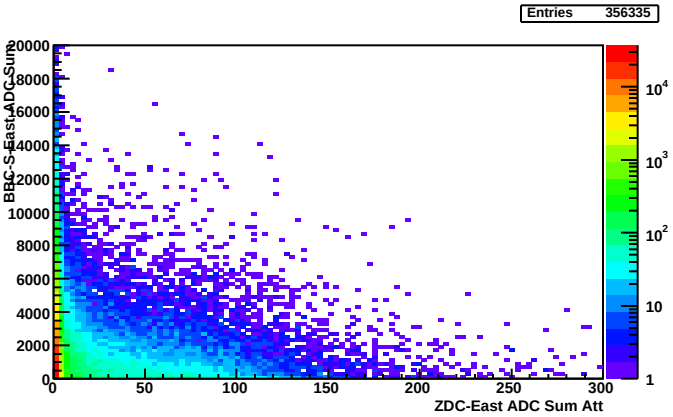

Entries 356335
TOF Mult
ZDC-West ADC Sum Att

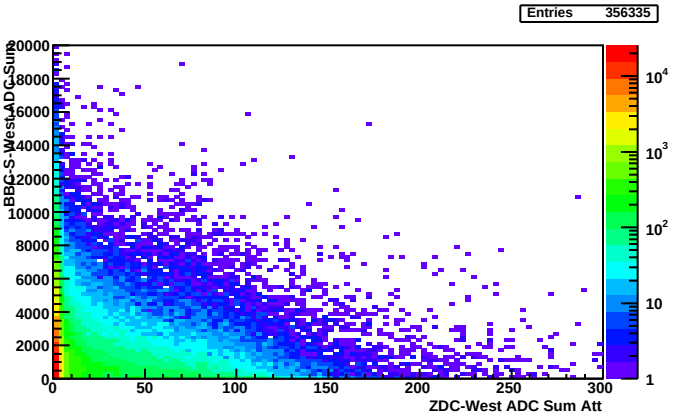

Entries 356335
BBC-L-East ADC Sum
BBC-S-East ADC Sum

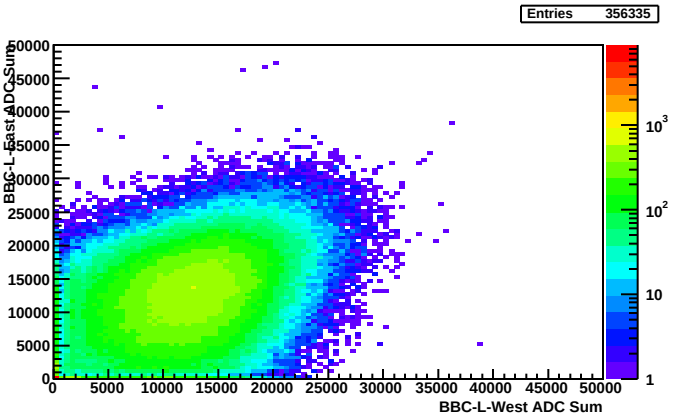

Entries 356335
BBC-L-West ADC Sum
BBC-S-West ADC Sum

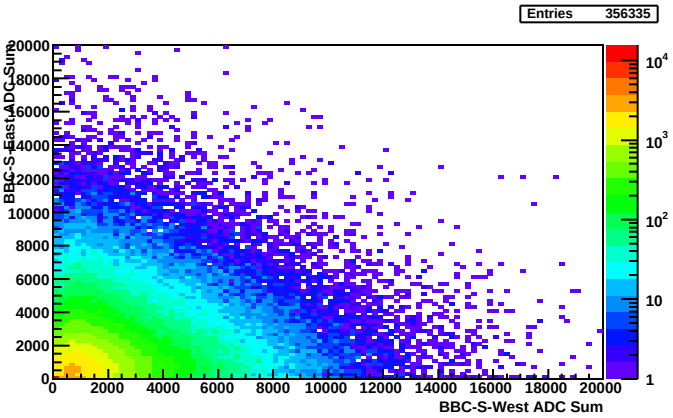

Entries 356335
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

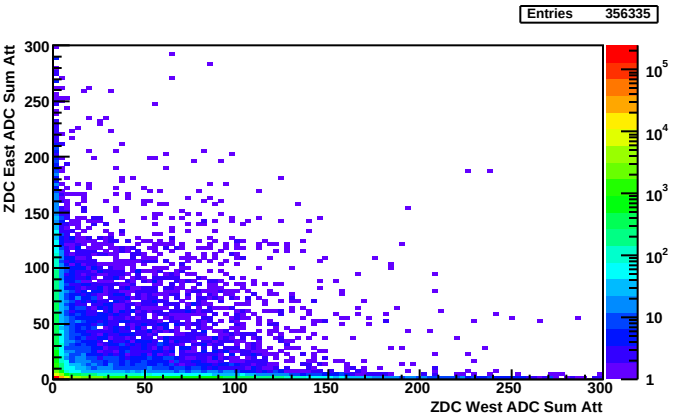

Entries 356335
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

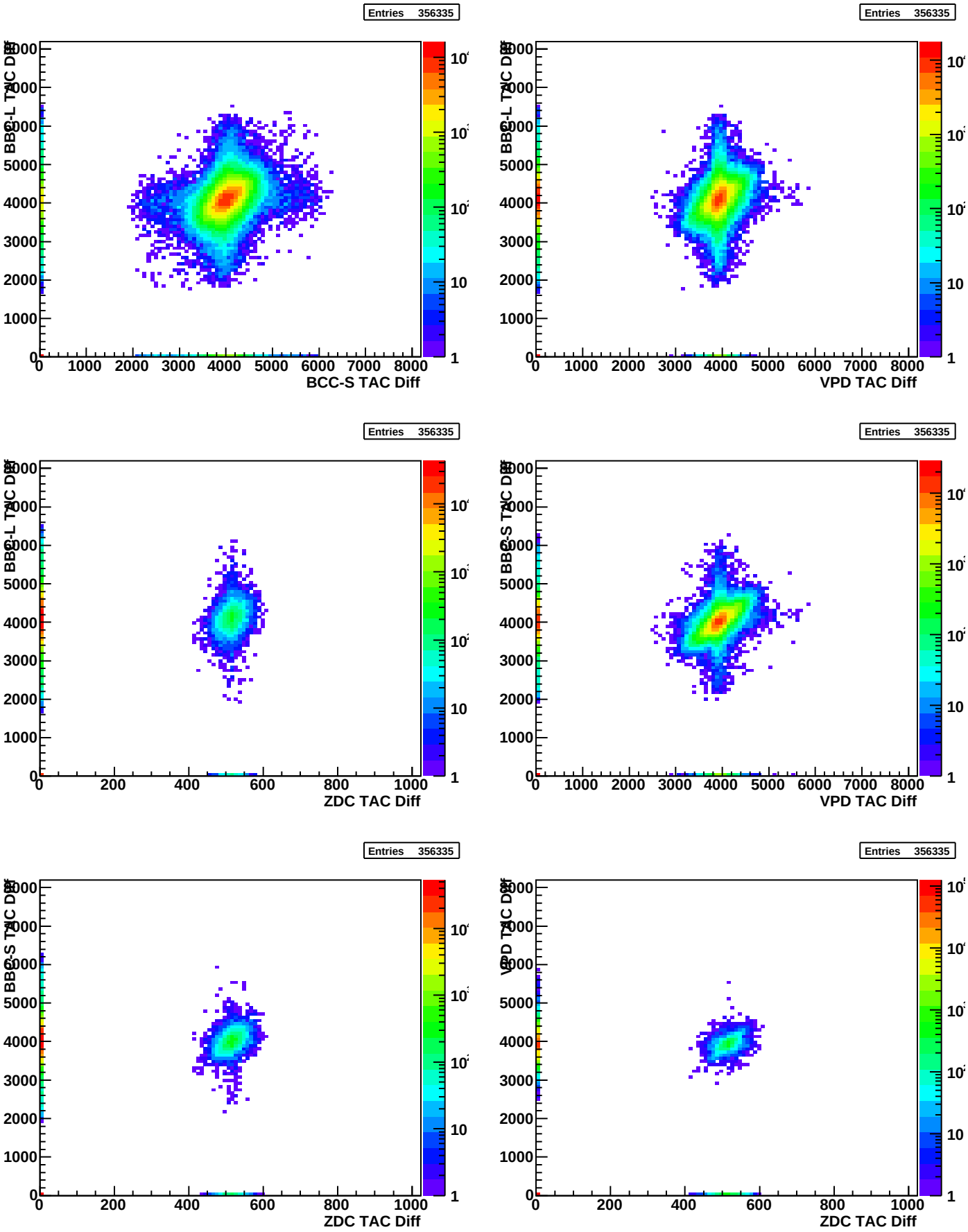

Entries 356335
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

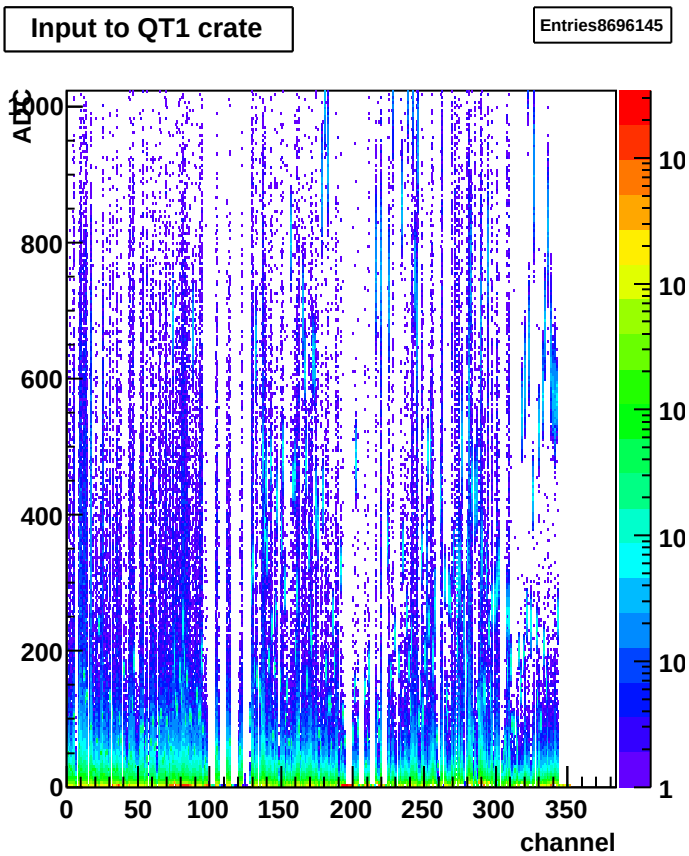

Entries 356335
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

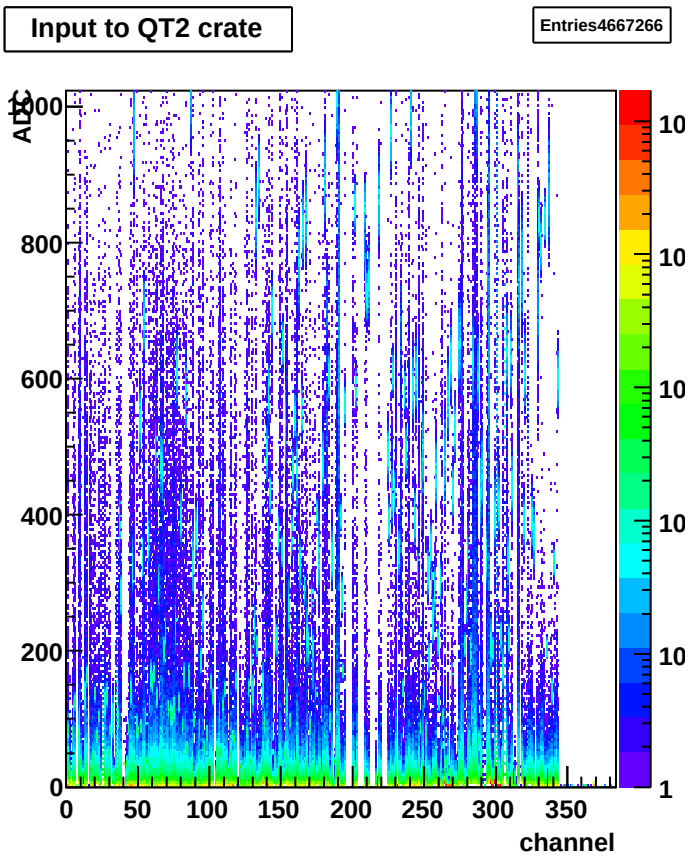

Entries 356335
BBC-L-East ADC Sum
BBC-L-West ADC Sum

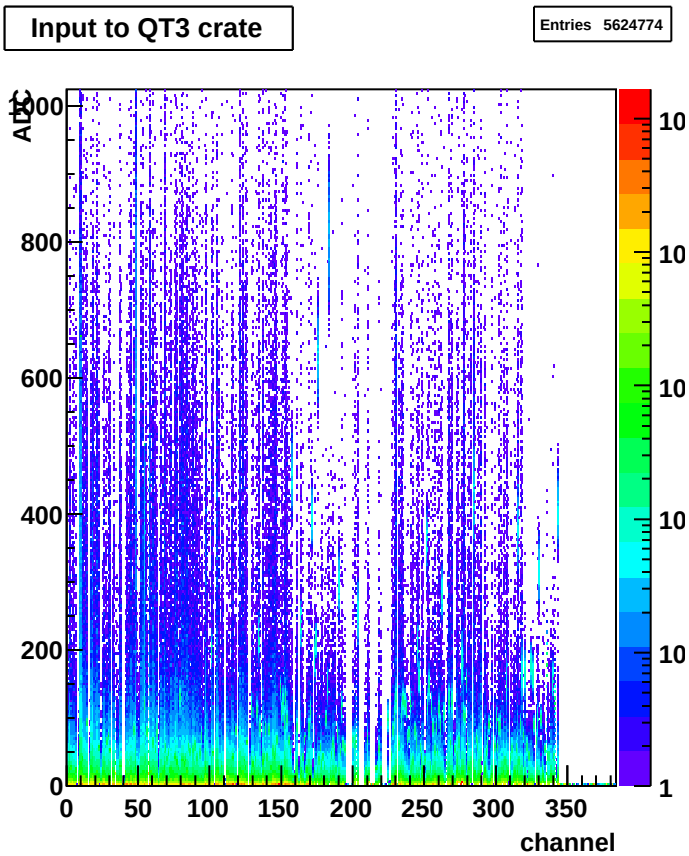

Entries 356335
BBC-S-East ADC Sum
BBC-S-West ADC Sum

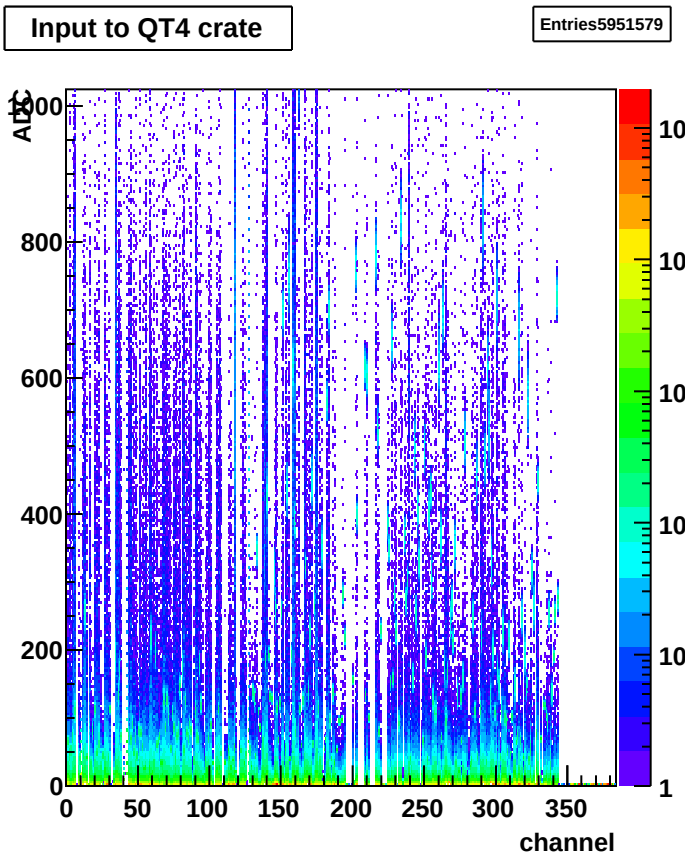

Entries 356335
ZDC East ADC Sum Att
ZDC West ADC Sum Att

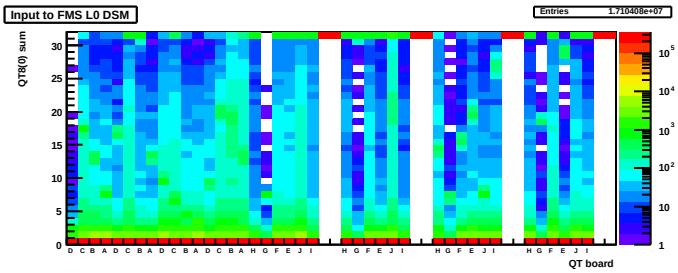

Entries 356335
BBC-L TAC Diff
BCC-S TAC Diff
Entries 356335
BBC-L TAC Diff
VPD TAC Diff
Entries 356335
BBC-L TAC Diff
ZDC TAC Diff
Entries 356335
BBC-S TAC Diff
VPD TAC Diff
Entries 356335
BBC-S TAC Diff
ZDC TAC Diff
Entries 356335
VPD TAC Diff
ZDC TAC Diff

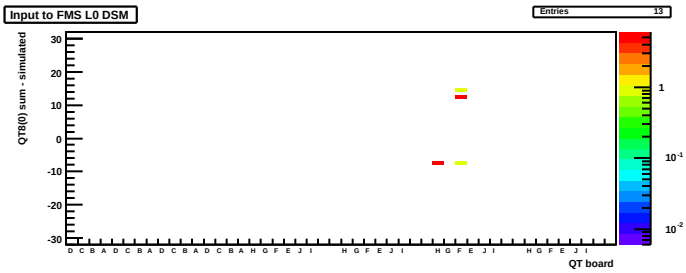

Input to QT1 crate
Entries8696145
ADC
channel

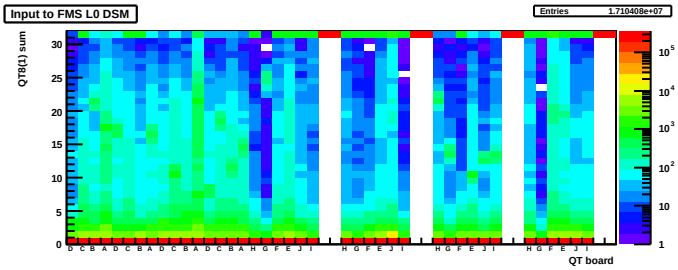

Input to QT2 crate
Entries4667266
ADC
channel

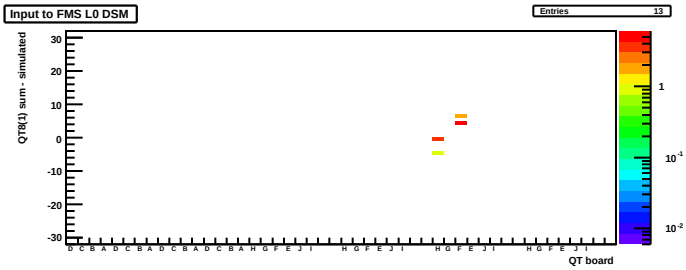

Input to QT3 crate
Entries 5624774
ADC
channel

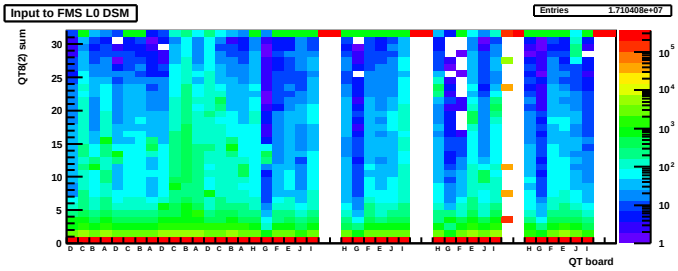

Input to QT4 crate
Entries5951579
ADC
channel

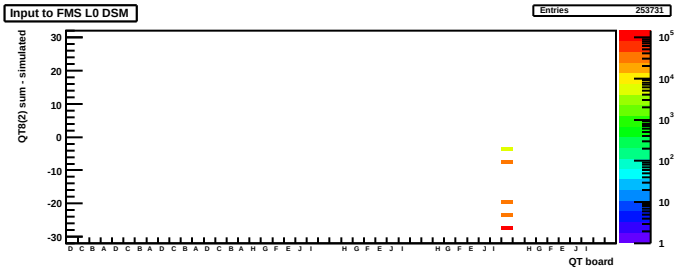

Input to FMS L0 DSM
Entries 1.710408e+07
QT8(0) sum
QT board

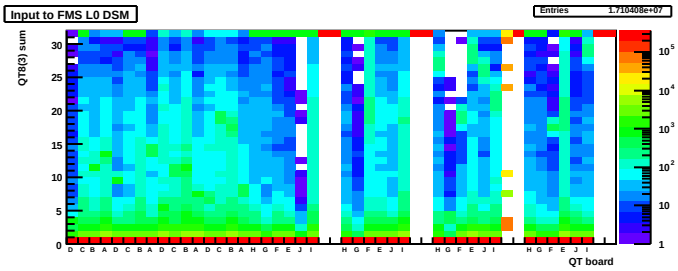

Input to FMS L0 DSM
Entries 13
QT8(0) sum - simulated
QT board

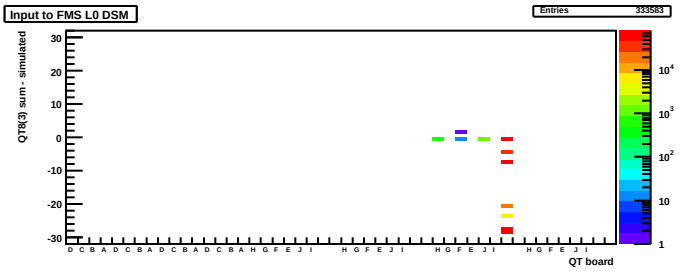

Input to FMS L0 DSM
Entries 1.710408e+07
QT8(1) sum
QT board

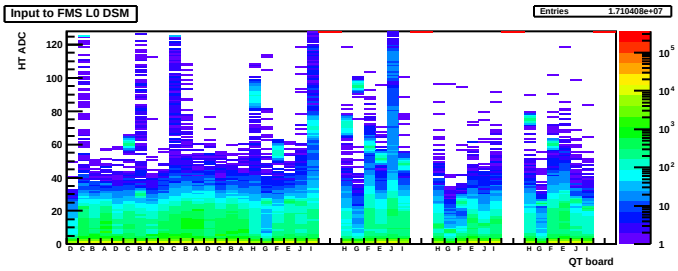

Input to FMS L0 DSM
Entries 13
QT8(1) sum - simulated
QT board

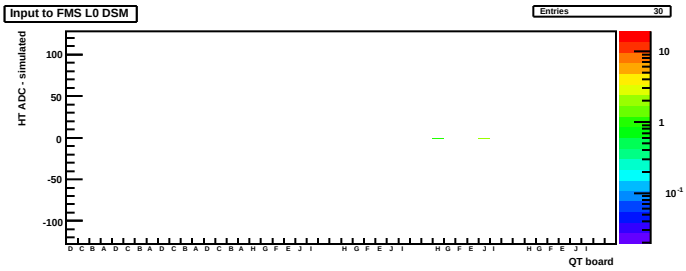

Input to FMS L0 DSM
Entries 1.710408e+07
QT8(2) sum
QT board

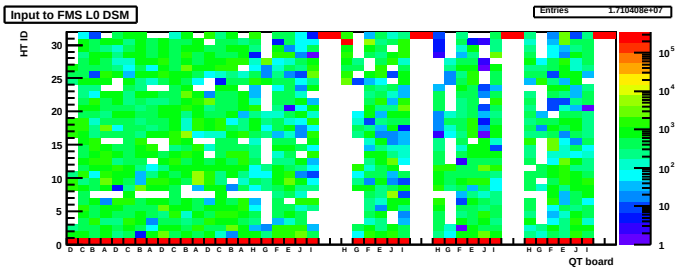

Input to FMS L0 DSM
Entries 253731
QT8(2) sum - simulated
QT board

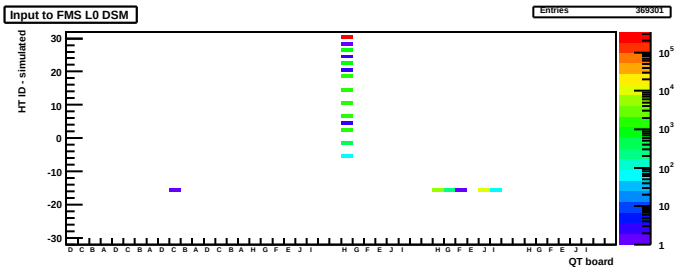

Input to FMS L0 DSM
Entries 1.710408e+07
QT8(3) sum
QT board

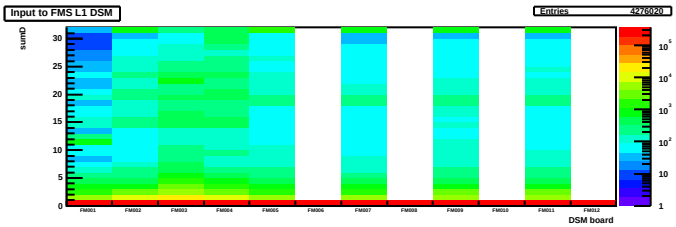

Input to FMS L0 DSM
Entries 333583
QT8(3) sum - simulated
QT board

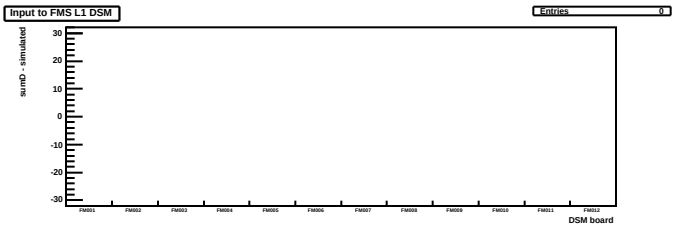

Input to FMS L0 DSM
Entries 1.710408e+07
HT ADC
QT board

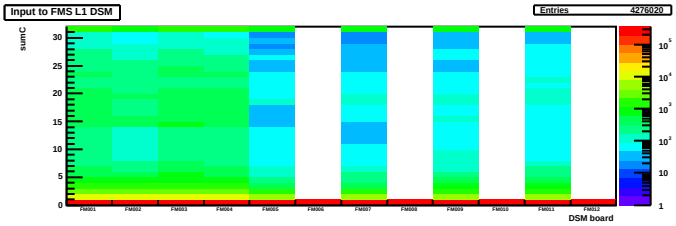

Input to FMS L0 DSM
Entries 30
HT ADC - simulated
QT board

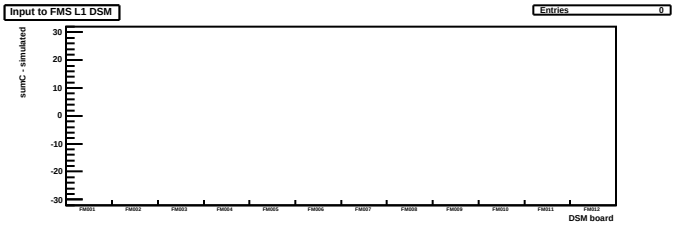

Input to FMS L0 DSM
Entries 1.710408e+07
HT ID
QT board

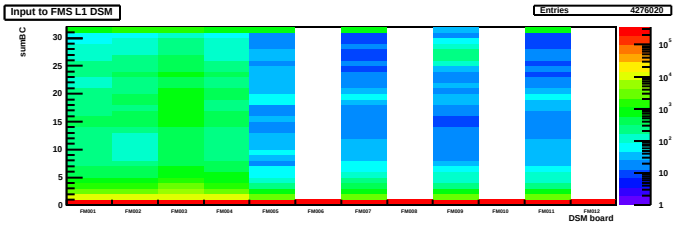

Input to FMS L0 DSM
Entries 369301
HT ID - simulated
QT board

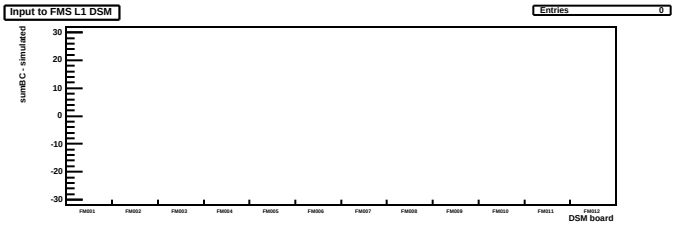

Input to FMS L1 DSM
Entries 4276020
sumD
DSM board

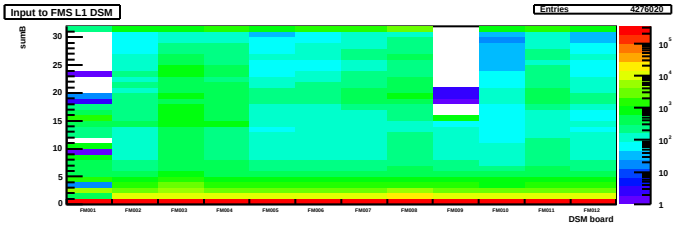

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

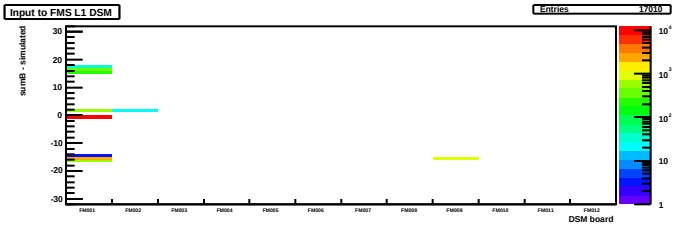

Input to FMS L1 DSM
Entries 4276020
sumC
DSM board

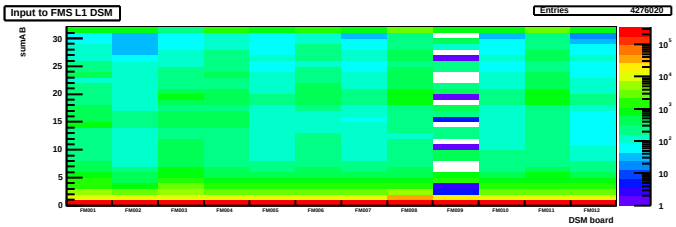

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

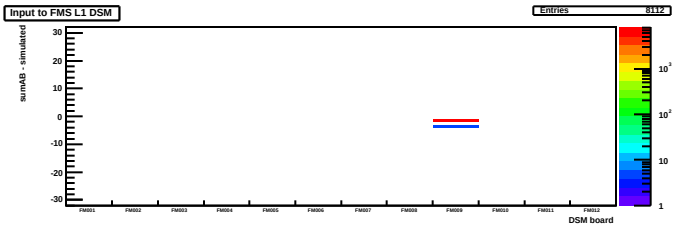

Input to FMS L1 DSM
Entries 4276020
sumBC
DSM board

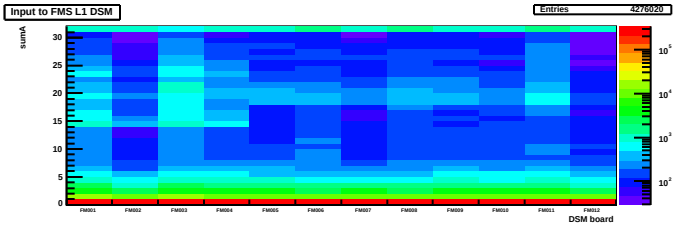

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

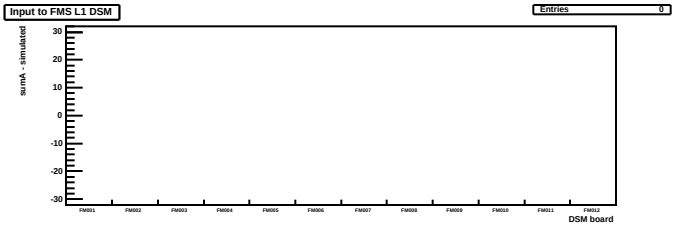

Input to FMS L1 DSM
Entries 4276020
sumB
DSM board

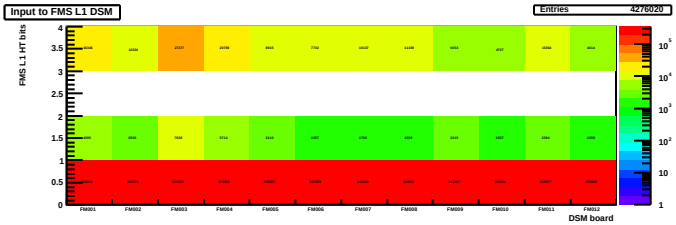

Input to FMS L1 DSM
Entries 17010
sumB - simulated
DSM board

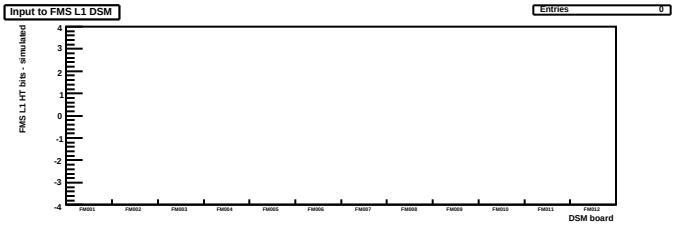

Input to FMS L1 DSM
Entries 4276020
sumAB
DSM board

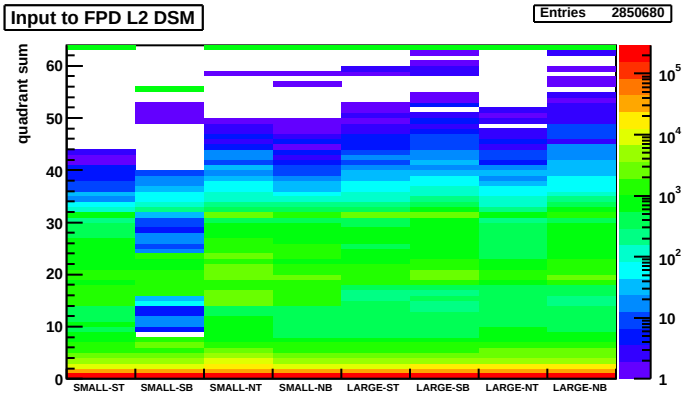

Input to FMS L1 DSM
Entries 8112
sumAB - simulated
DSM board

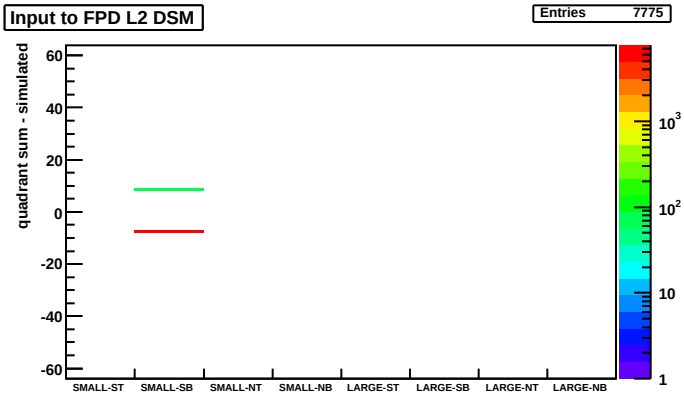

Input to FMS L1 DSM
Entries 4276020
sumA
DSM board

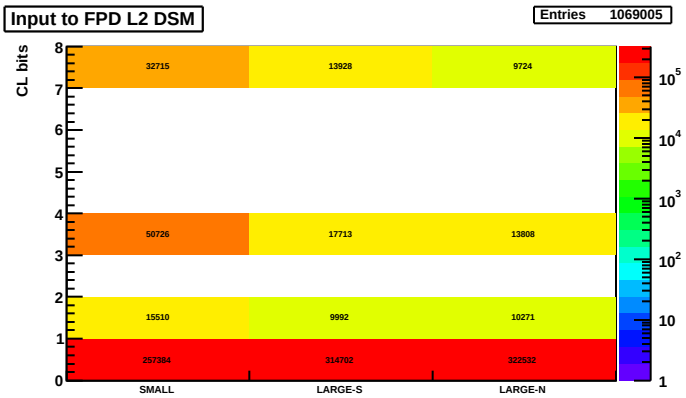

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

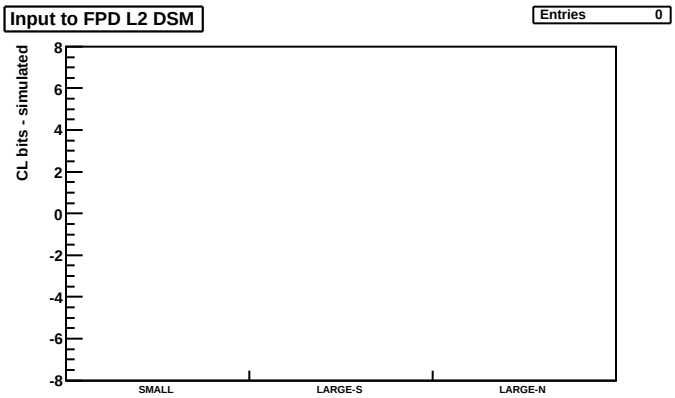

Input to FMS L1 DSM
Entries 4276020
FMS L1 HT bits
DSM board

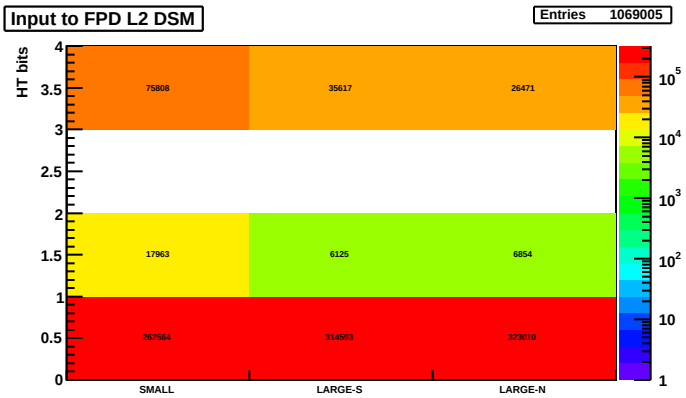

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

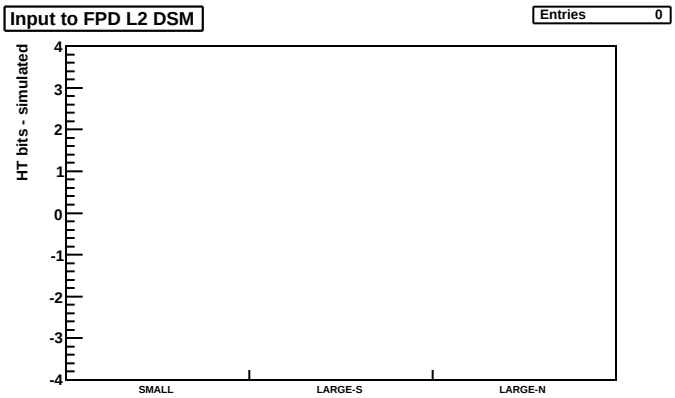

Input to FPD L2 DSM
Entries 2850680
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

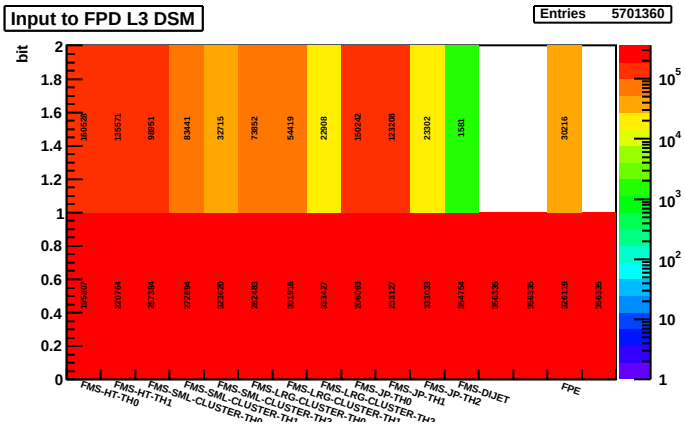

Input to FPD L2 DSM
Entries 7775
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

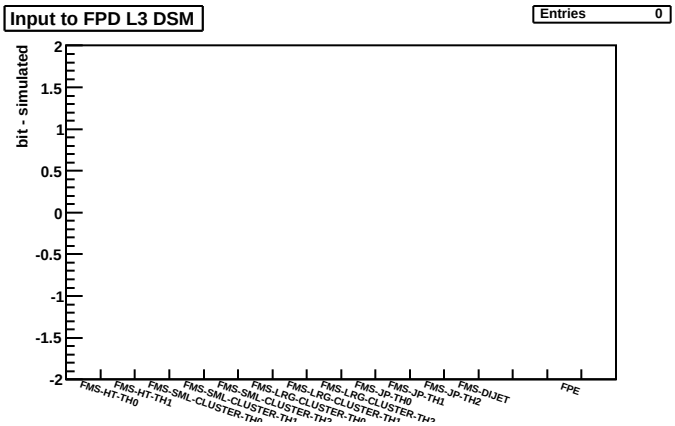

Input to FPD L2 DSM
Entries 1069005
CL bits
32715 13928 9724
50726 17713 13808
15510 9992 10271
257384 314702 322532
SMALL LARGE-S LARGE-N

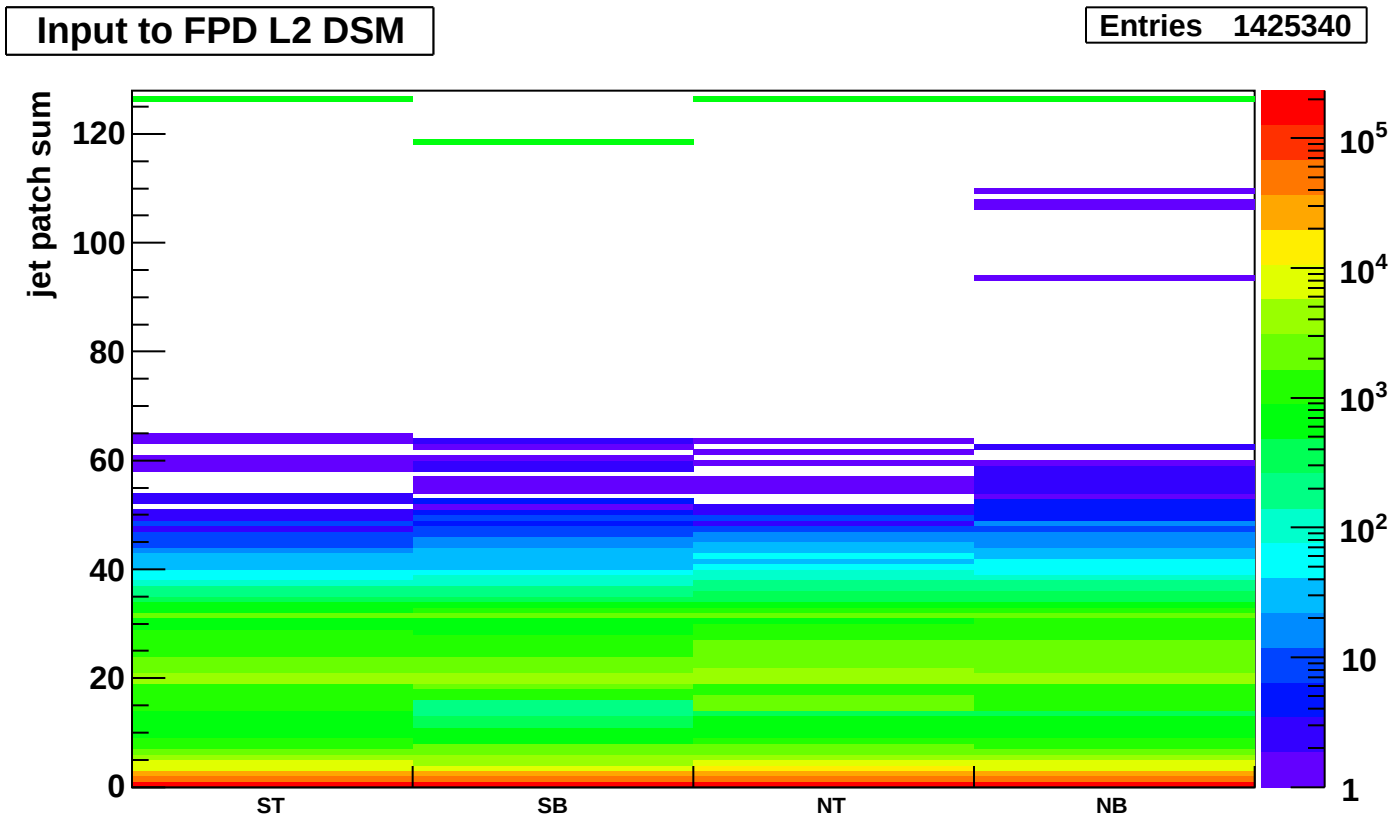

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

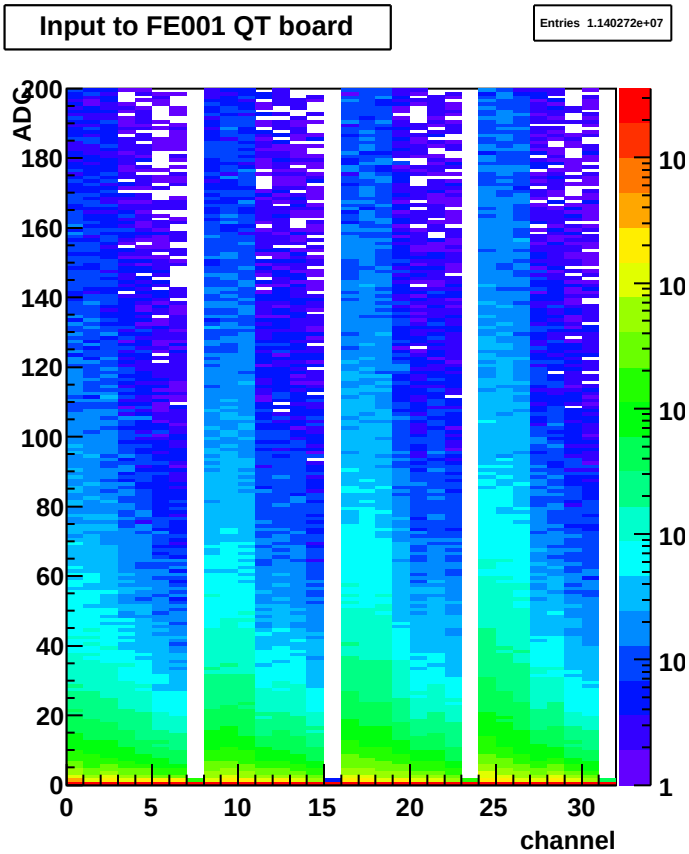

Input to FPD L2 DSM
Entries 1069005
HT bits
75808 35617 26471
17963 6125 6854
262564 314593 323010
SMALL LARGE-S LARGE-N

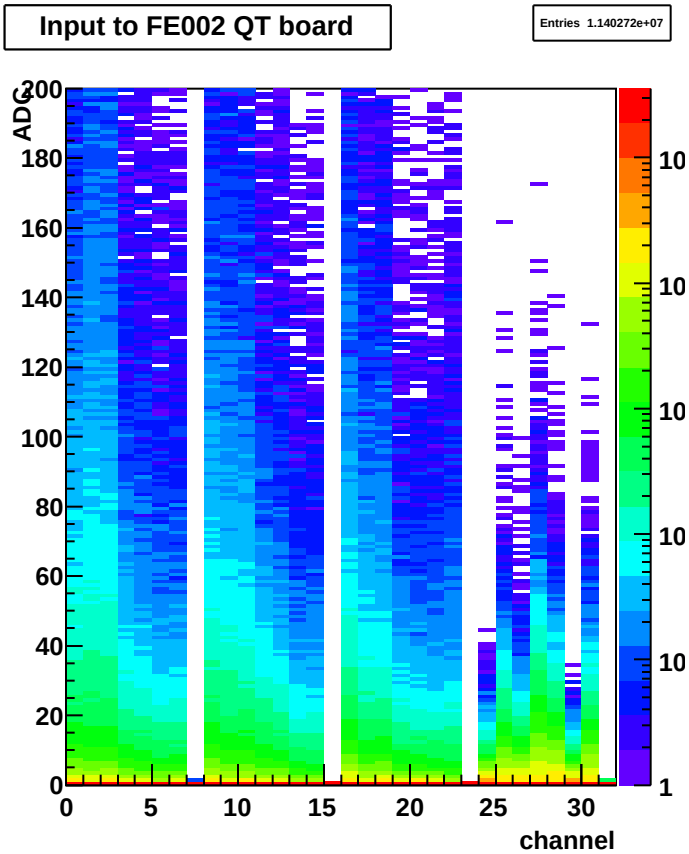

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

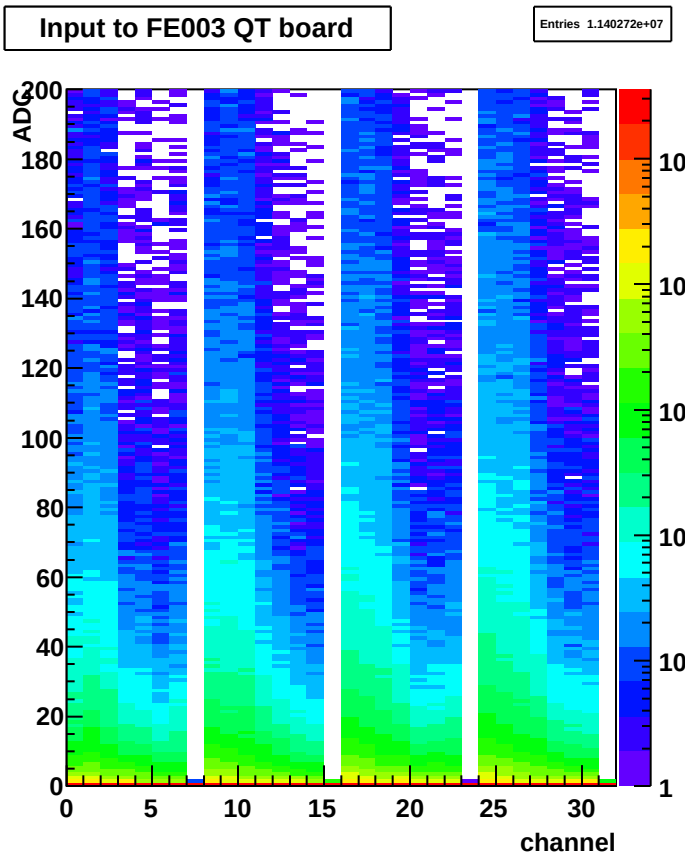

Input to FPD L3 DSM
Entries 5701360
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

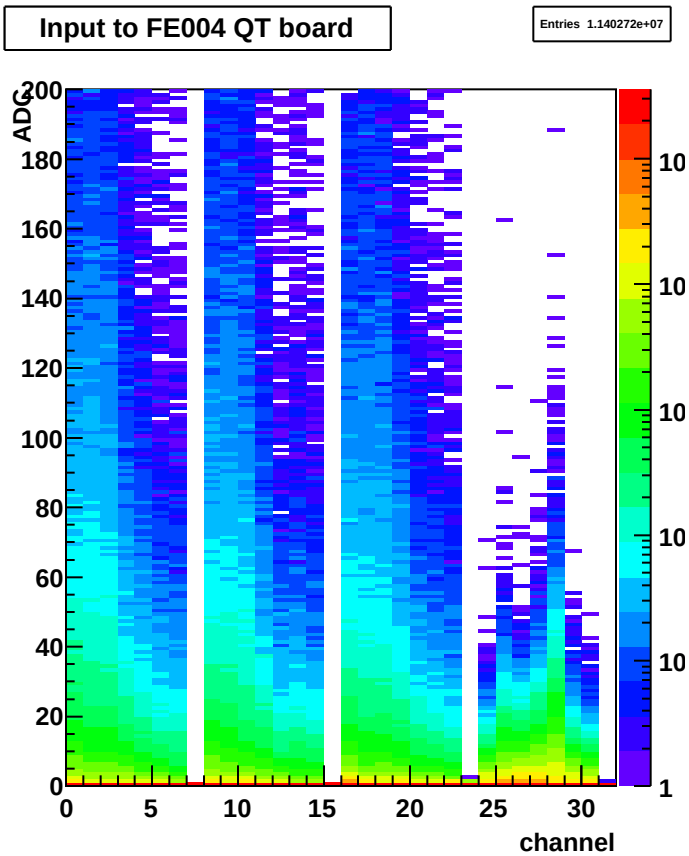

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

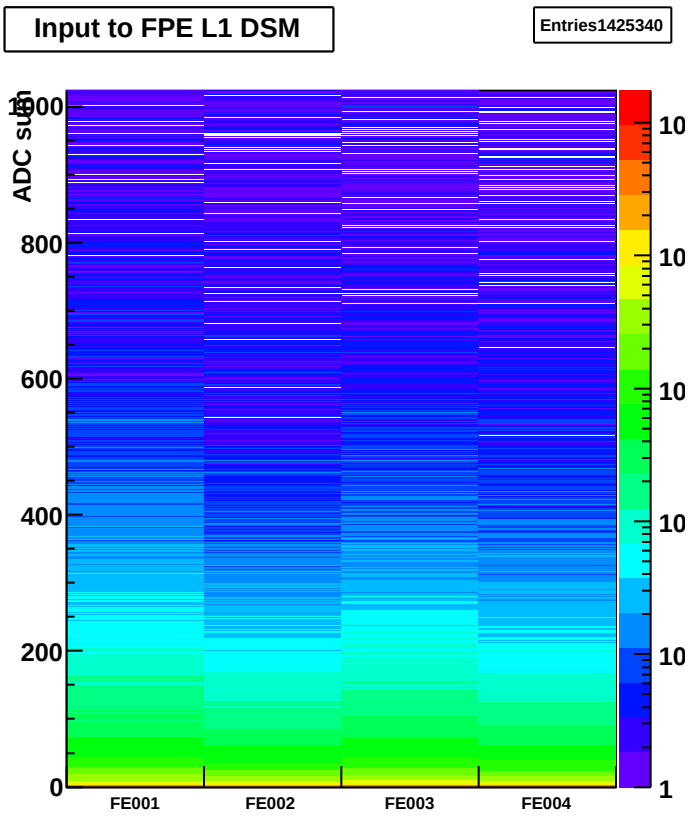

Input to FPD L2 DSM
Entries 1425340
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

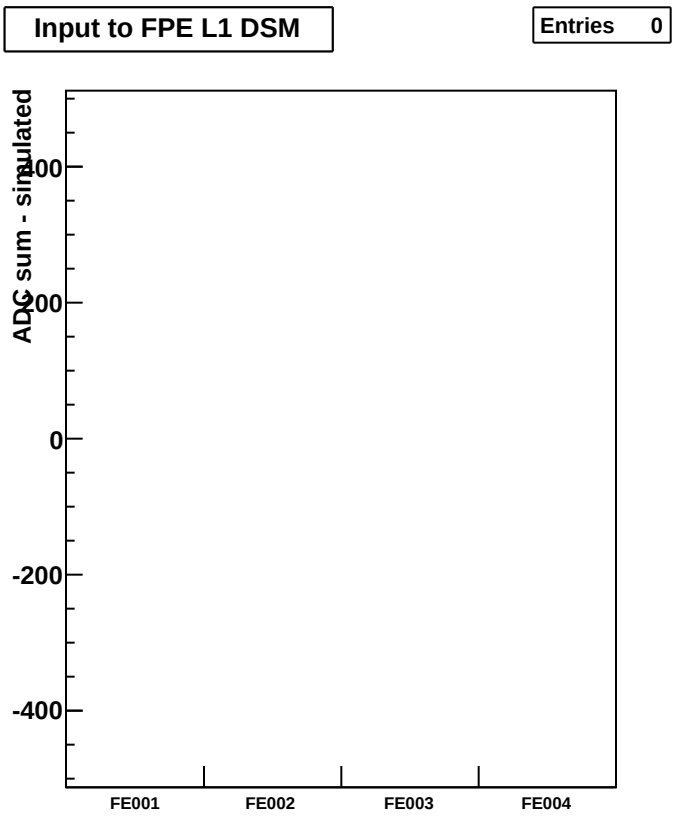

Input to FE001 QT board
Entries 1.140272e+07
ADC
channel

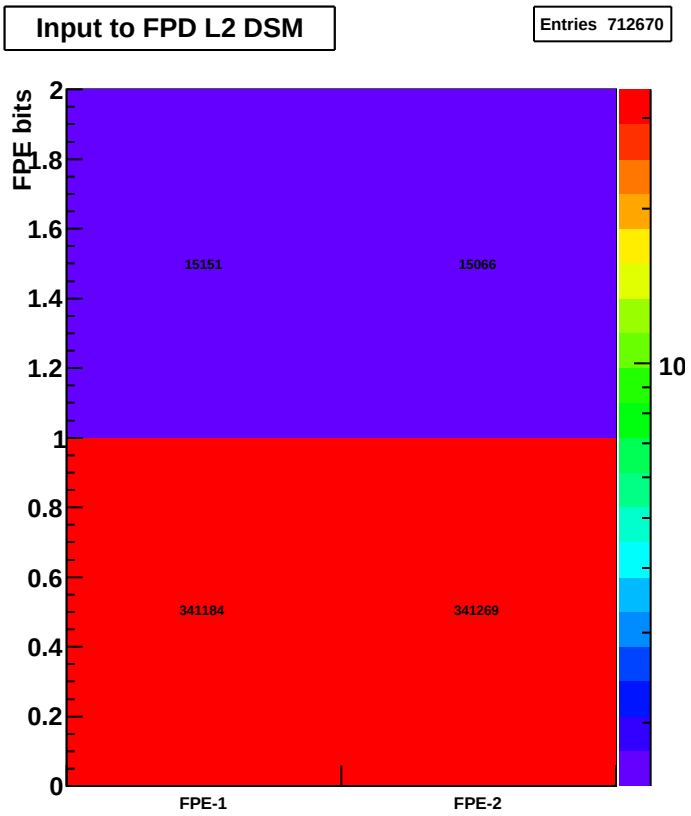

Input to FE002 QT board
Entries 1.140272e+07
ADC
channel

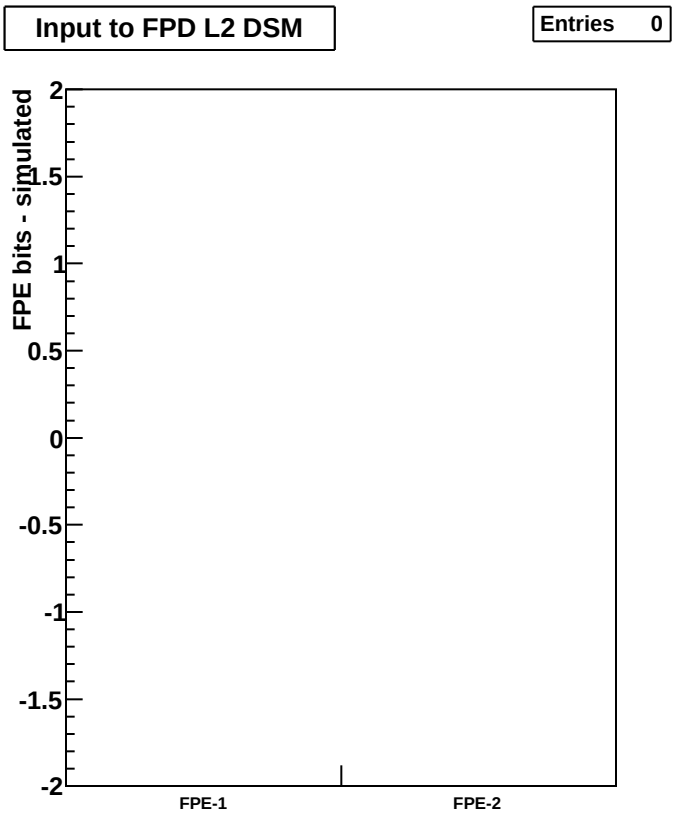

Input to FE003 QT board
Entries 1.140272e+07
ADC
channel


Input to FE004 QT board
Entries 1.140272e+07
ADC
channel


Input to FPE L1 DSM
Entries1425340
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 712670
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
15151
15066
341184
341269
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2