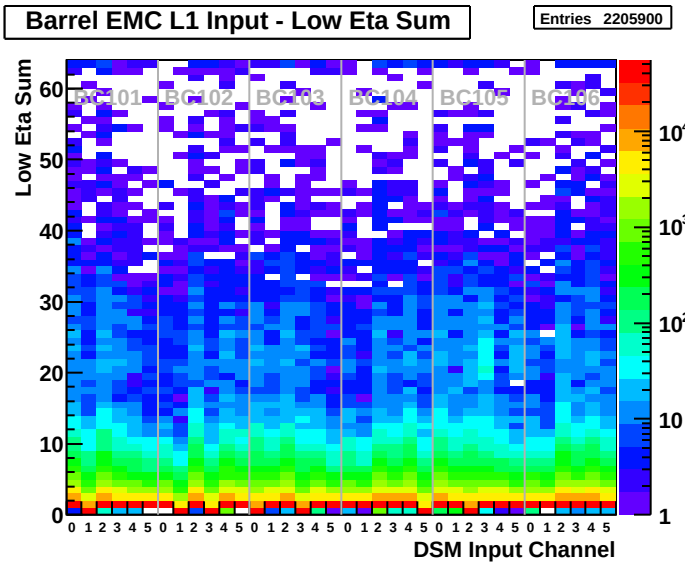
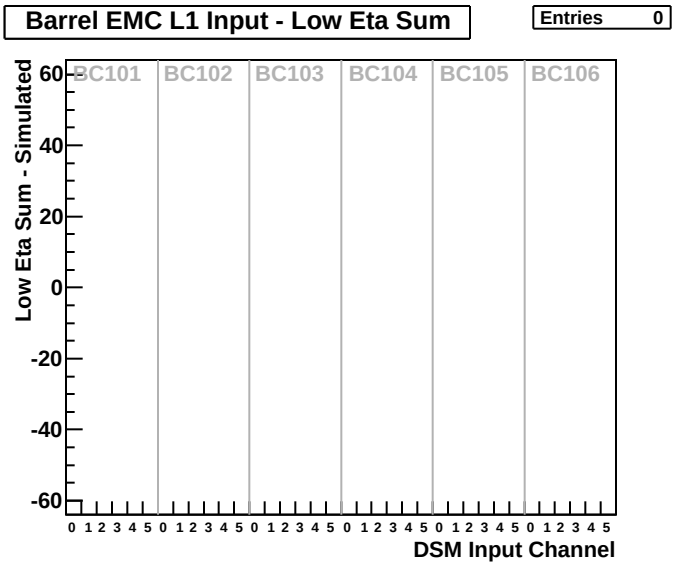
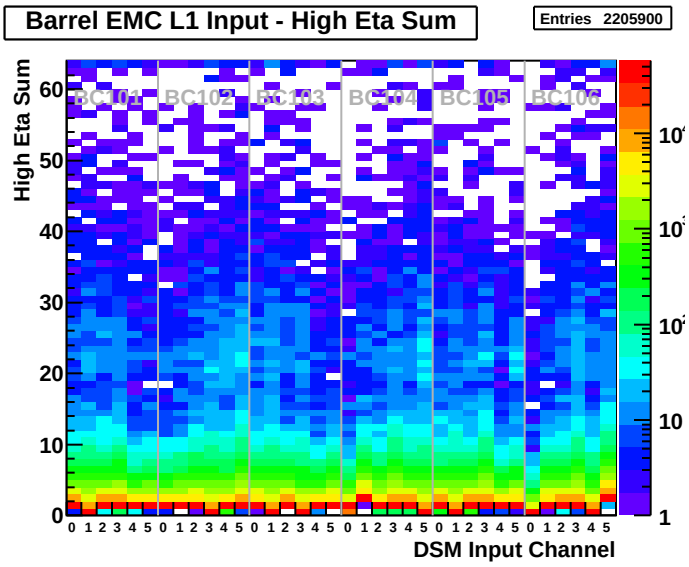
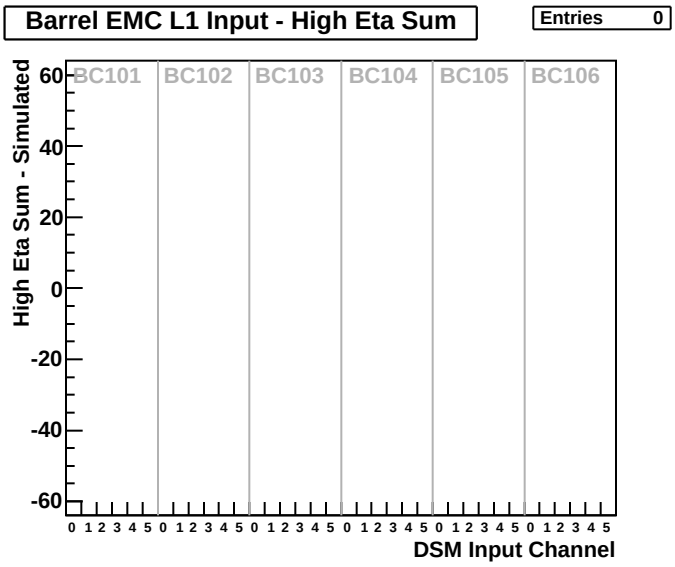
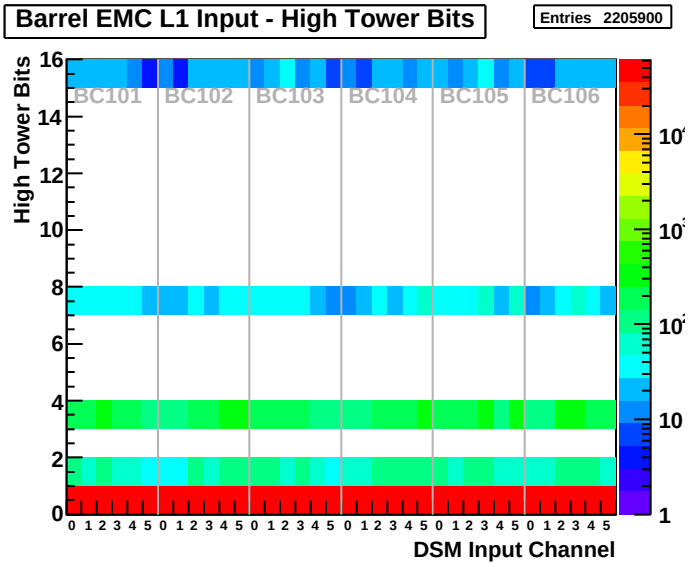
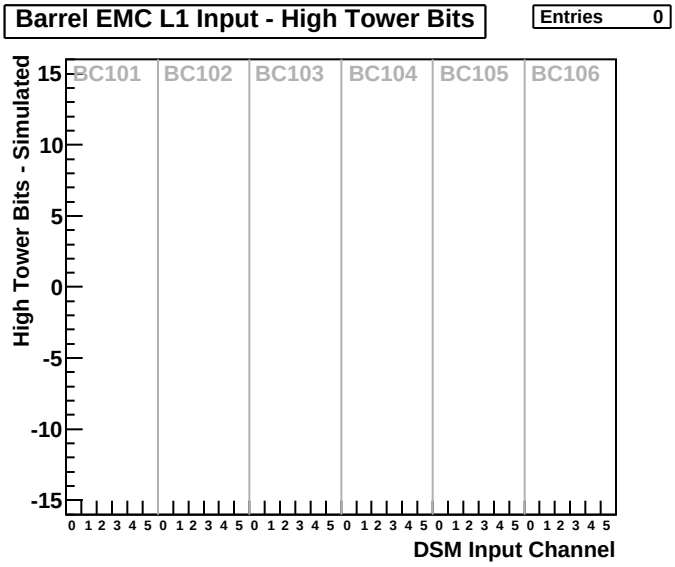

Barrel EMC L1 Input - Low Eta Sum
Entries 2205900
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

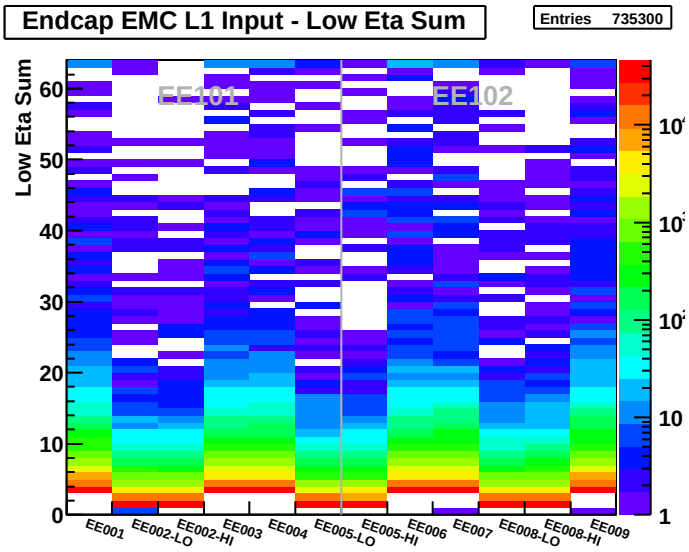

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

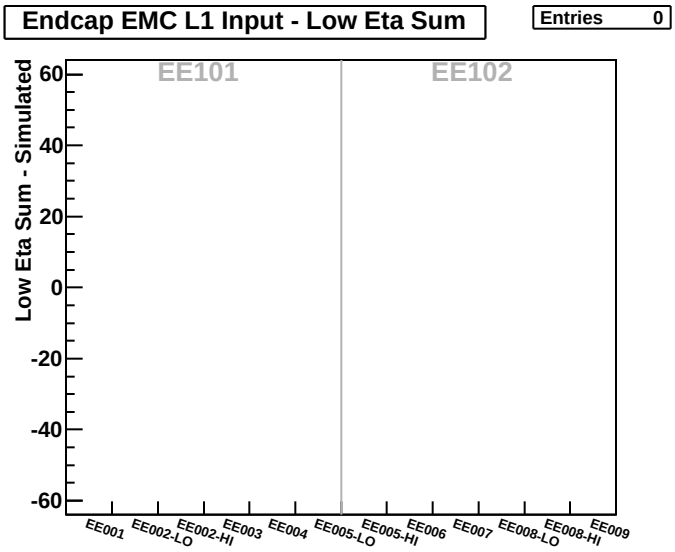

Barrel EMC L1 Input - High Eta Sum
Entries 2205900
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

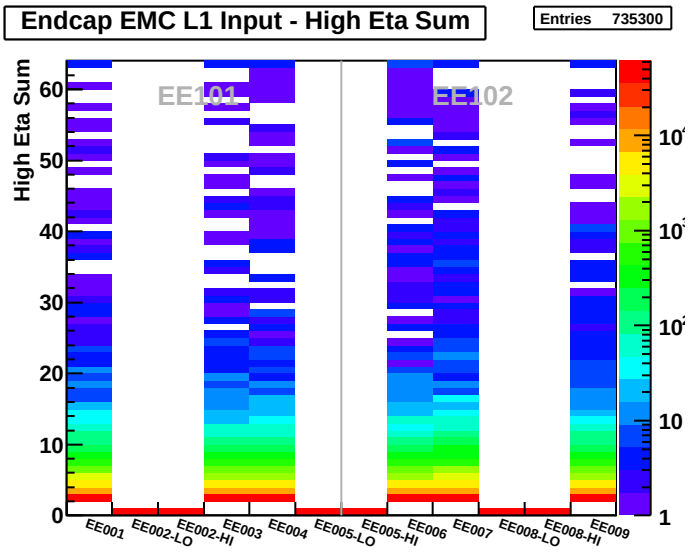

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

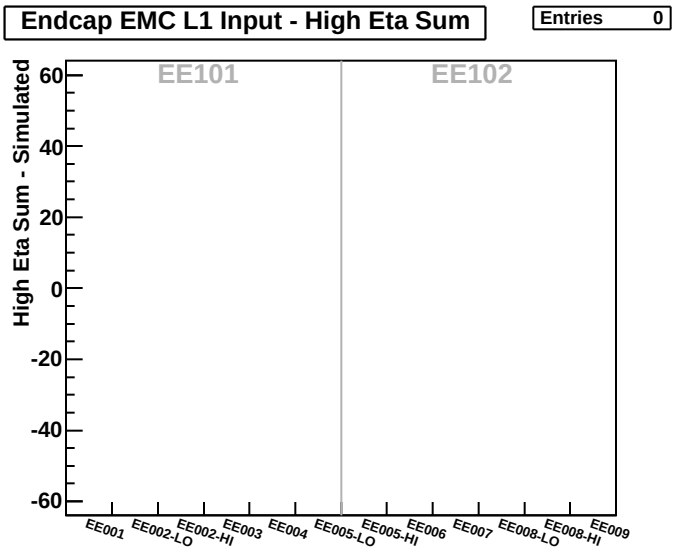

Barrel EMC L1 Input - High Tower Bits
Entries 2205900
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

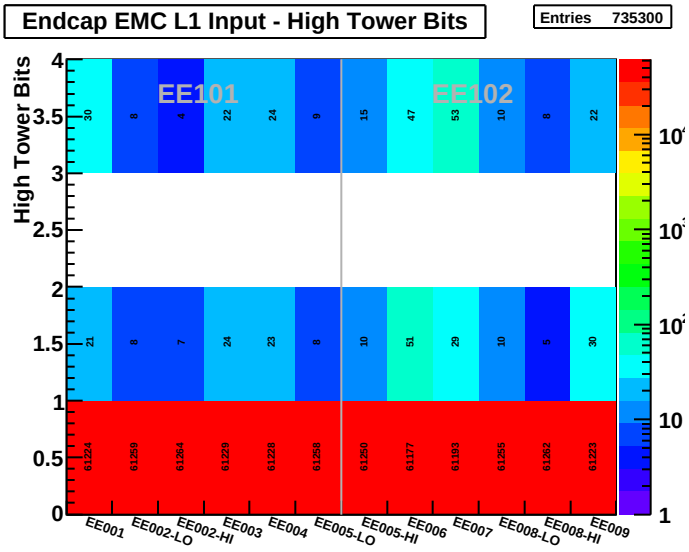

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

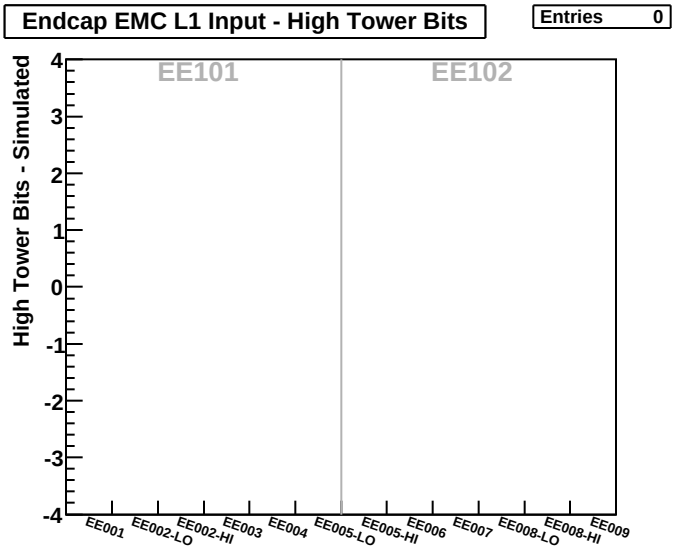

Endcap EMC L1 Input - Low Eta Sum
Entries 735300
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

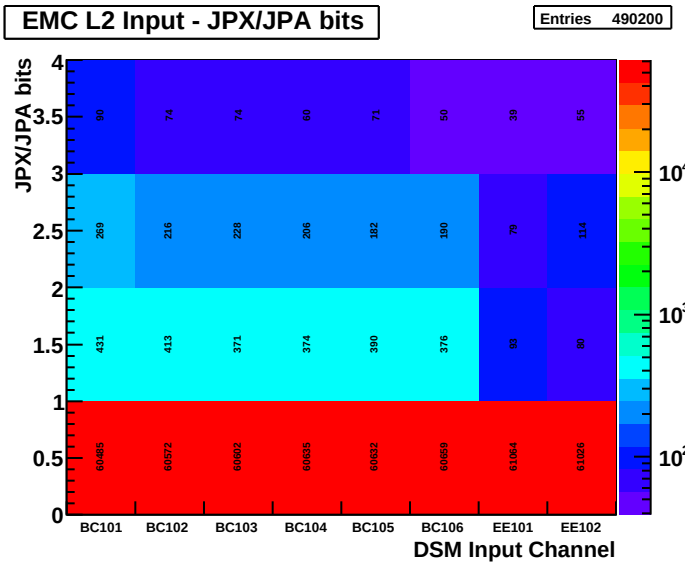

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

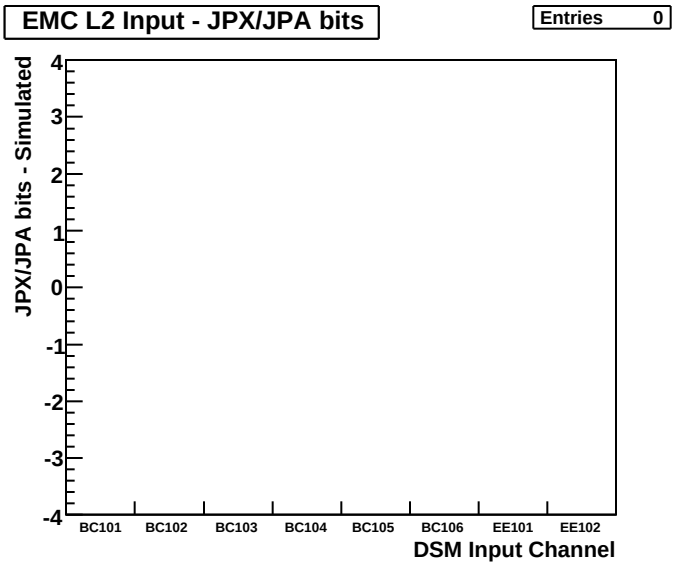

Endcap EMC L1 Input - High Eta Sum
Entries 735300
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

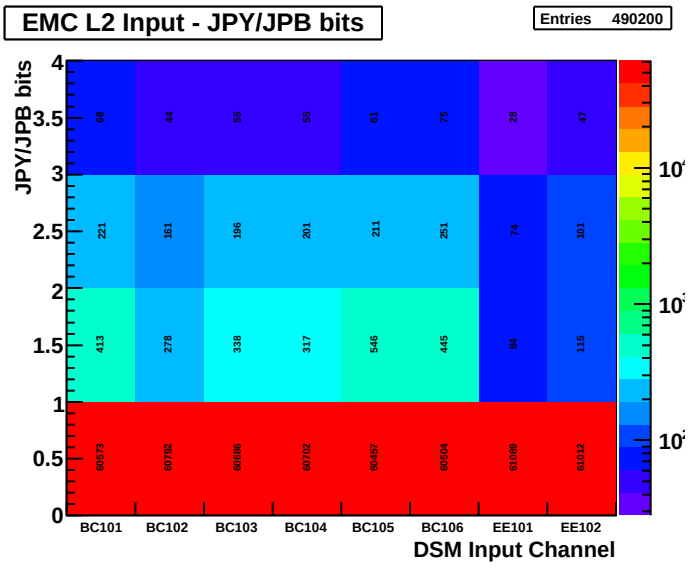

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

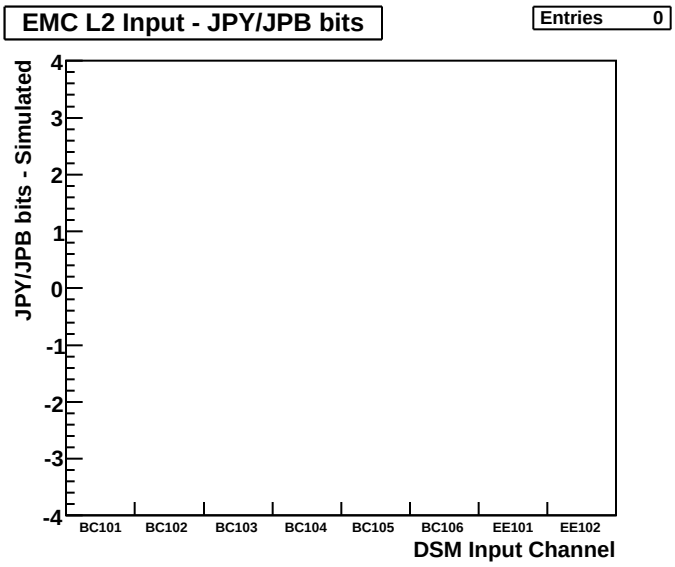

Endcap EMC L1 Input - High Tower Bits
Entries 735300
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

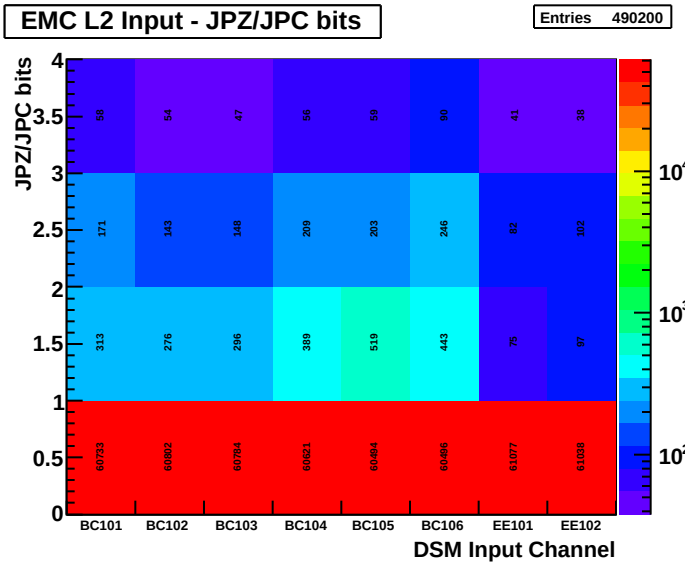

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

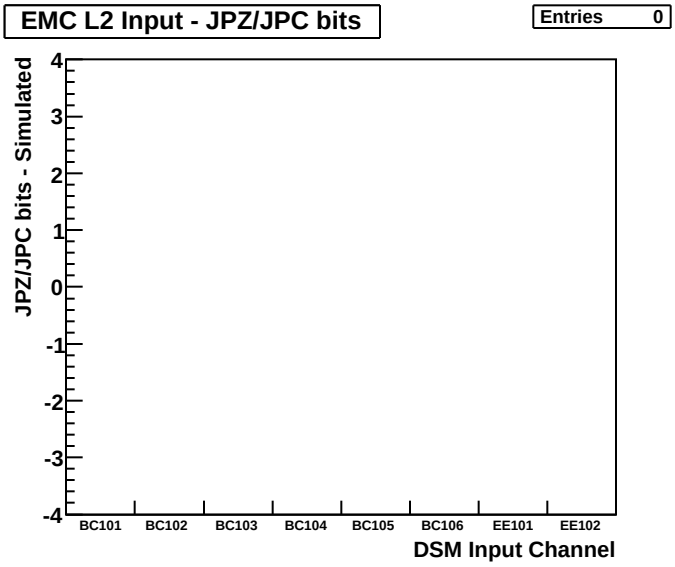

EMC L2 Input - JPX/JPA bits
Entries 490200
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
90 74 74 60 71 50 39 55
269 216 228 206 182 190 79 114
431 413 371 374 390 376 53 80
60485 60572 60602 60635 60632 60659 61064 61026

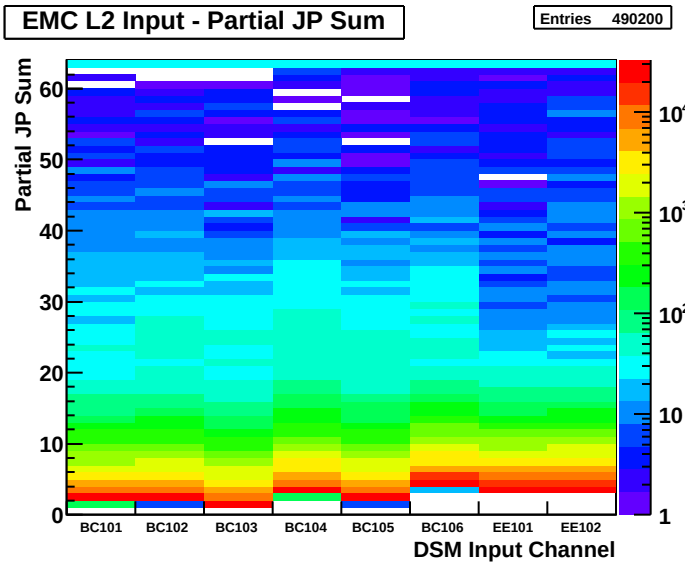

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

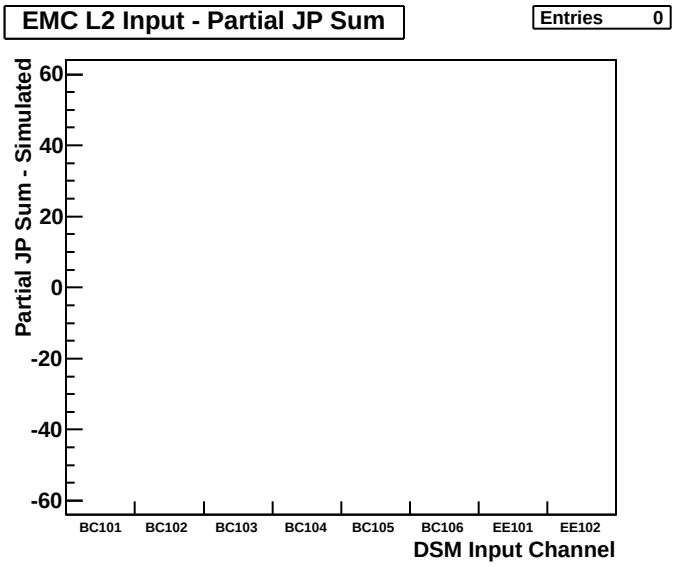

EMC L2 Input - JPY/JPB bits
Entries 490200
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
68 44 55 55 61 75 28 47
221 161 196 201 211 251 74 101
413 278 338 317 546 445 84 115
60573 60792 60686 60702 60457 60504 61089 61012

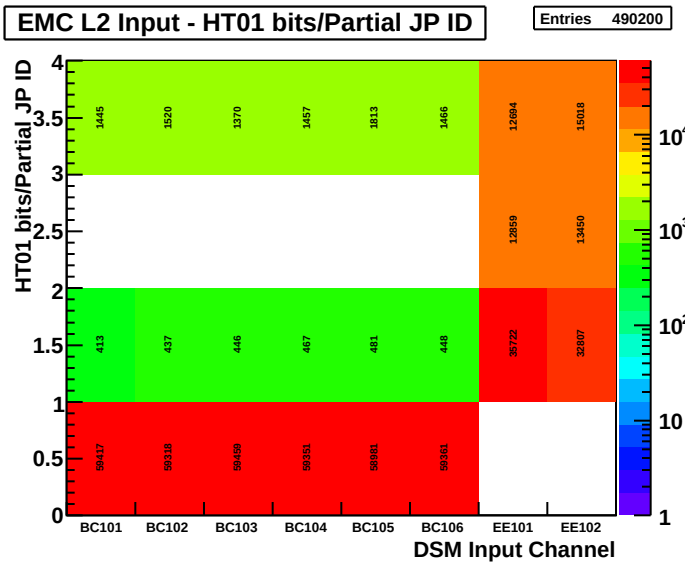

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

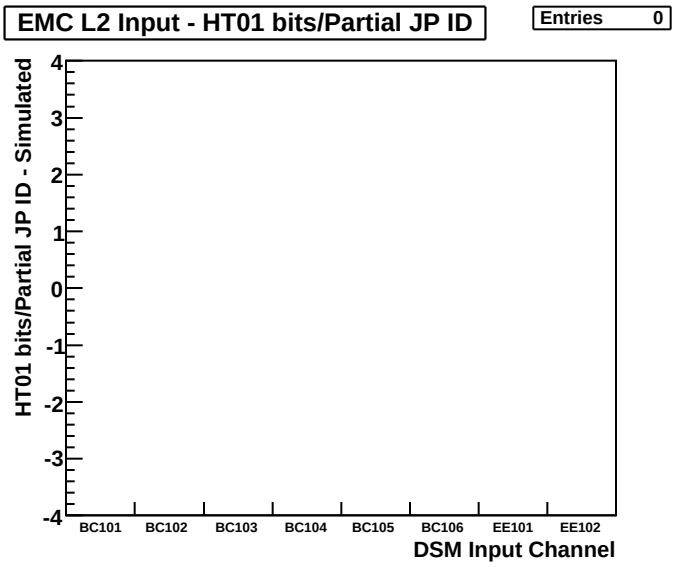

EMC L2 Input - JPZ/JPC bits
Entries 490200
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
58 54 47 56 59 90 41 38
171 143 148 209 203 246 82 102
313 276 296 389 519 443 75 97
60733 60802 60784 60621 60494 60496 61077 61038

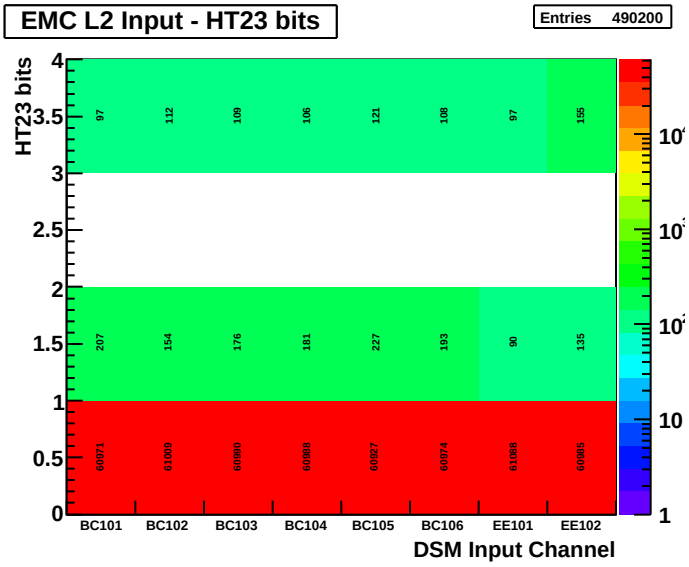

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

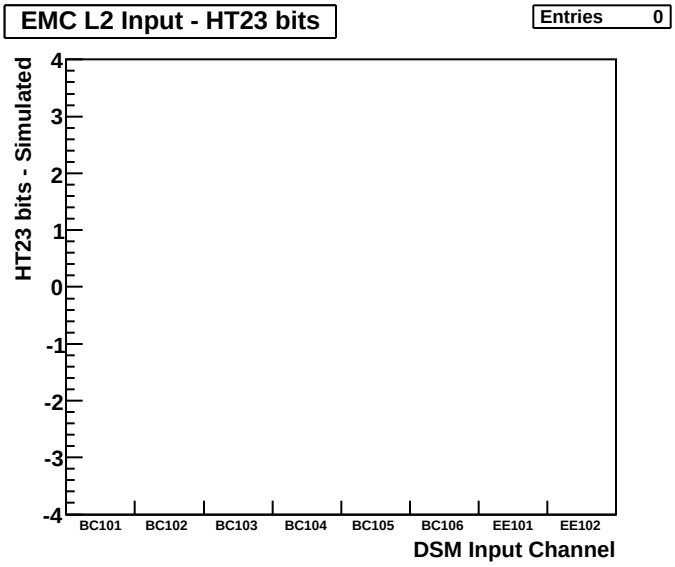

EMC L2 Input - Partial JP Sum
Entries 490200
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

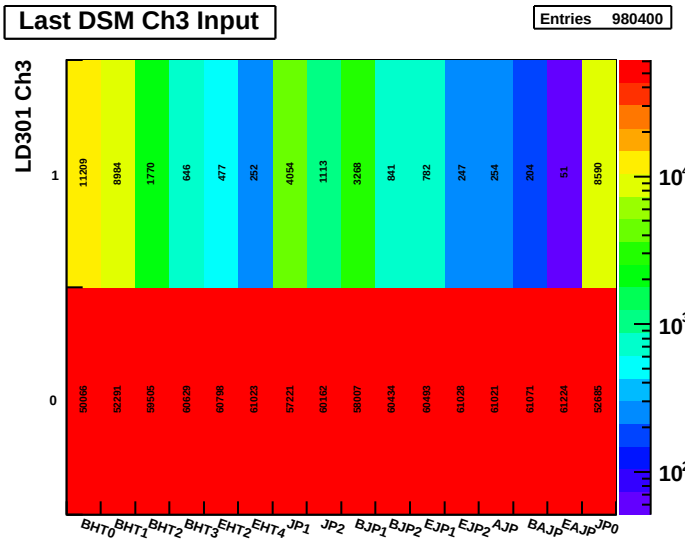

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

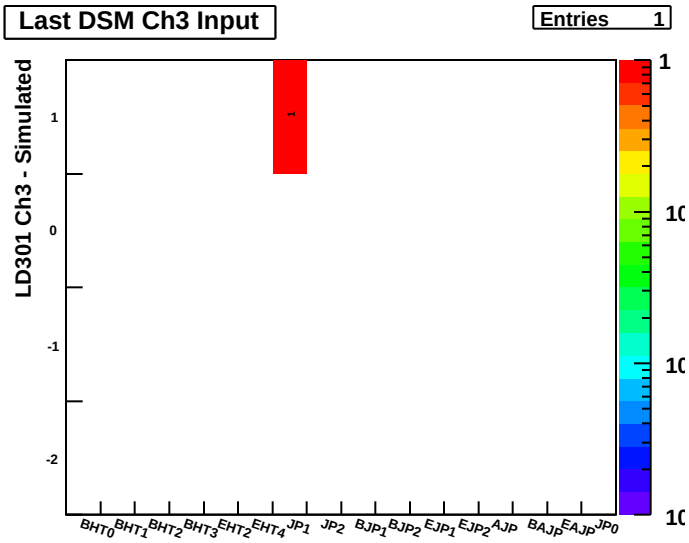

EMC L2 Input - HT01 bits/Partial JP ID
Entries 490200
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

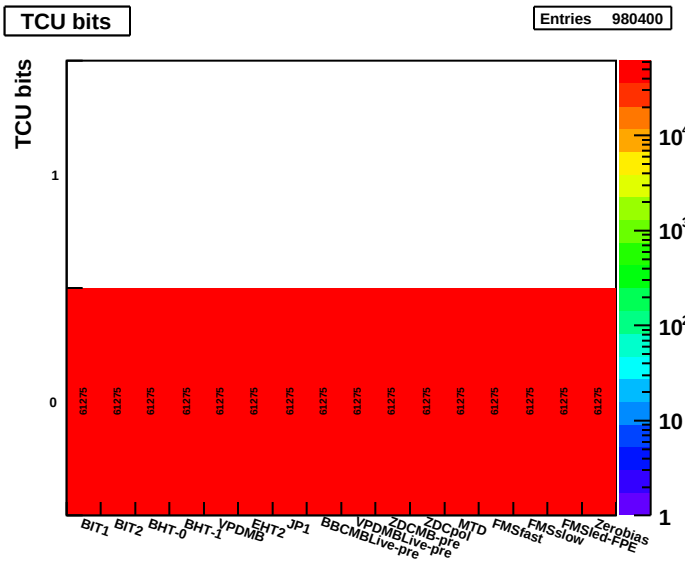

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

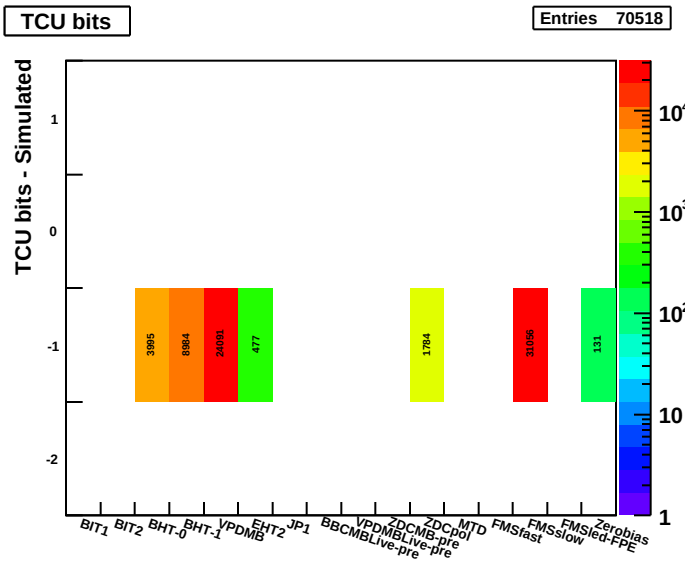

EMC L2 Input - HT23 bits
Entries 490200
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

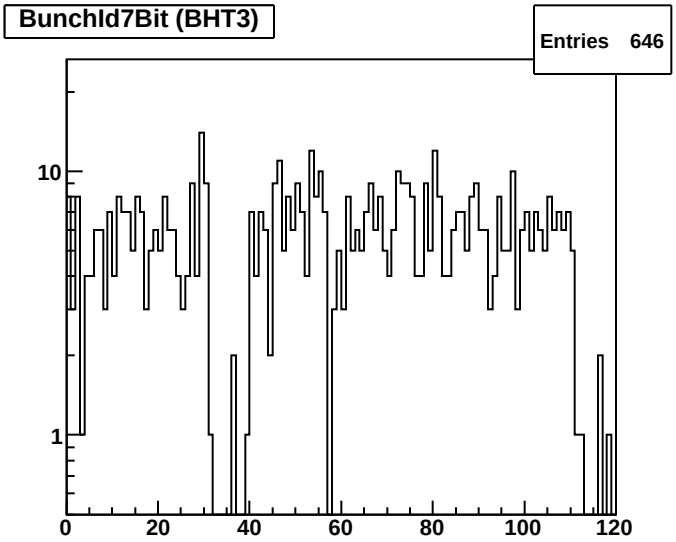

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

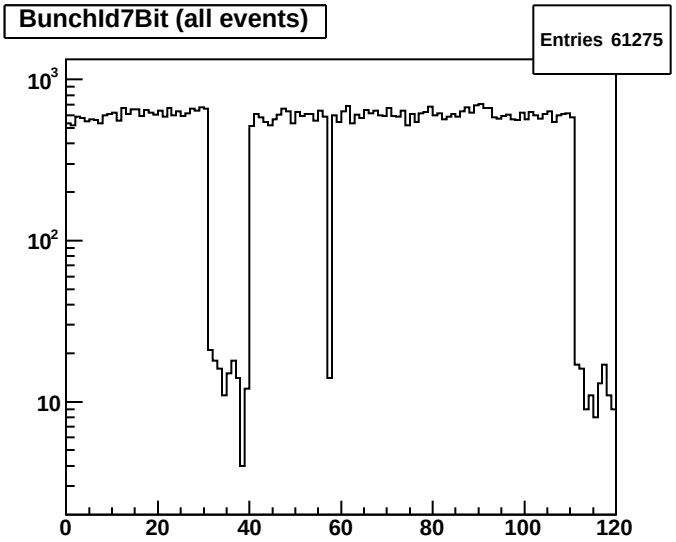

Last DSM Ch3 Input
Entries 980400
LD301 Ch3
11209 8984 1770 646 477 252 4054 1113 3268 941 782 247 254 204 51 8590
50066 52291 59505 60629 60798 61023 57221 60162 58007 60434 60493 61028 61021 61071 61224 52685
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

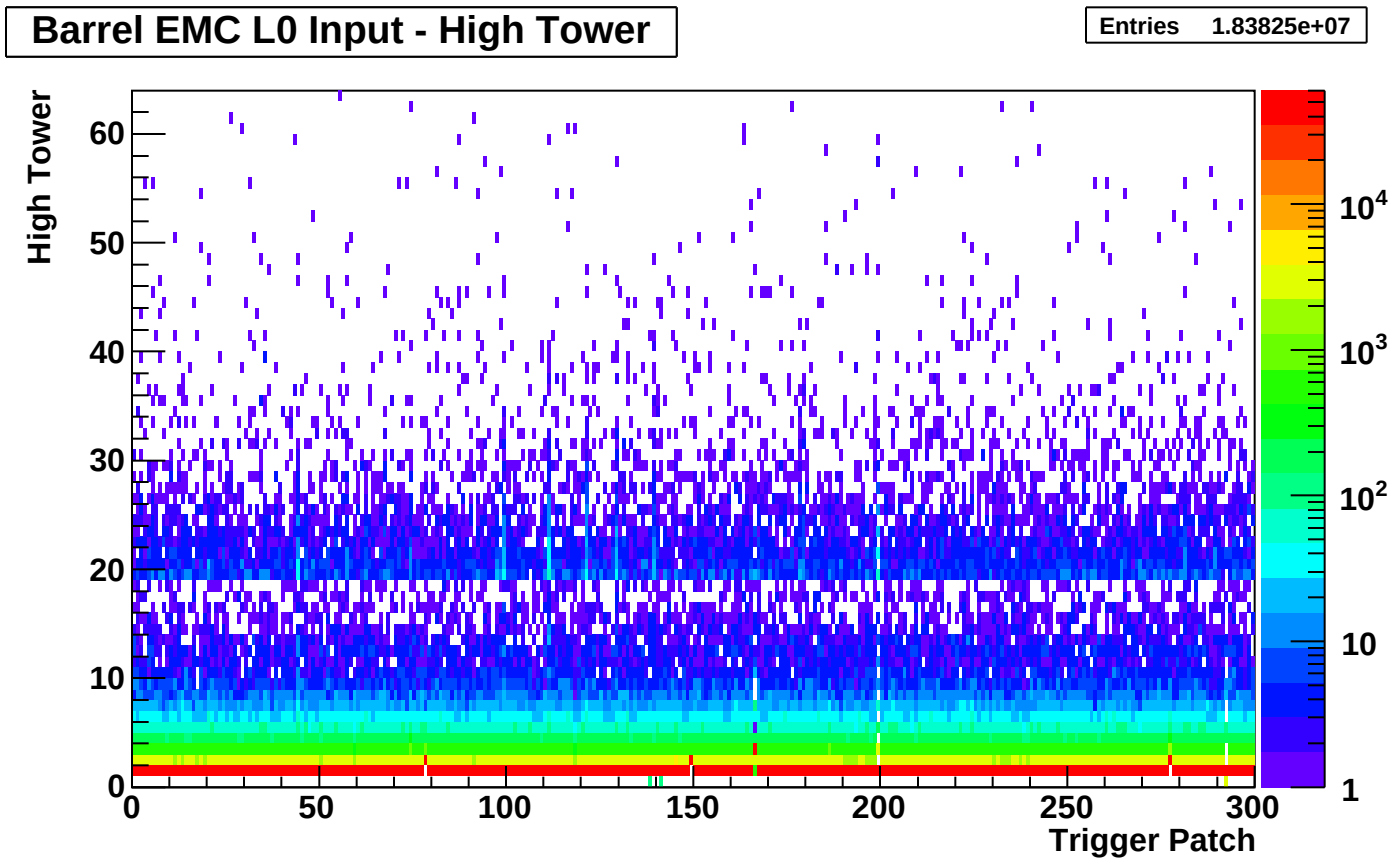

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

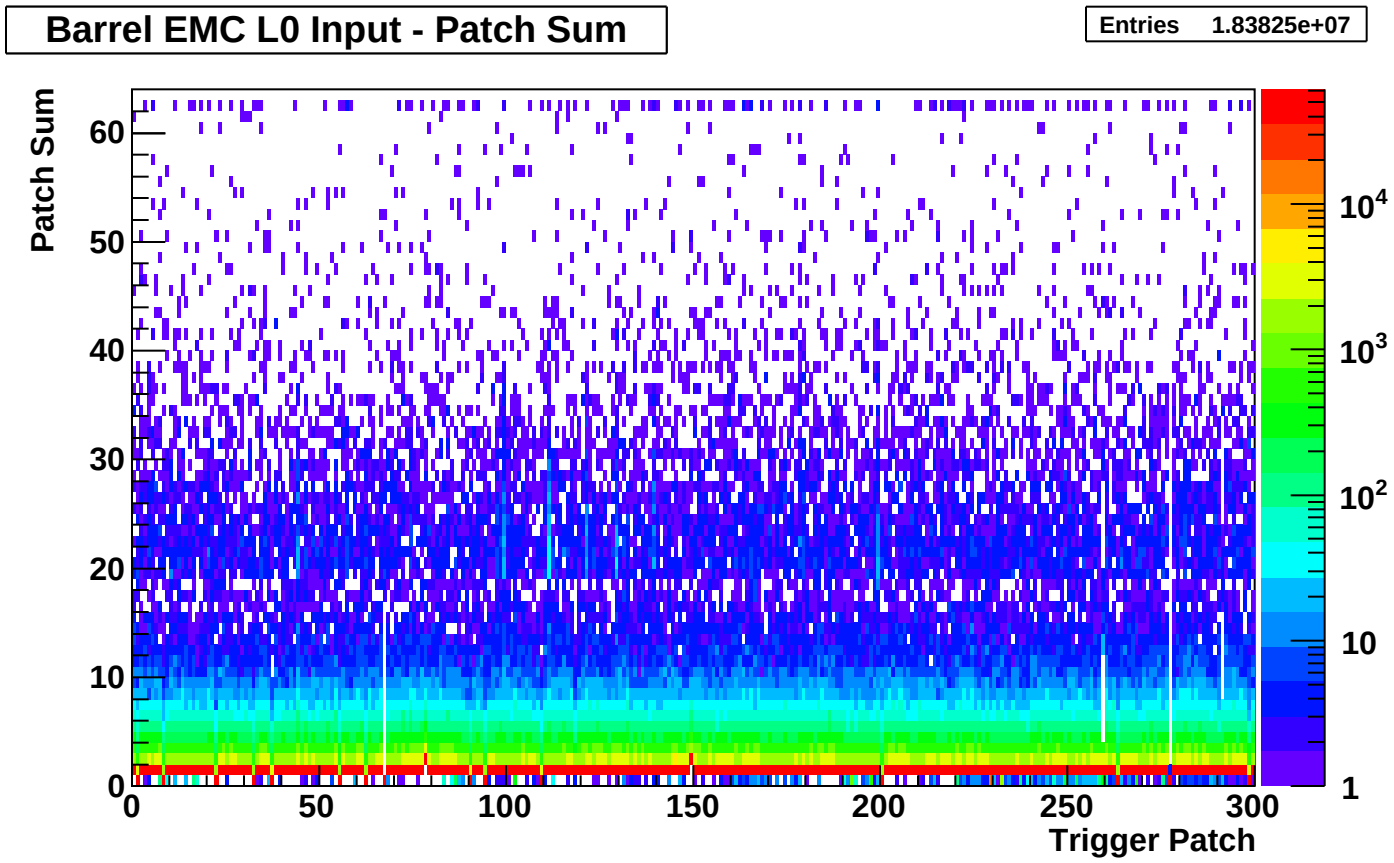

TCU bits
Entries 980400
TCU bits
61275 61275 61275 61275 61275 61275 61275 61275 61275 61275 61275 61275 61275 61275 61275 61275
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

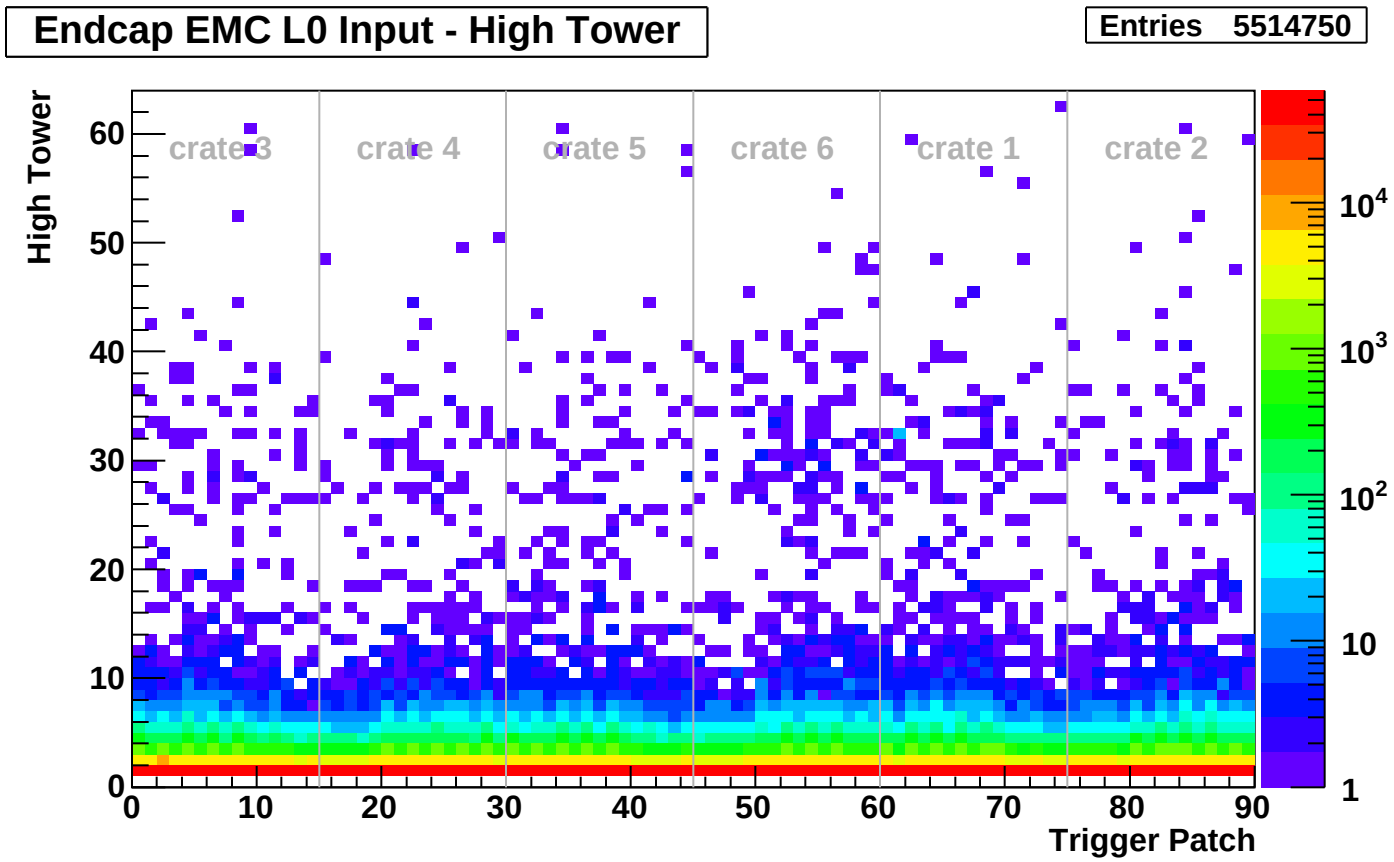

TCU bits
Entries 70518
TCU bits - Simulated
3995 8984 24091 477 1784 31056 131
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

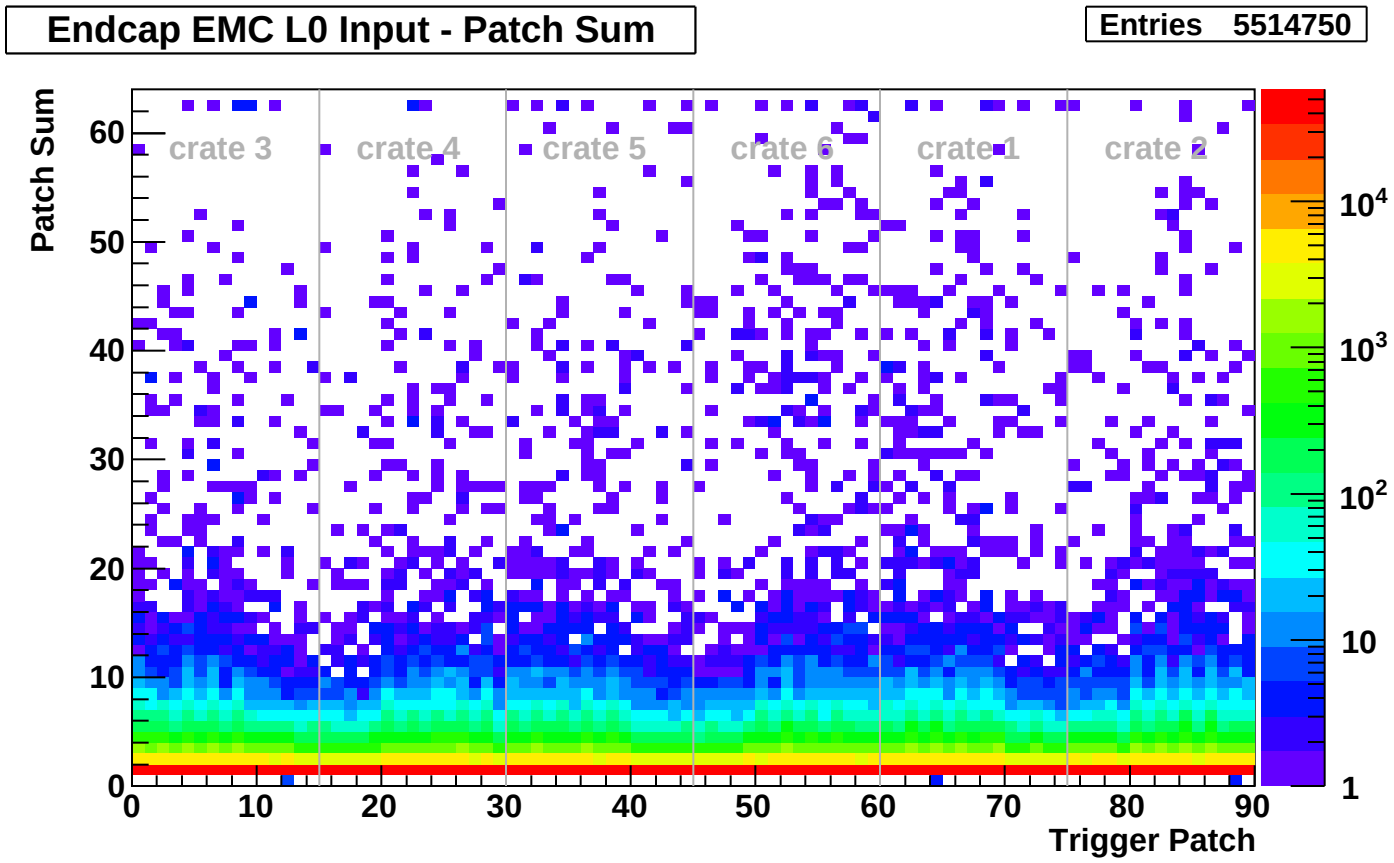

BunchId7Bit (BHT3)
Entries 646

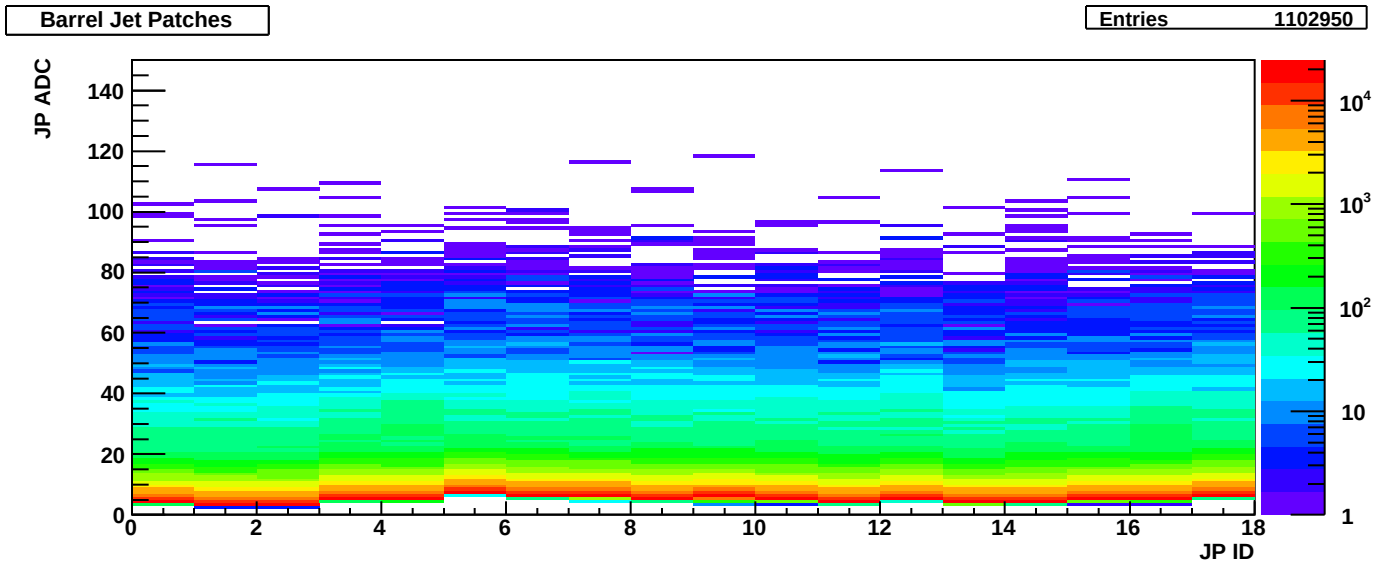

BunchId7Bit (all events)
Entries 61275

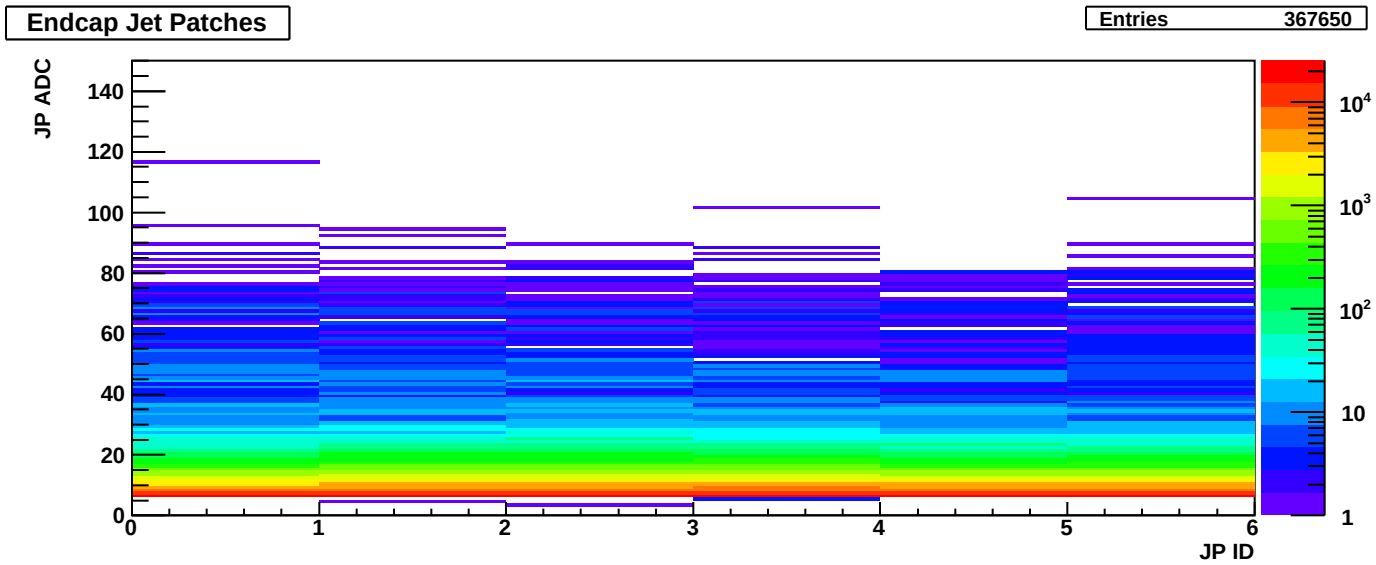

Barrel EMC L0 Input - High Tower
Entries 1.83825e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

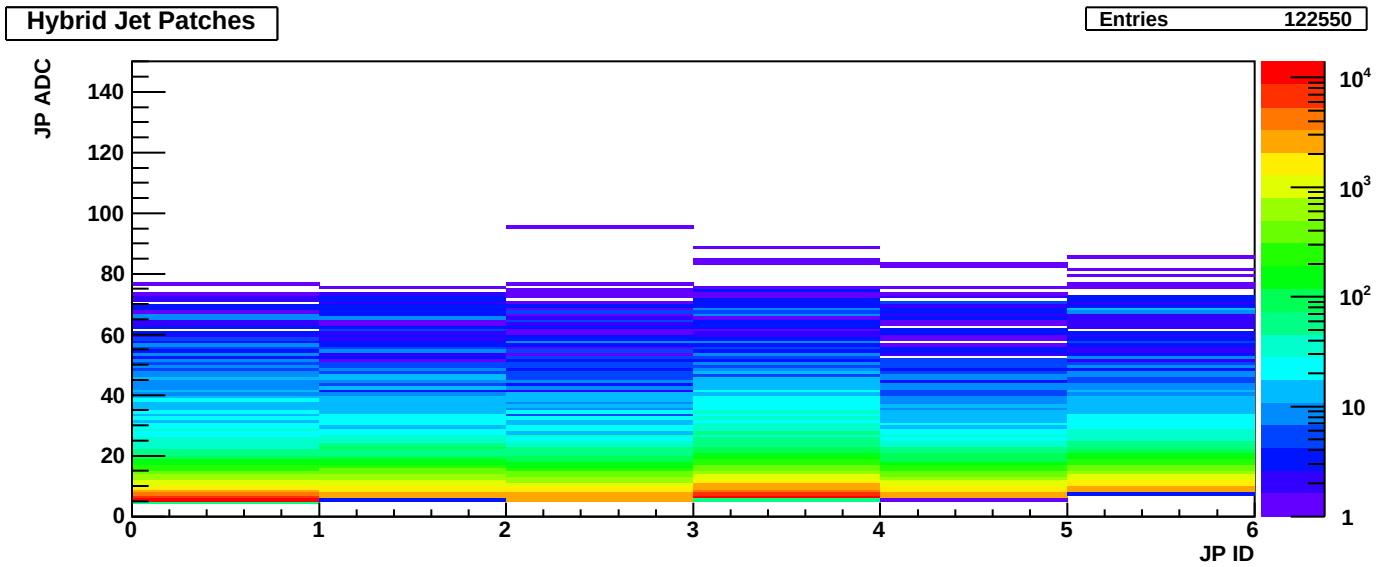

Barrel EMC L0 Input - Patch Sum
Entries 1.83825e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

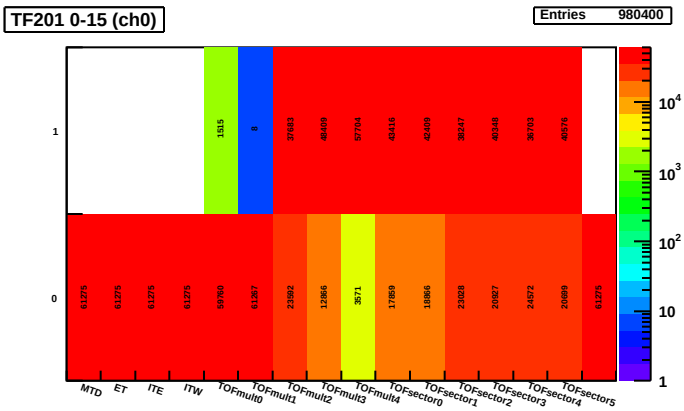

Endcap EMC L0 Input - High Tower
Entries 5514750
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

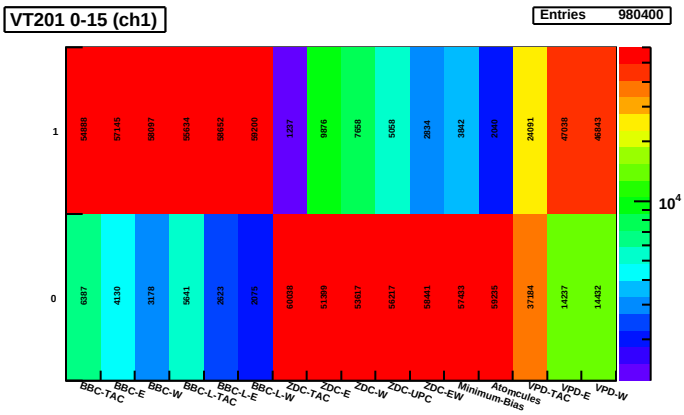

Endcap EMC L0 Input - Patch Sum
Entries 5514750
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

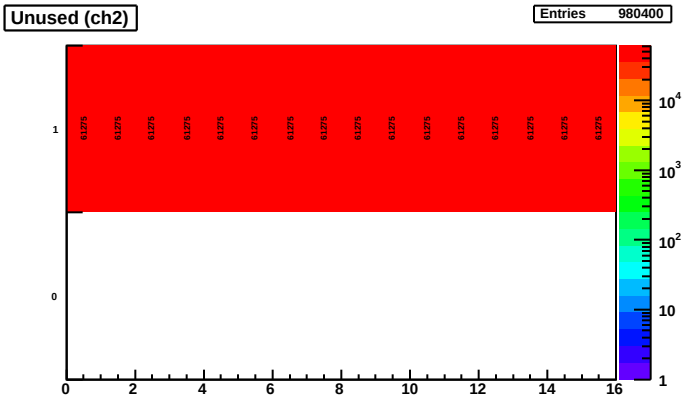

Barrel Jet Patches
Entries 1102950
JP ADC
JP ID

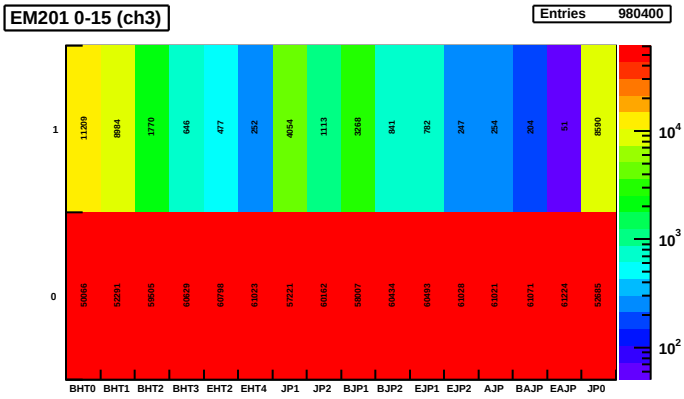

Endcap Jet Patches
Entries 367650
JP ADC
JP ID

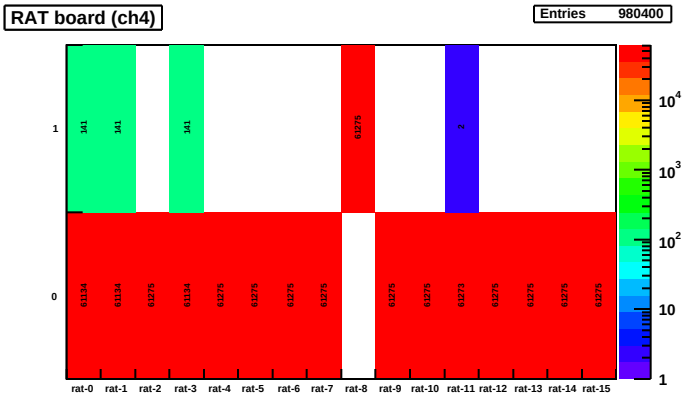

Hybrid Jet Patches
Entries 122550
JP ADC
JP ID

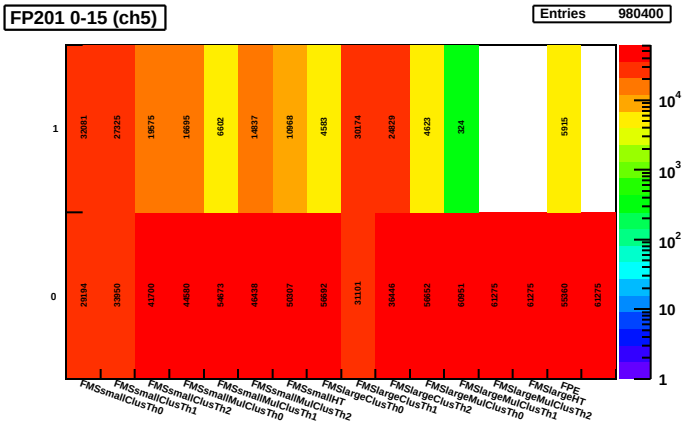

TF201 0-15 (ch0)
Entries 980400
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

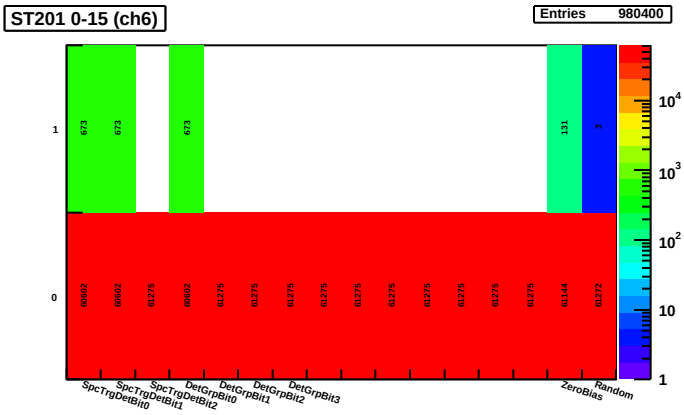

VT201 0-15 (ch1)
Entries 980400
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

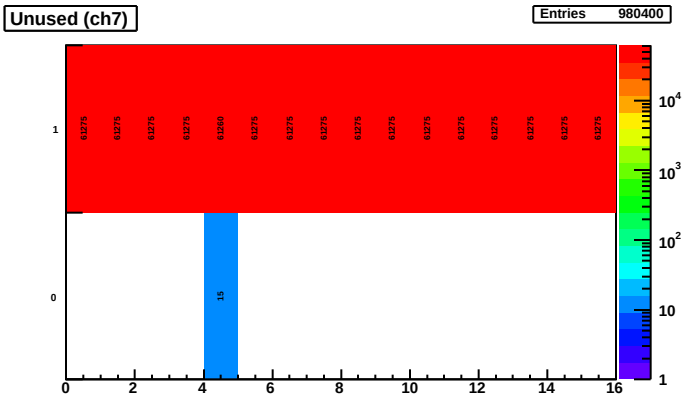

Unused (ch2)
Entries 980400

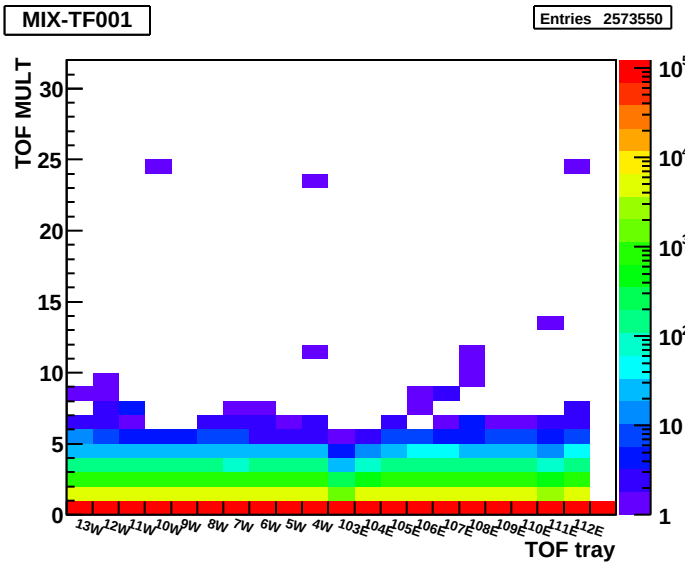

EM201 0-15 (ch3)
Entries 980400
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

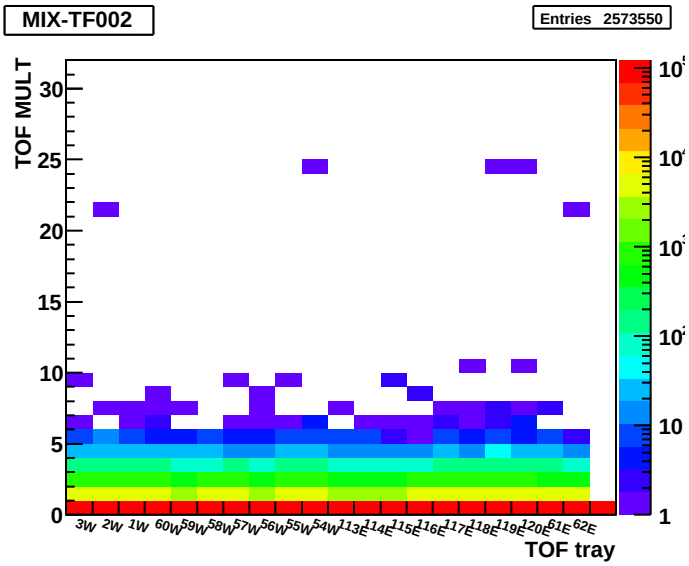

RAT board (ch4)
Entries 980400
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

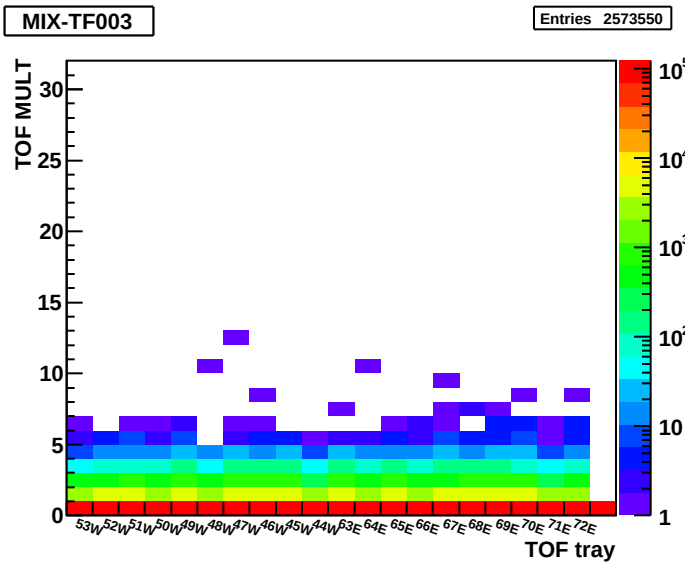

FP201 0-15 (ch5)
Entries 980400
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

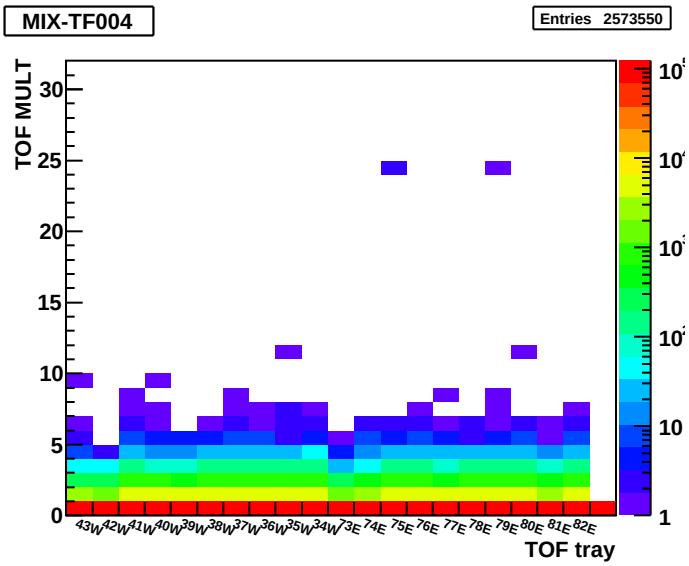

ST201 0-15 (ch6)
Entries 980400
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

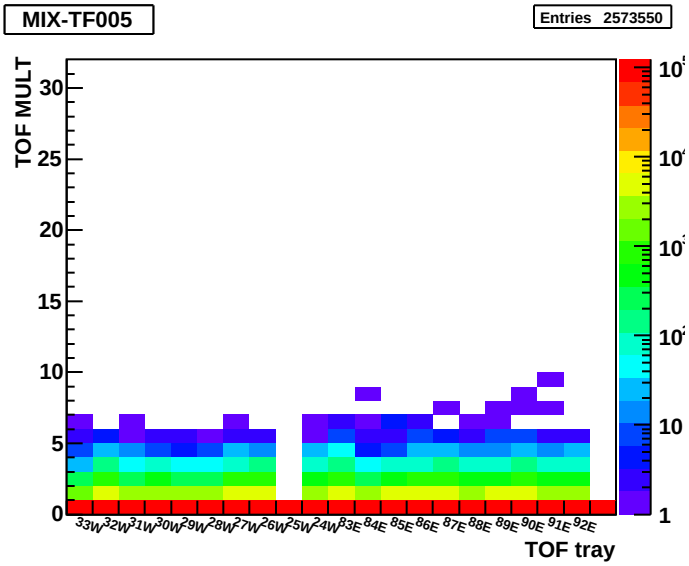

Unused (ch7)
Entries 980400

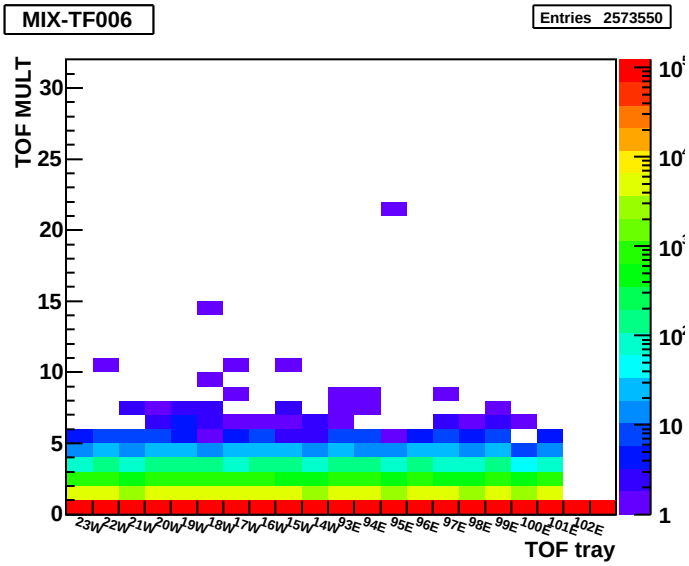

MIX-TF001
Entries 2573550
TOF MULT
TOF tray

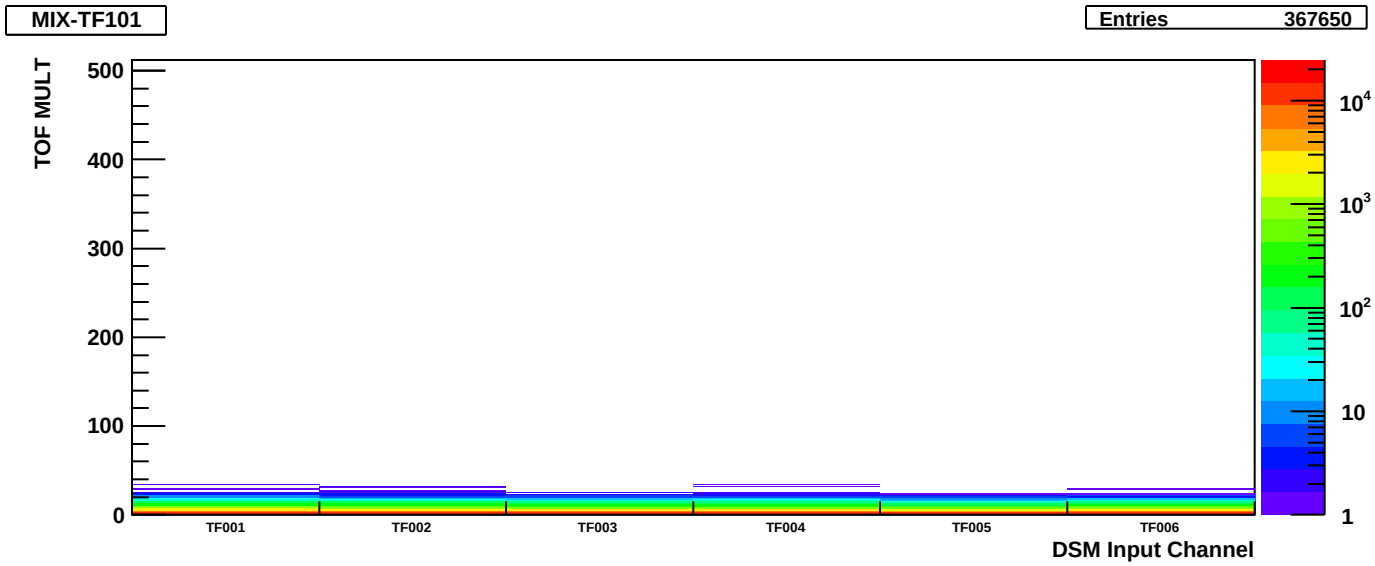

MIX-TF002
Entries 2573550
TOF MULT
TOF tray

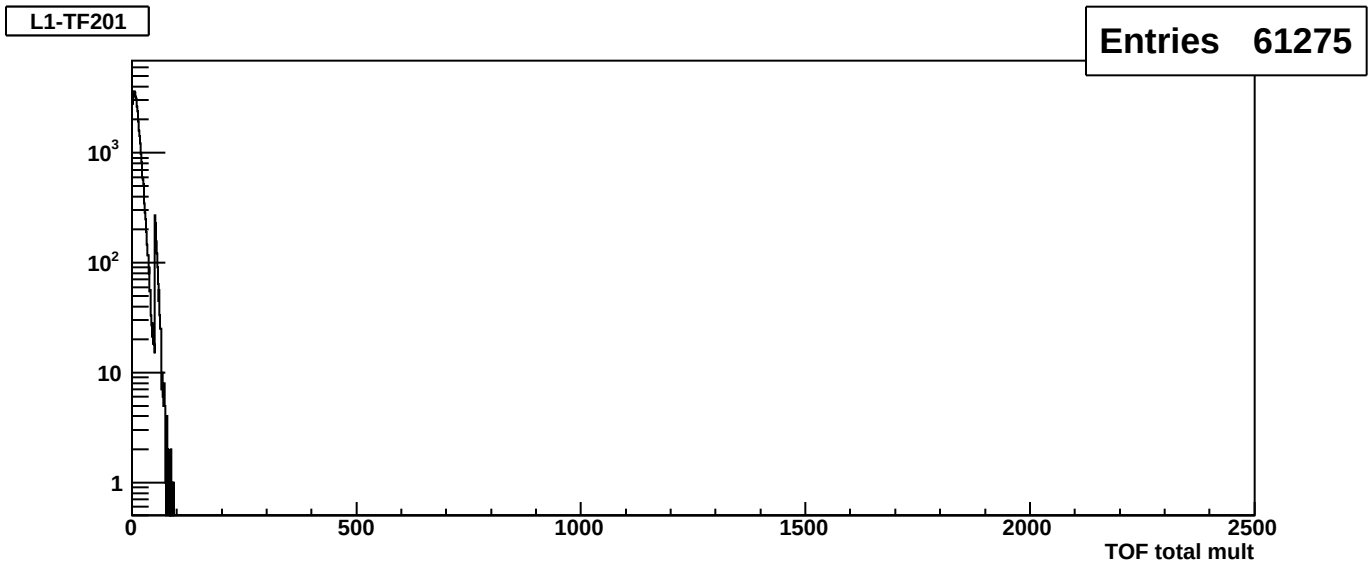

MIX-TF003
Entries 2573550
TOF MULT
TOF tray

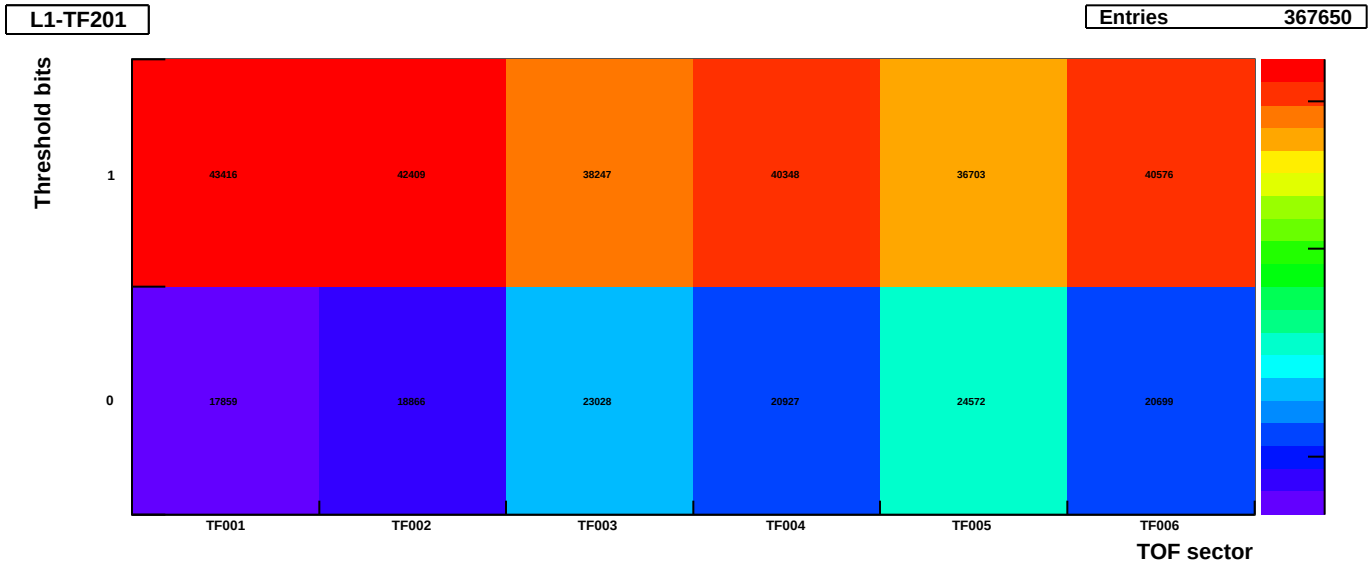

MIX-TF004
Entries 2573550
TOF MULT
TOF tray

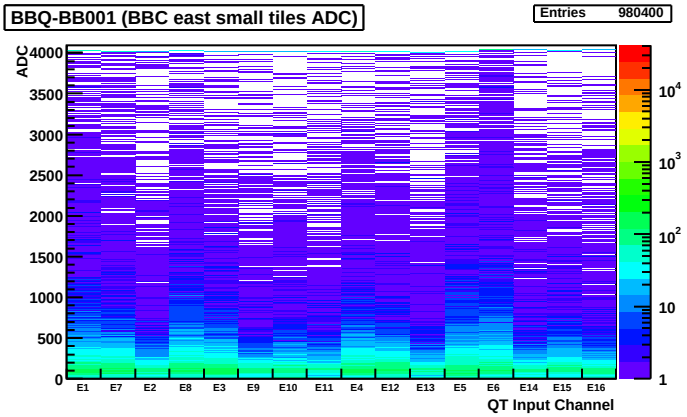

MIX-TF005
Entries 2573550
TOF MULT
TOF tray

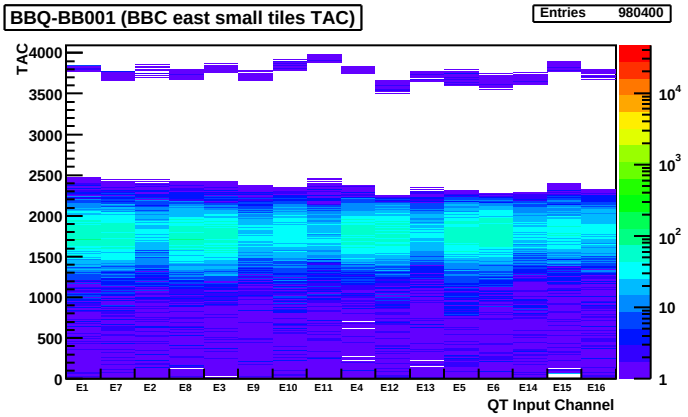

MIX-TF006
Entries 2573550
TOF MULT
TOF tray

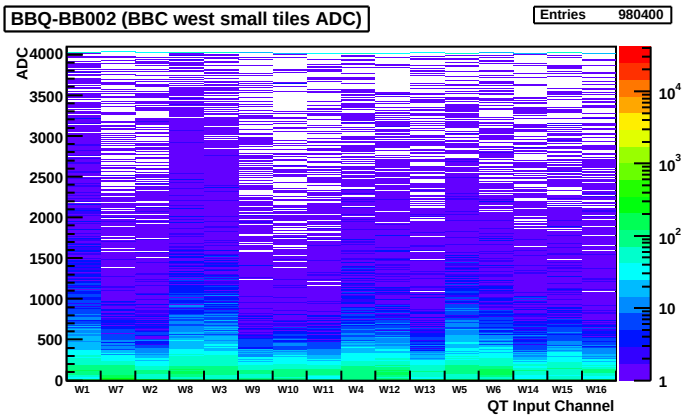

MIX-TF101
Entries 367650
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

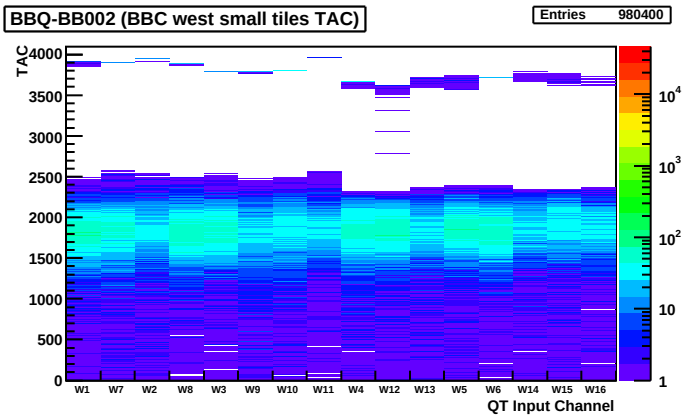

L1-TF201
Entries 61275
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

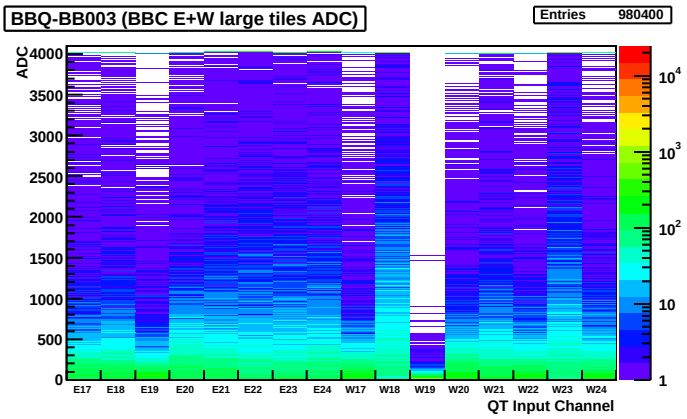

L1-TF201
Entries 367650
Threshold bits
1
0
43416
42409
38247
40348
36703
40576
17859
18866
23028
20927
24572
20699
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

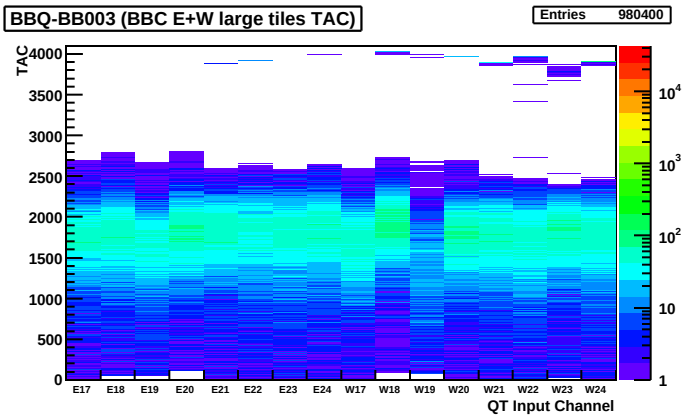

BBQ-BB001 (BBC east small tiles ADC)
Entries 980400
ADC
QT Input Channel

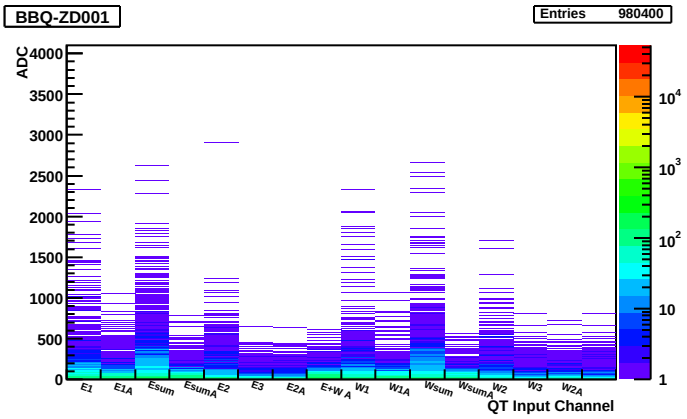

BBQ-BB001 (BBC east small tiles TAC)
Entries 980400
TAC
QT Input Channel

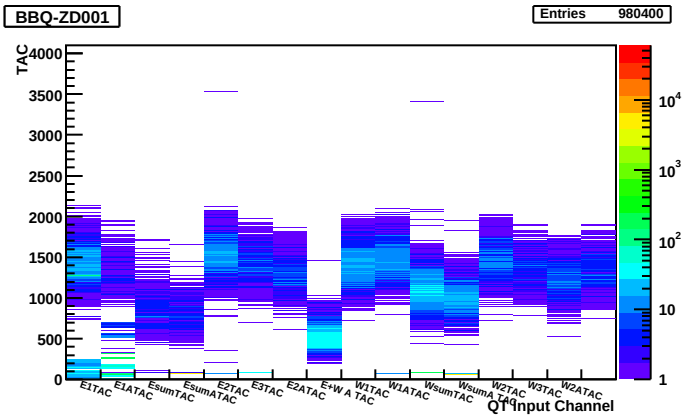

BBQ-BB002 (BBC west small tiles ADC)
Entries 980400
ADC
QT Input Channel

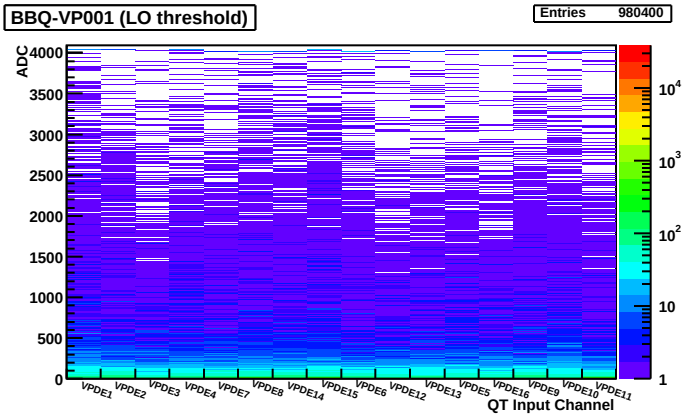

BBQ-BB002 (BBC west small tiles TAC)
Entries 980400
TAC
QT Input Channel

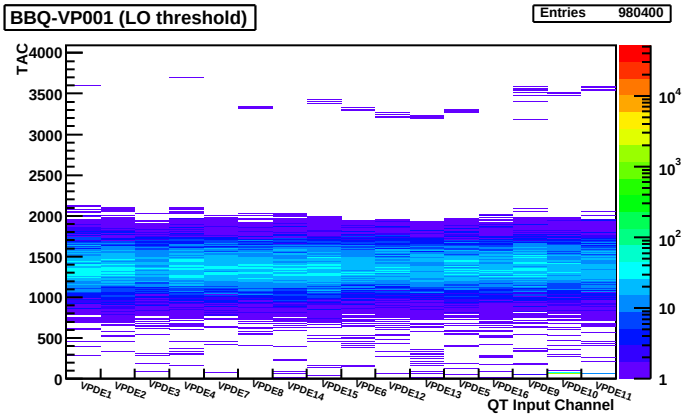

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 980400
ADC
QT Input Channel

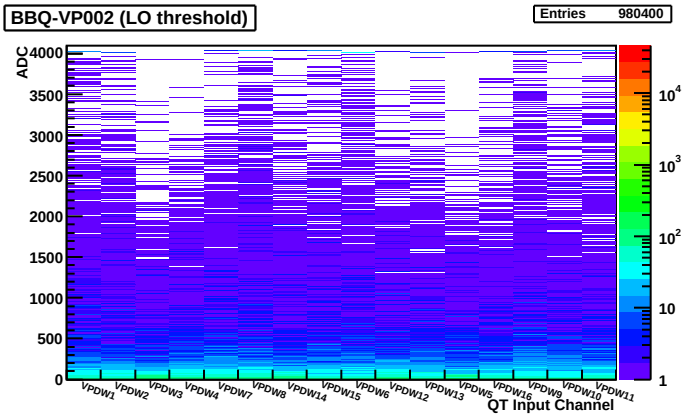

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 980400
TAC
QT Input Channel

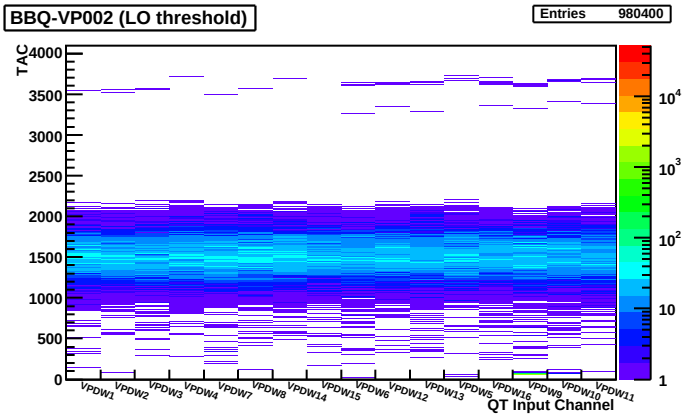

BBQ-ZD001
Entries 980400
ADC
QT Input Channel

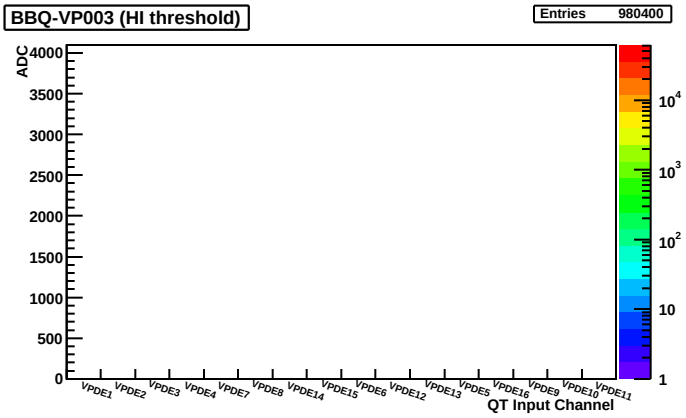

BBQ-ZD001
Entries 980400
TAC
QT Input Channel

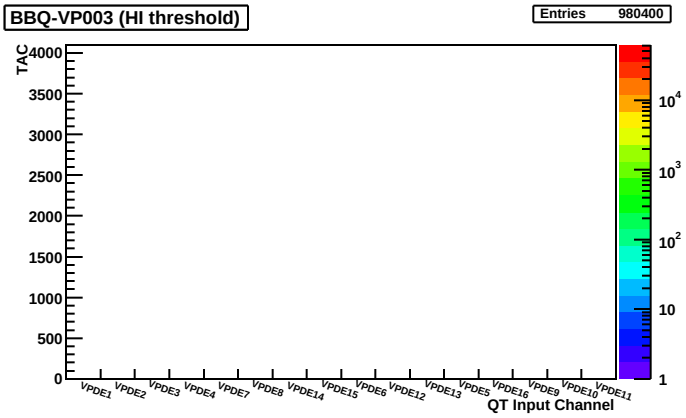

BBQ-VP001 (LO threshold)
Entries 980400
ADC
QT Input Channel

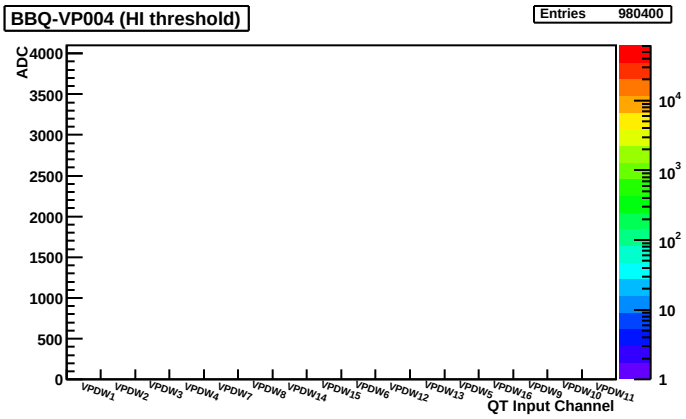

BBQ-VP001 (LO threshold)
Entries 980400
TAC
QT Input Channel

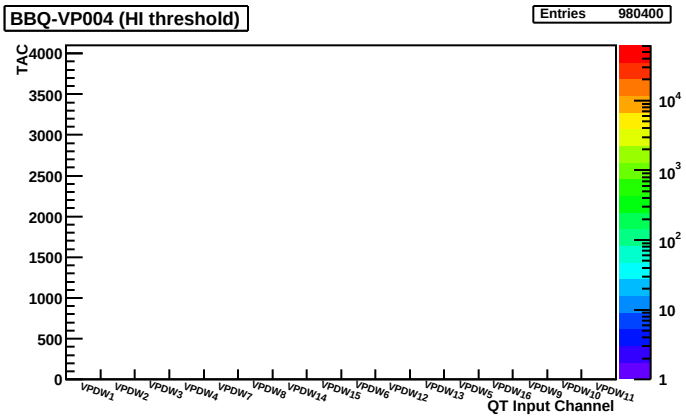

BBQ-VP002 (LO threshold)
Entries 980400
ADC
QT Input Channel

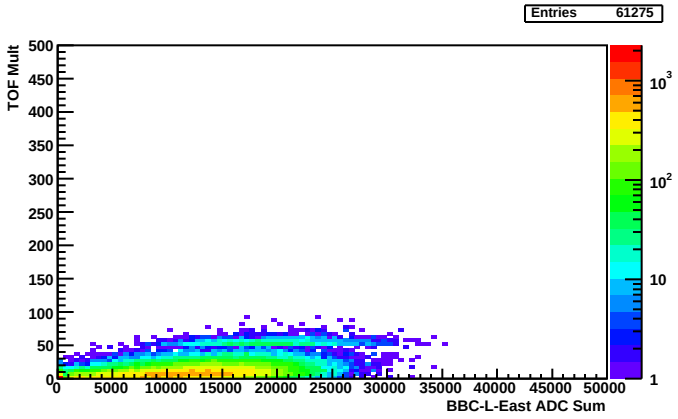

BBQ-VP002 (LO threshold)
Entries 980400
TAC
QT Input Channel

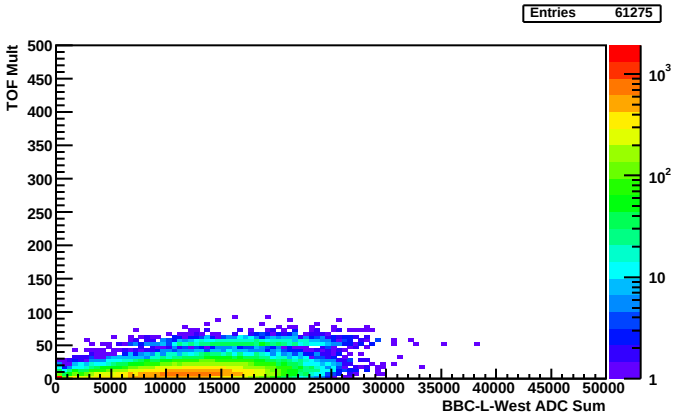

BBQ-VP003 (HI threshold)
Entries 980400
ADC
QT Input Channel

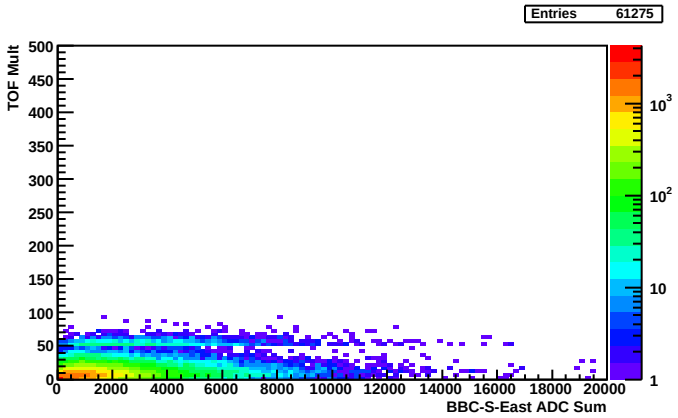

BBQ-VP003 (HI threshold)
Entries 980400
TAC
QT Input Channel

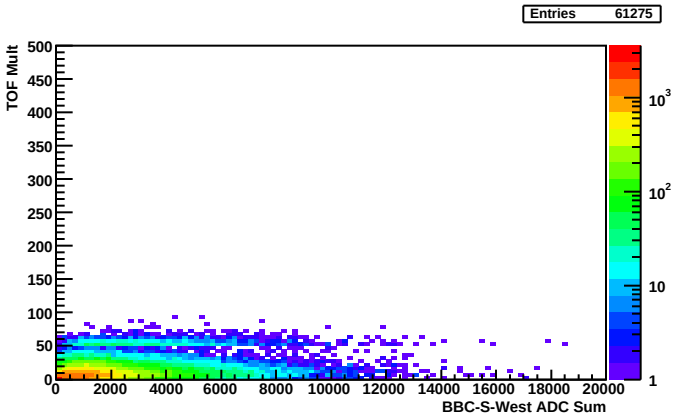

BBQ-VP004 (HI threshold)
Entries 980400
ADC
QT Input Channel

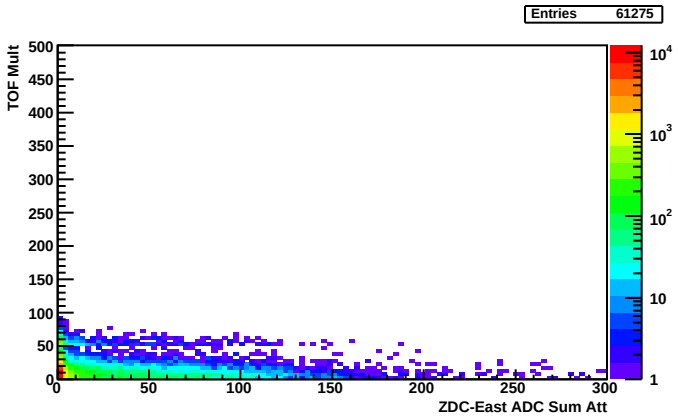

BBQ-VP004 (HI threshold)
Entries 980400
TAC
QT Input Channel

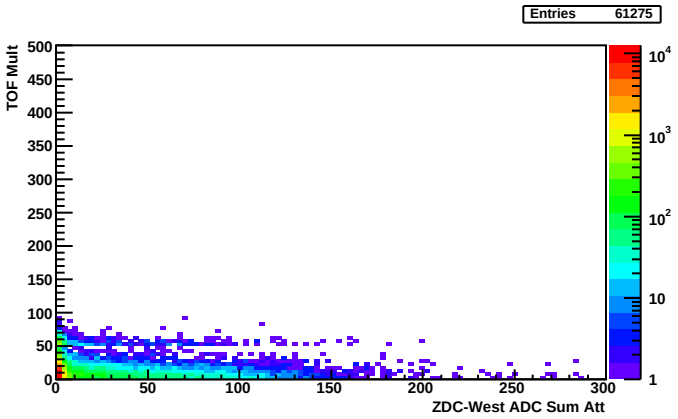

Entries 61275
TOF Mult
BBC-L-East ADC Sum

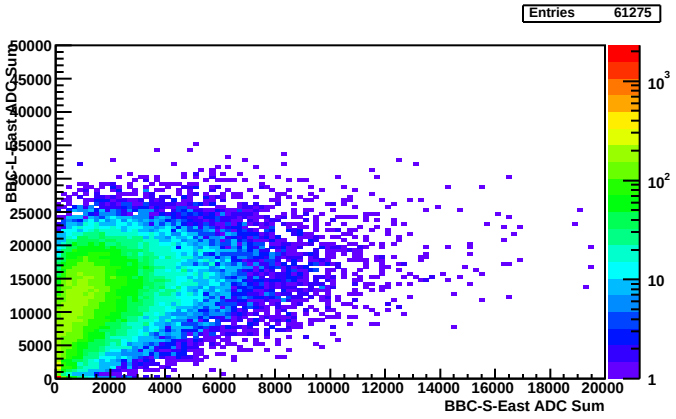

Entries 61275
TOF Mult
BBC-L-West ADC Sum

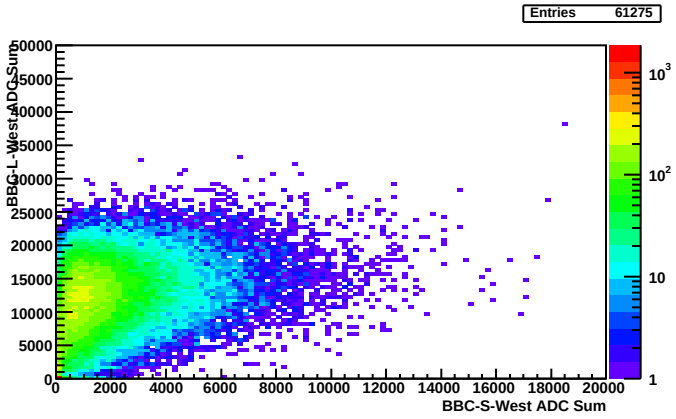

Entries 61275
TOF Mult
BBC-S-East ADC Sum

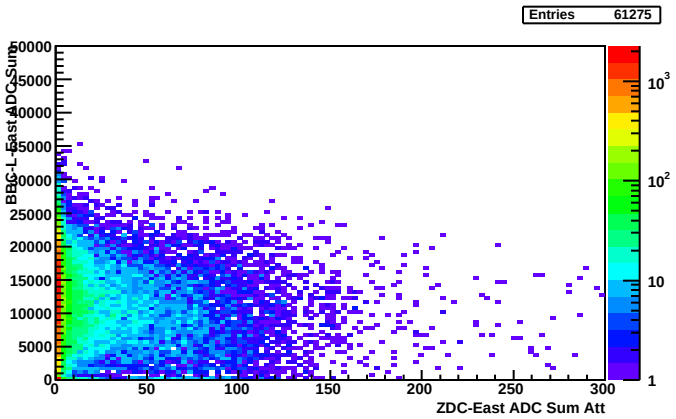

Entries 61275
TOF Mult
BBC-S-West ADC Sum

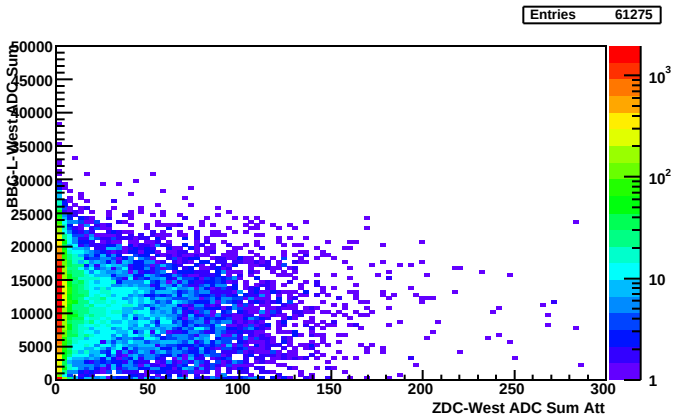

Entries 61275
TOF Mult
ZDC-East ADC Sum Att

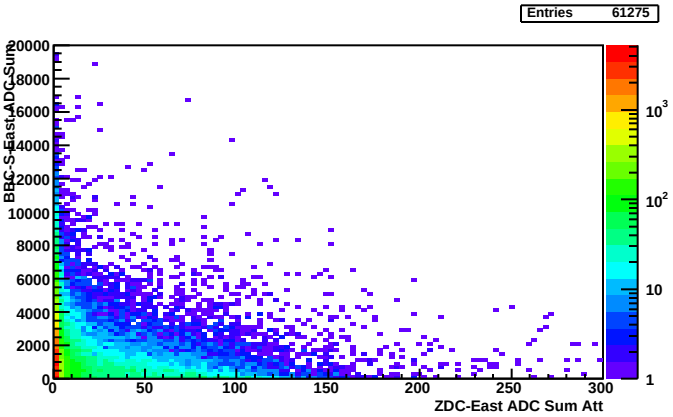

Entries 61275
TOF Mult
ZDC-West ADC Sum Att

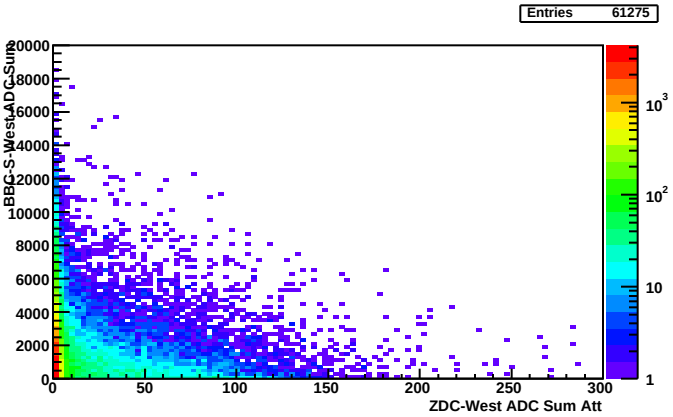

Entries 61275
BBC-L-East ADC Sum
BBC-S-East ADC Sum

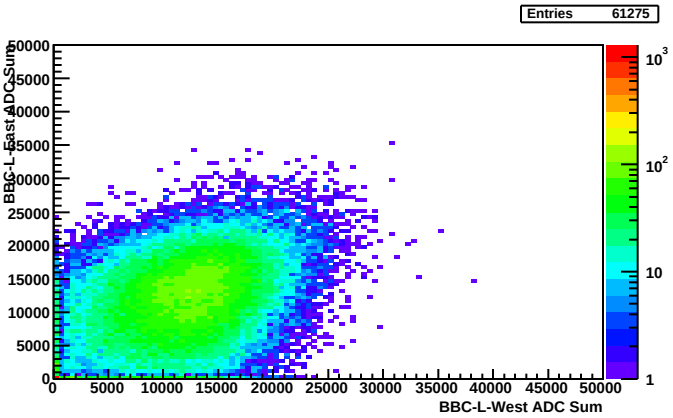

Entries 61275
BBC-L-West ADC Sum
BBC-S-West ADC Sum

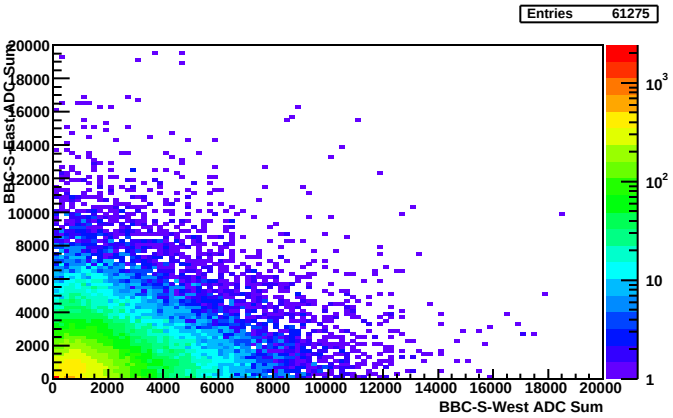

Entries 61275
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

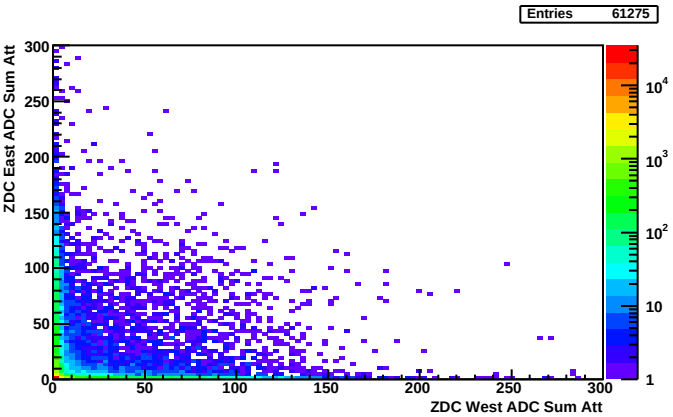

Entries 61275
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

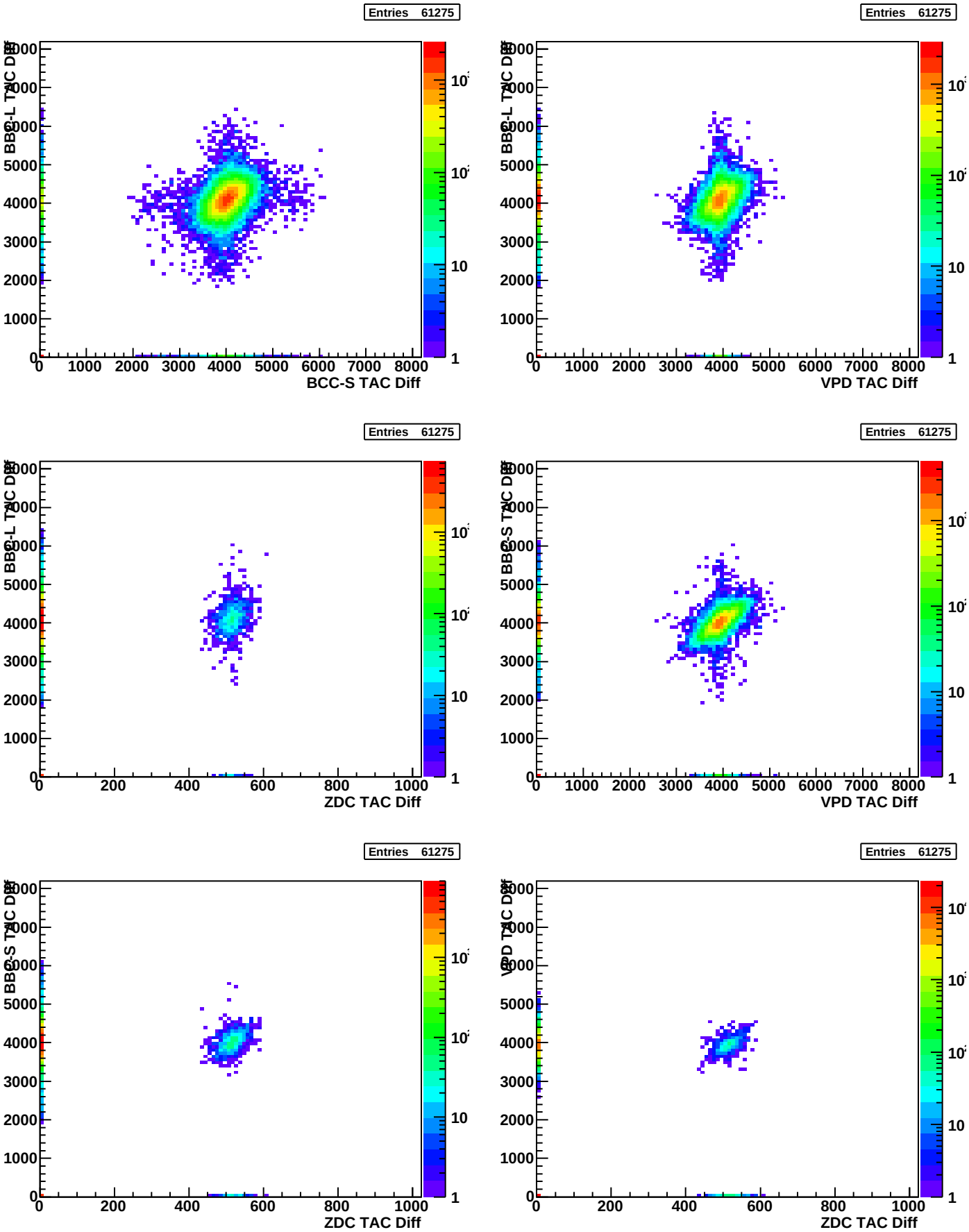

Entries 61275
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

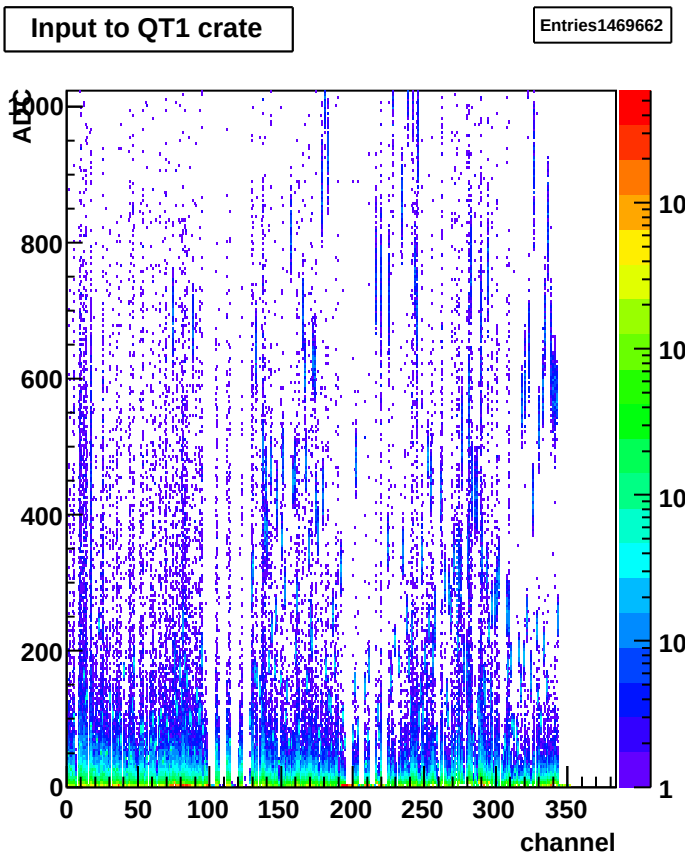

Entries 61275
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

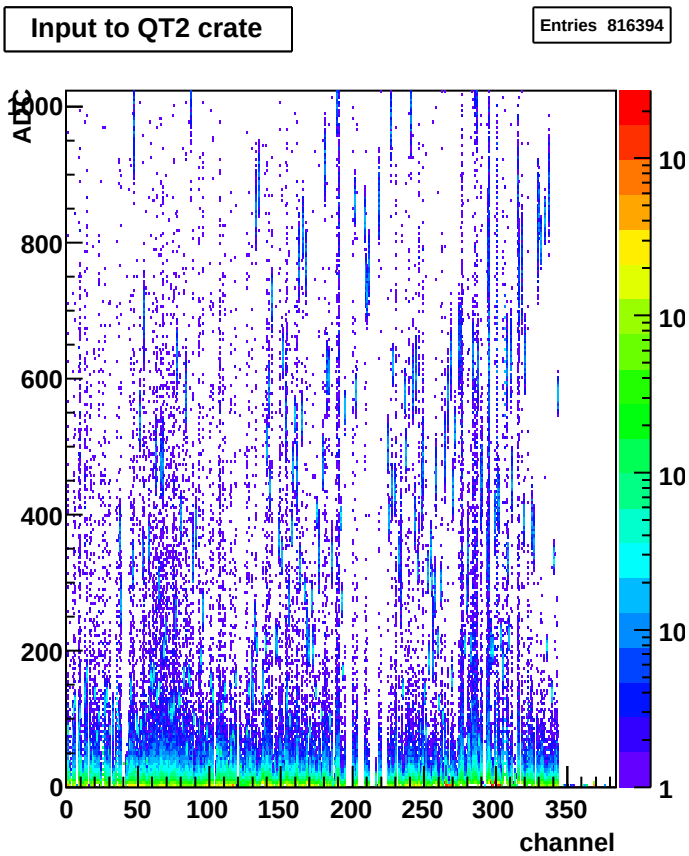

Entries 61275
BBC-L-East ADC Sum
BBC-L-West ADC Sum

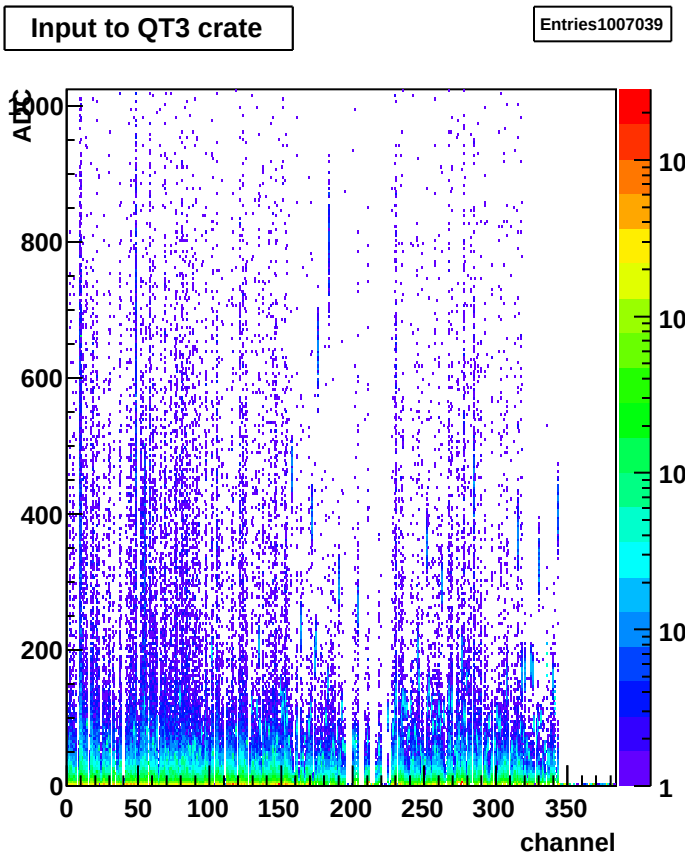

Entries 61275
BBC-S-East ADC Sum
BBC-S-West ADC Sum

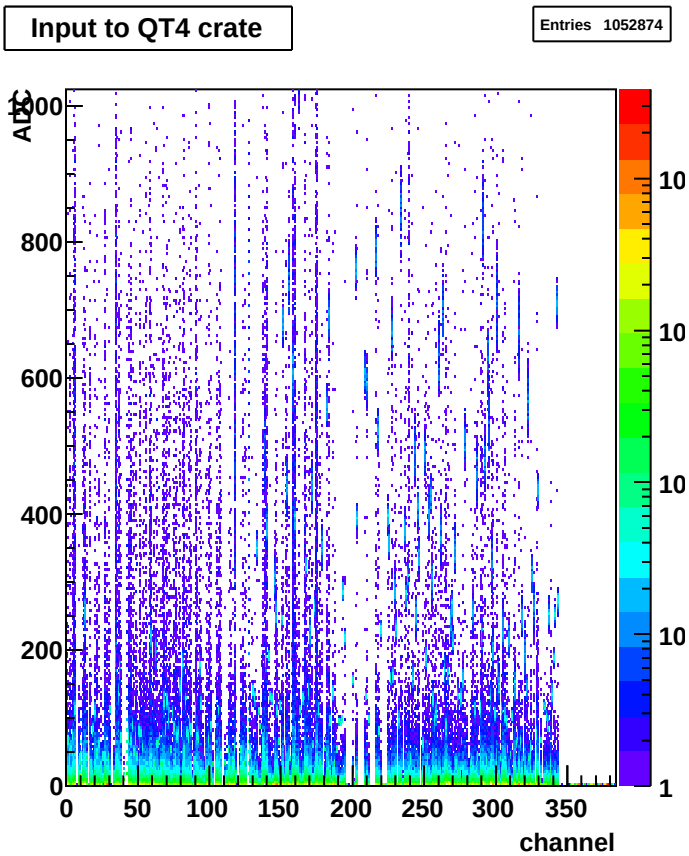

Entries 61275
ZDC East ADC Sum Att
ZDC West ADC Sum Att

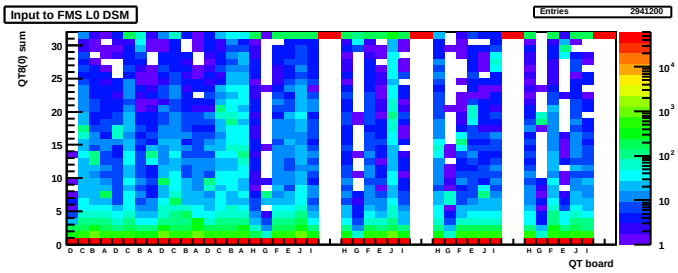

Entries 61275
BBC-L TAC Diff
BCC-S TAC Diff
Entries 61275
BBC-L TAC Diff
VPD TAC Diff
Entries 61275
BBC-L TAC Diff
ZDC TAC Diff
Entries 61275
BBC-S TAC Diff
VPD TAC Diff
Entries 61275
BBC-S TAC Diff
ZDC TAC Diff
Entries 61275
VPD TAC Diff
ZDC TAC Diff

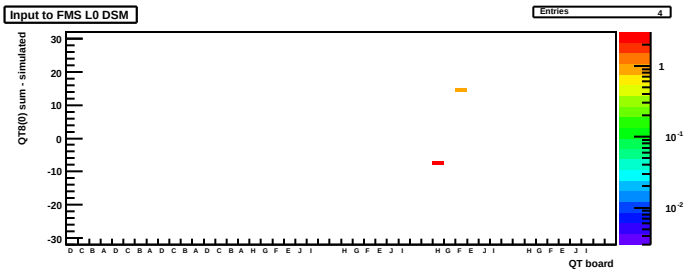

Input to QT1 crate
Entries1469662
ADC
channel

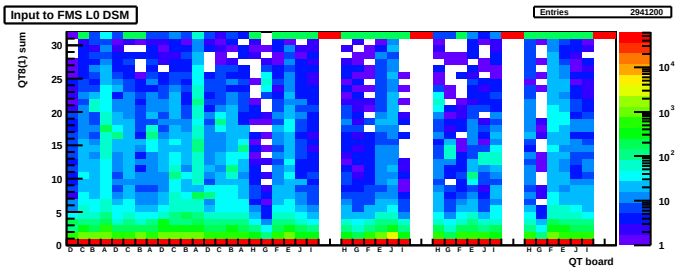

Input to QT2 crate
Entries 816394
ADC
channel

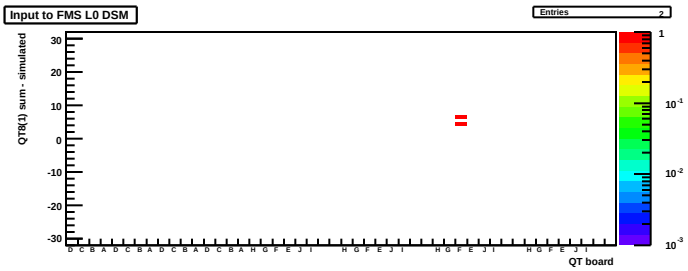

Input to QT3 crate
Entries1007039
ADC
channel

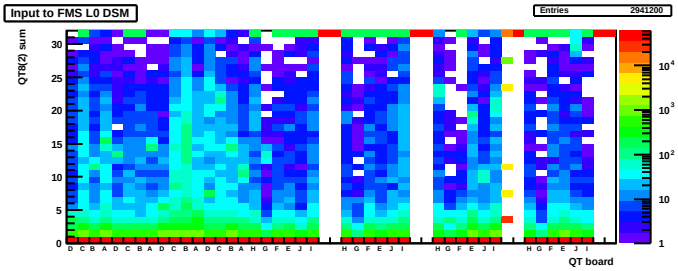

Input to QT4 crate
Entries 1052874
ADC
channel

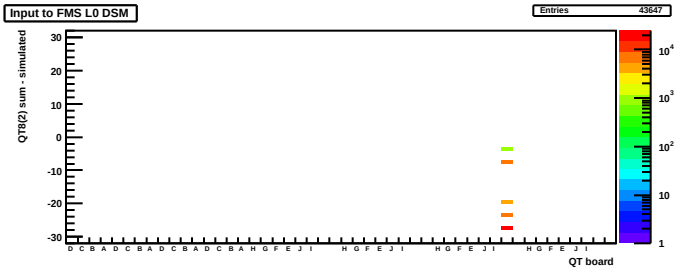

Input to FMS L0 DSM
Entries 2941200
QT8(0) sum
QT board

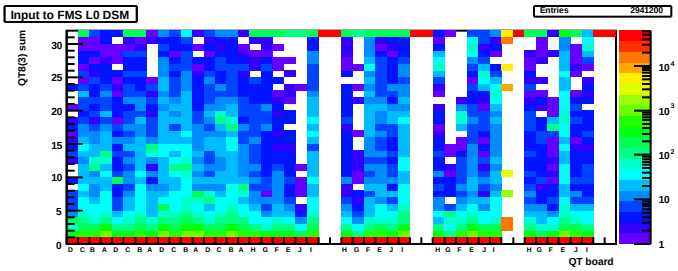

Input to FMS L0 DSM
Entries 4
QT8(0) sum - simulated
QT board

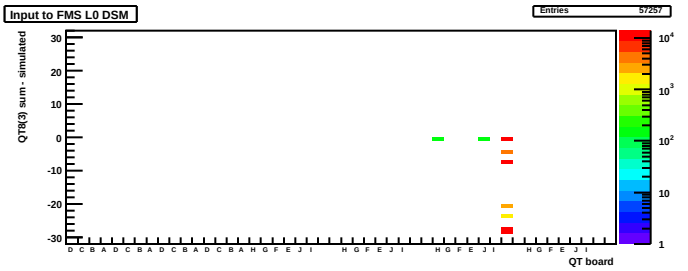

Input to FMS L0 DSM
Entries 2941200
QT8(1) sum
QT board

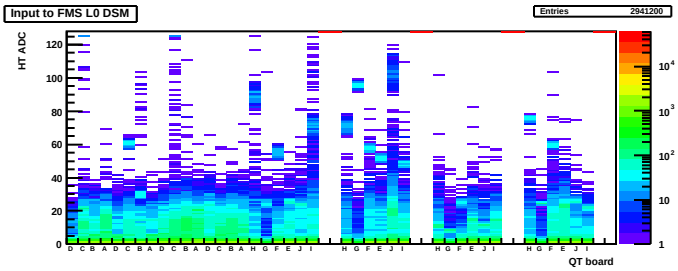

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

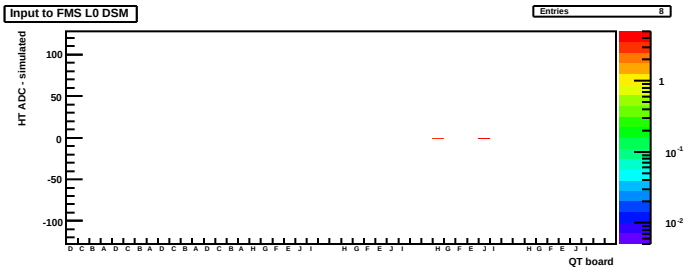

Input to FMS L0 DSM
Entries 2941200
QT8(2) sum
QT board

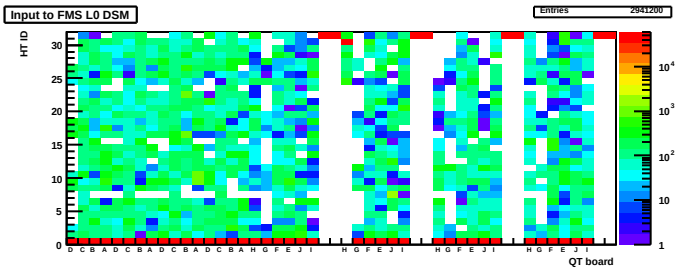

Input to FMS L0 DSM
Entries 43647
QT8(2) sum - simulated
QT board

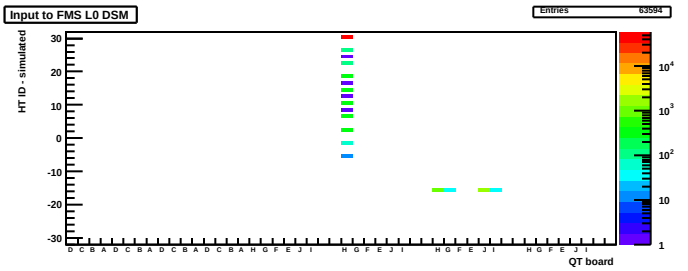

Input to FMS L0 DSM
Entries 2941200
QT8(3) sum
QT board

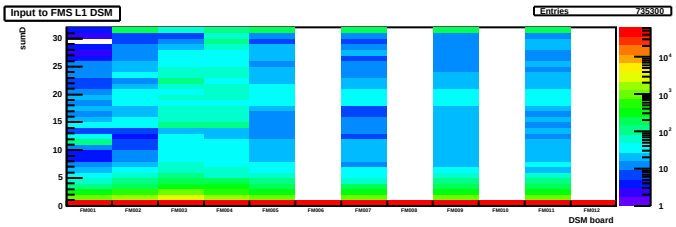

Input to FMS L0 DSM
Entries 57257
QT8(3) sum - simulated
QT board

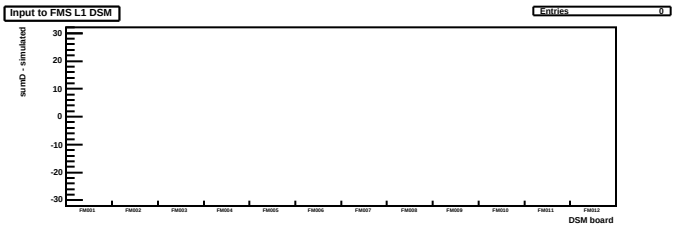

Input to FMS L0 DSM
Entries 2941200
HT ADC
QT board

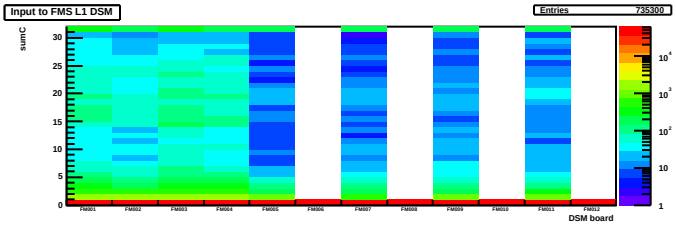

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

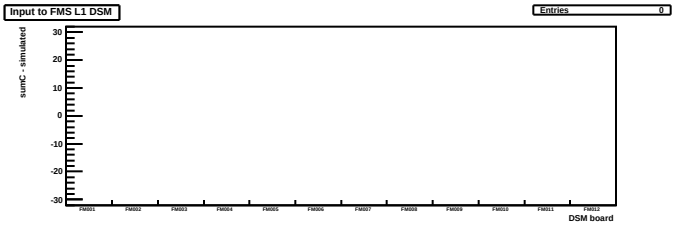

Input to FMS L0 DSM
Entries 2941200
HT ID
QT board

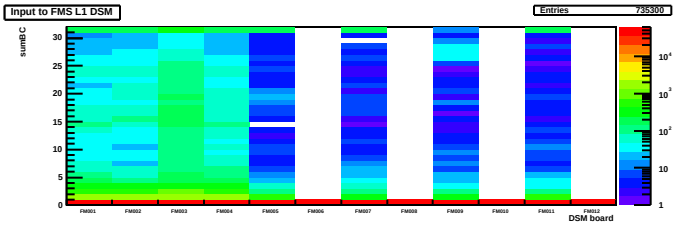

Input to FMS L0 DSM
Entries 63594
HT ID - simulated
QT board

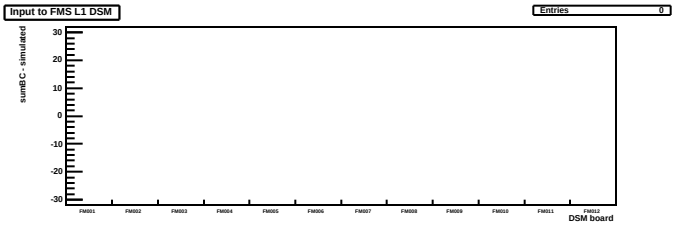

Input to FMS L1 DSM
Entries 735300
sumD
DSM board

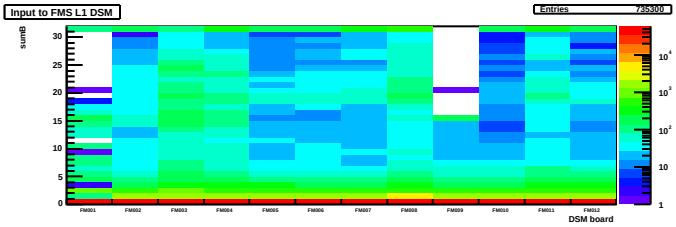

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

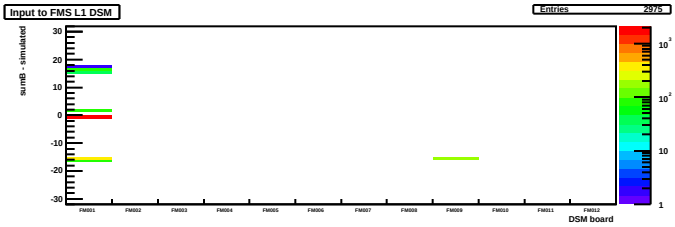

Input to FMS L1 DSM
Entries 735300
sumC
DSM board

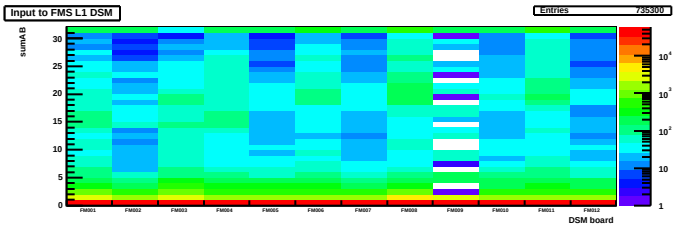

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

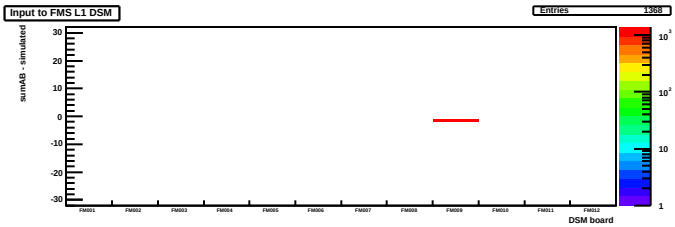

Input to FMS L1 DSM
Entries 735300
sumBC
DSM board

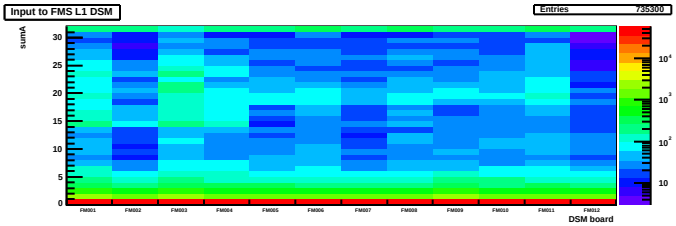

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

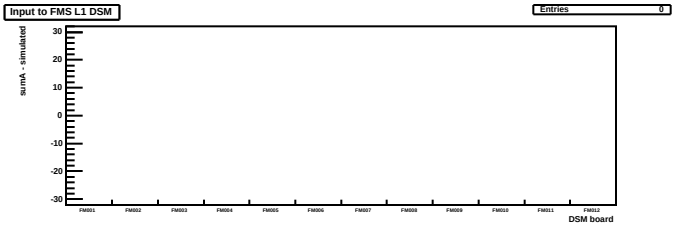

Input to FMS L1 DSM
Entries 735300
sumB
DSM board

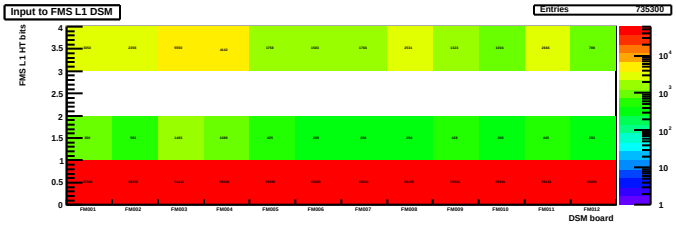

Input to FMS L1 DSM
Entries 2975
sumB - simulated
DSM board

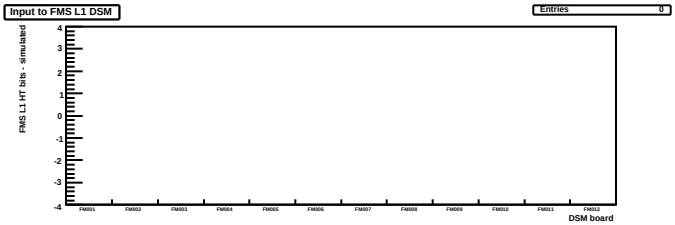

Input to FMS L1 DSM
Entries 735300
sumAB
DSM board

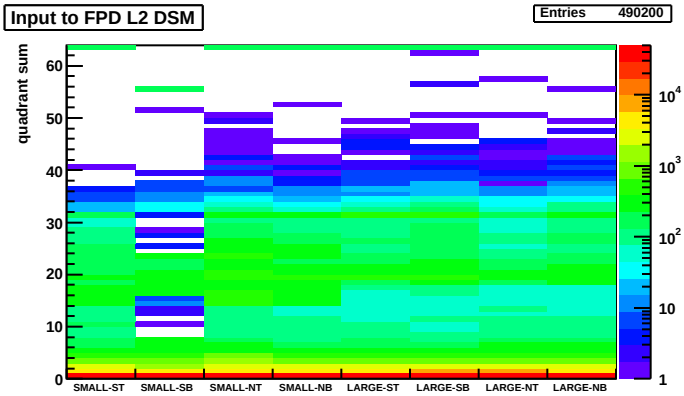

Input to FMS L1 DSM
Entries 1368
sumAB - simulated
DSM board

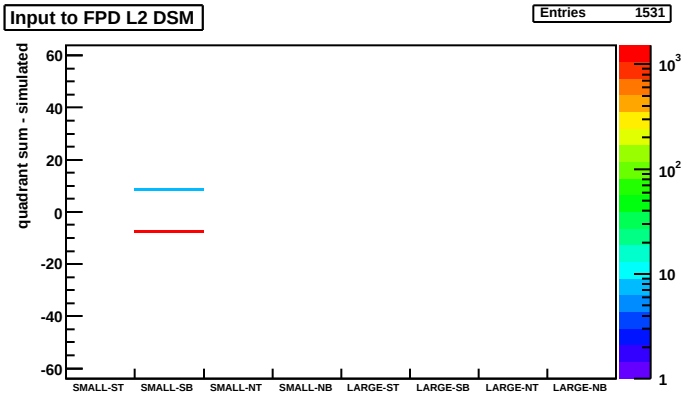

Input to FMS L1 DSM
Entries 735300
sumA
DSM board

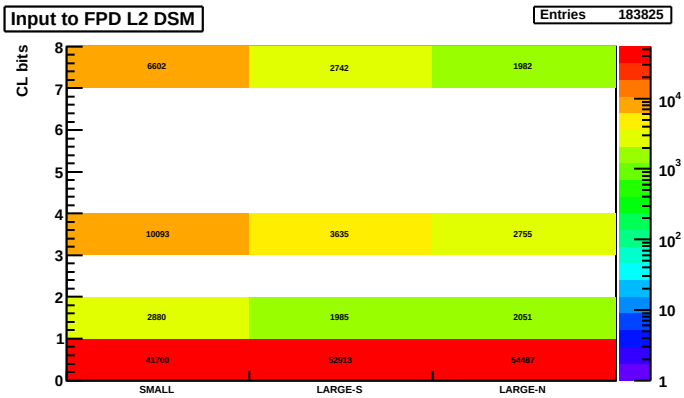

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

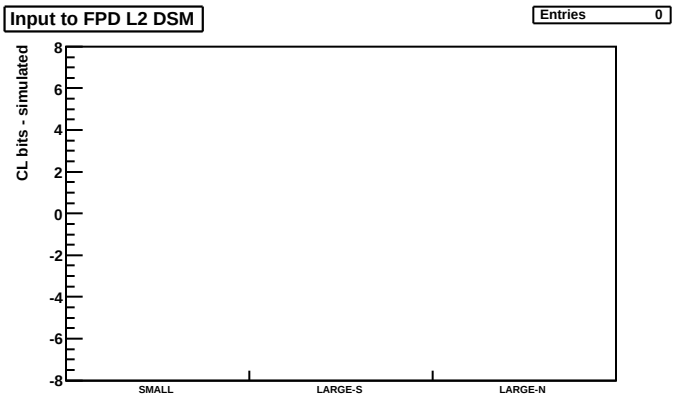

Input to FMS L1 DSM
Entries 735300
FMS L1 HT bits
DSM board

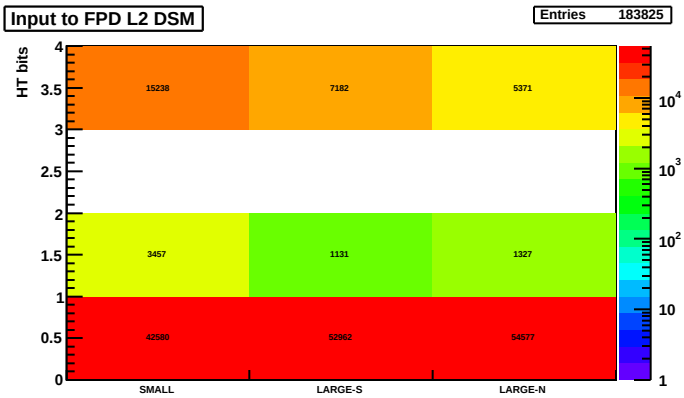

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

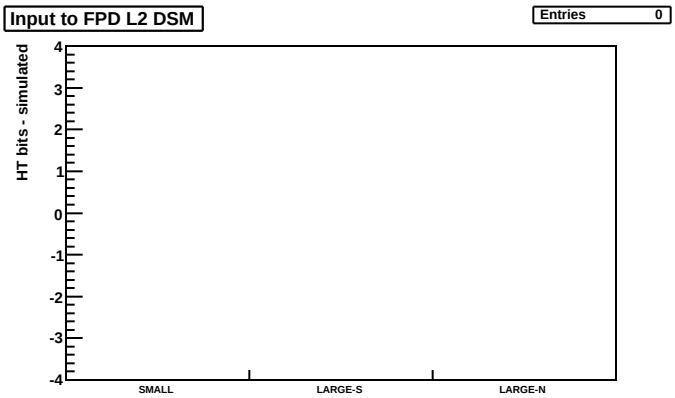

Input to FPD L2 DSM
Entries 490200
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

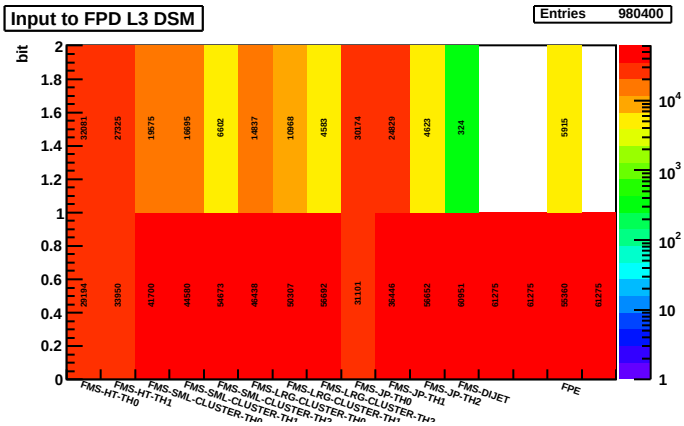

Input to FPD L2 DSM
Entries 1531
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

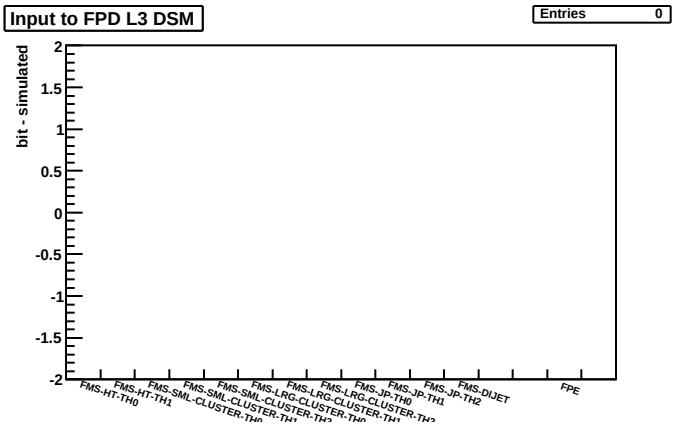

Input to FPD L2 DSM
Entries 183825
CL bits
6602
2742
1982
10093
3635
2755
2880
1985
2051
41700
52913
54487
SMALL
LARGE-S
LARGE-N

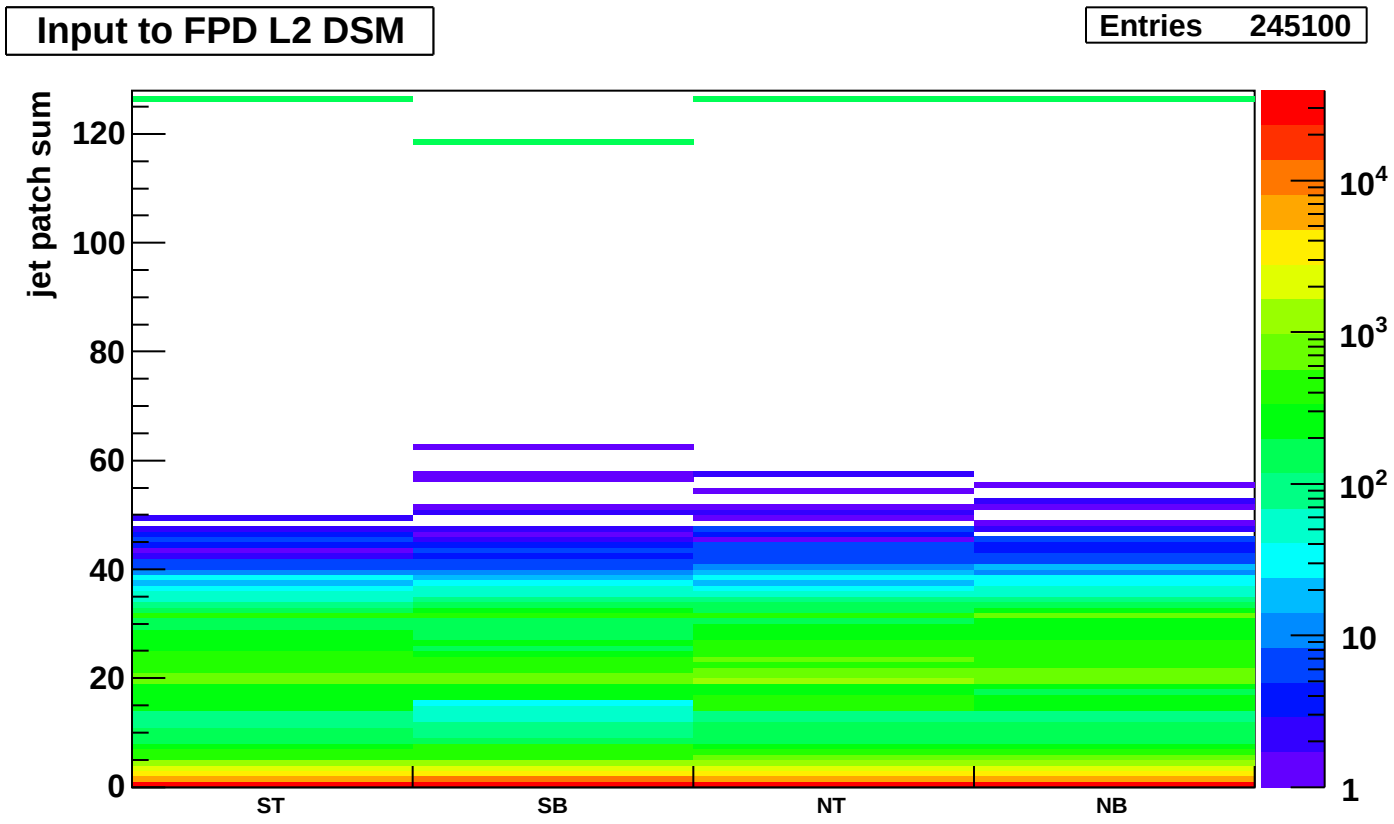

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

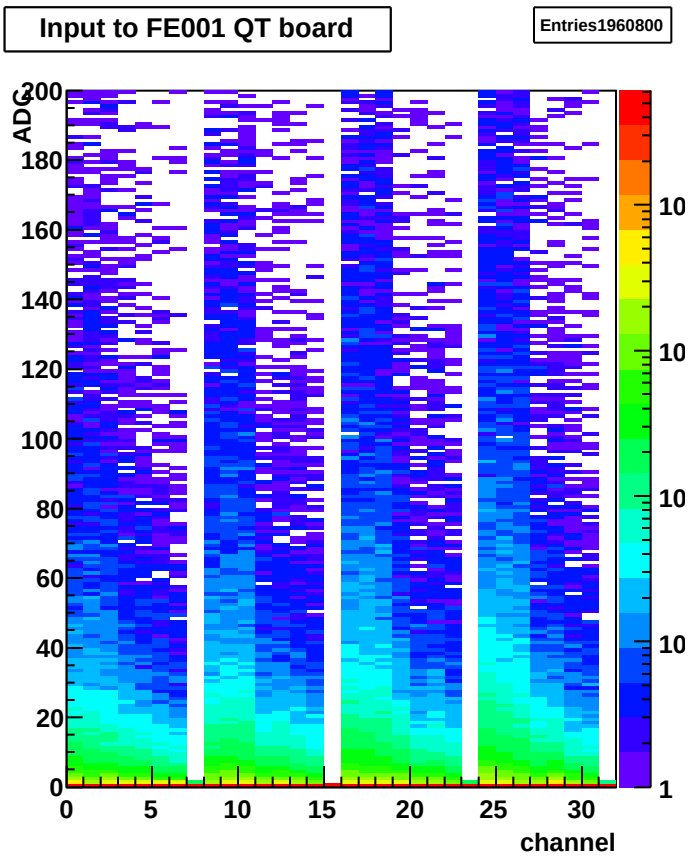

Input to FPD L2 DSM
Entries 183825
HT bits
15238
7182
5371
3457
1131
1327
42580
52962
54577
SMALL
LARGE-S
LARGE-N

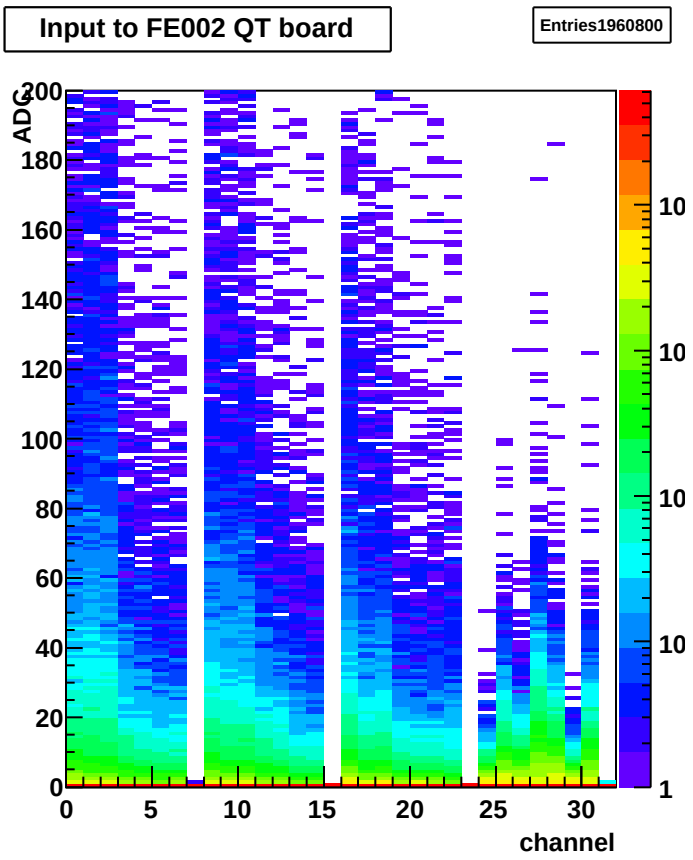

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

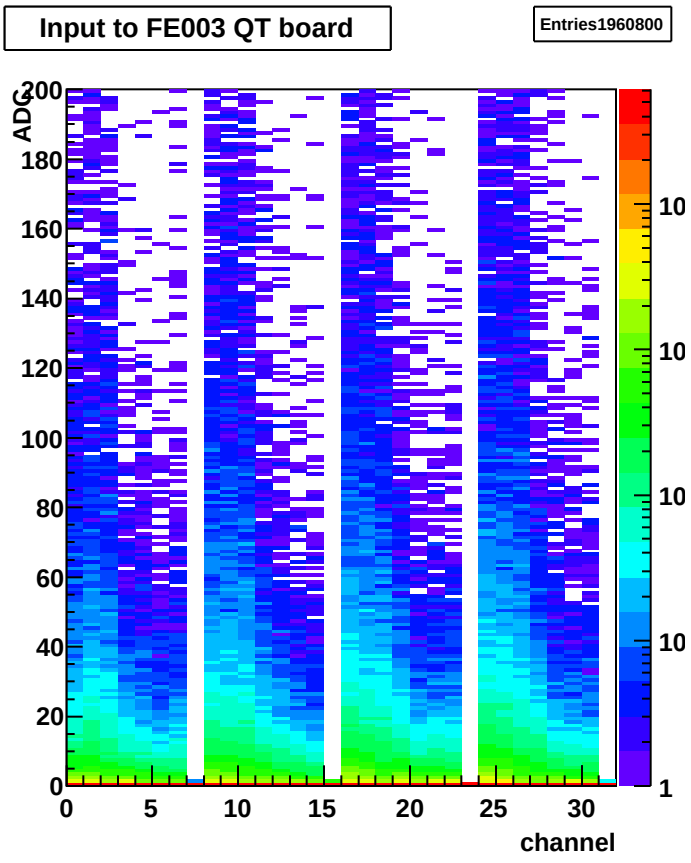

Input to FPD L3 DSM
Entries 980400
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

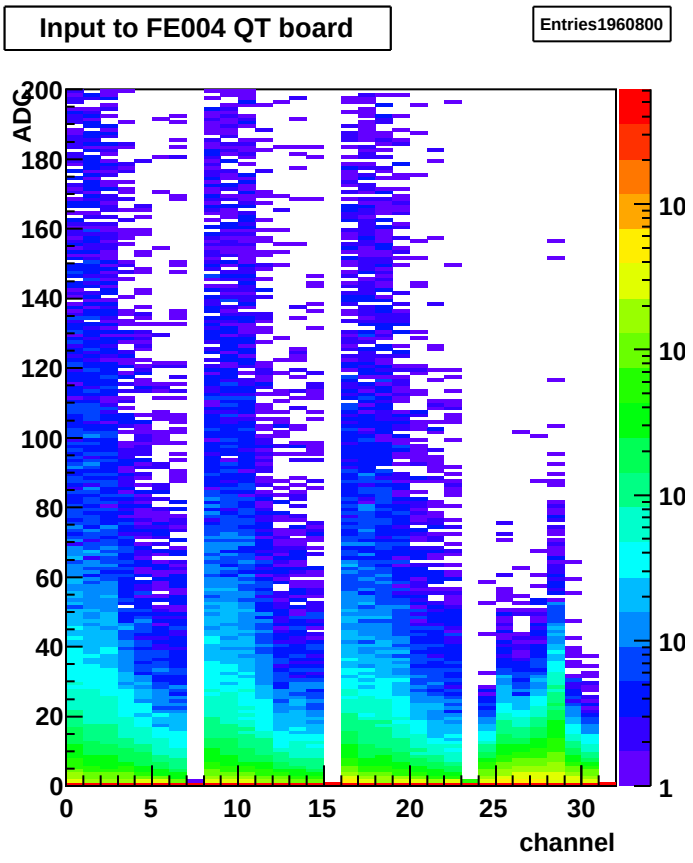

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

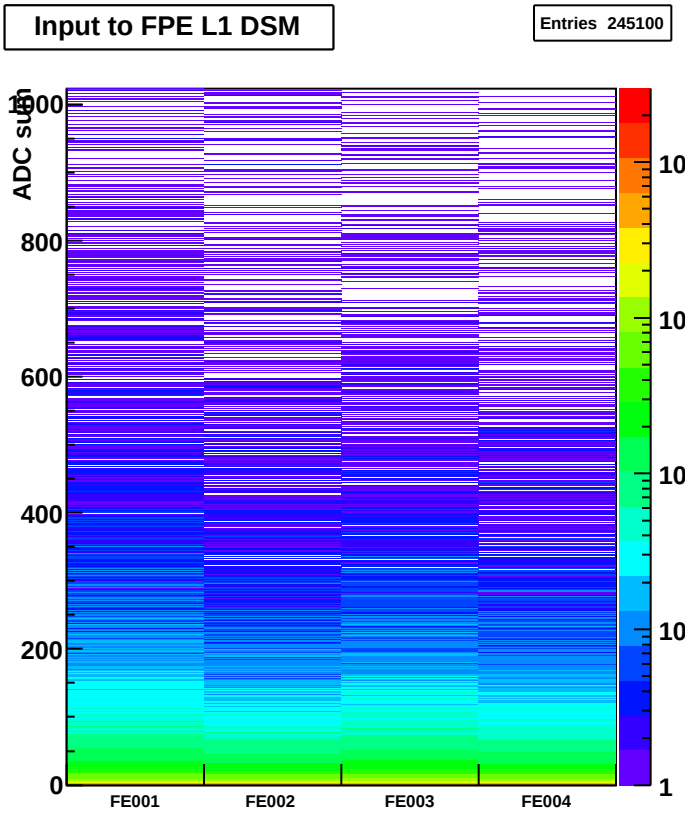

Input to FPD L2 DSM
Entries 245100
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

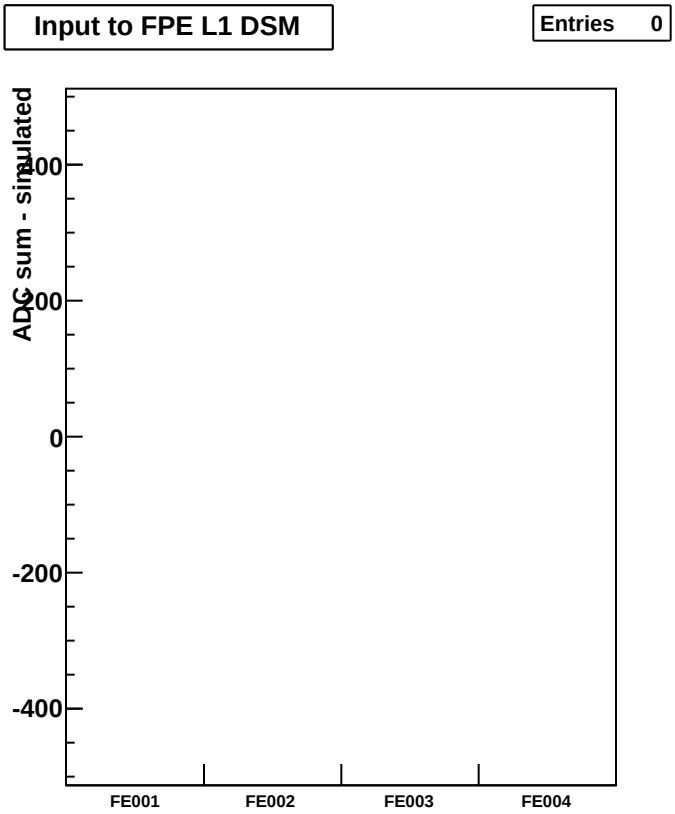

Input to FE001 QT board
Entries1960800
ADC
channel

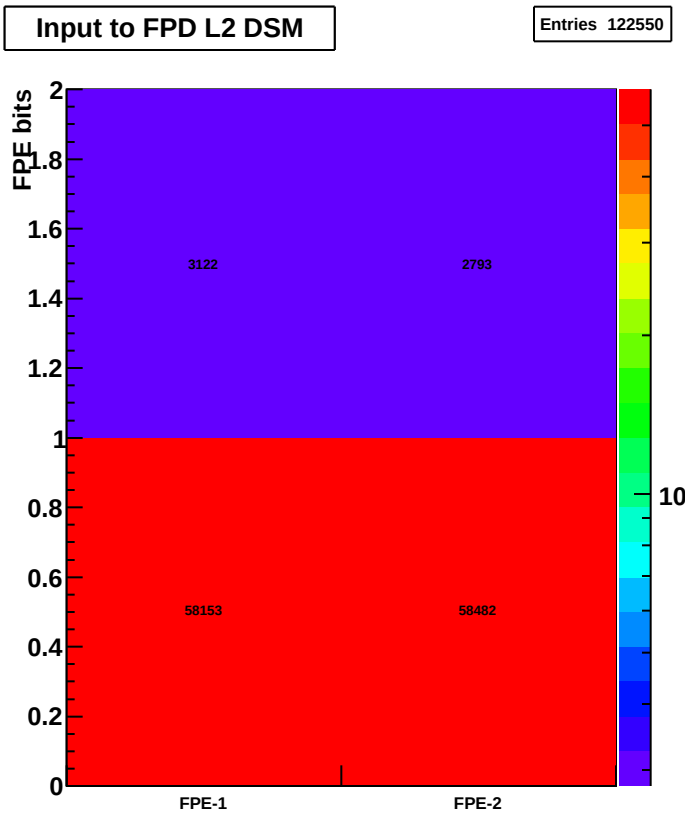

Input to FE002 QT board
Entries1960800
ADC
channel

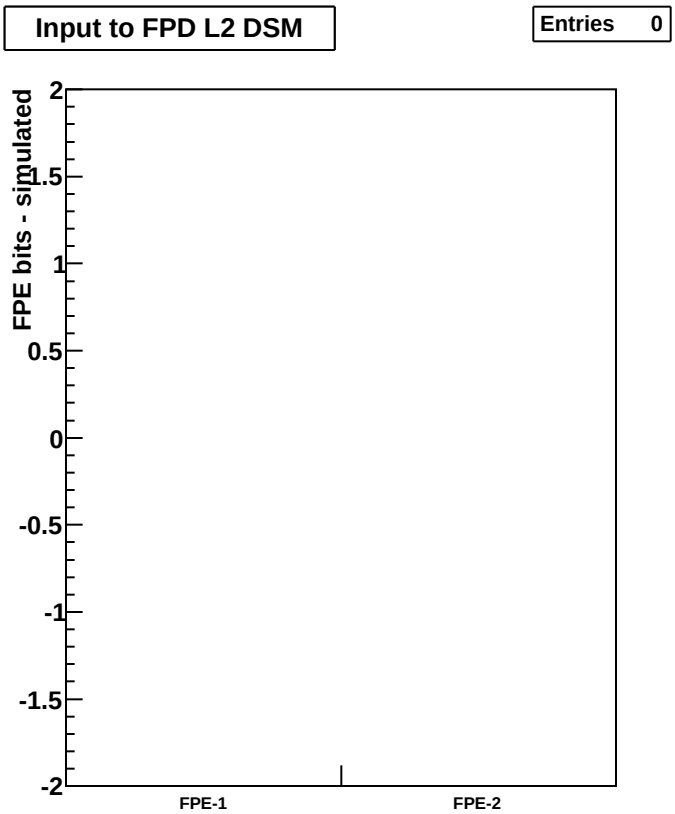

Input to FE003 QT board
Entries1960800
ADC
channel


Input to FE004 QT board
Entries1960800
ADC
channel


Input to FPE L1 DSM
Entries 245100
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 122550
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3122
2793
58153
58482
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2