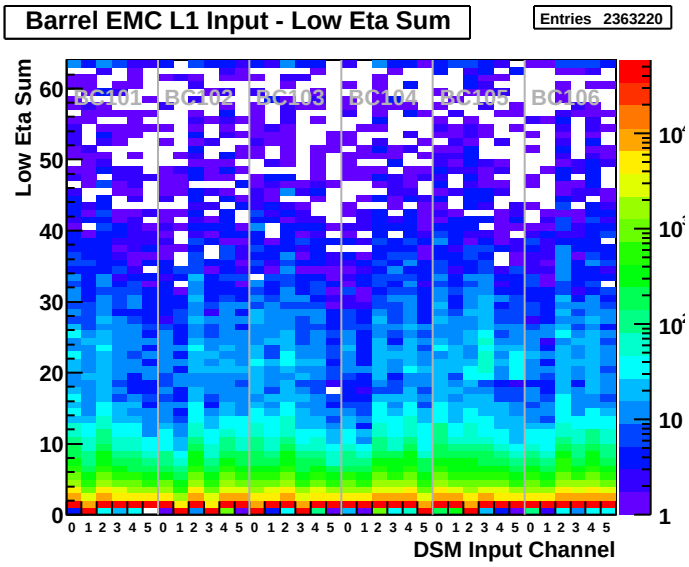
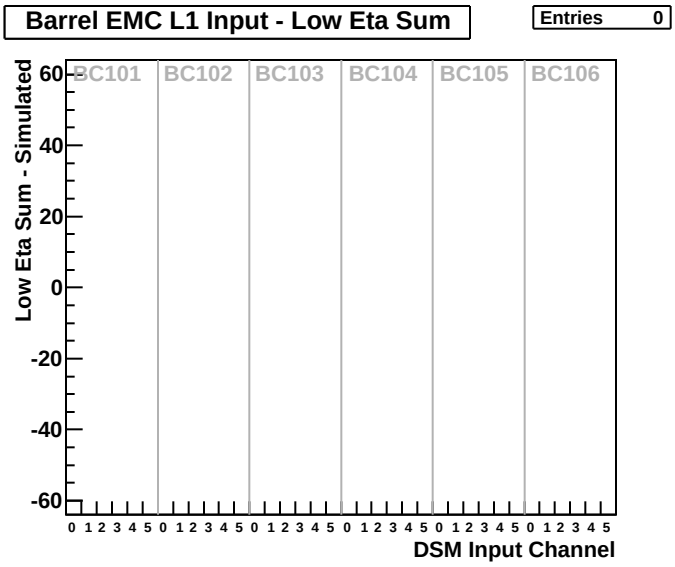
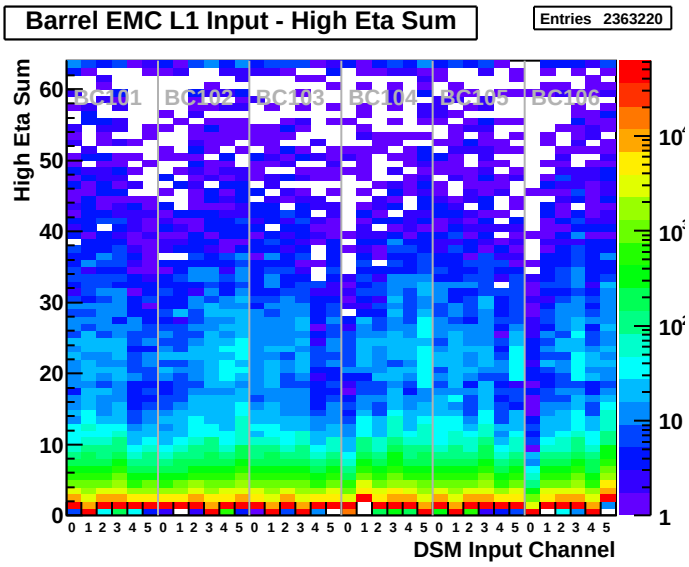
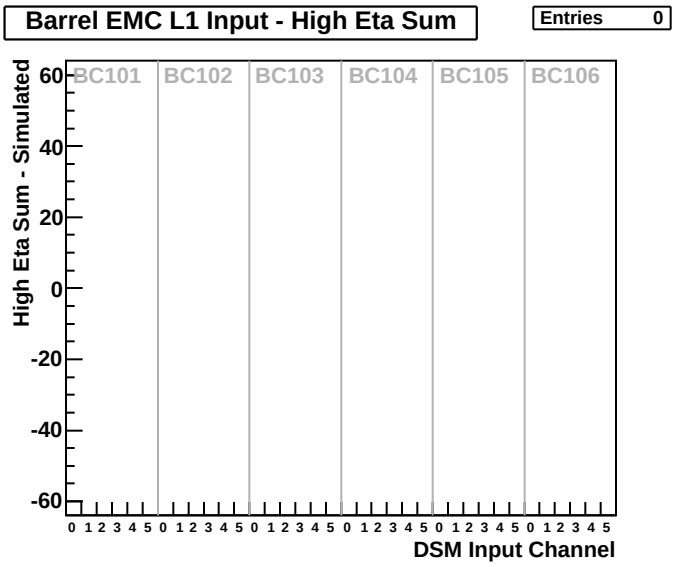
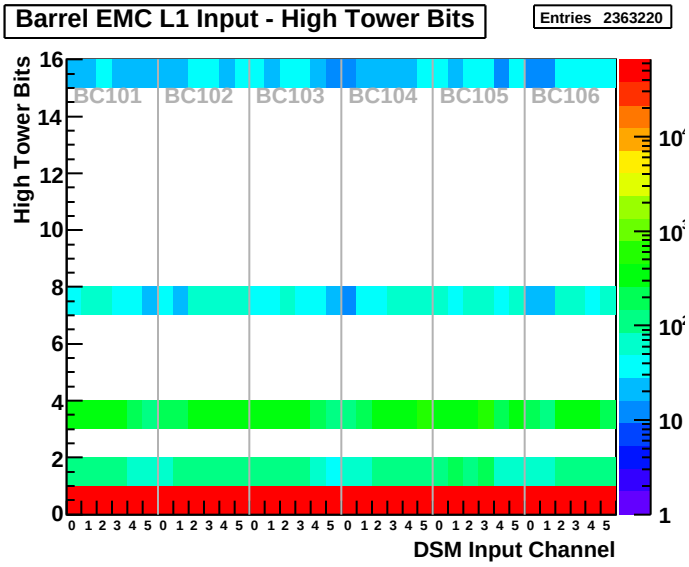
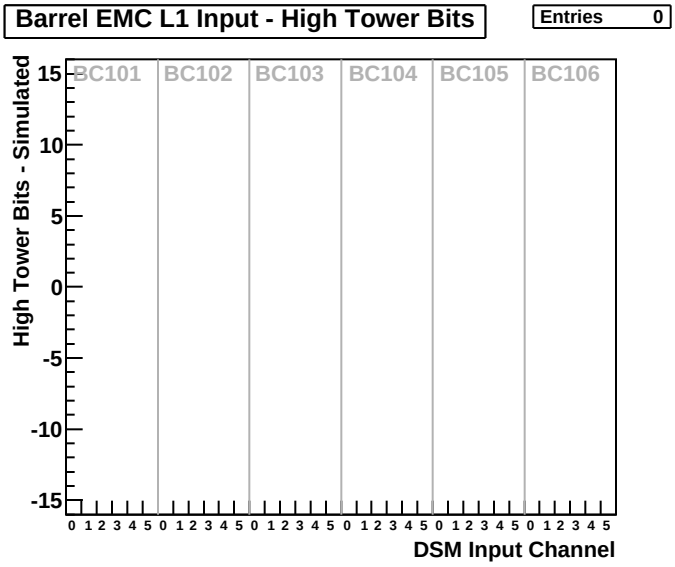

Barrel EMC L1 Input - Low Eta Sum
Entries 2363220
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

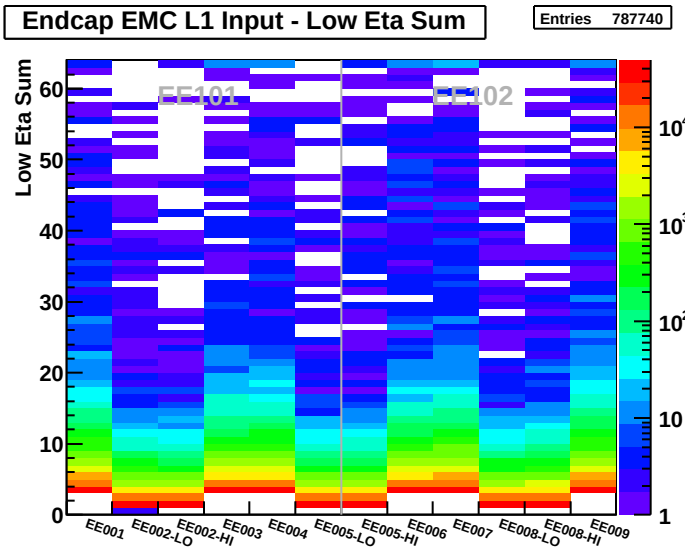

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

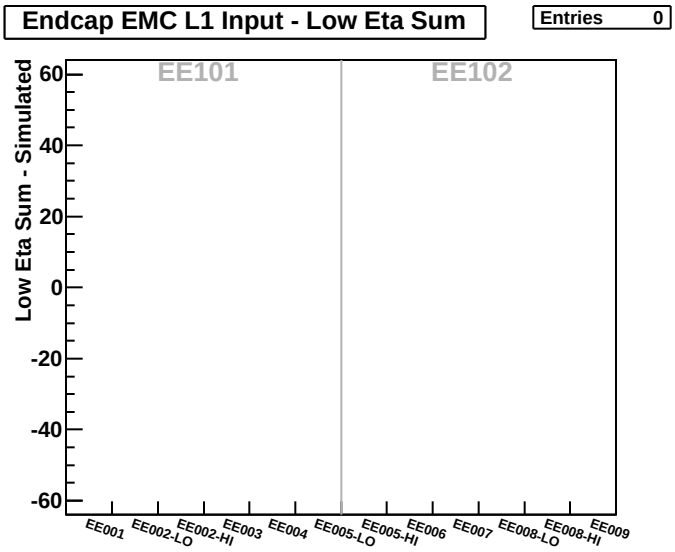

Barrel EMC L1 Input - High Eta Sum
Entries 2363220
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

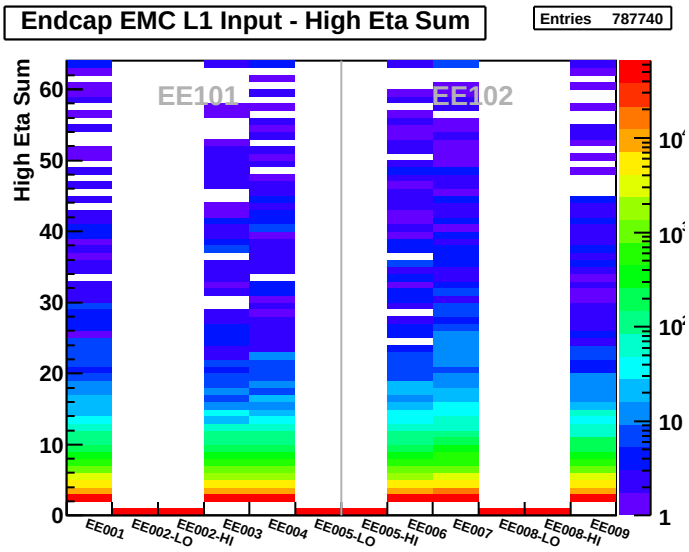

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

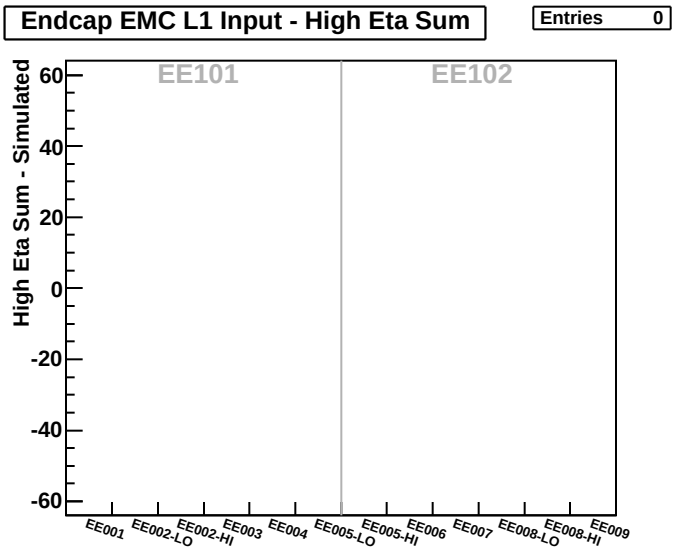

Barrel EMC L1 Input - High Tower Bits
Entries 2363220
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

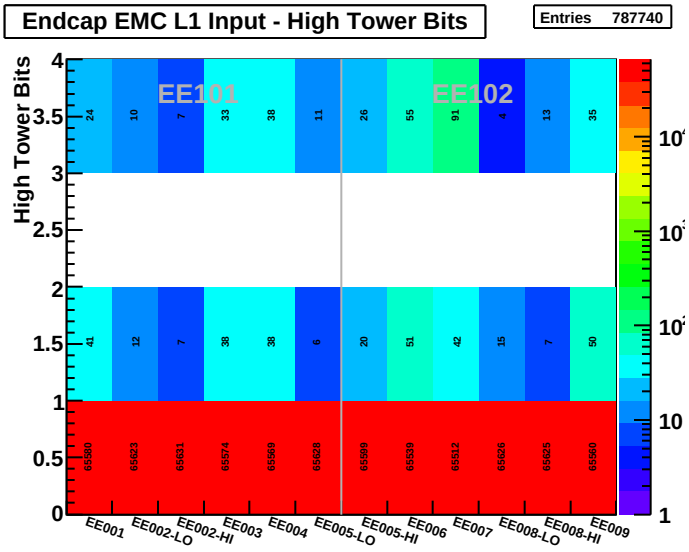

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

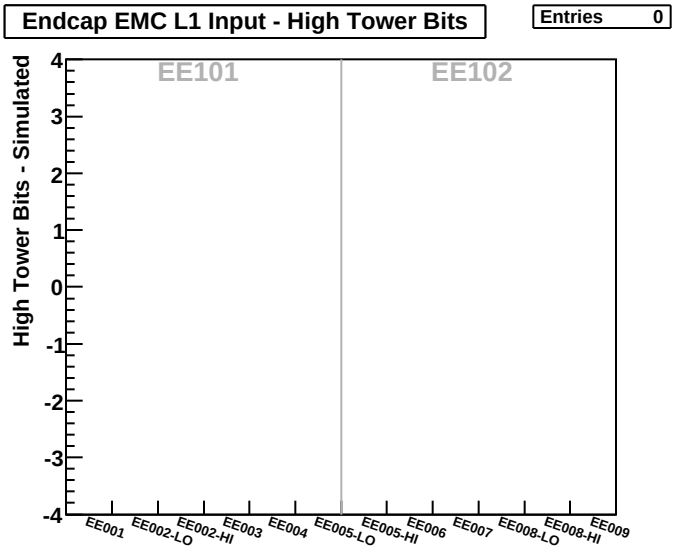

Endcap EMC L1 Input - Low Eta Sum
Entries 787740
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

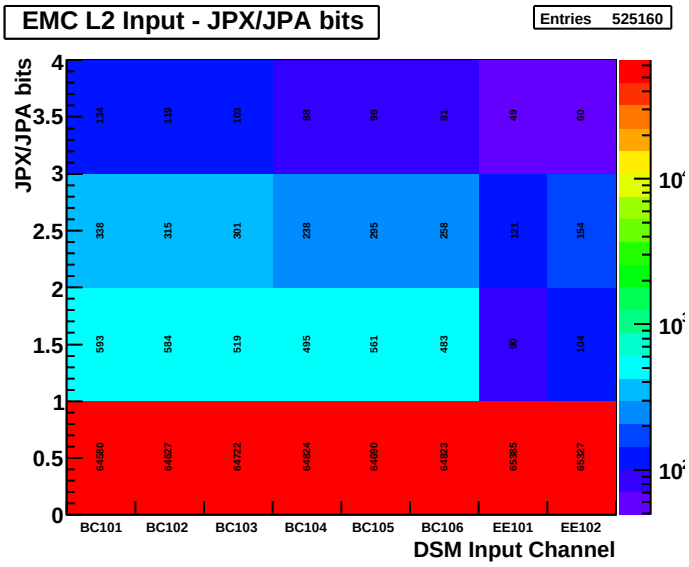

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

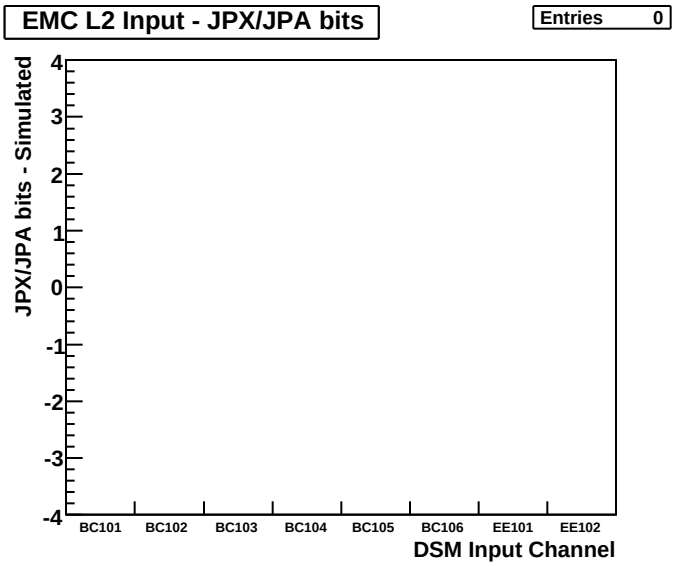

Endcap EMC L1 Input - High Eta Sum
Entries 787740
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

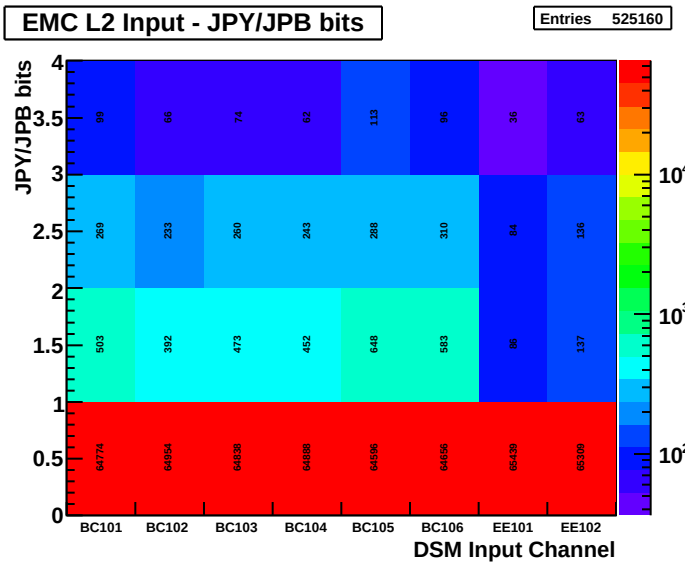

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

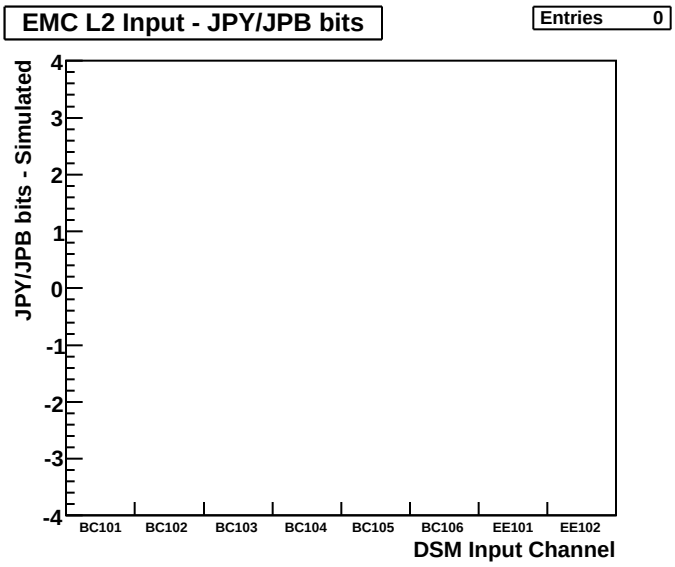

Endcap EMC L1 Input - High Tower Bits
Entries 787740
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

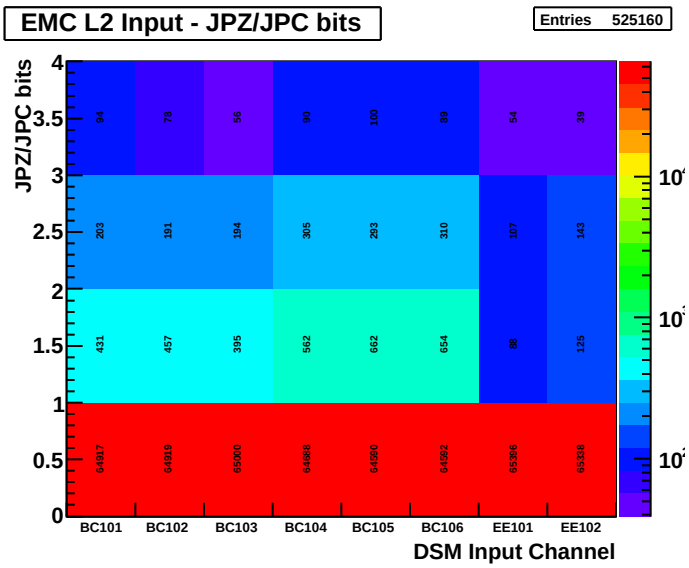

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

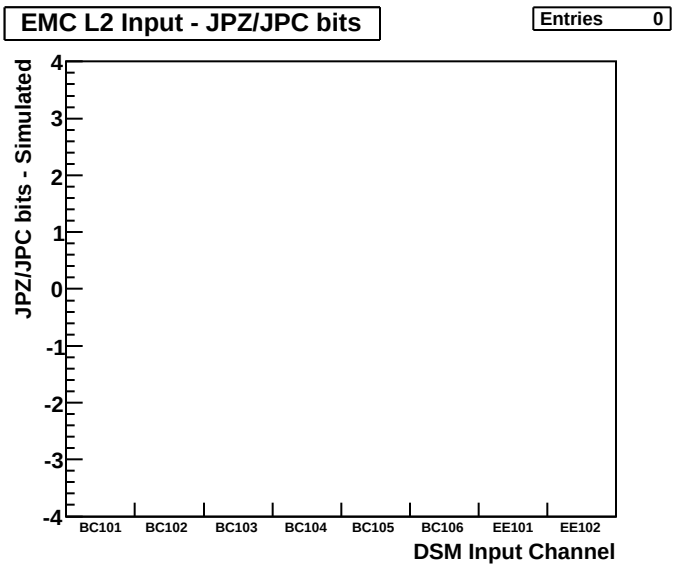

EMC L2 Input - JPX/JPA bits
Entries 525160
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
134 119 103 88 99 81 49 60
338 315 301 238 295 258 121 154
593 584 519 495 561 483 90 104
64580 64627 64722 64824 64690 64823 65385 65327

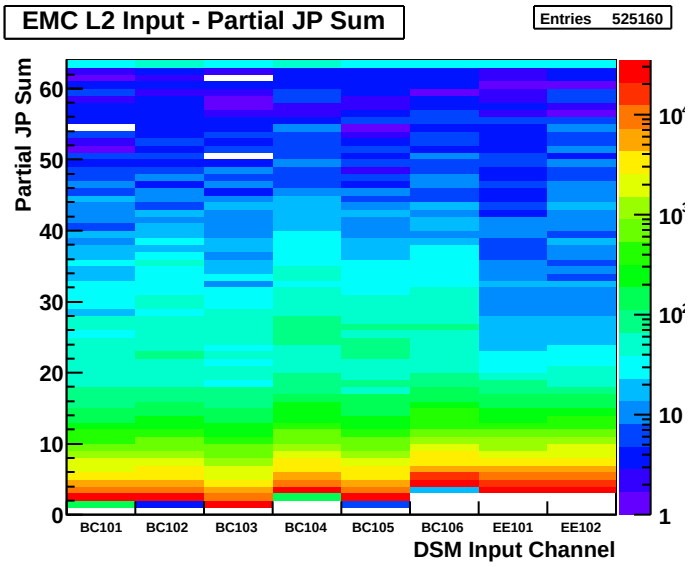

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

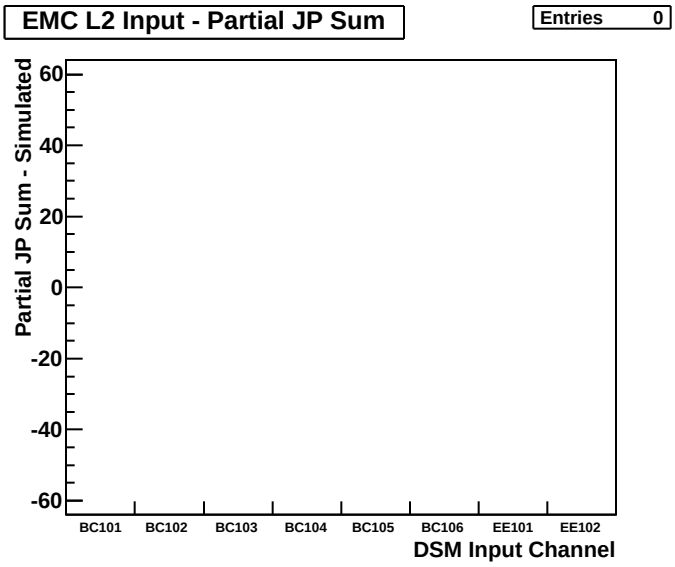

EMC L2 Input - JPY/JPB bits
Entries 525160
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
99 66 74 62 113 96 36 63
269 233 260 243 288 310 84 136
503 392 473 452 648 583 86 137
64774 64954 64838 64888 64596 64656 65439 65309

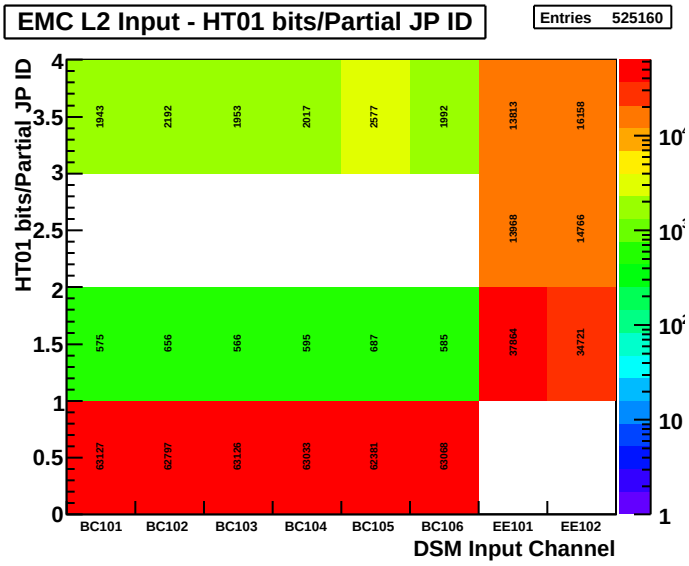

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

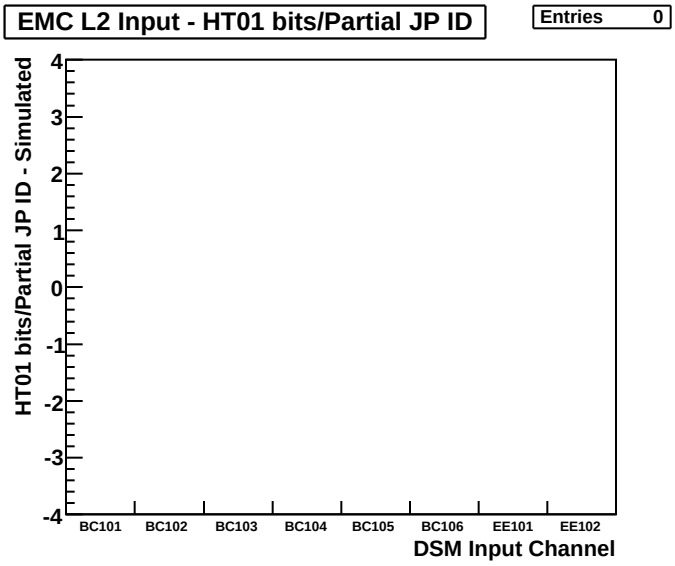

EMC L2 Input - JPZ/JPC bits
Entries 525160
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
94 78 56 90 100 89 54 39
203 191 194 305 293 310 107 143
431 457 395 562 662 654 88 125
64917 64819 65000 64688 64590 64592 65386 65338

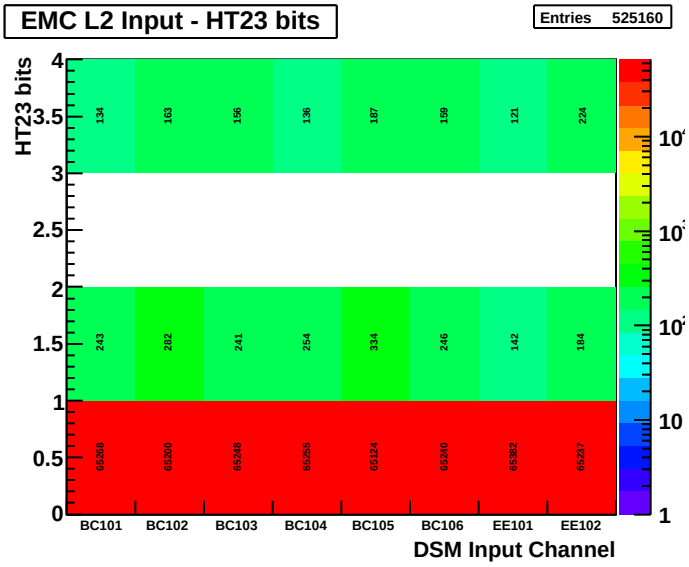

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

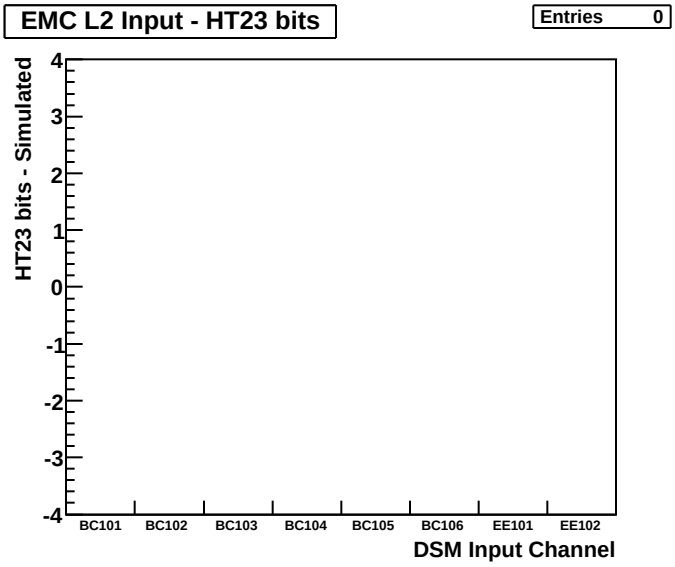

EMC L2 Input - Partial JP Sum
Entries 525160
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

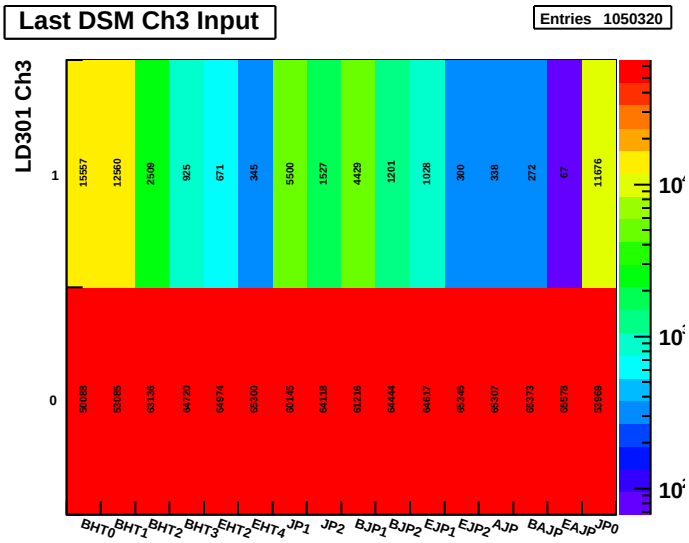

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

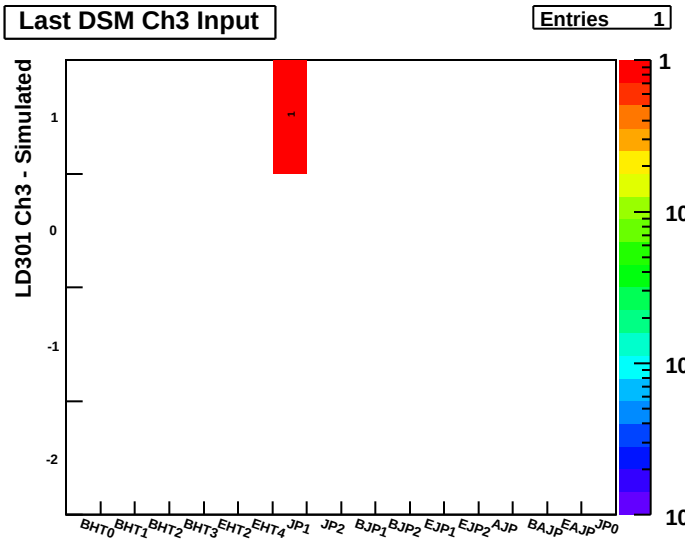

EMC L2 Input - HT01 bits/Partial JP ID
Entries 525160
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

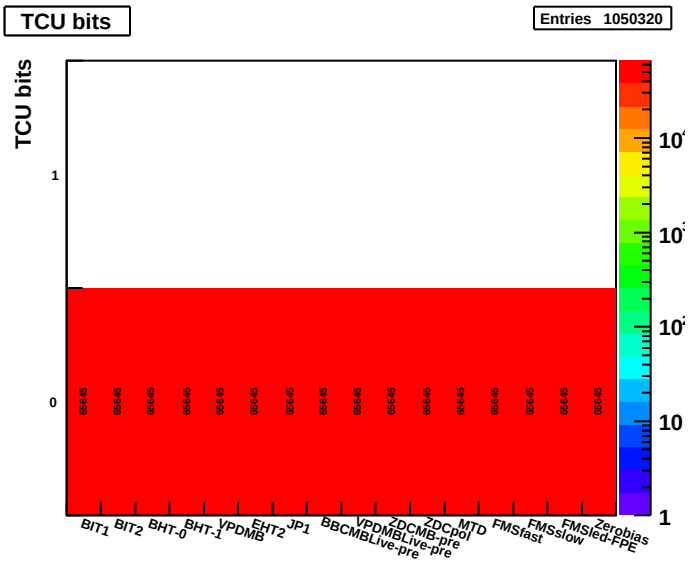

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

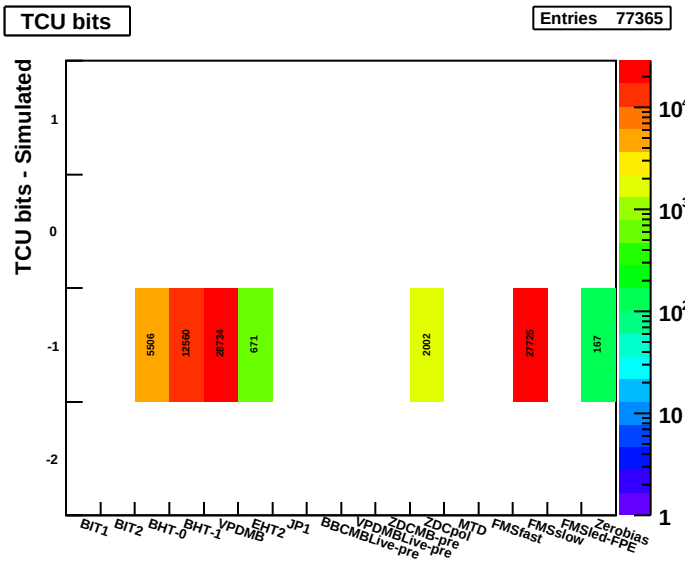

EMC L2 Input - HT23 bits
Entries 525160
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

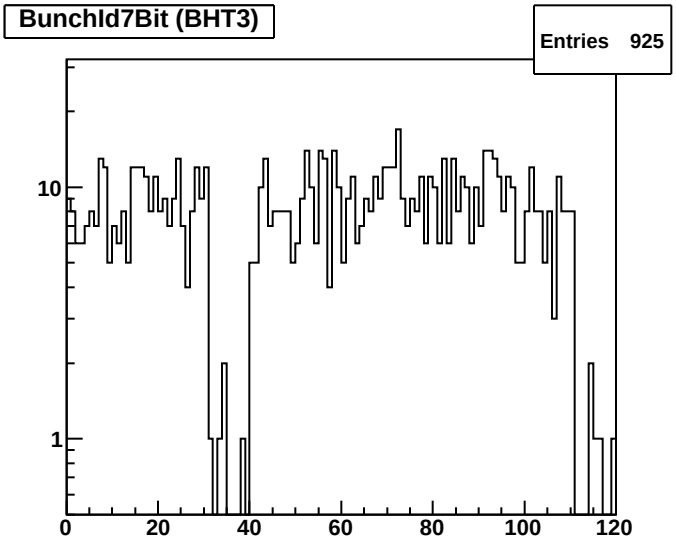

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

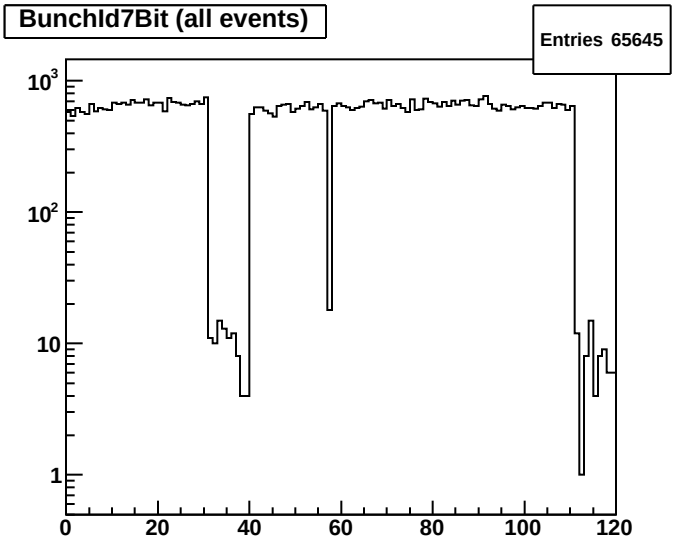

Last DSM Ch3 Input
Entries 1050320
LD301 Ch3
15557 12560 2509 925 671 345 5500 1527 4429 1201 1028 300 338 272 67 11676
50088 53085 63136 64720 64974 65300 60145 64118 61216 64444 64617 65345 65307 65373 65578 53969
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

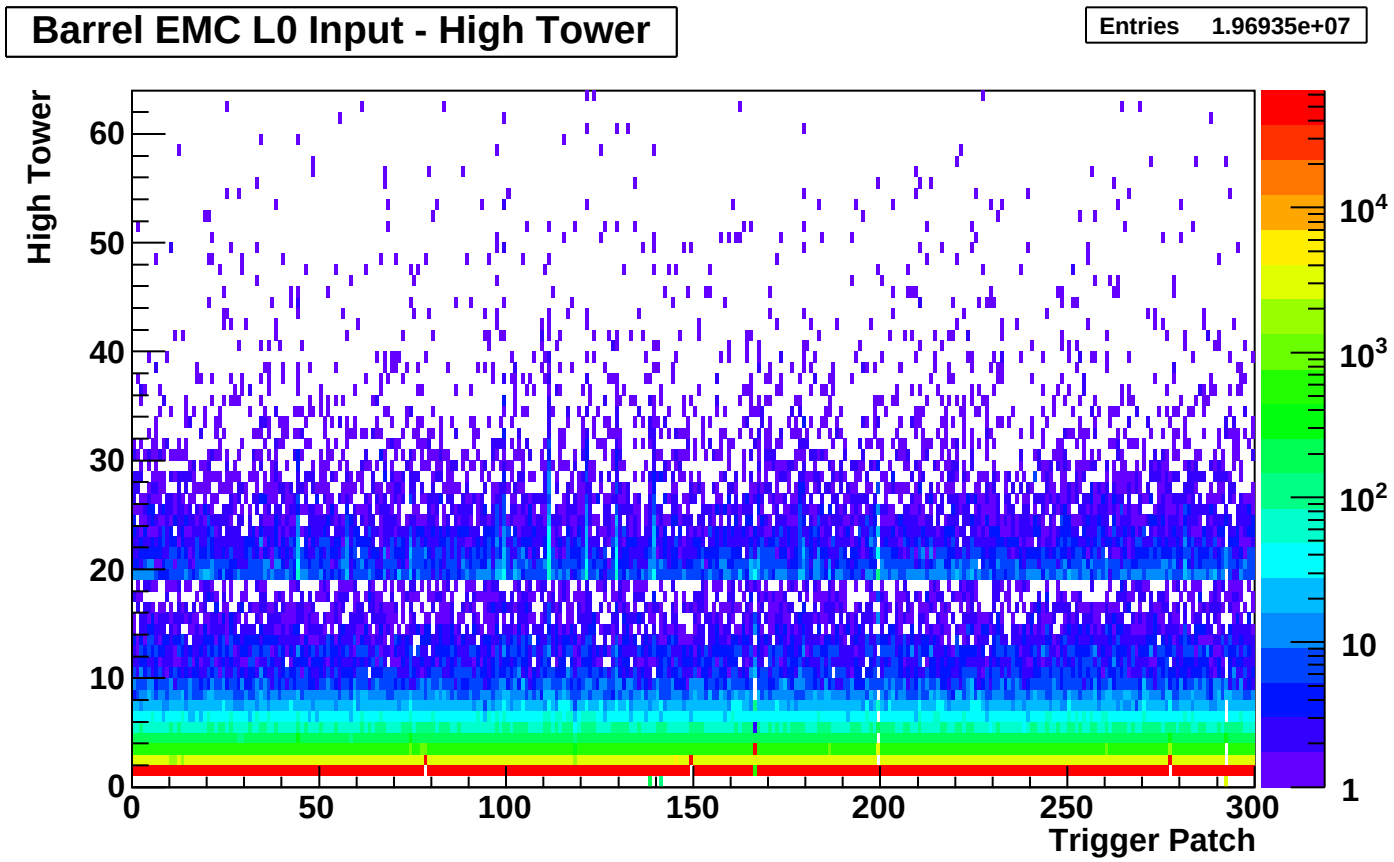

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

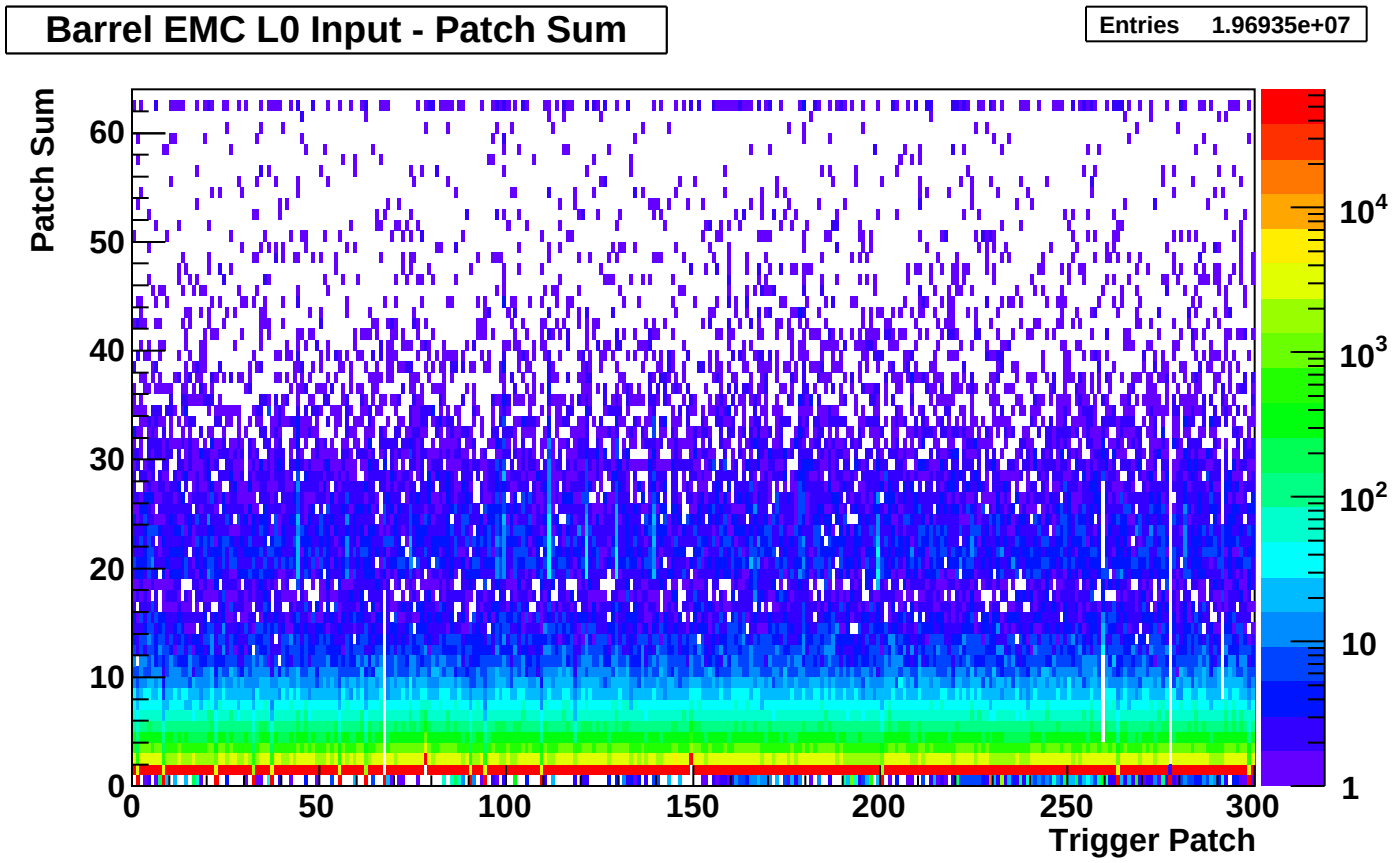

TCU bits
Entries 1050320
TCU bits
65645
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

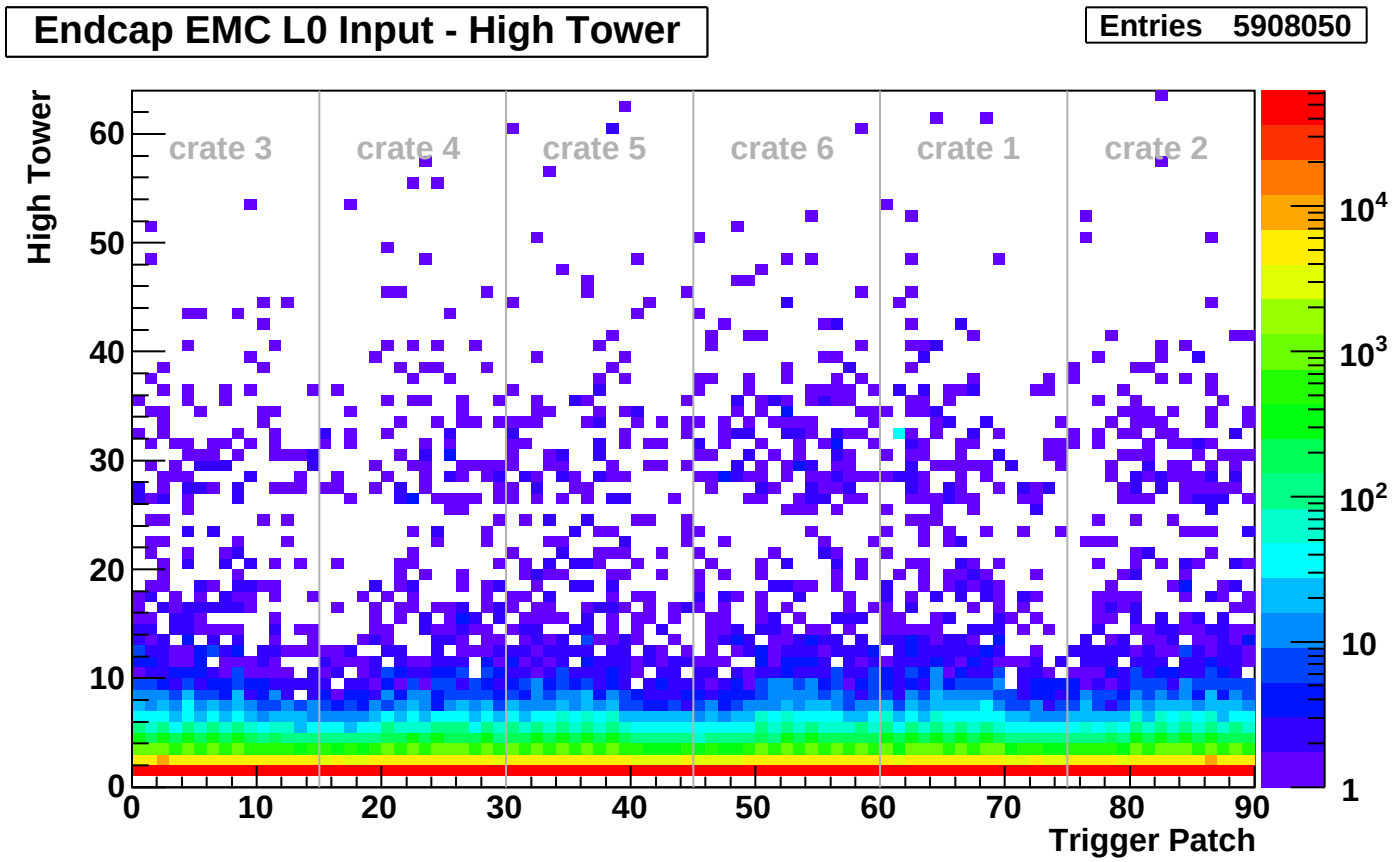

TCU bits
Entries 77365
TCU bits - Simulated
5506 12560 28734 671 2002 27725 167
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

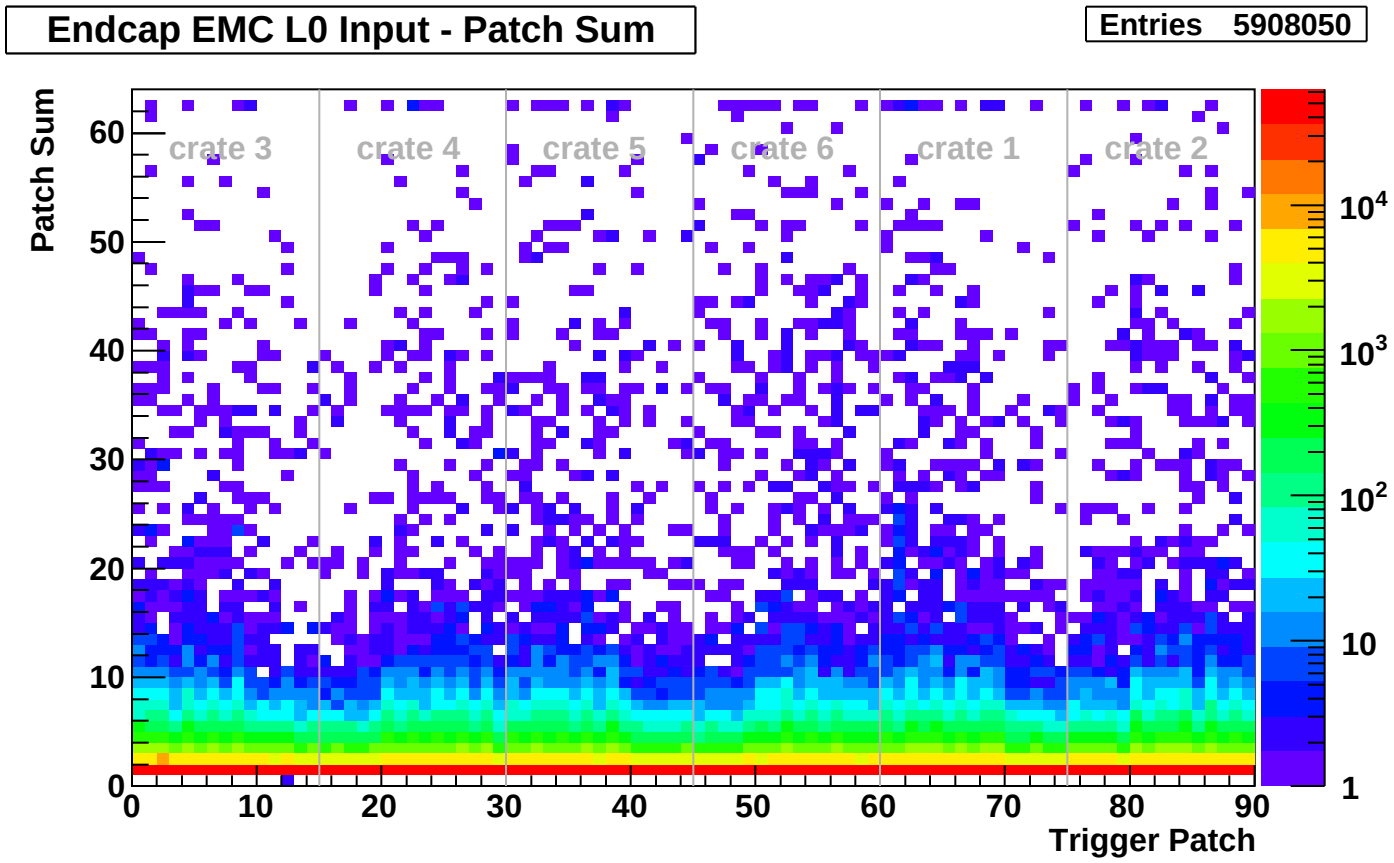

BunchId7Bit (BHT3)
Entries 925

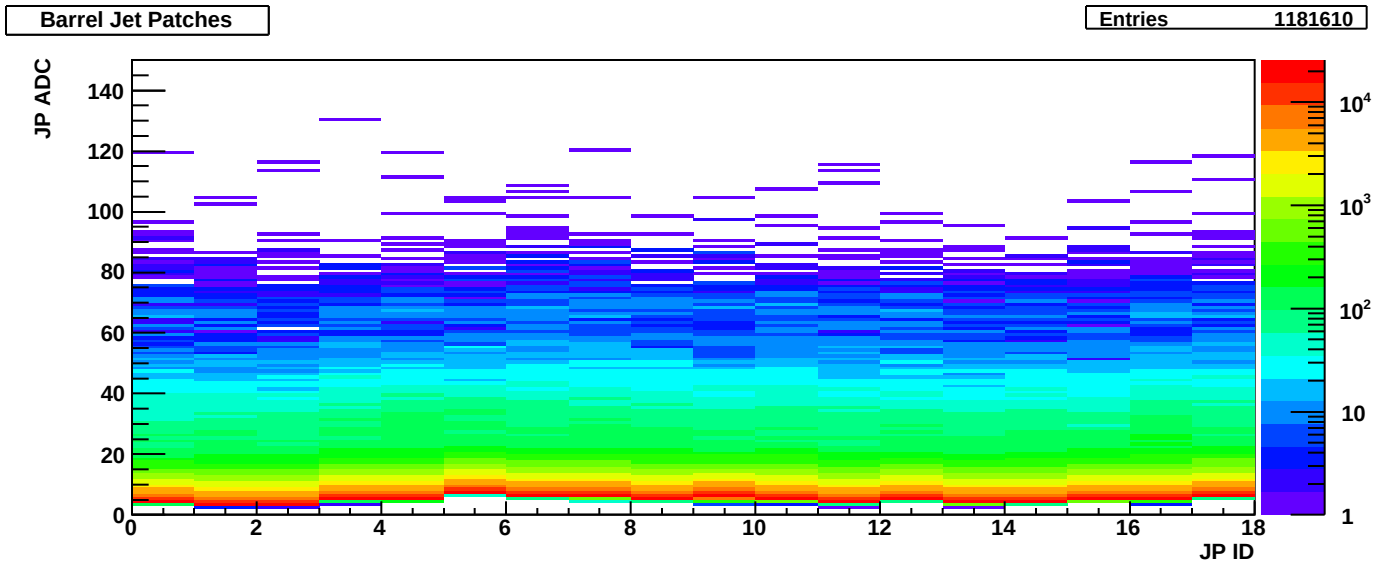

BunchId7Bit (all events)
Entries 65645

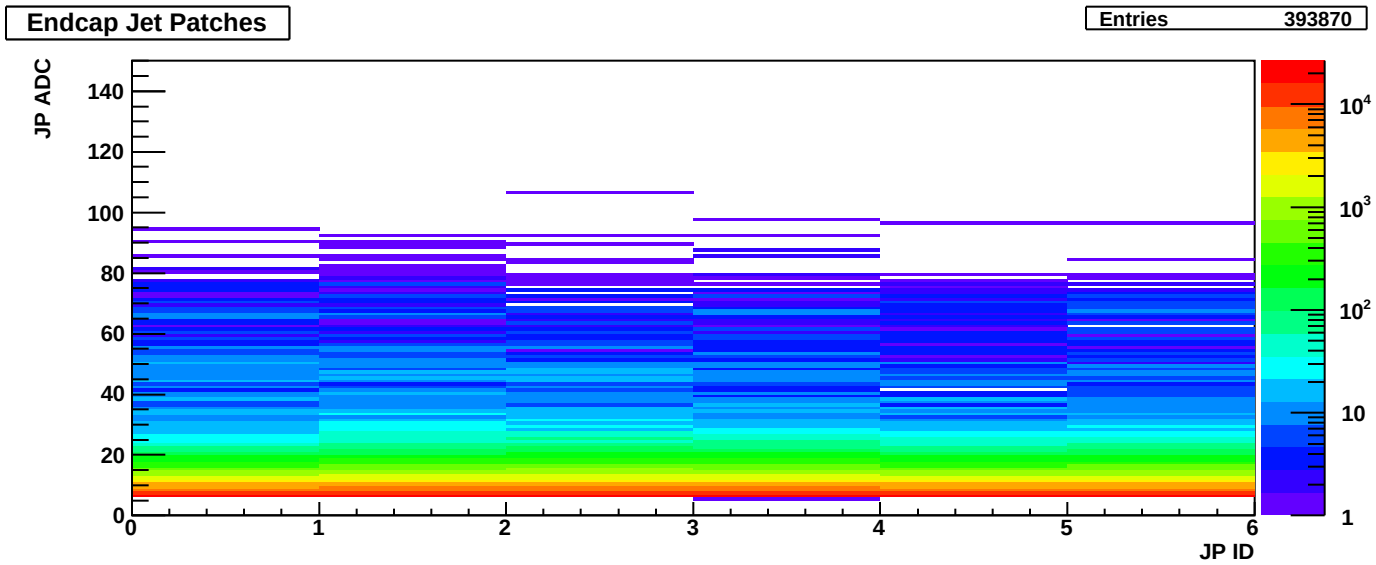

Barrel EMC L0 Input - High Tower
Entries 1.96935e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

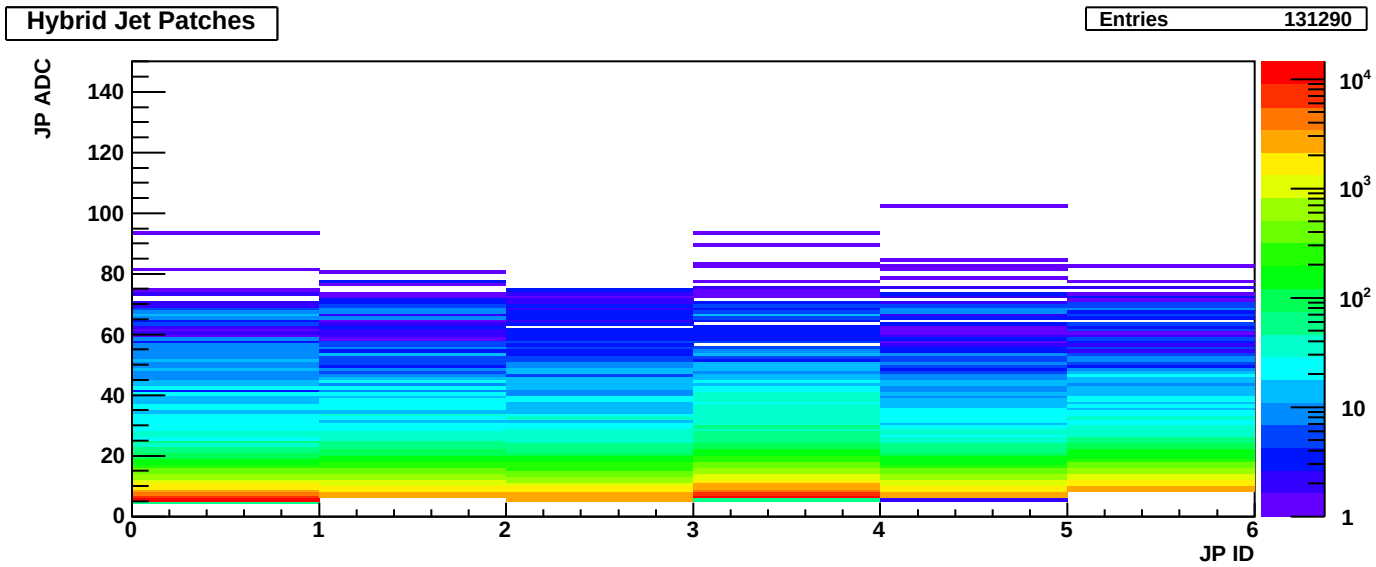

Barrel EMC L0 Input - Patch Sum
Entries 1.96935e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

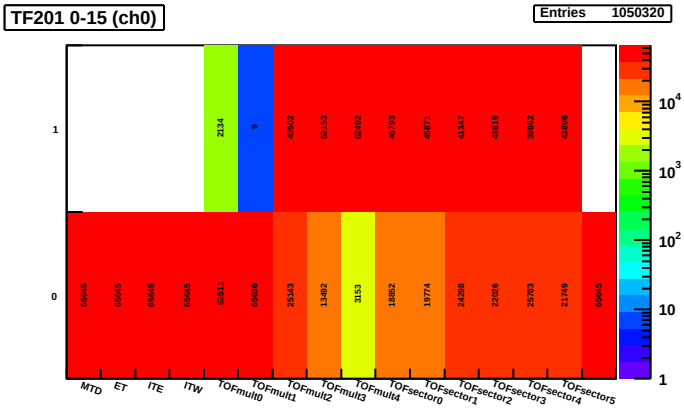

Endcap EMC L0 Input - High Tower
Entries 5908050
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

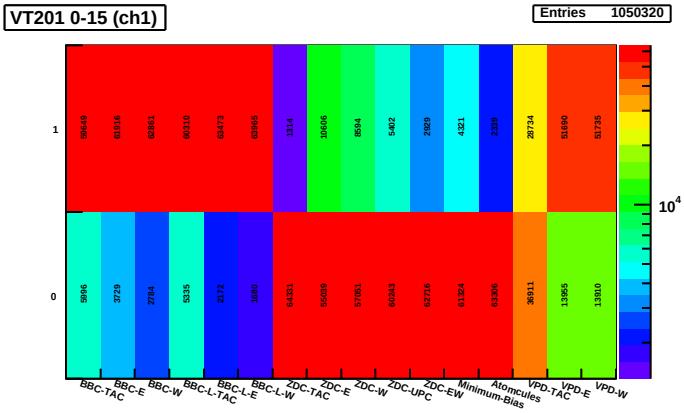

Endcap EMC L0 Input - Patch Sum
Entries 5908050
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

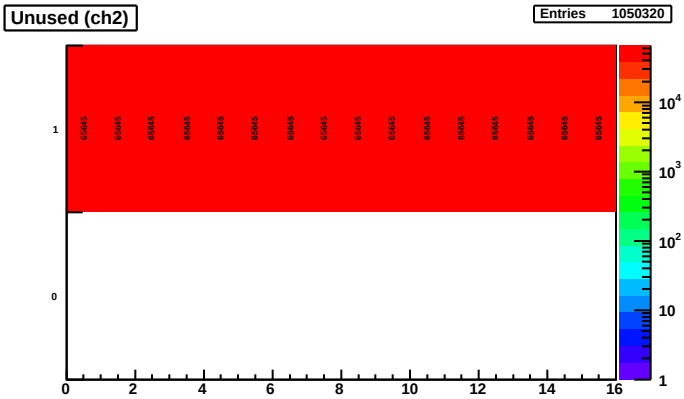

Barrel Jet Patches
Entries 1181610
JP ADC
JP ID

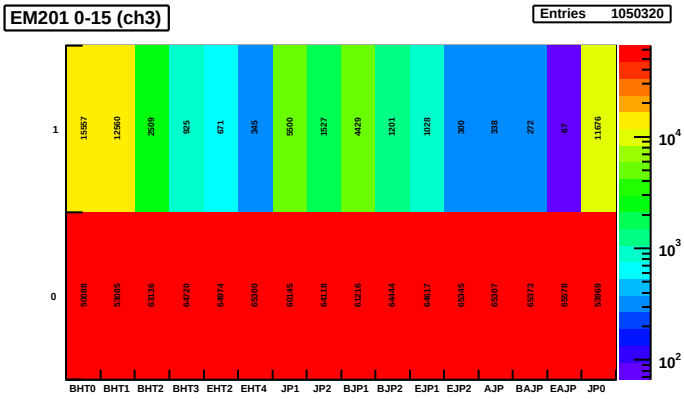

Endcap Jet Patches
Entries 393870
JP ADC
JP ID

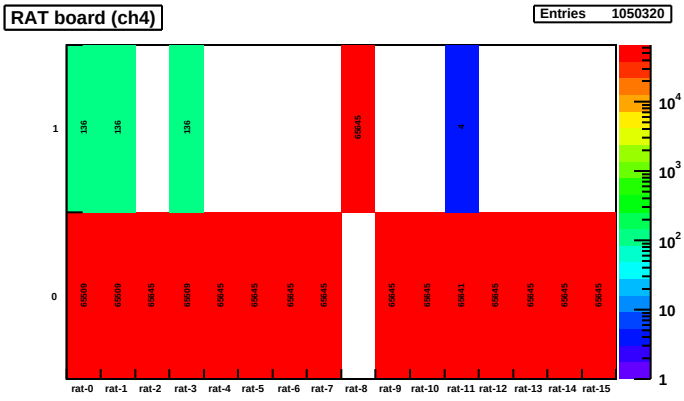

Hybrid Jet Patches
Entries 131290
JP ADC
JP ID

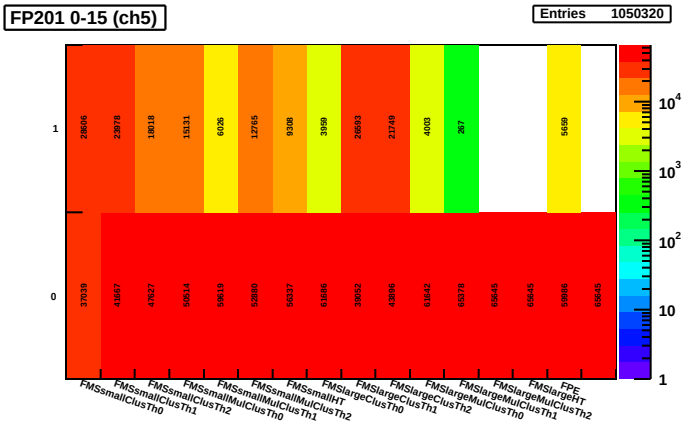

TF201 0-15 (ch0)
Entries 1050320
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

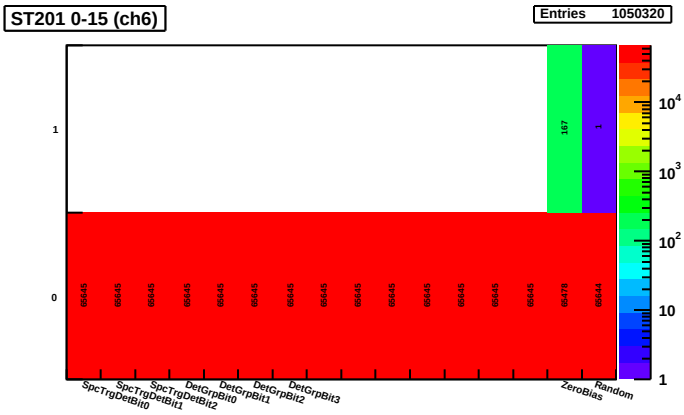

VT201 0-15 (ch1)
Entries 1050320
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

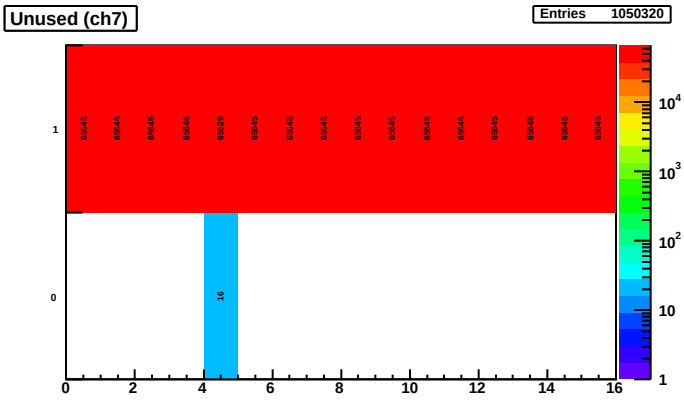

Unused (ch2)
Entries 1050320

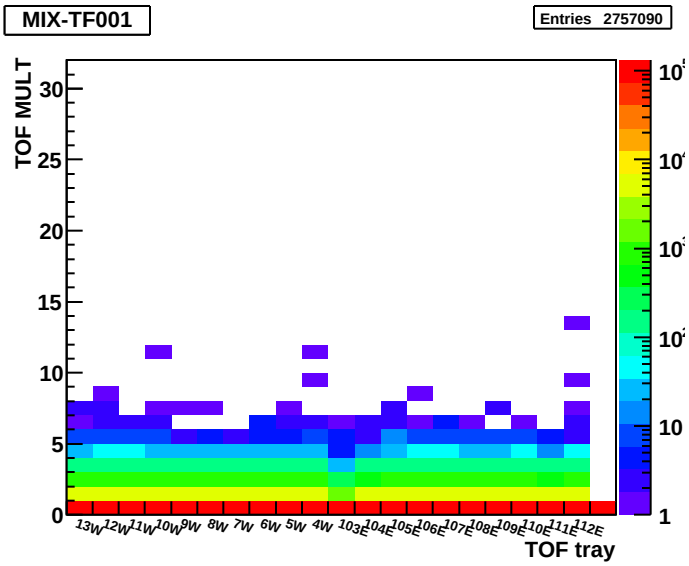

EM201 0-15 (ch3)
Entries 1050320
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

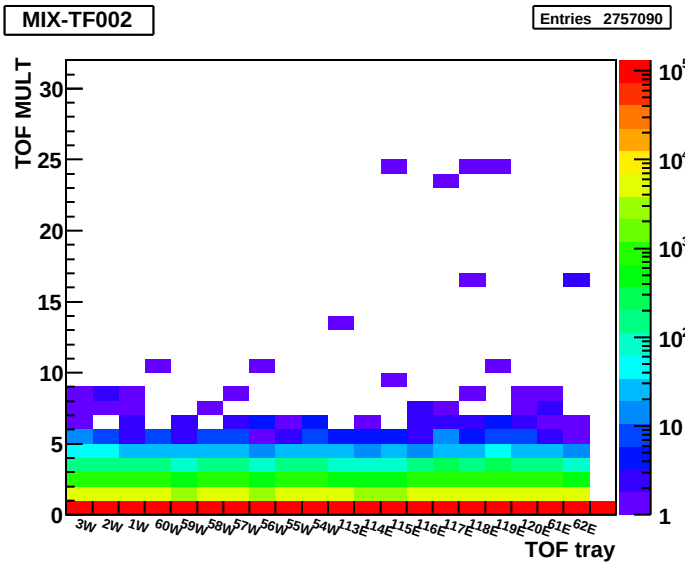

RAT board (ch4)
Entries 1050320
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

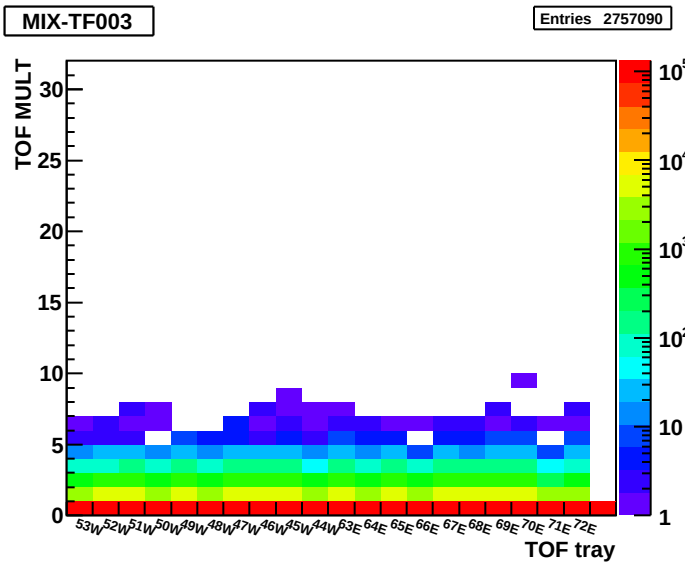

FP201 0-15 (ch5)
Entries 1050320
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

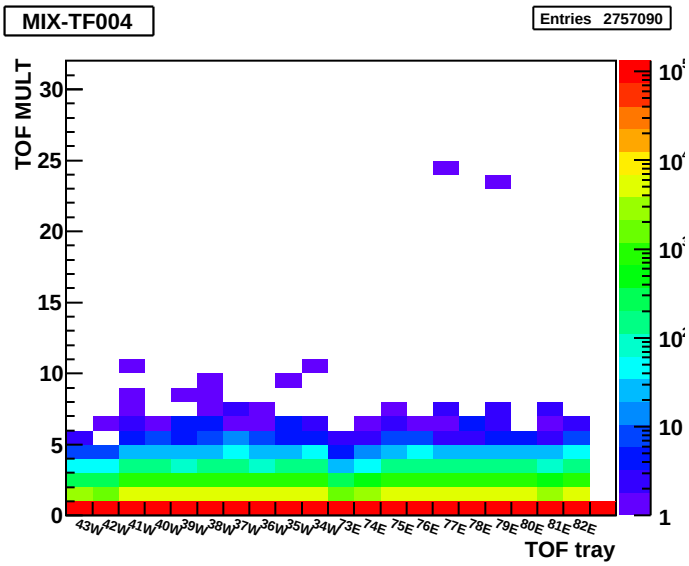

ST201 0-15 (ch6)
Entries 1050320
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

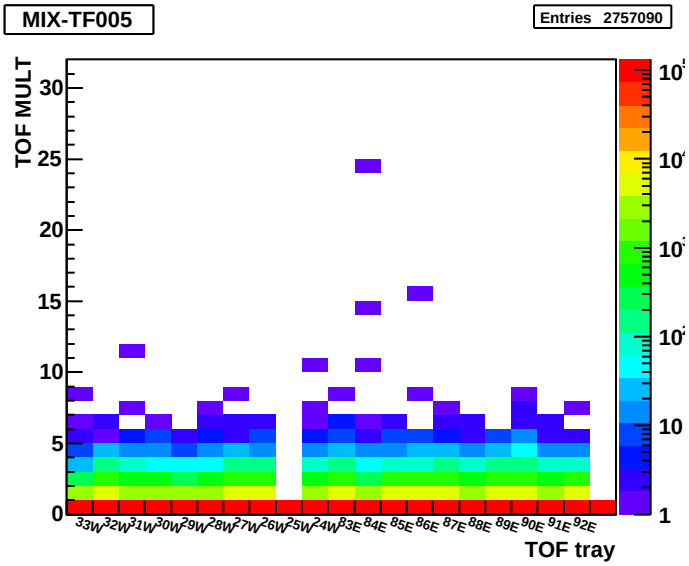

Unused (ch7)
Entries 1050320

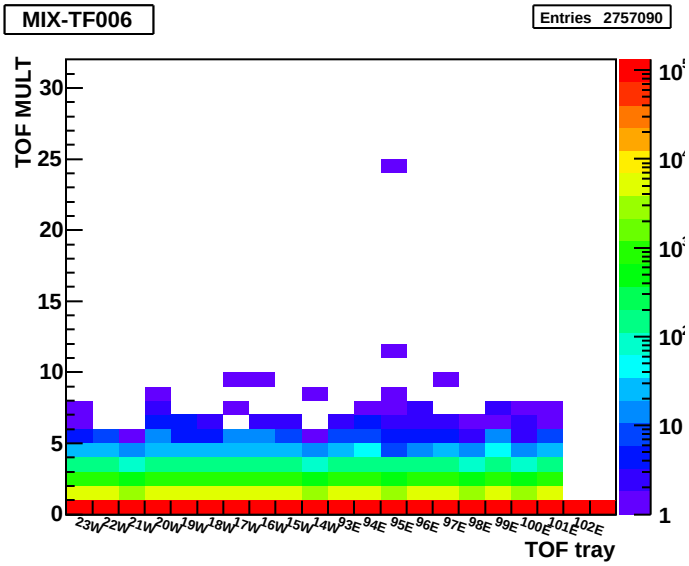

MIX-TF001
Entries 2757090
TOF MULT
TOF tray

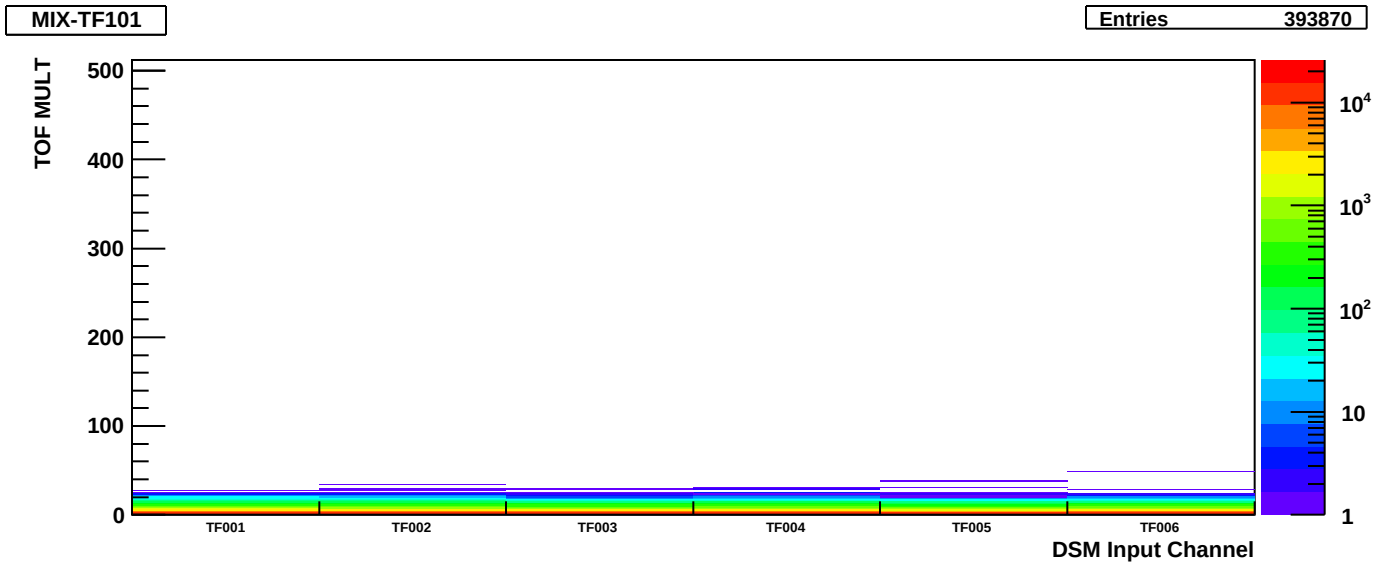

MIX-TF002
Entries 2757090
TOF MULT
TOF tray

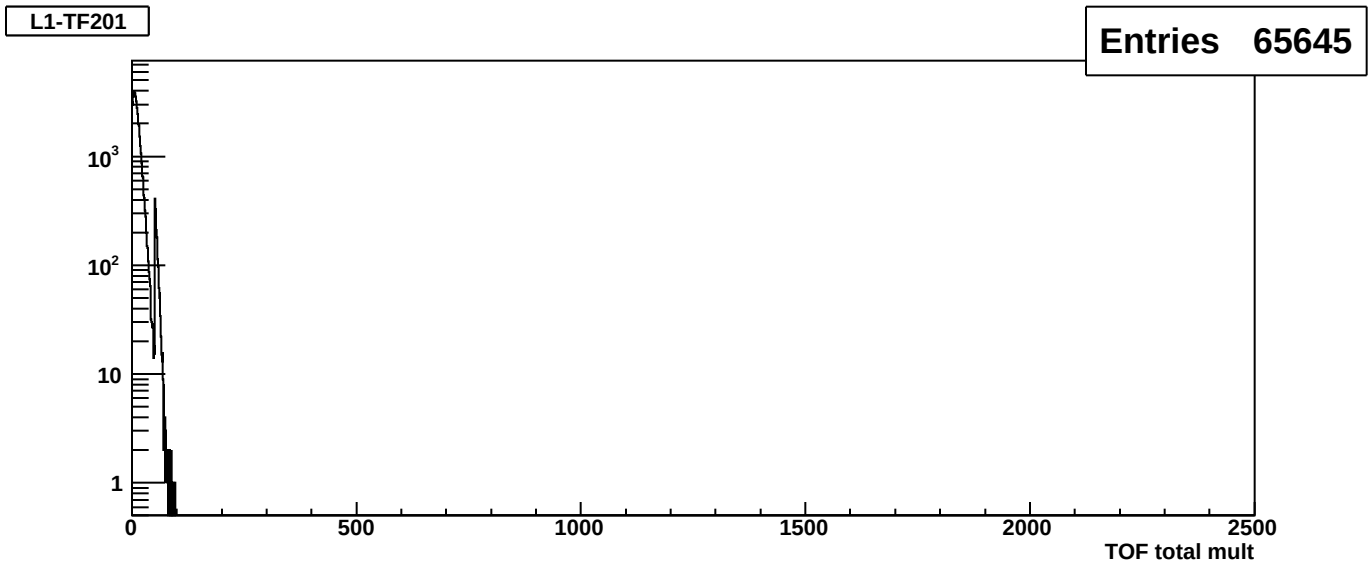

MIX-TF003
Entries 2757090
TOF MULT
TOF tray

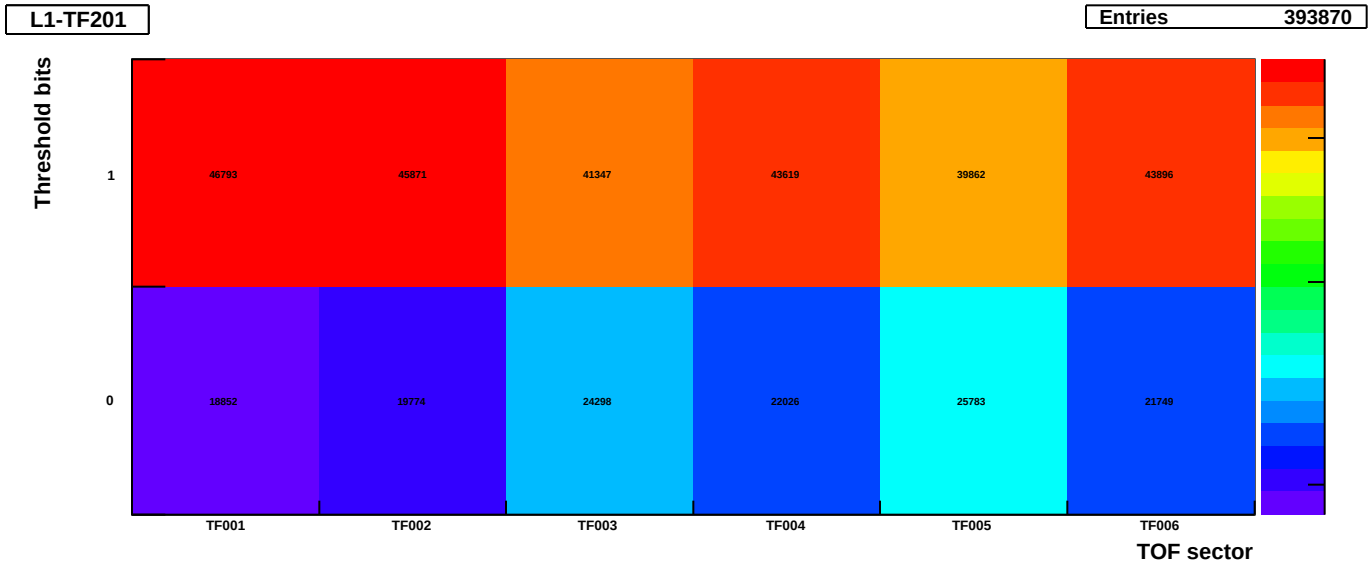

MIX-TF004
Entries 2757090
TOF MULT
TOF tray

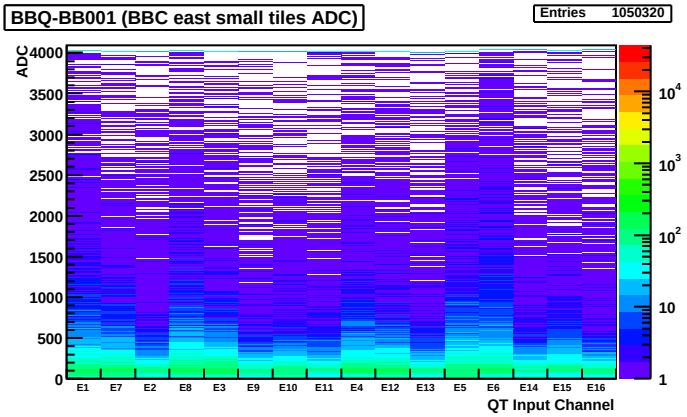

MIX-TF005
Entries 2757090
TOF MULT
TOF tray

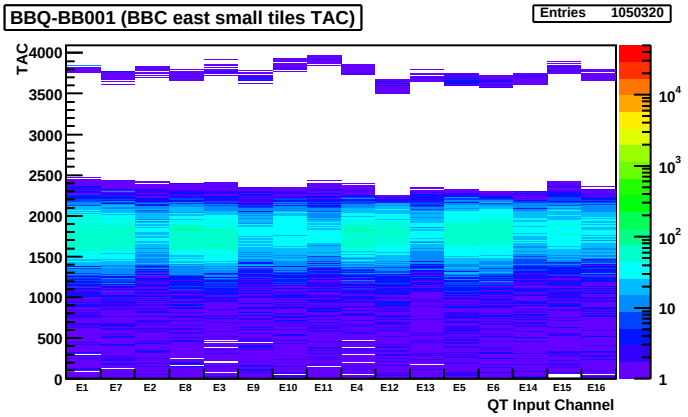

MIX-TF006
Entries 2757090
TOF MULT
TOF tray

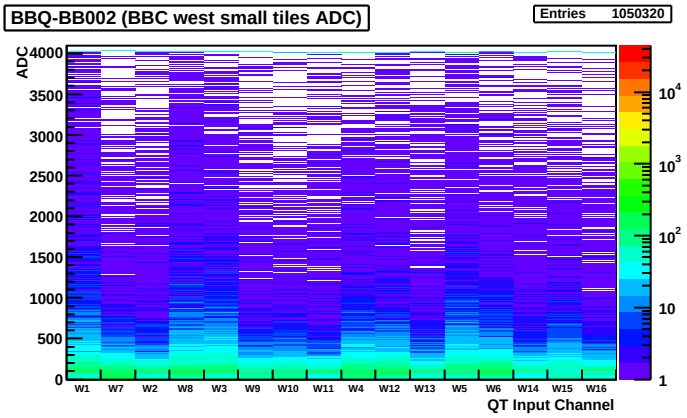

MIX-TF101
Entries 393870
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

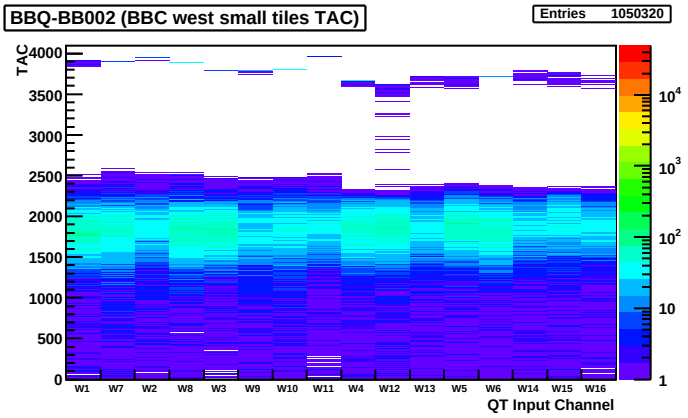

L1-TF201
Entries 65645
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

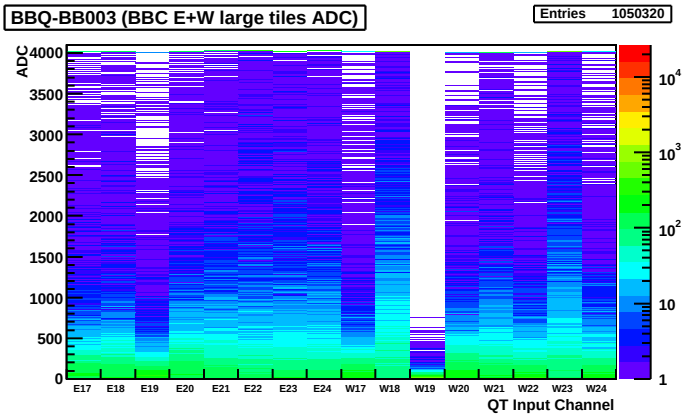

L1-TF201
Entries 393870
Threshold bits
1
0
46793
45871
41347
43619
39862
43896
18852
19774
24298
22026
25783
21749
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

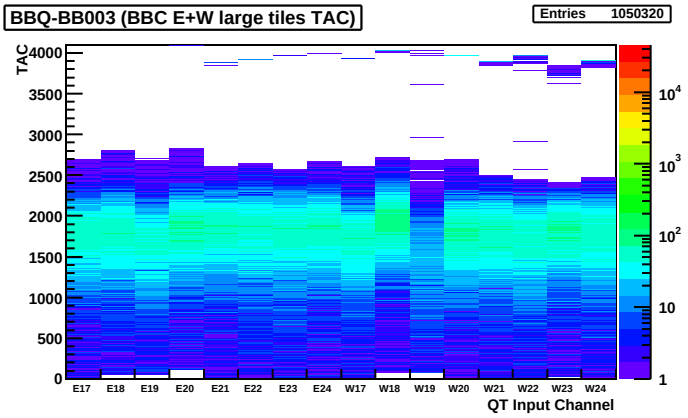

BBQ-BB001 (BBC east small tiles ADC)
Entries 1050320
ADC
QT Input Channel

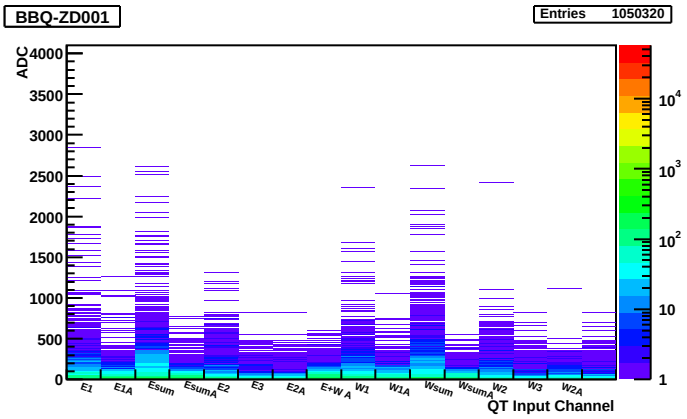

BBQ-BB001 (BBC east small tiles TAC)
Entries 1050320
TAC
QT Input Channel

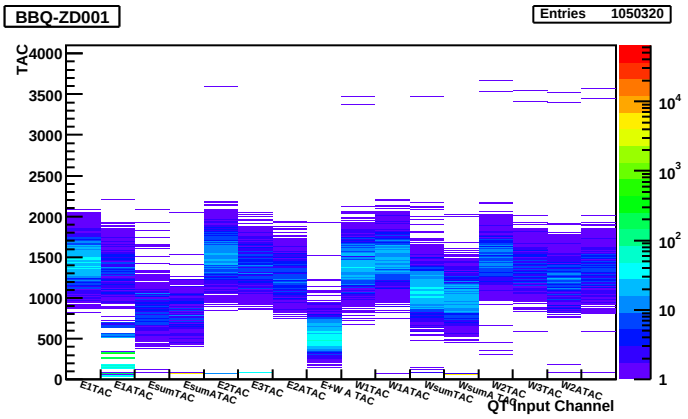

BBQ-BB002 (BBC west small tiles ADC)
Entries 1050320
ADC
QT Input Channel

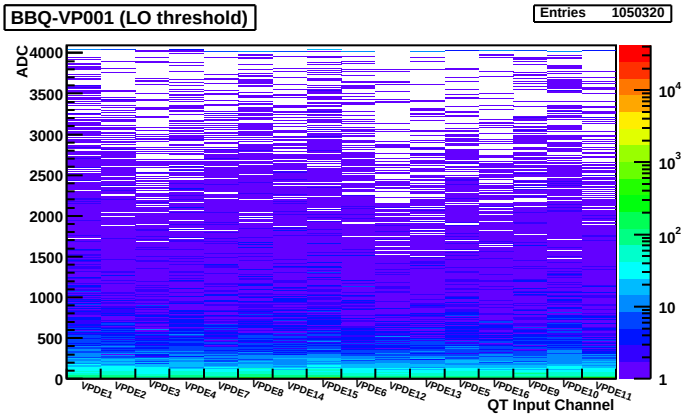

BBQ-BB002 (BBC west small tiles TAC)
Entries 1050320
TAC
QT Input Channel

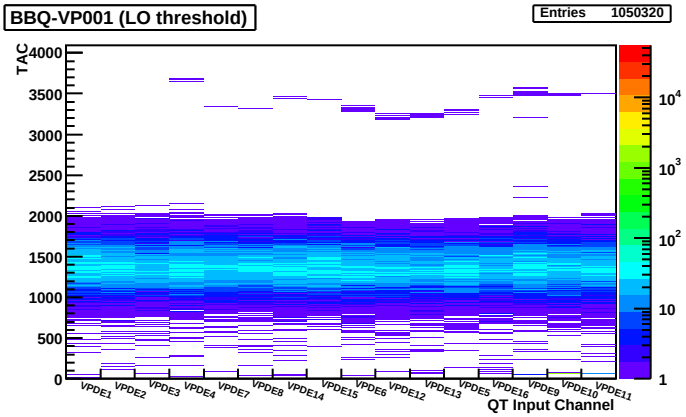

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1050320
ADC
QT Input Channel

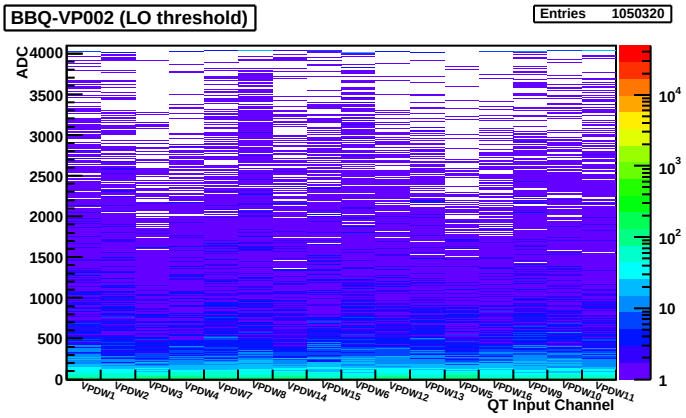

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1050320
TAC
QT Input Channel

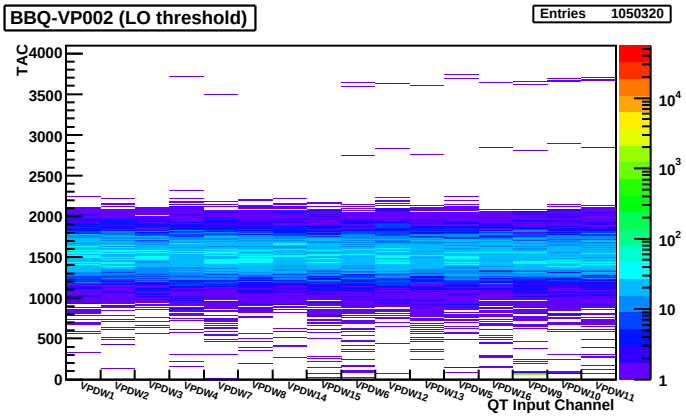

BBQ-ZD001
Entries 1050320
ADC
QT Input Channel

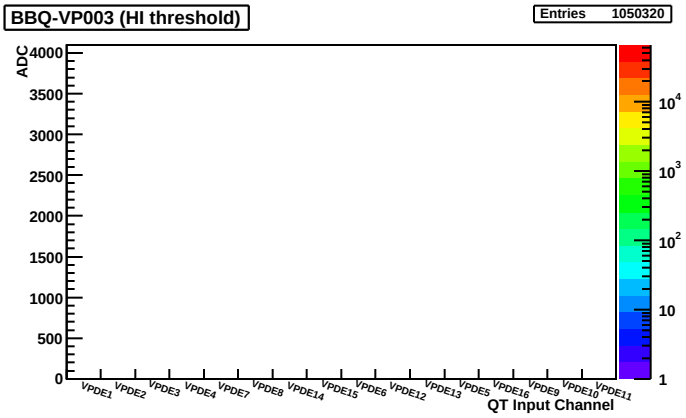

BBQ-ZD001
Entries 1050320
TAC
QT Input Channel

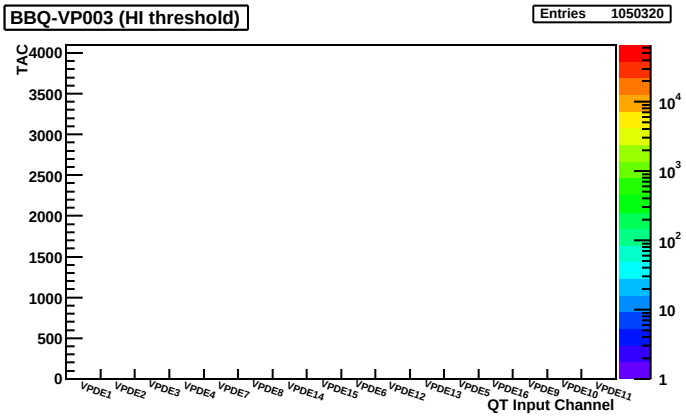

BBQ-VP001 (LO threshold)
Entries 1050320
ADC
QT Input Channel

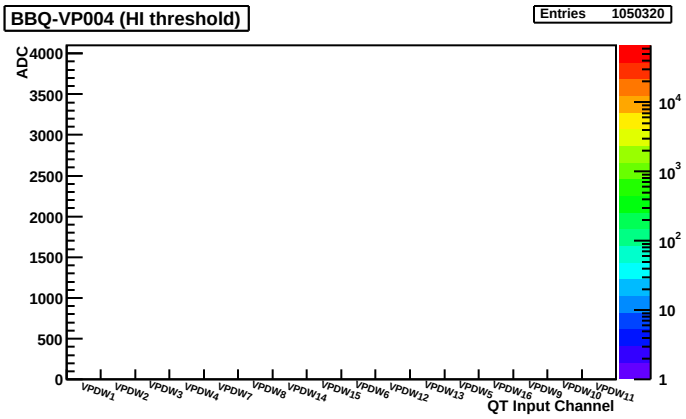

BBQ-VP001 (LO threshold)
Entries 1050320
TAC
QT Input Channel

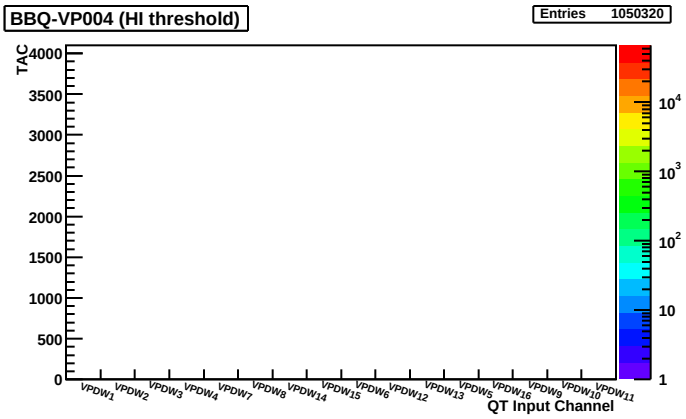

BBQ-VP002 (LO threshold)
Entries 1050320
ADC
QT Input Channel

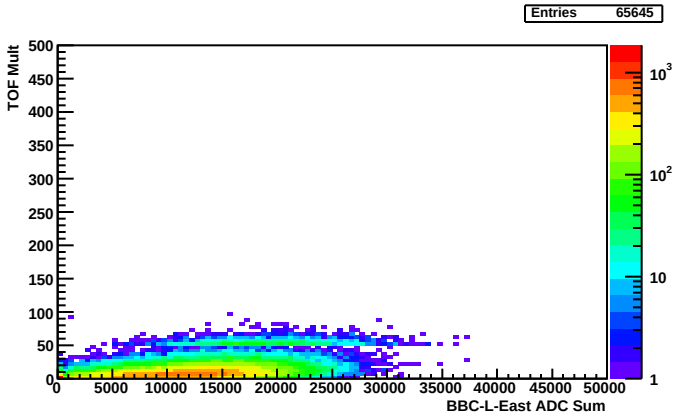

BBQ-VP002 (LO threshold)
Entries 1050320
TAC
QT Input Channel

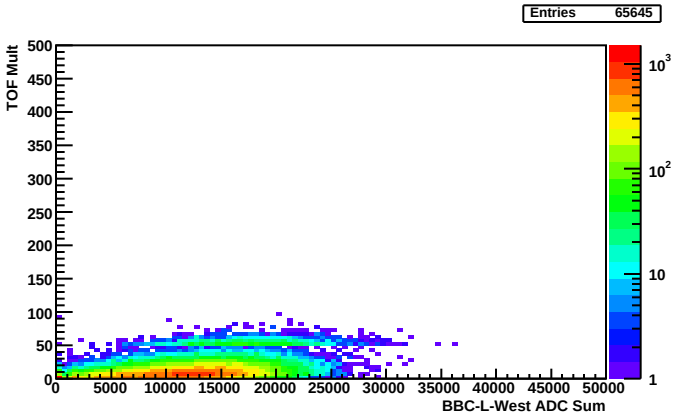

BBQ-VP003 (HI threshold)
Entries 1050320
ADC
QT Input Channel

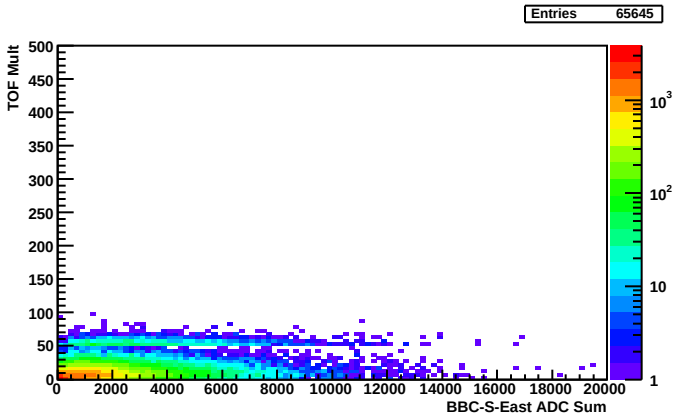

BBQ-VP003 (HI threshold)
Entries 1050320
TAC
QT Input Channel

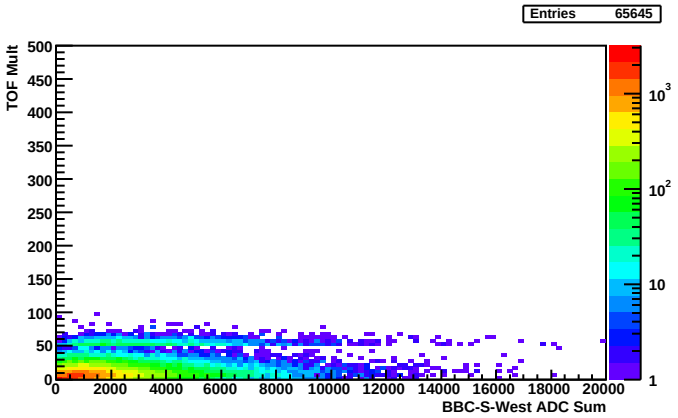

BBQ-VP004 (HI threshold)
Entries 1050320
ADC
QT Input Channel

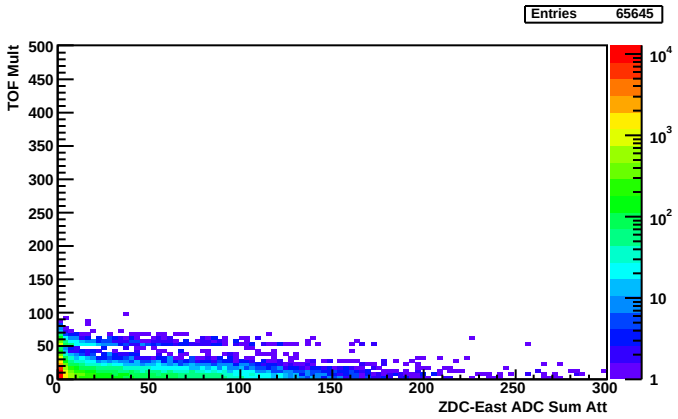

BBQ-VP004 (HI threshold)
Entries 1050320
TAC
QT Input Channel

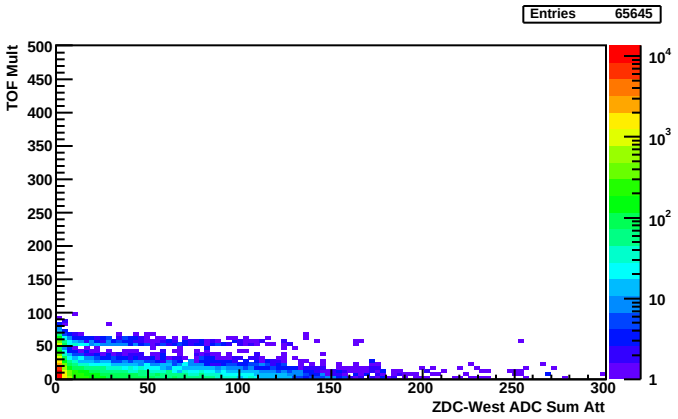

Entries 65645
TOF Mult
BBC-L-East ADC Sum

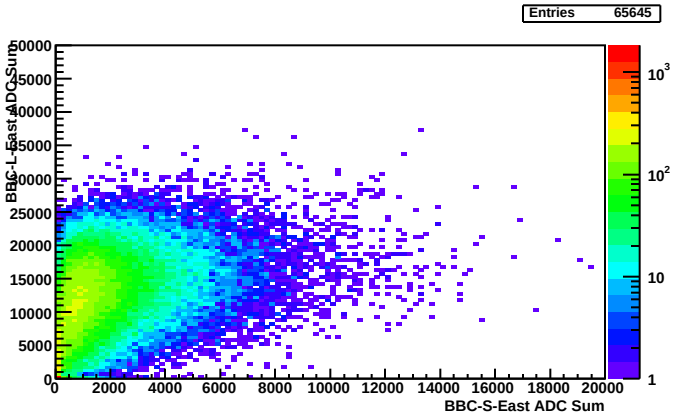

Entries 65645
TOF Mult
BBC-L-West ADC Sum

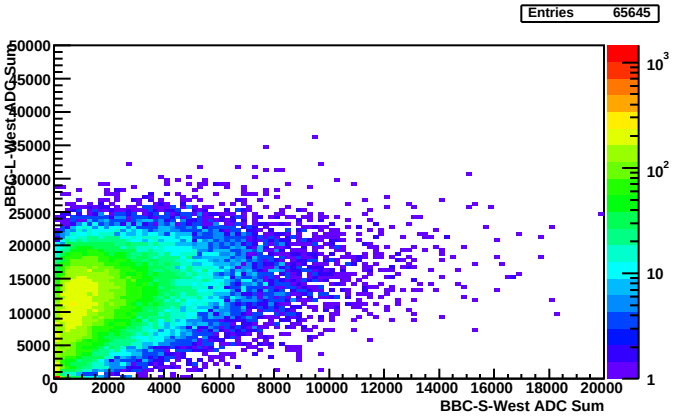

Entries 65645
TOF Mult
BBC-S-East ADC Sum

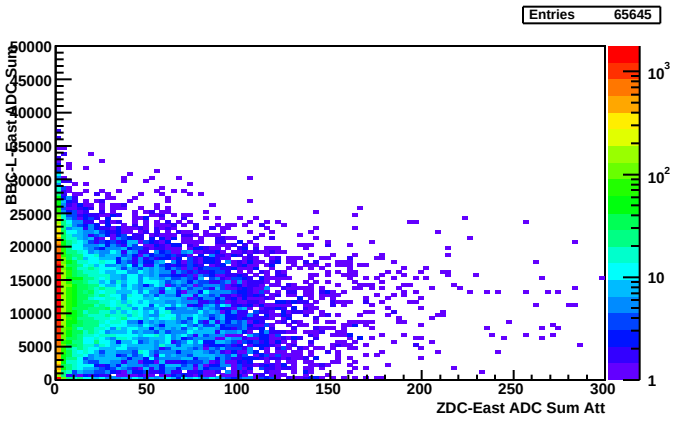

Entries 65645
TOF Mult
BBC-S-West ADC Sum

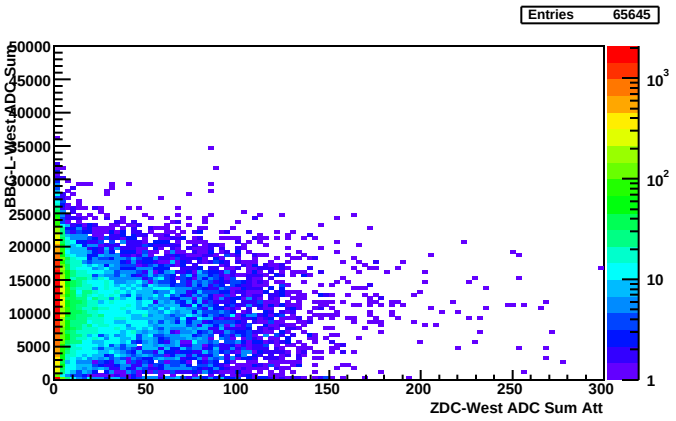

Entries 65645
TOF Mult
ZDC-East ADC Sum Att

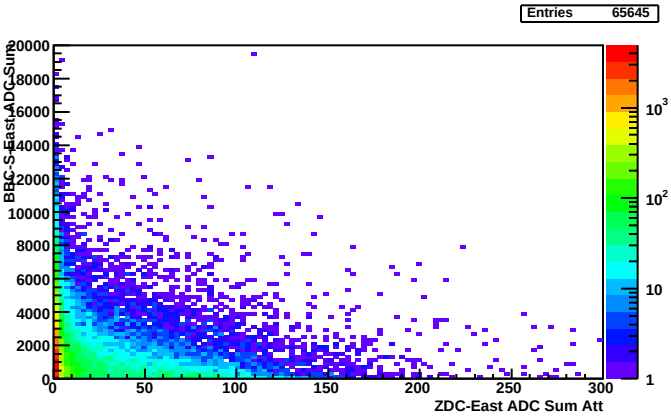

Entries 65645
TOF Mult
ZDC-West ADC Sum Att

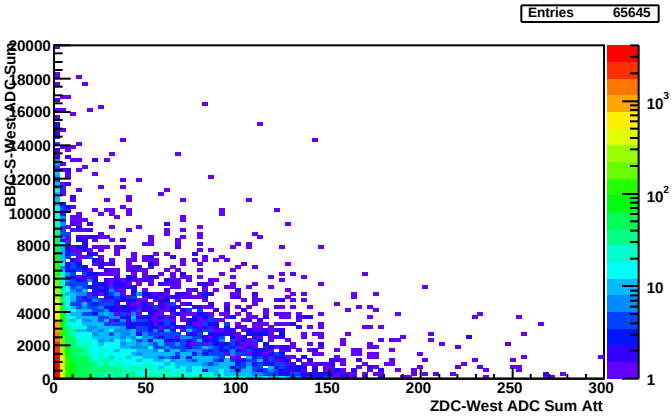

Entries 65645
BBC-L-East ADC Sum
BBC-S-East ADC Sum

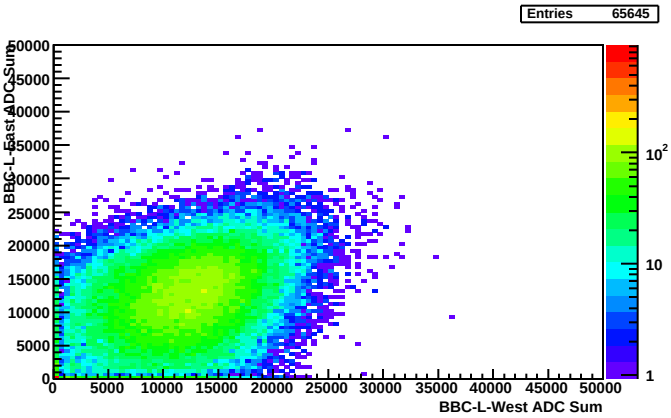

Entries 65645
BBC-L-West ADC Sum
BBC-S-West ADC Sum

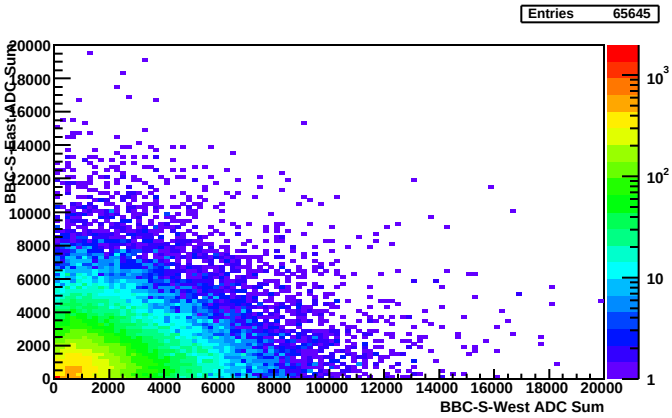

Entries 65645
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

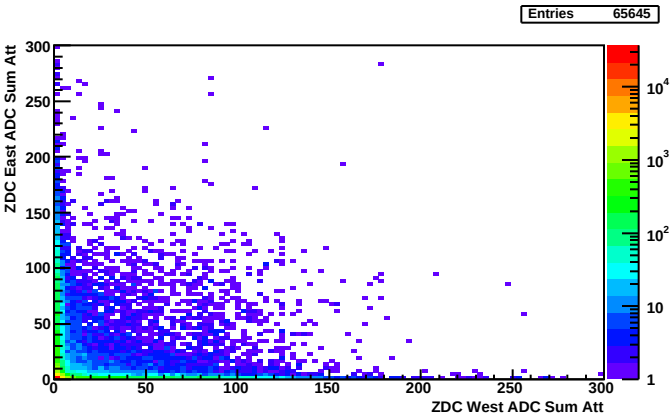

Entries 65645
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

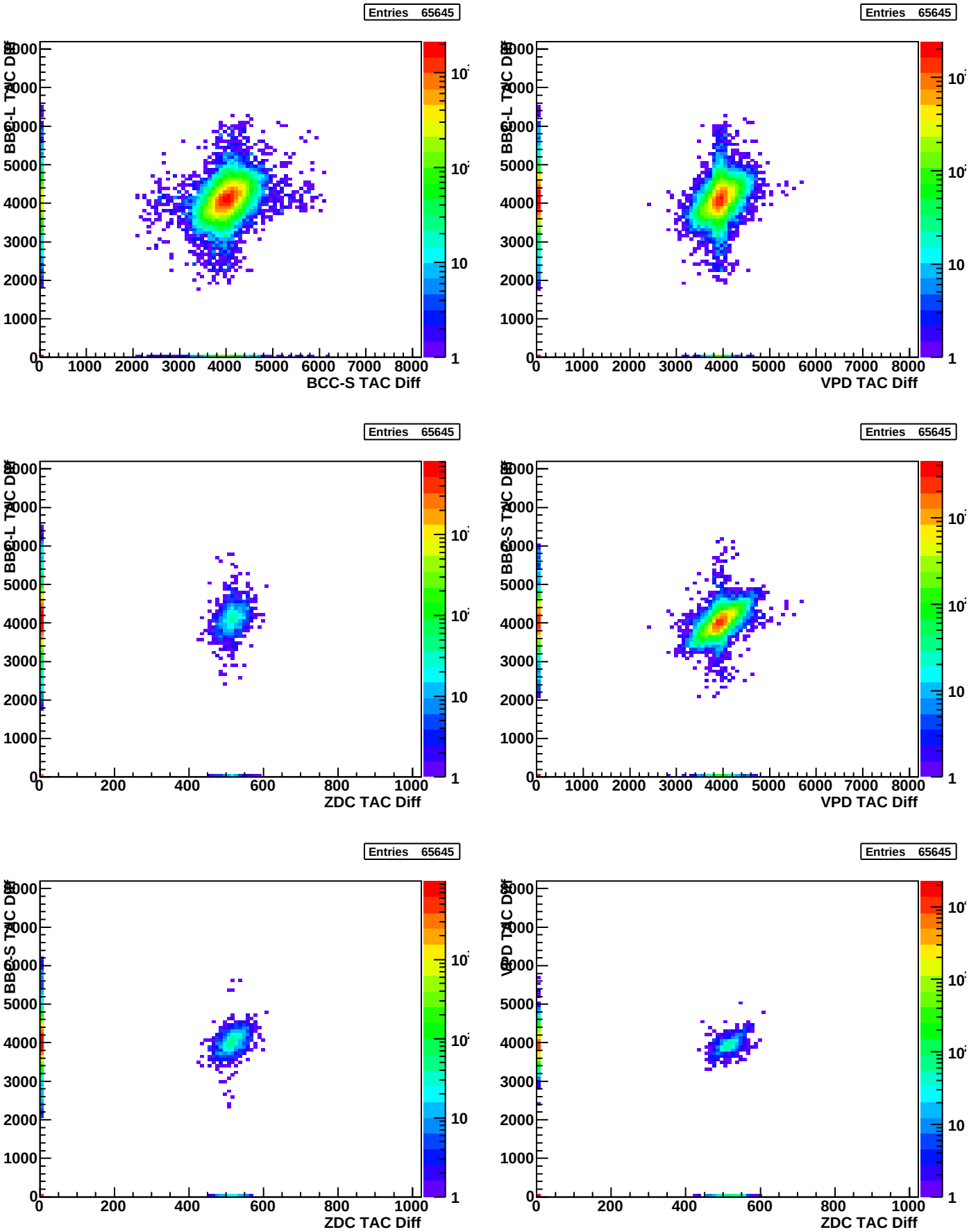

Entries 65645
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

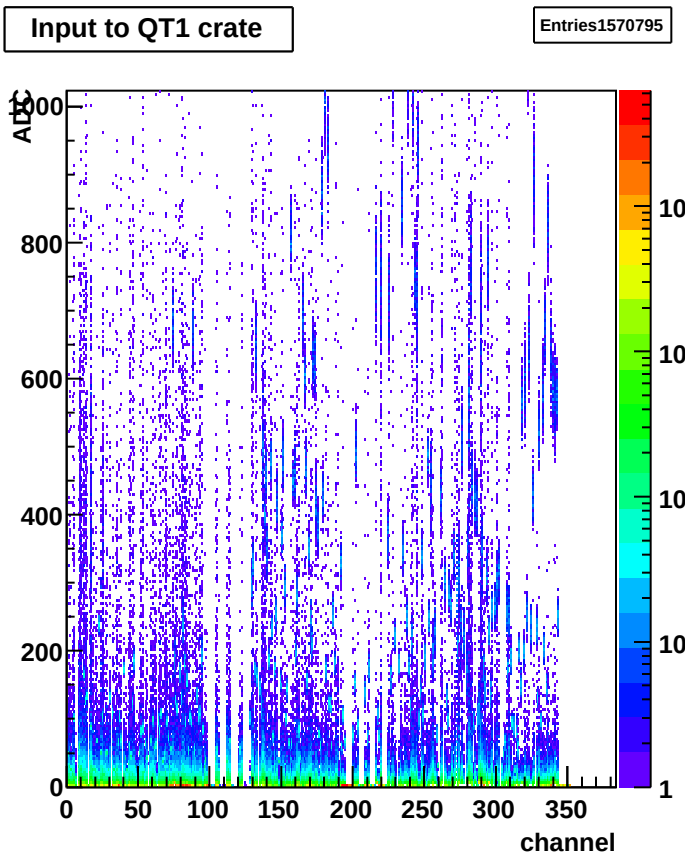

Entries 65645
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

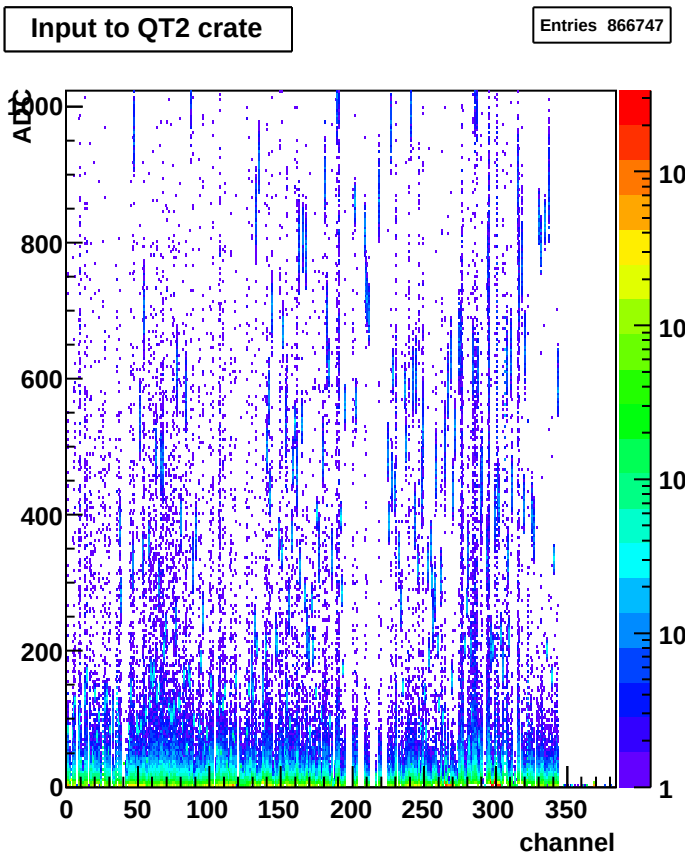

Entries 65645
BBC-L-East ADC Sum
BBC-L-West ADC Sum

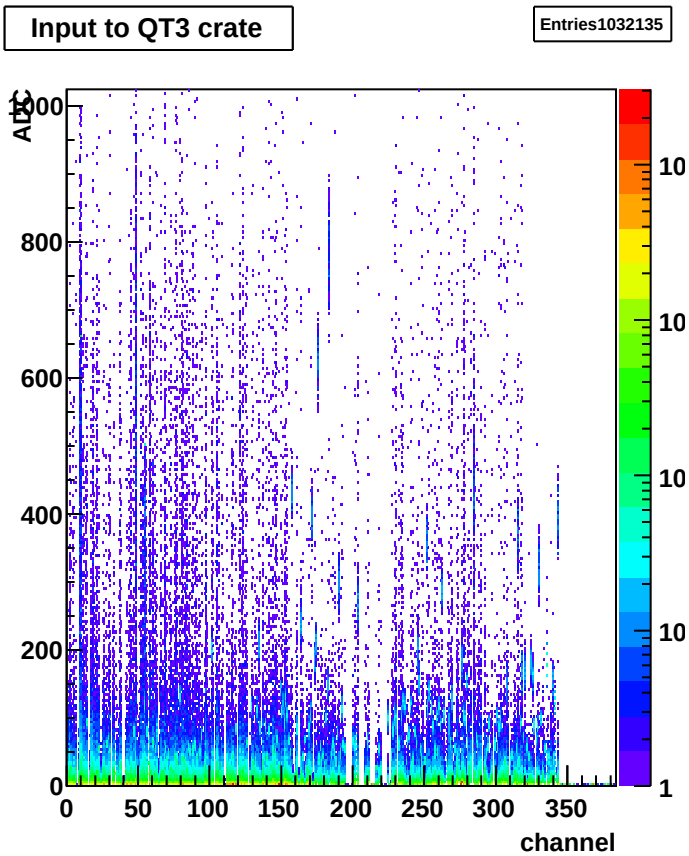

Entries 65645
BBC-S-East ADC Sum
BBC-S-West ADC Sum

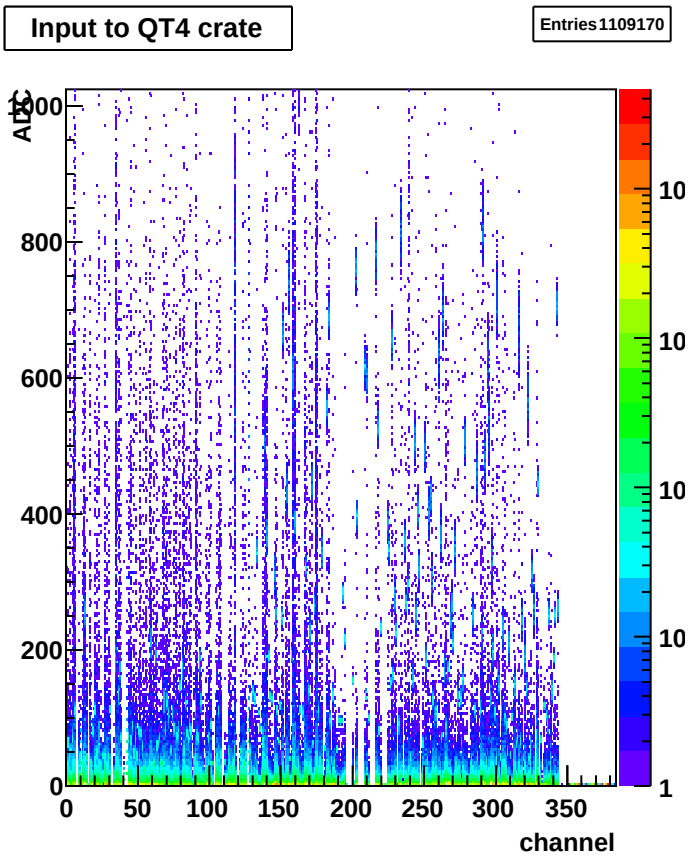

Entries 65645
ZDC East ADC Sum Att
ZDC West ADC Sum Att

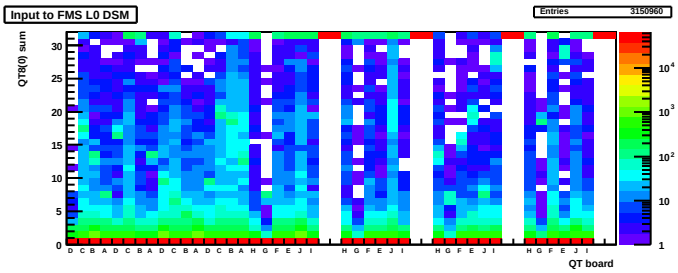

Entries 65645
BBC-L TAC Diff
BCC-S TAC Diff
Entries 65645
BBC-L TAC Diff
VPD TAC Diff
Entries 65645
BBC-L TAC Diff
ZDC TAC Diff
Entries 65645
BBC-S TAC Diff
VPD TAC Diff
Entries 65645
BBC-S TAC Diff
ZDC TAC Diff
Entries 65645
VPD TAC Diff
ZDC TAC Diff

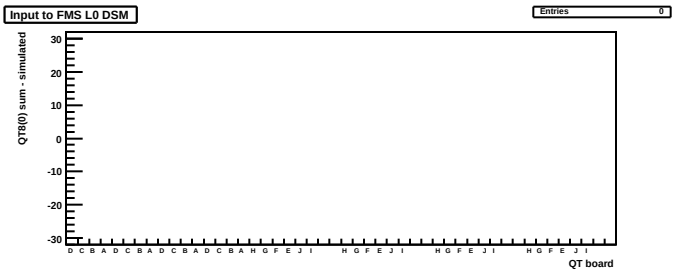

Input to QT1 crate
Entries1570795
ADC
channel

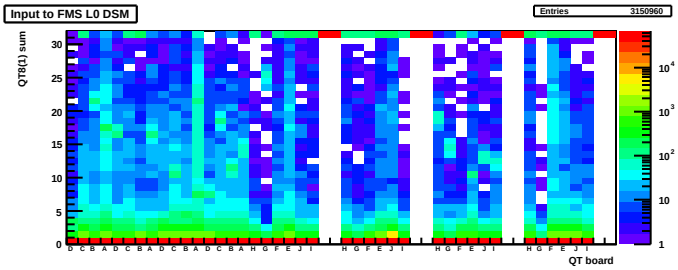

Input to QT2 crate
Entries 866747
ADC
channel

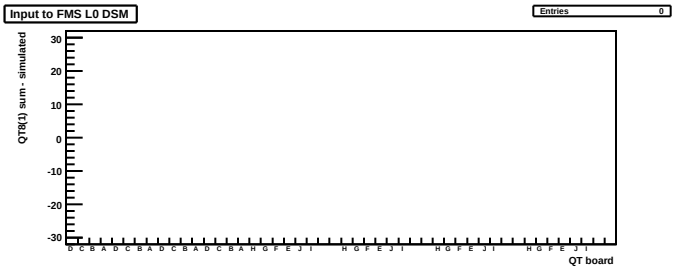

Input to QT3 crate
Entries1032135
ADC
channel

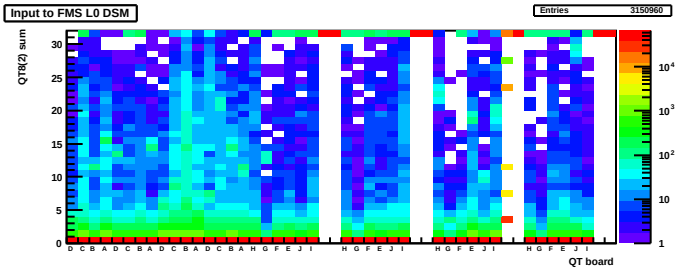

Input to QT4 crate
Entries1109170
ADC
channel

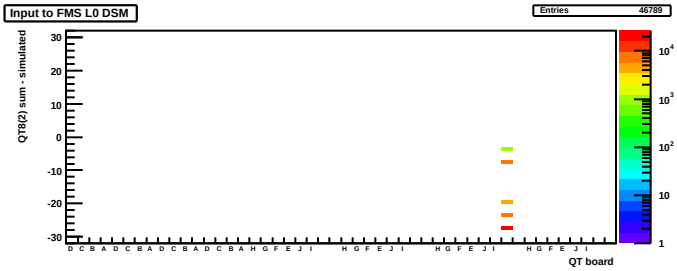

Input to FMS L0 DSM
Entries 3150960
QT8(0) sum
QT board

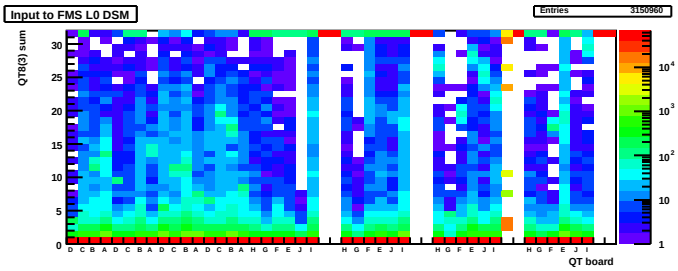

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

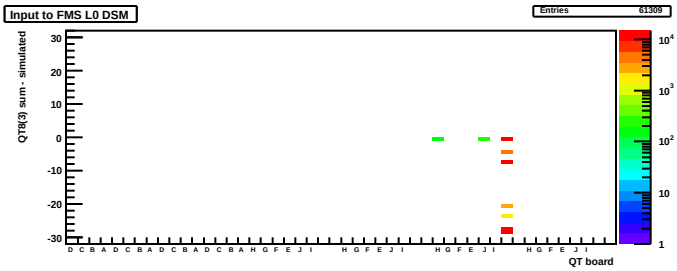

Input to FMS L0 DSM
Entries 3150960
QT8(1) sum
QT board

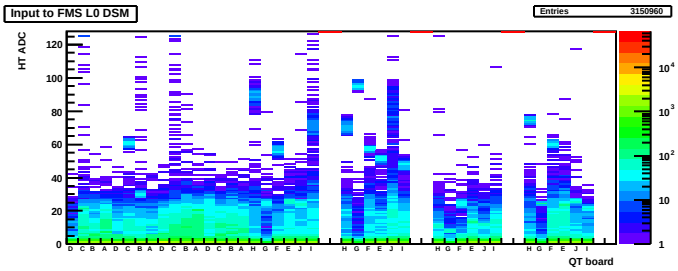

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

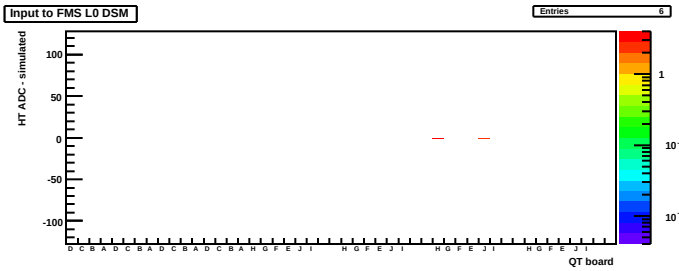

Input to FMS L0 DSM
Entries 3150960
QT8(2) sum
QT board

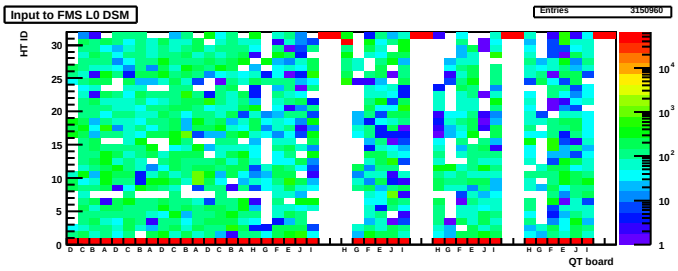

Input to FMS L0 DSM
Entries 46789
QT8(2) sum - simulated
QT board

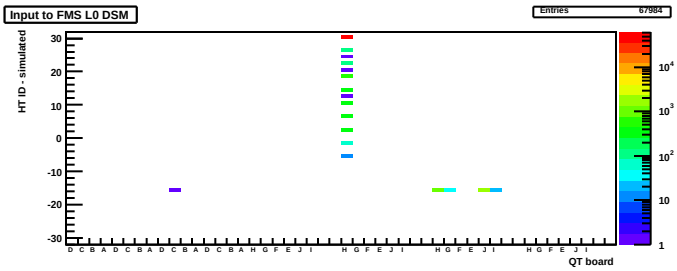

Input to FMS L0 DSM
Entries 3150960
QT8(3) sum
QT board

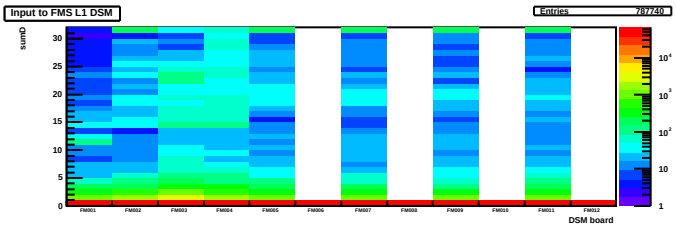

Input to FMS L0 DSM
Entries 61309
QT8(3) sum - simulated
QT board

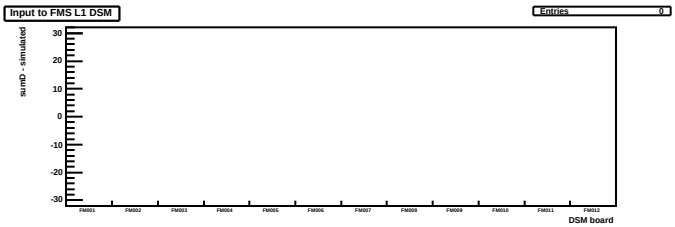

Input to FMS L0 DSM
Entries 3150960
HT ADC
QT board

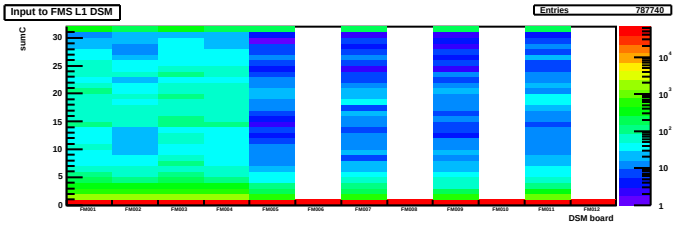

Input to FMS L0 DSM
Entries 6
HT ADC - simulated
QT board

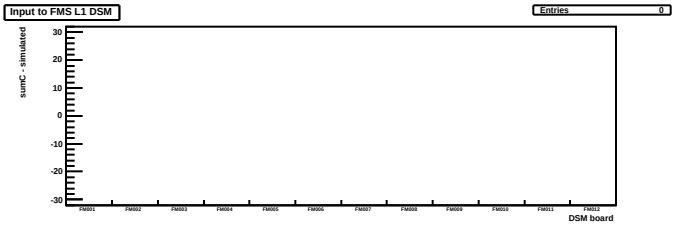

Input to FMS L0 DSM
Entries 3150960
HT ID
QT board

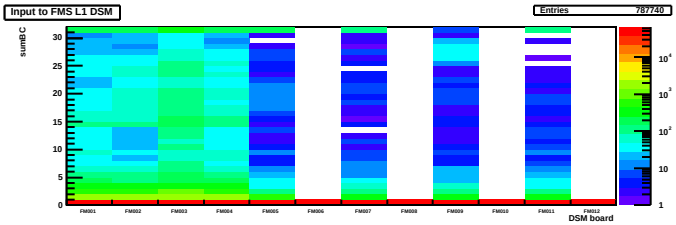

Input to FMS L0 DSM
Entries 67984
HT ID - simulated
QT board

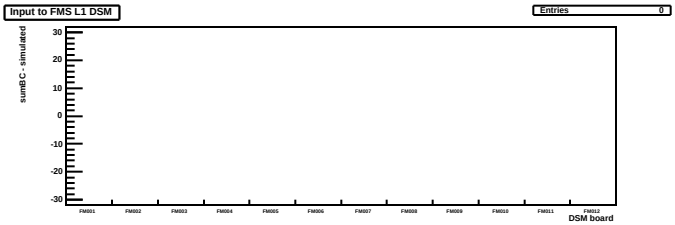

Input to FMS L1 DSM
Entries 787740
sumD
DSM board

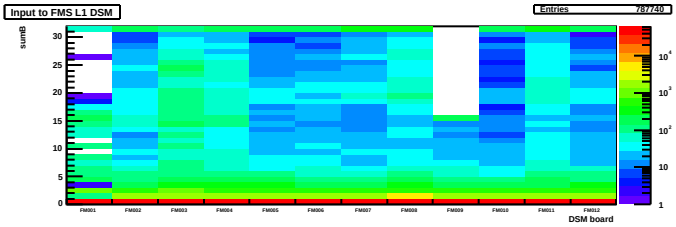

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

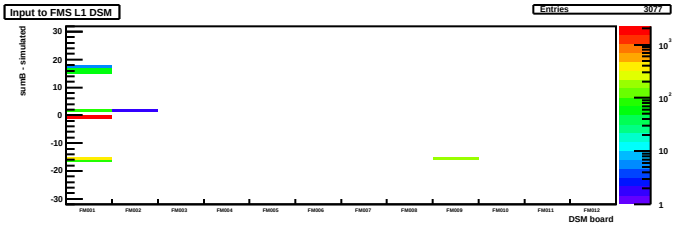

Input to FMS L1 DSM
Entries 787740
sumC
DSM board

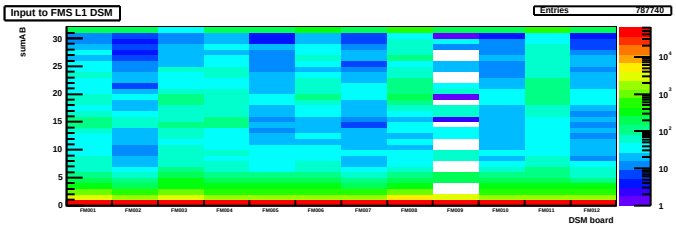

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

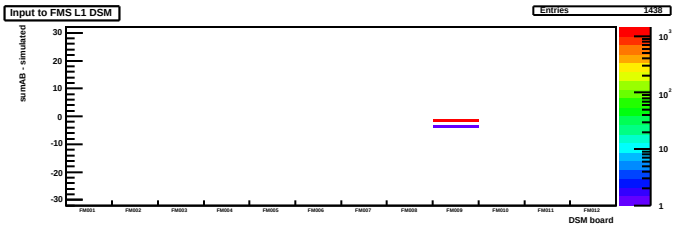

Input to FMS L1 DSM
Entries 787740
sumBC
DSM board

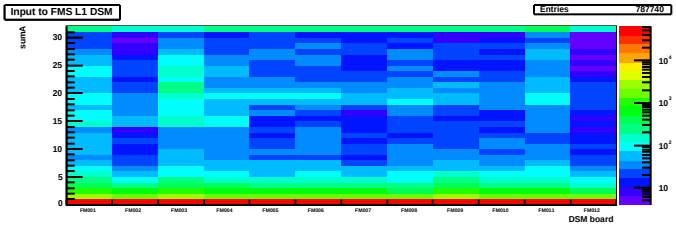

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

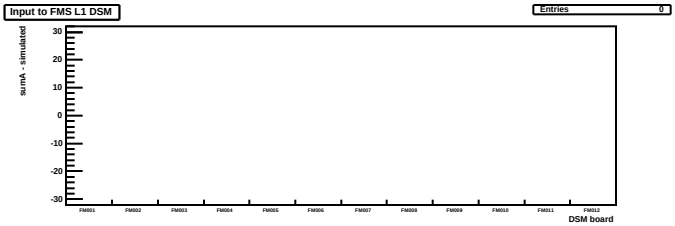

Input to FMS L1 DSM
Entries 787740
sumB
DSM board

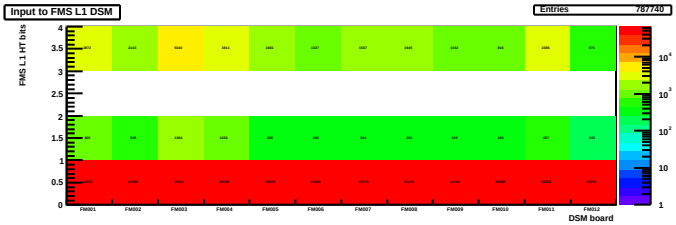

Input to FMS L1 DSM
Entries 3077
sumB - simulated
DSM board

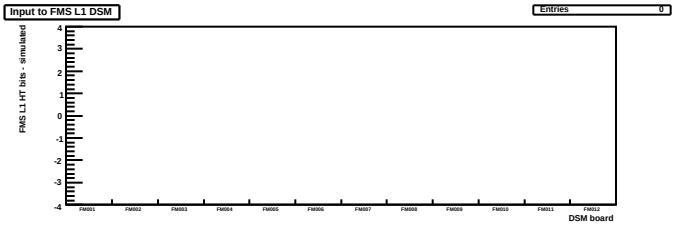

Input to FMS L1 DSM
Entries 787740
sumAB
DSM board

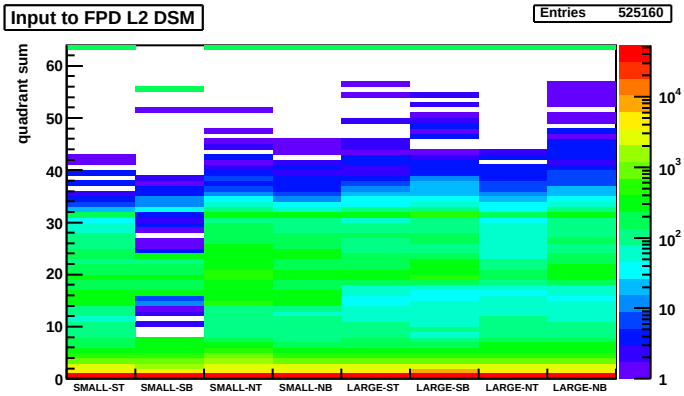

Input to FMS L1 DSM
Entries 1438
sumAB - simulated
DSM board

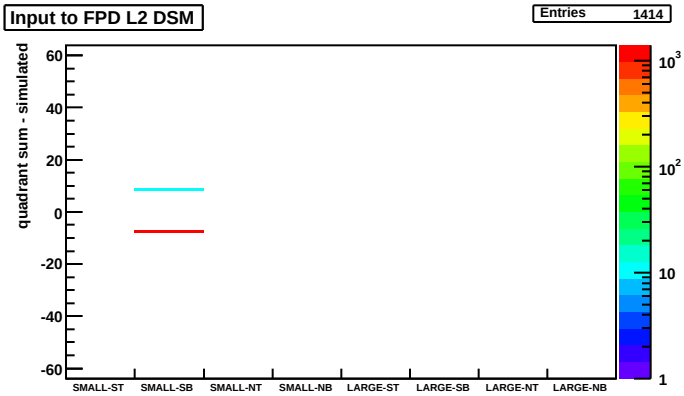

Input to FMS L1 DSM
Entries 787740
sumA
DSM board

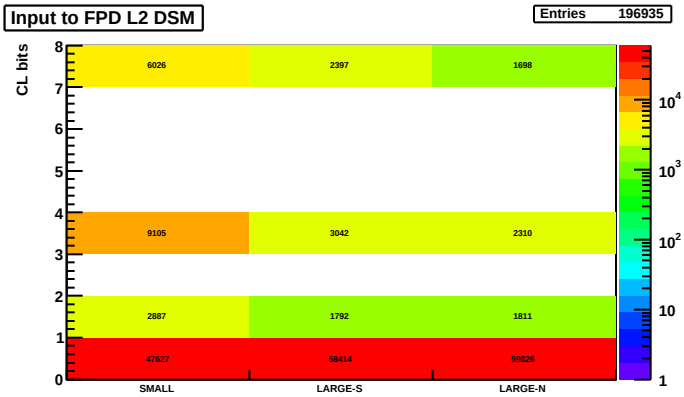

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

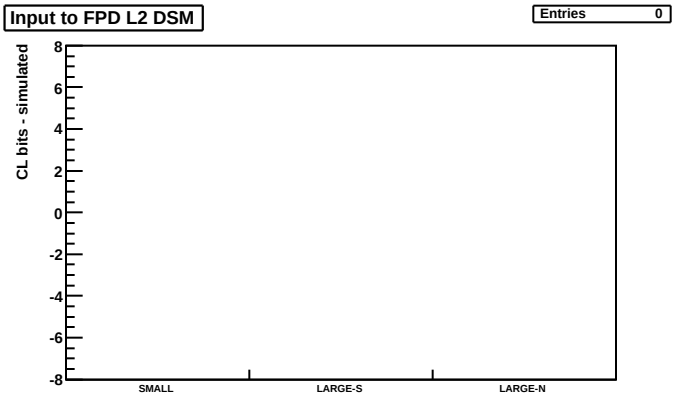

Input to FMS L1 DSM
Entries 787740
FMS L1 HT bits
DSM board

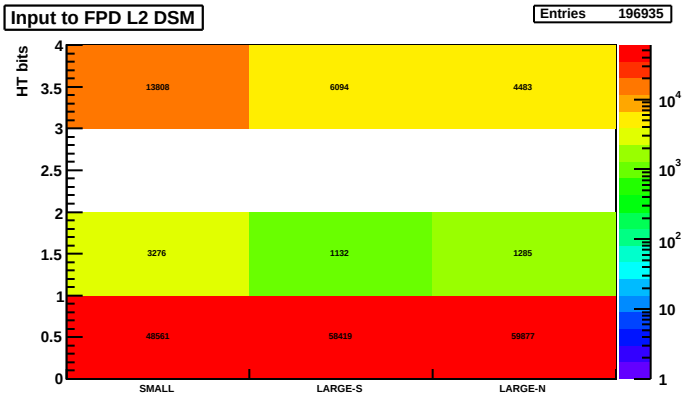

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

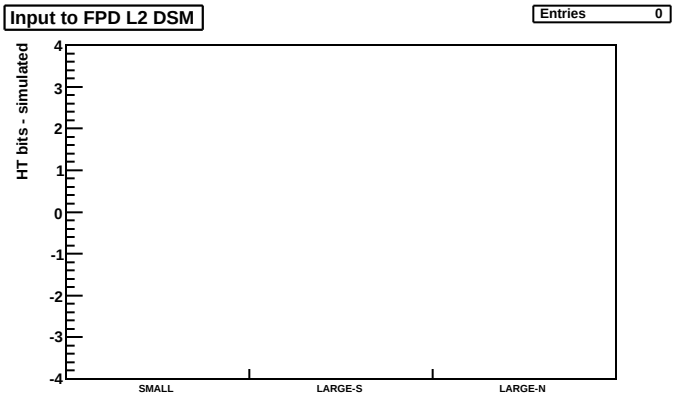

Input to FPD L2 DSM
Entries 525160
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

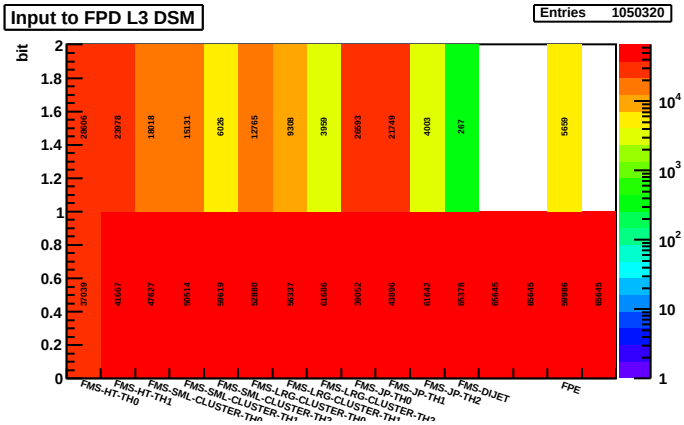

Input to FPD L2 DSM
Entries 1414
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

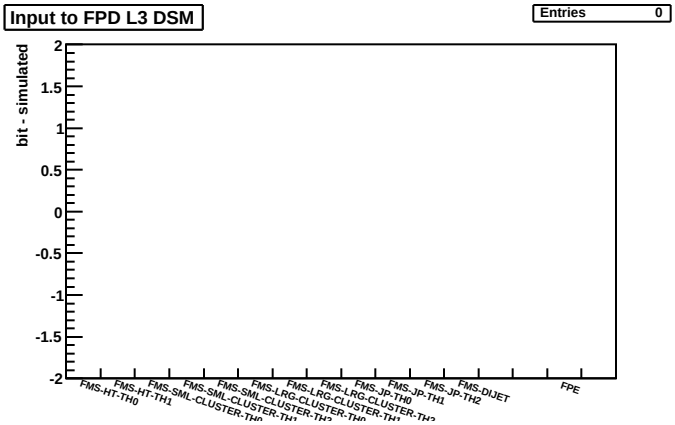

Input to FPD L2 DSM
Entries 196935
CL bits
6026
2397
1698
9105
3042
2310
2887
1792
1811
47627
58414
59826
SMALL
LARGE-S
LARGE-N

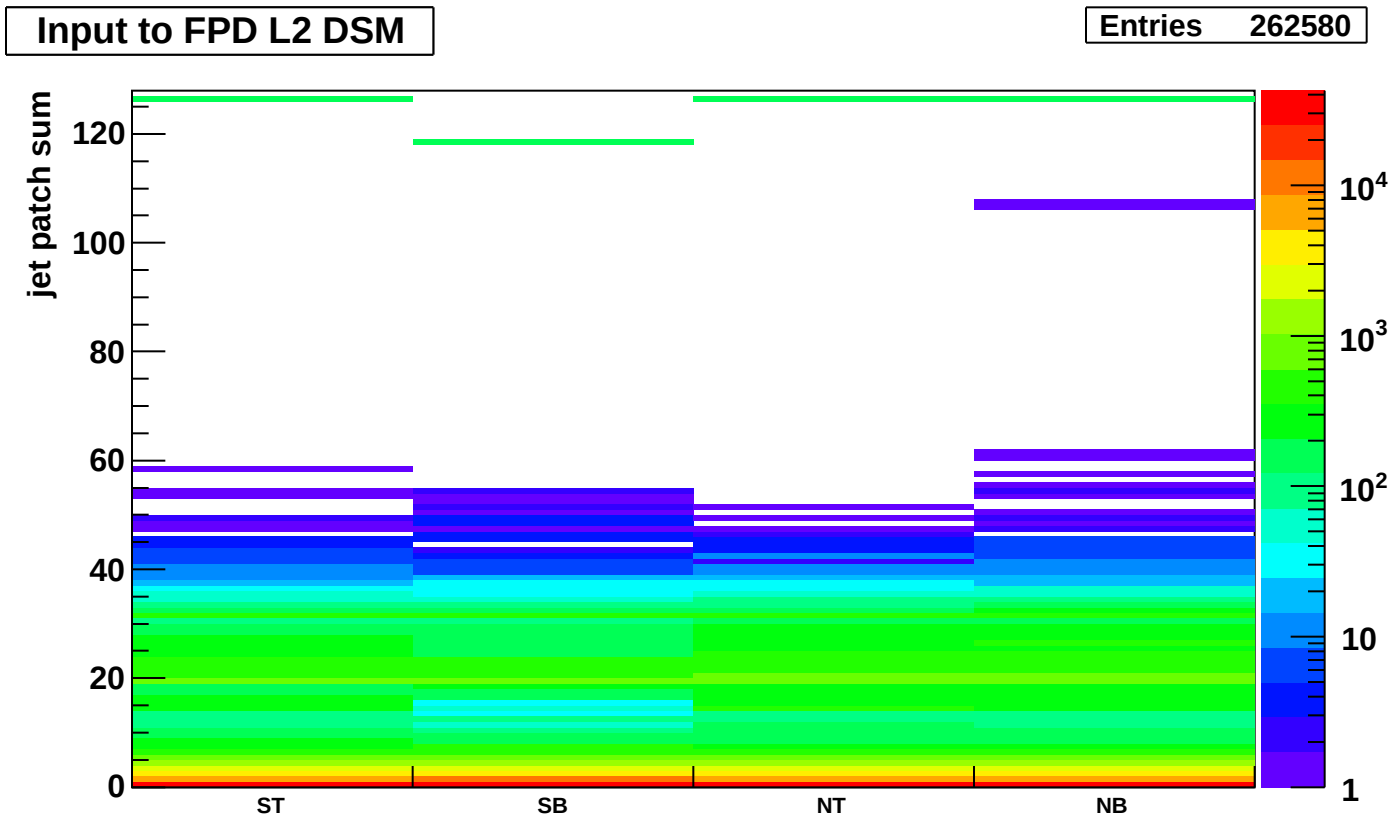

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

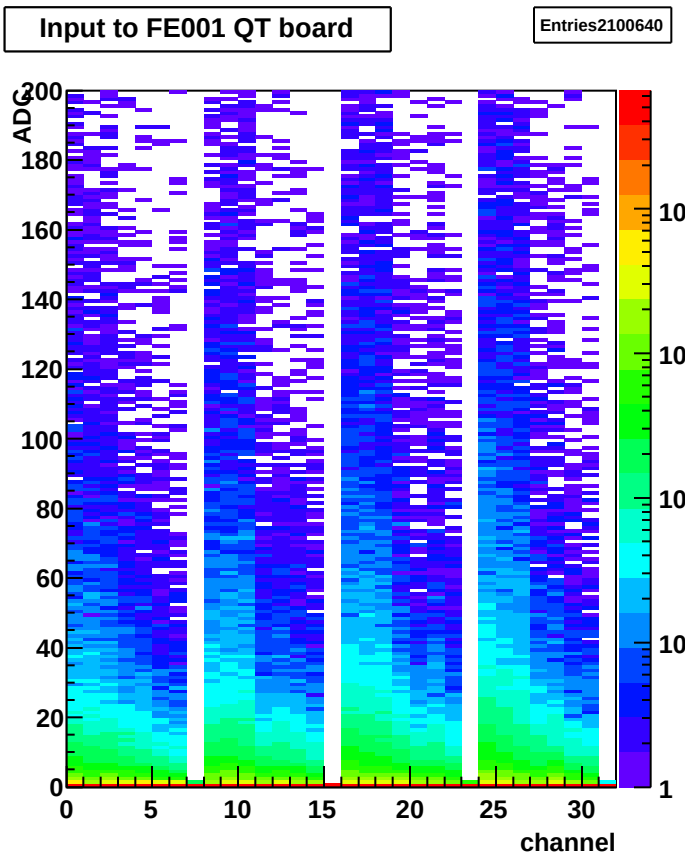

Input to FPD L2 DSM
Entries 196935
HT bits
13808
6094
4483
3276
1132
1285
48561
58419
59877
SMALL
LARGE-S
LARGE-N

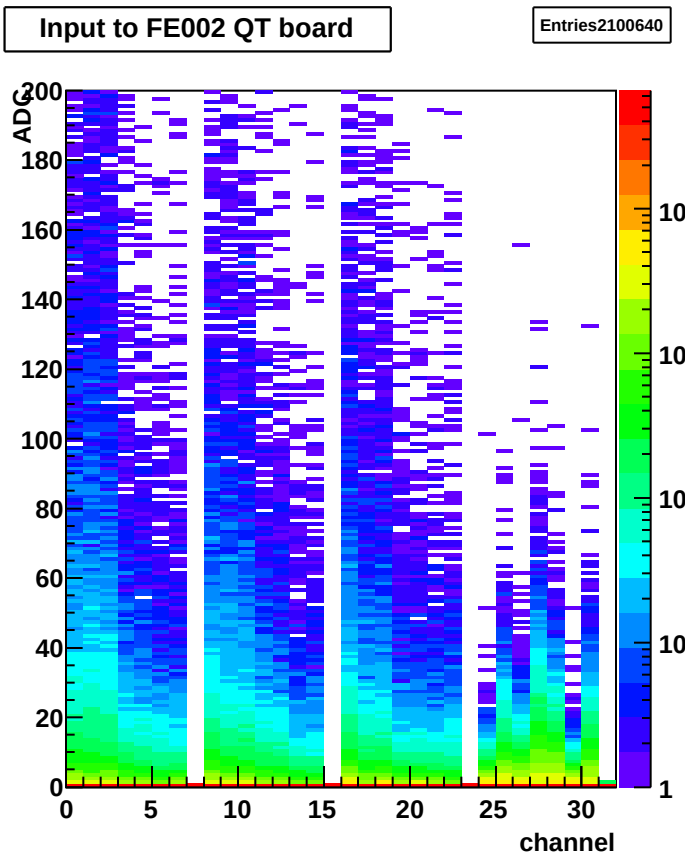

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

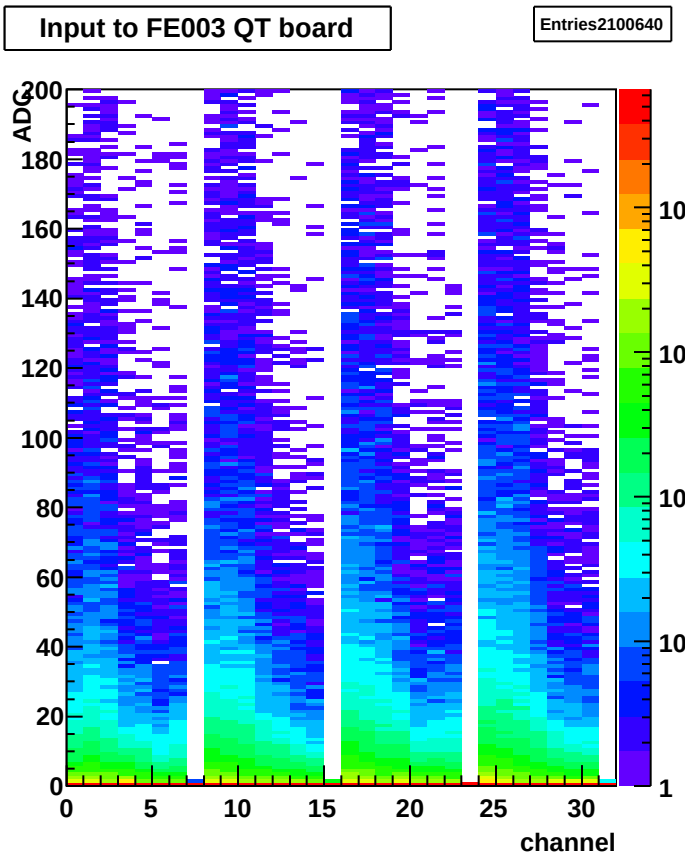

Input to FPD L3 DSM
Entries 1050320
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

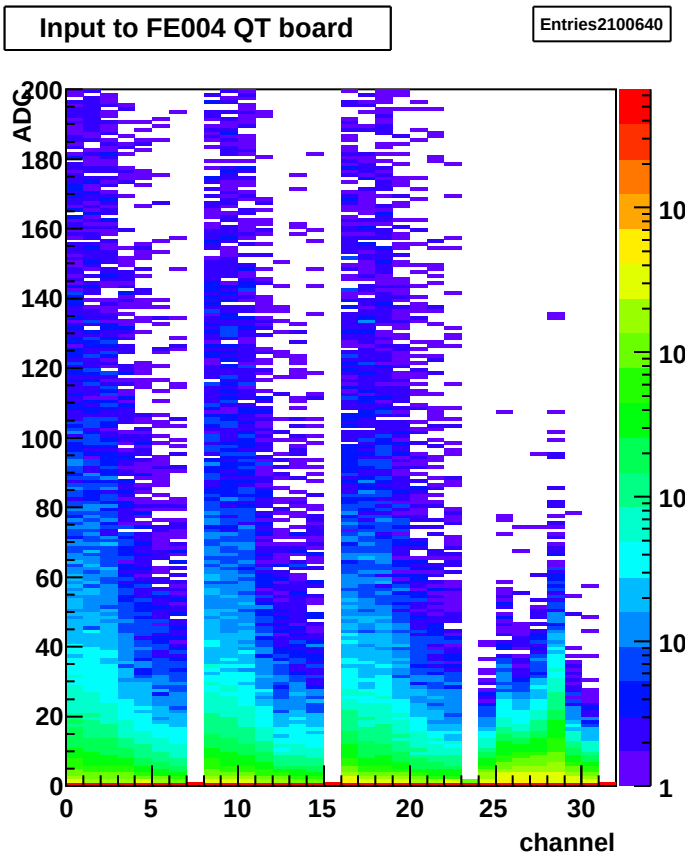

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

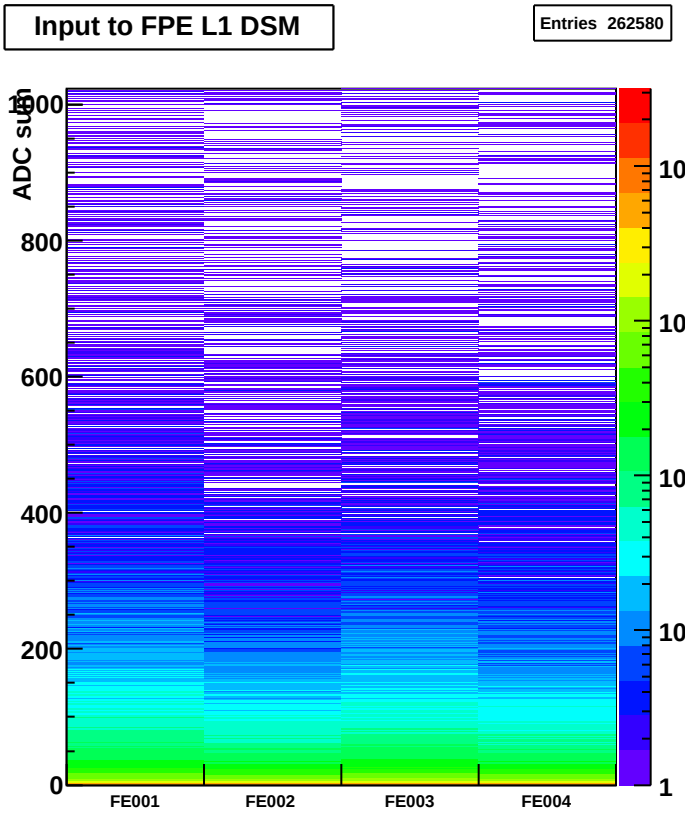

Input to FPD L2 DSM
Entries 262580
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

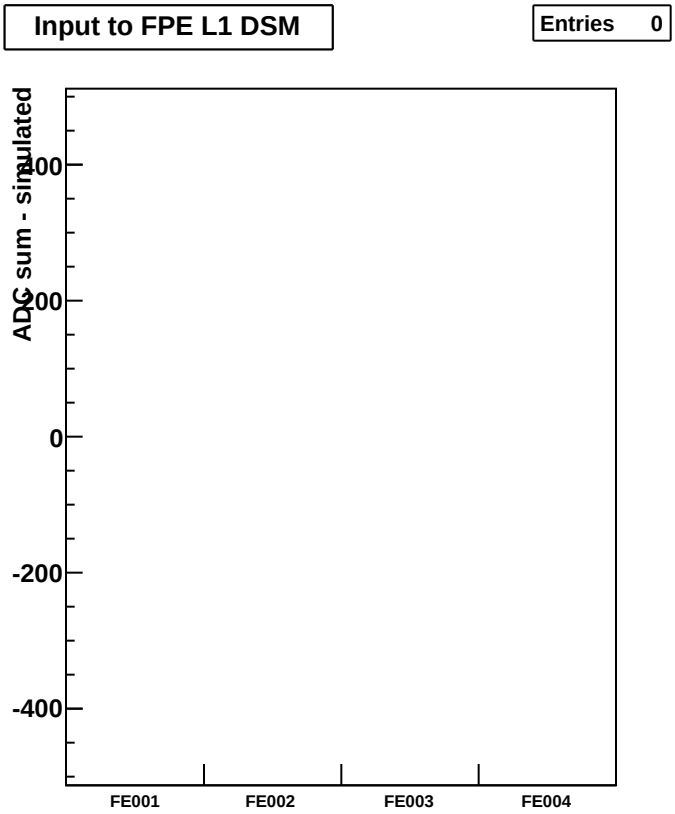

Input to FE001 QT board
Entries2100640
ADC
channel

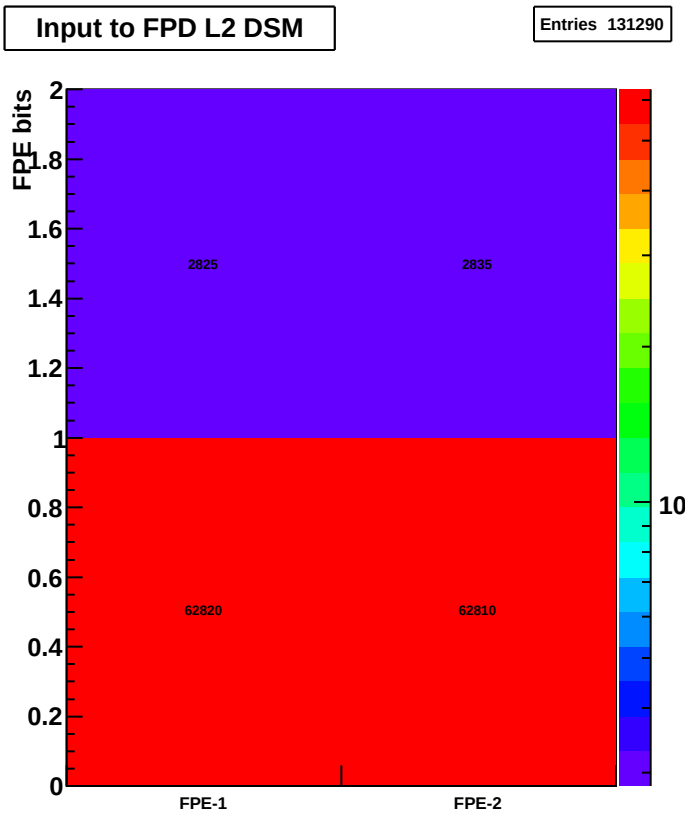

Input to FE002 QT board
Entries2100640
ADC
channel

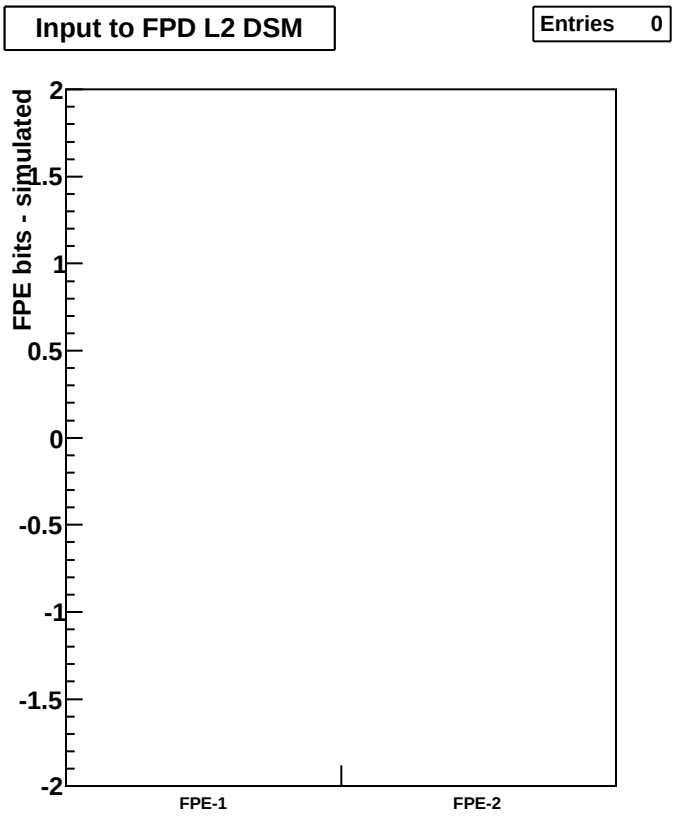

Input to FE003 QT board
Entries2100640
ADC
channel


Input to FE004 QT board
Entries2100640
ADC
channel


Input to FPE L1 DSM
Entries 262580
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 131290
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
2825
2835
62820
62810
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2