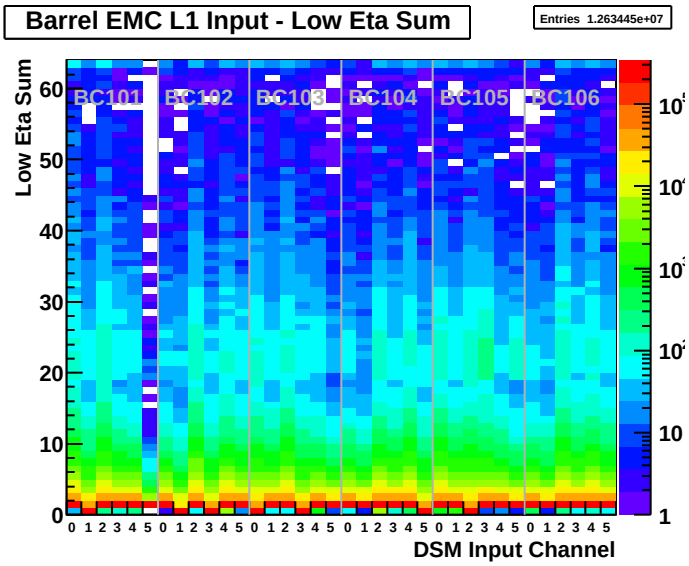
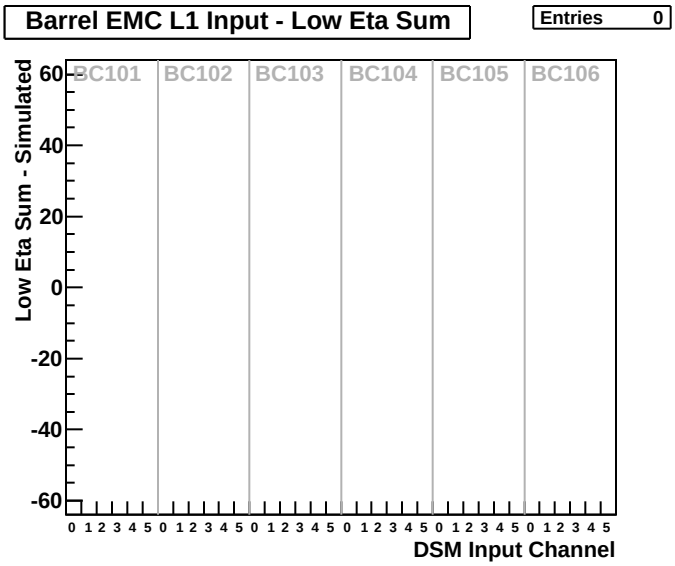
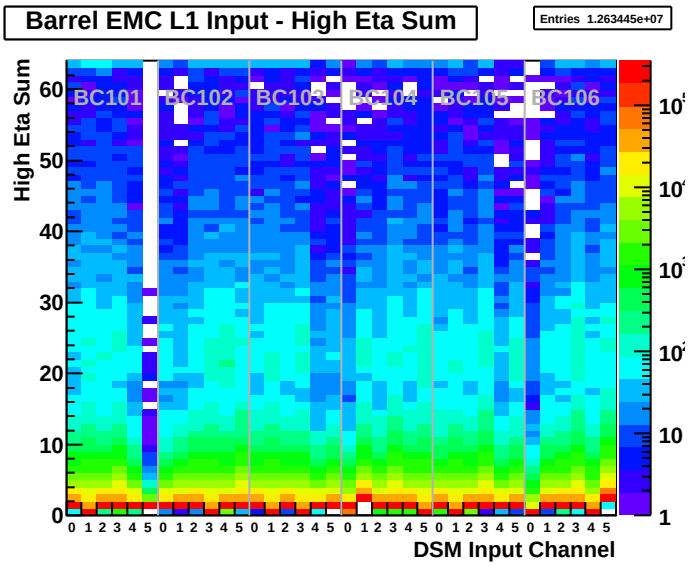
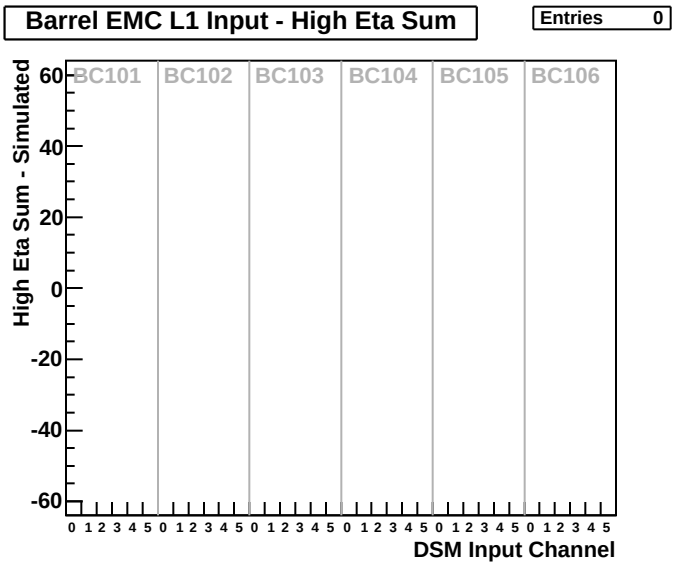
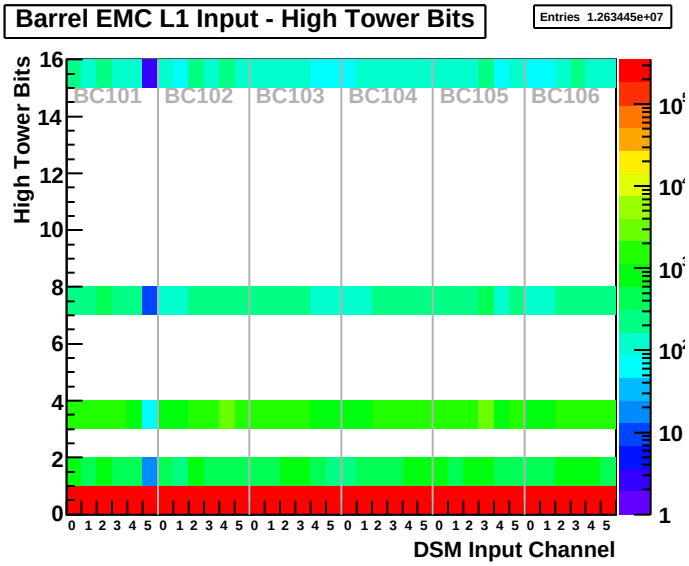
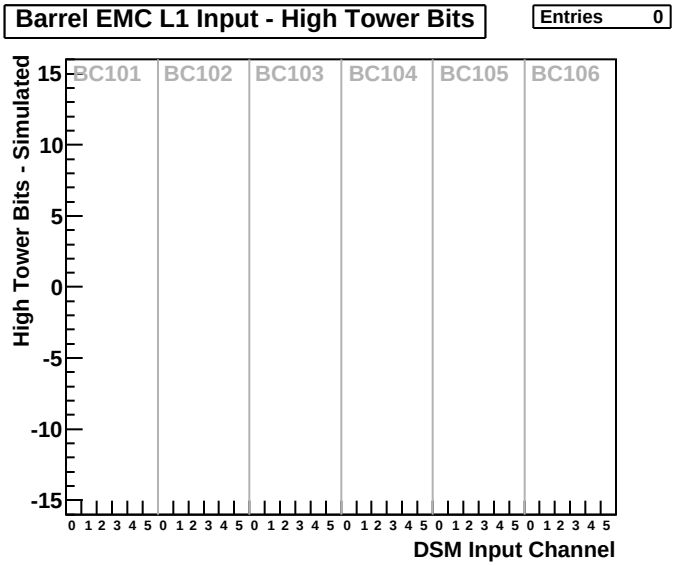

Barrel EMC L1 Input - Low Eta Sum
Entries 1.263445e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

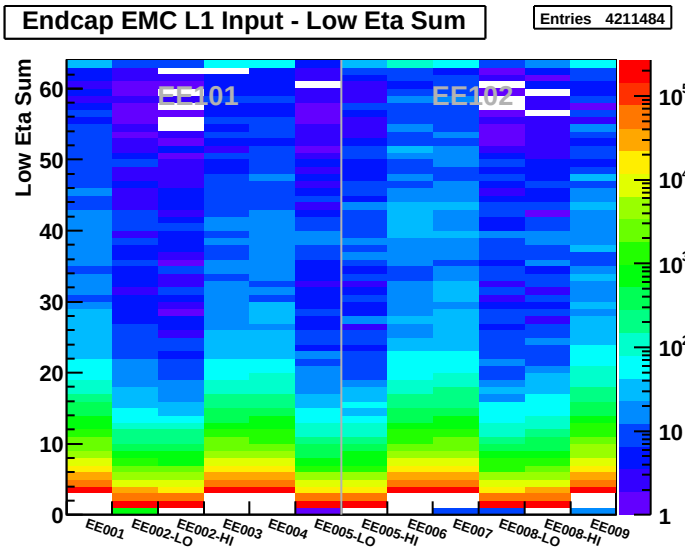

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

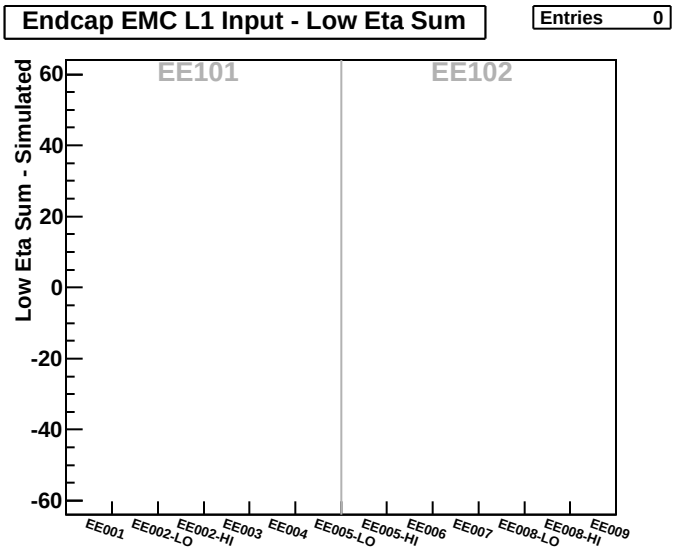

Barrel EMC L1 Input - High Eta Sum
Entries 1.263445e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

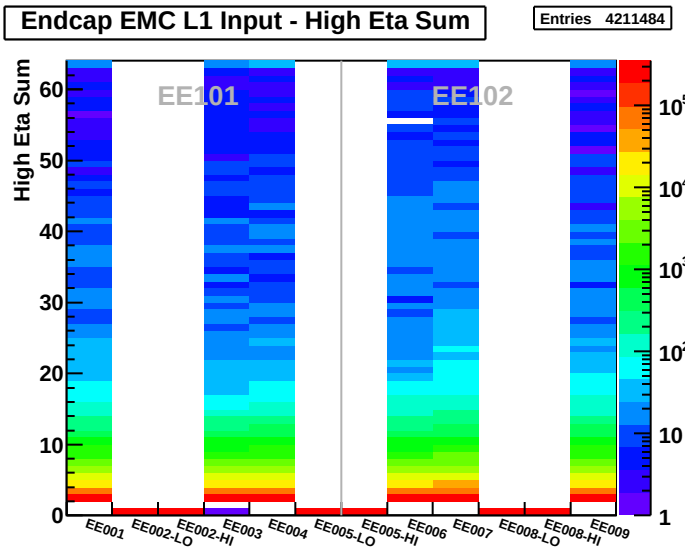

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

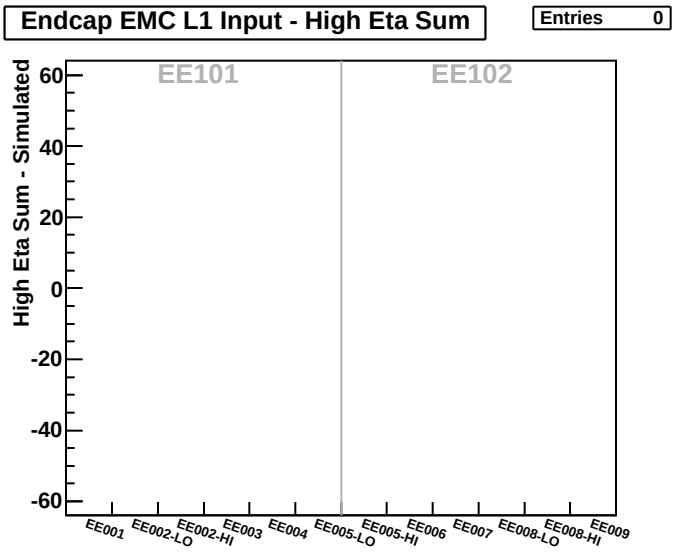

Barrel EMC L1 Input - High Tower Bits
Entries 1.263445e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

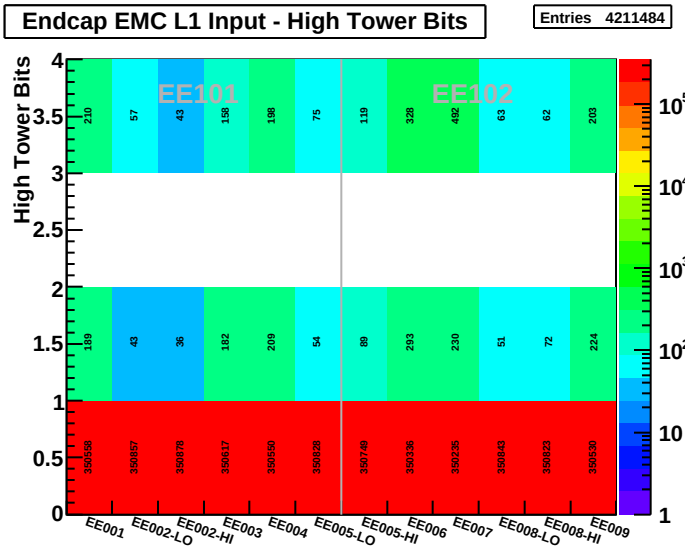

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

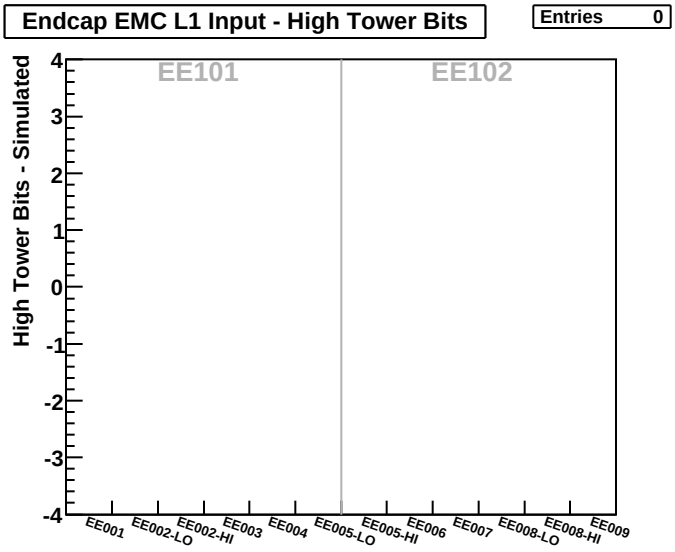

Endcap EMC L1 Input - Low Eta Sum
Entries 4211484
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

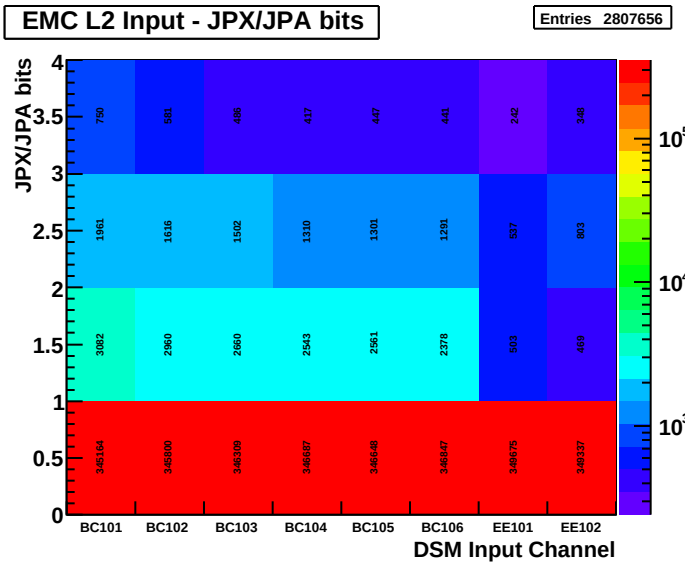

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

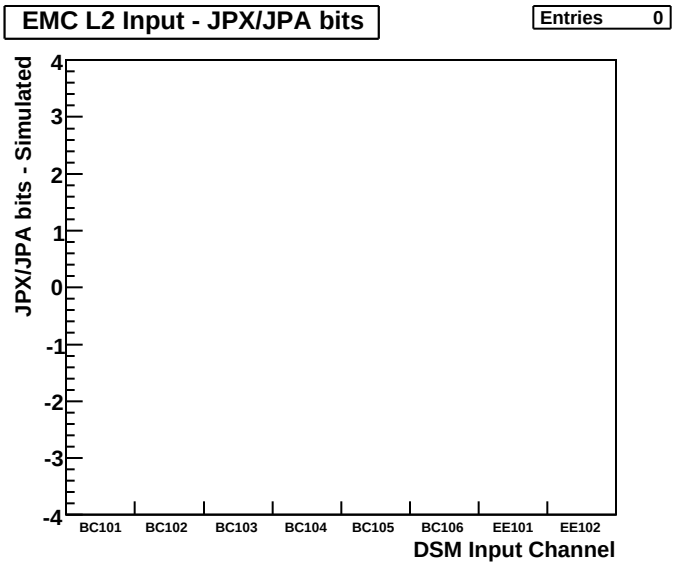

Endcap EMC L1 Input - High Eta Sum
Entries 4211484
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

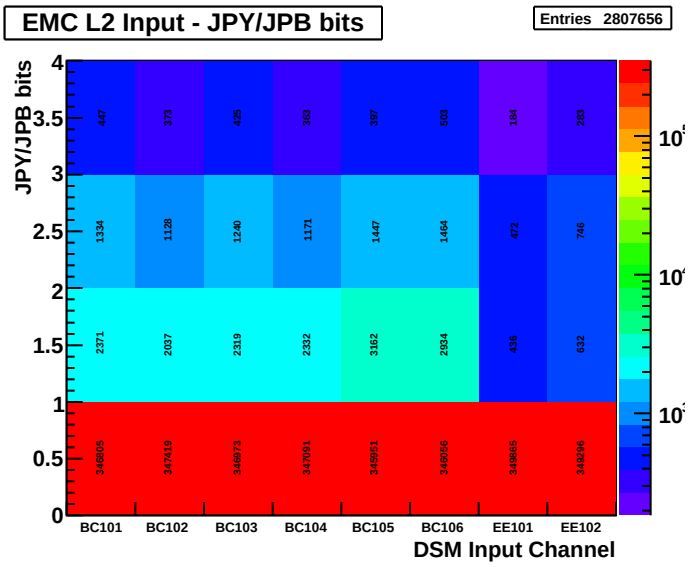

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

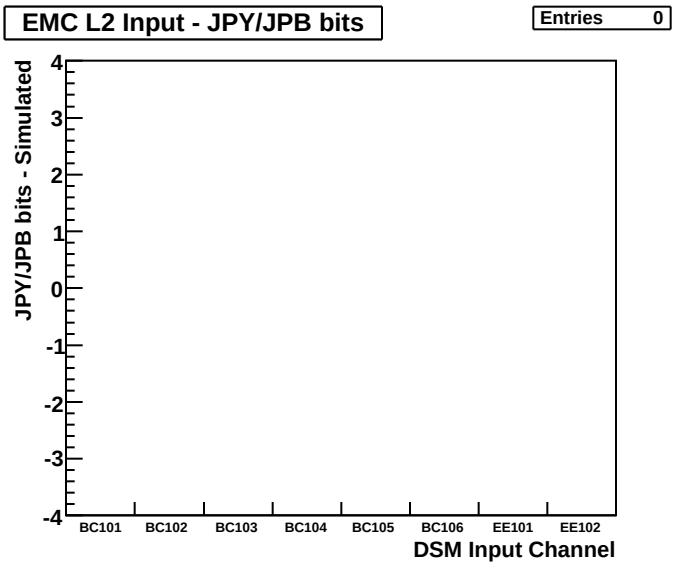

Endcap EMC L1 Input - High Tower Bits
Entries 4211484
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

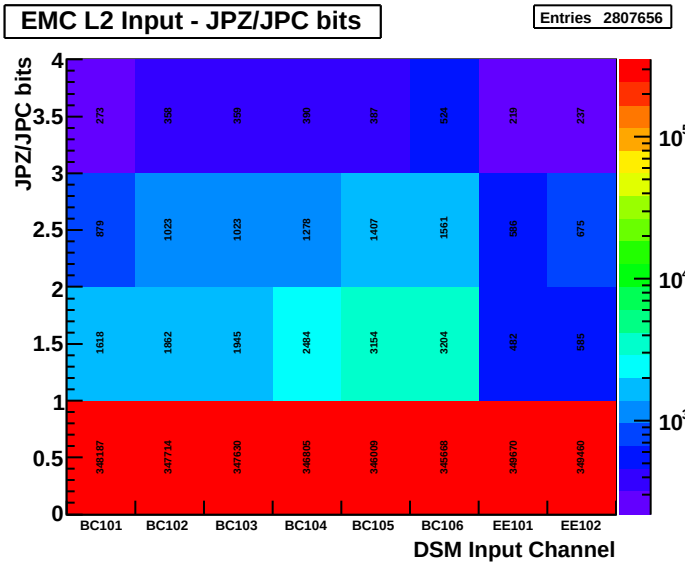

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

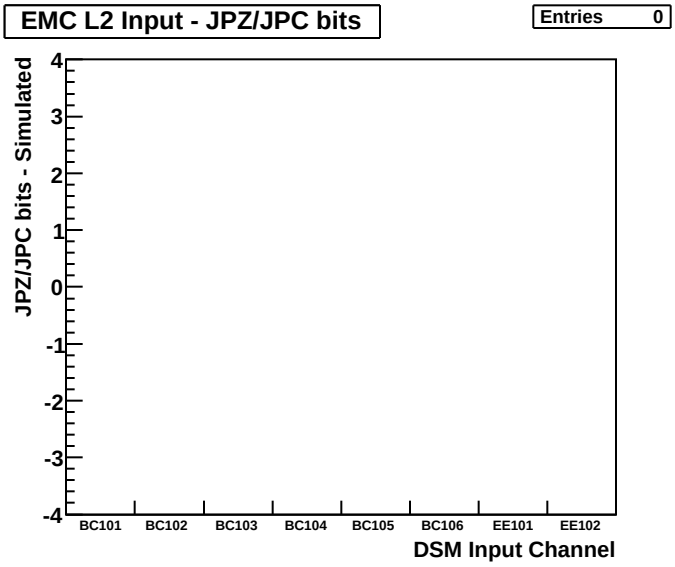

EMC L2 Input - JPX/JPA bits
Entries 2807656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
750 581 486 417 447 441 242 348
1961 1616 1502 1310 1301 1291 537 803
3062 2960 2660 2543 2561 2378 503 469
345164 345800 346309 346687 346648 346947 349675 349337

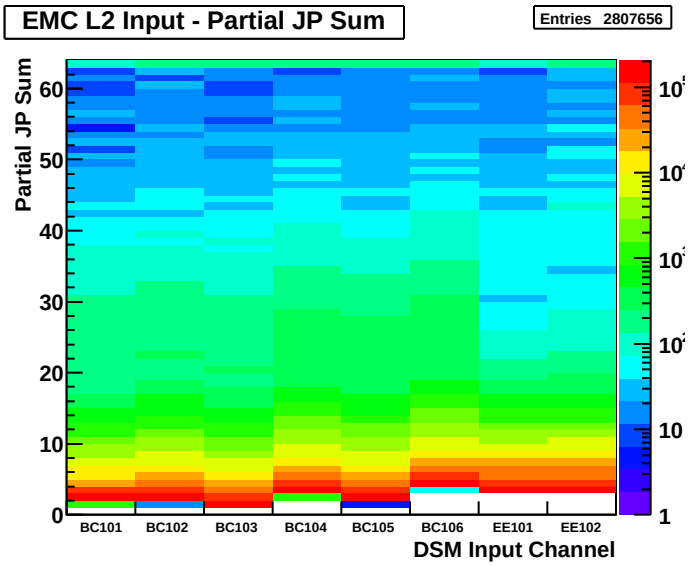

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

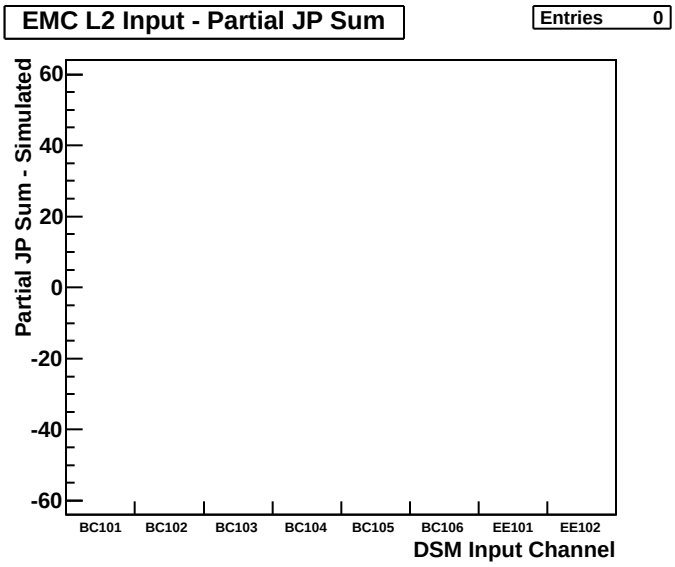

EMC L2 Input - JPY/JPB bits
Entries 2807656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
447 373 425 368 397 503 184 283
1334 1128 1240 1171 1447 1464 472 746
2371 2037 2319 2332 3162 2934 436 632
346805 347419 346973 347091 345951 346056 349865 349296

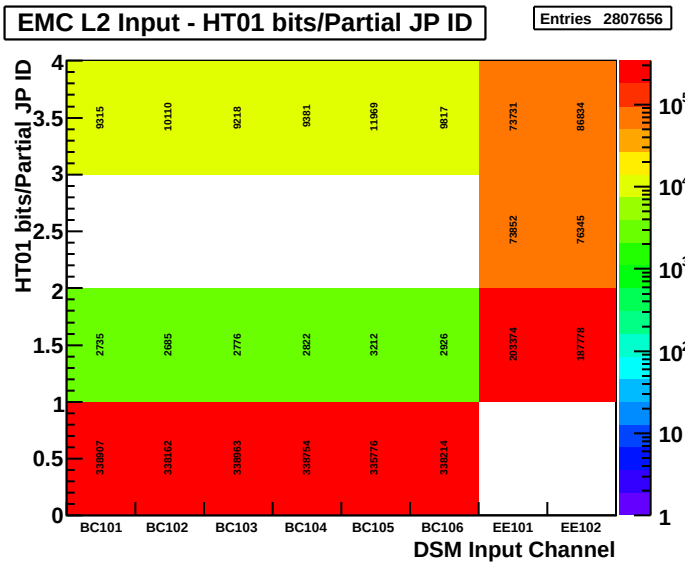

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

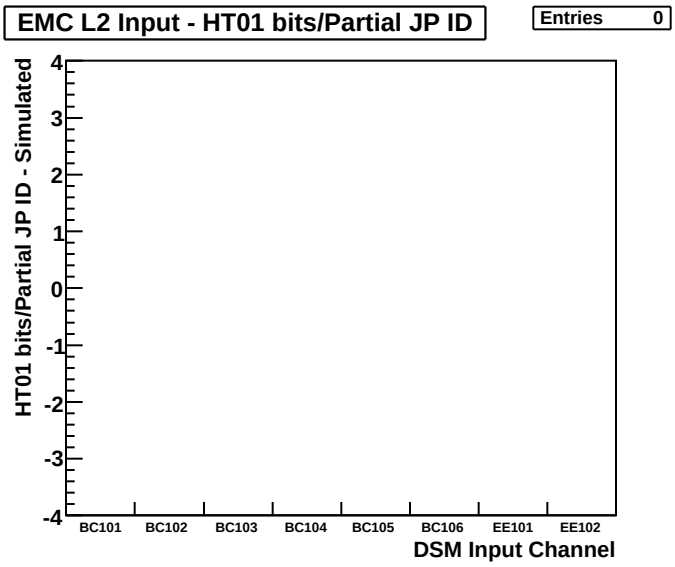

EMC L2 Input - JPZ/JPC bits
Entries 2807656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
273 358 359 390 387 524 219 237
879 1023 1023 1278 1407 1561 586 675
1618 1862 1945 2484 3154 3204 432 535
348187 347714 347830 346805 346009 345568 349670 349460

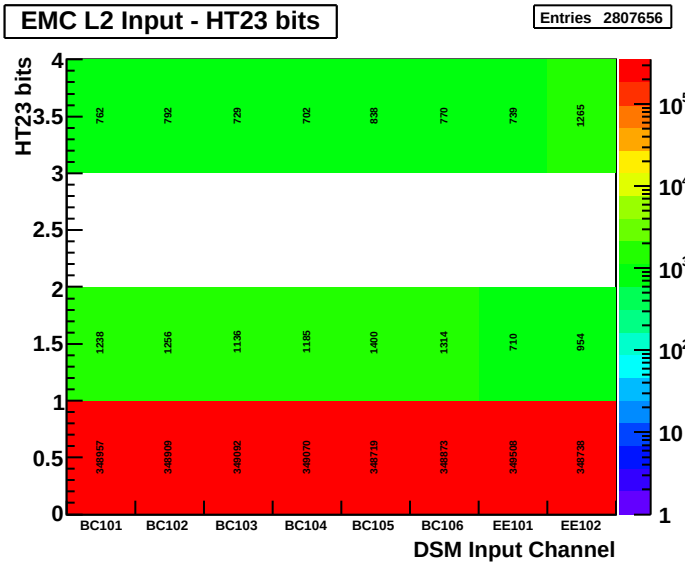

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

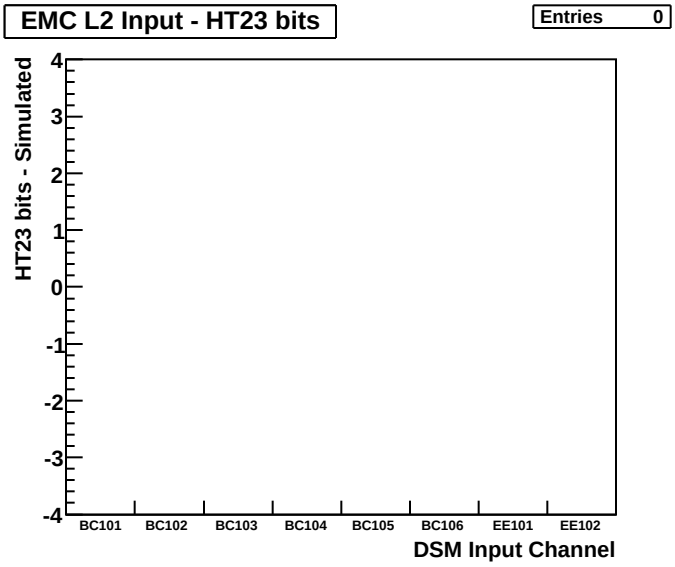

EMC L2 Input - Partial JP Sum
Entries 2807656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

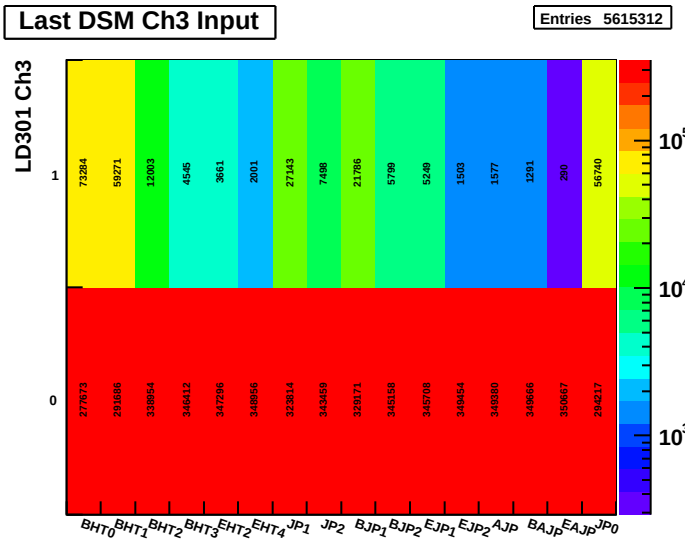

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

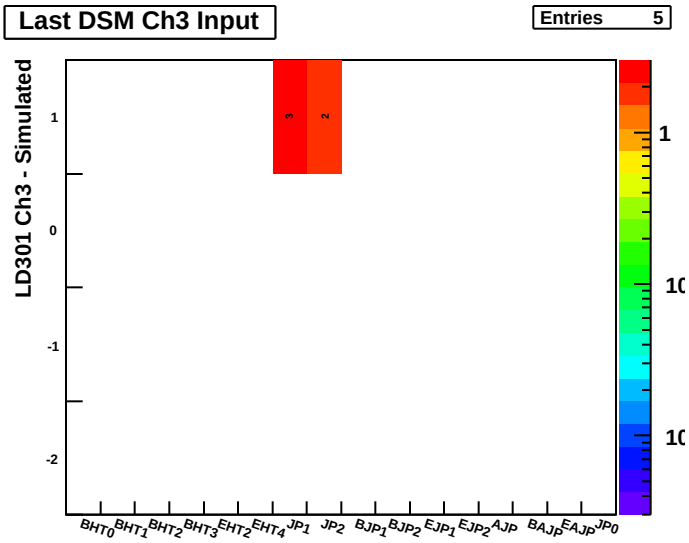

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2807656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

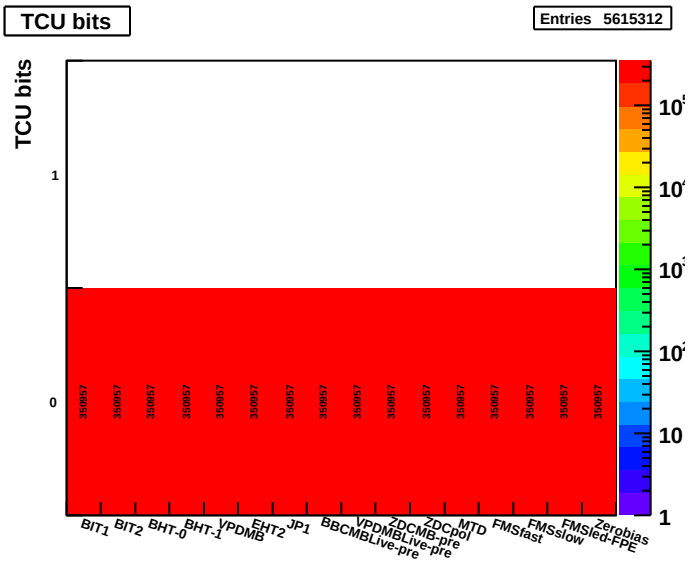

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

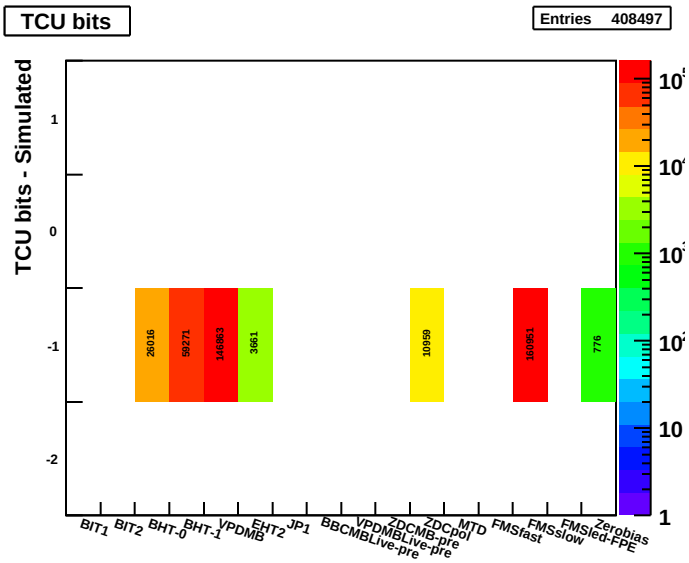

EMC L2 Input - HT23 bits
Entries 2807656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

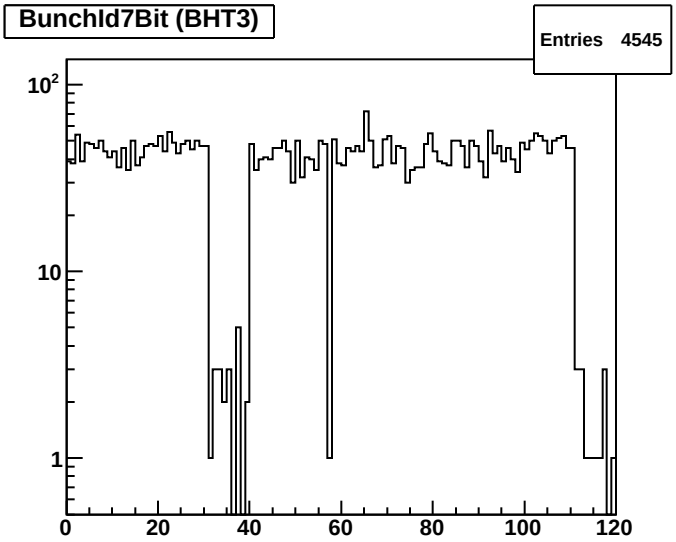

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

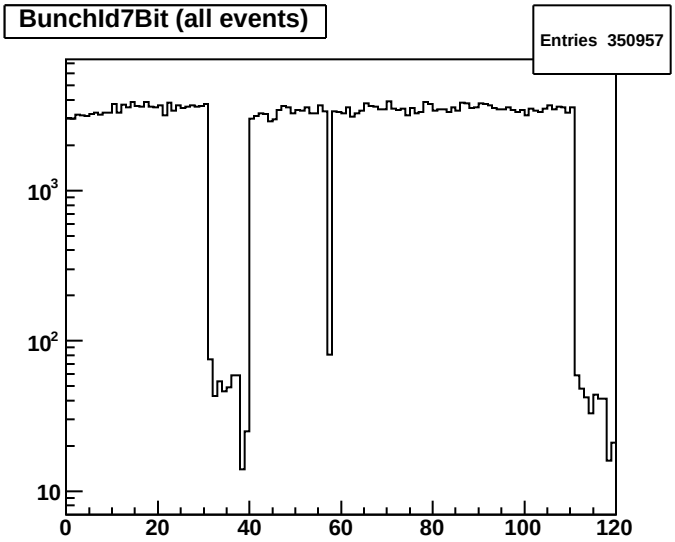

Last DSM Ch3 Input
Entries 5615312
LD301 Ch3
73284 59271 12003 4545 3661 2001 27143 7498 21786 5799 5249 1503 1577 1291 290 56740
277673 291686 338954 346412 347296 348956 323814 343459 329171 345158 345708 349454 349380 349666 350667 294217
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

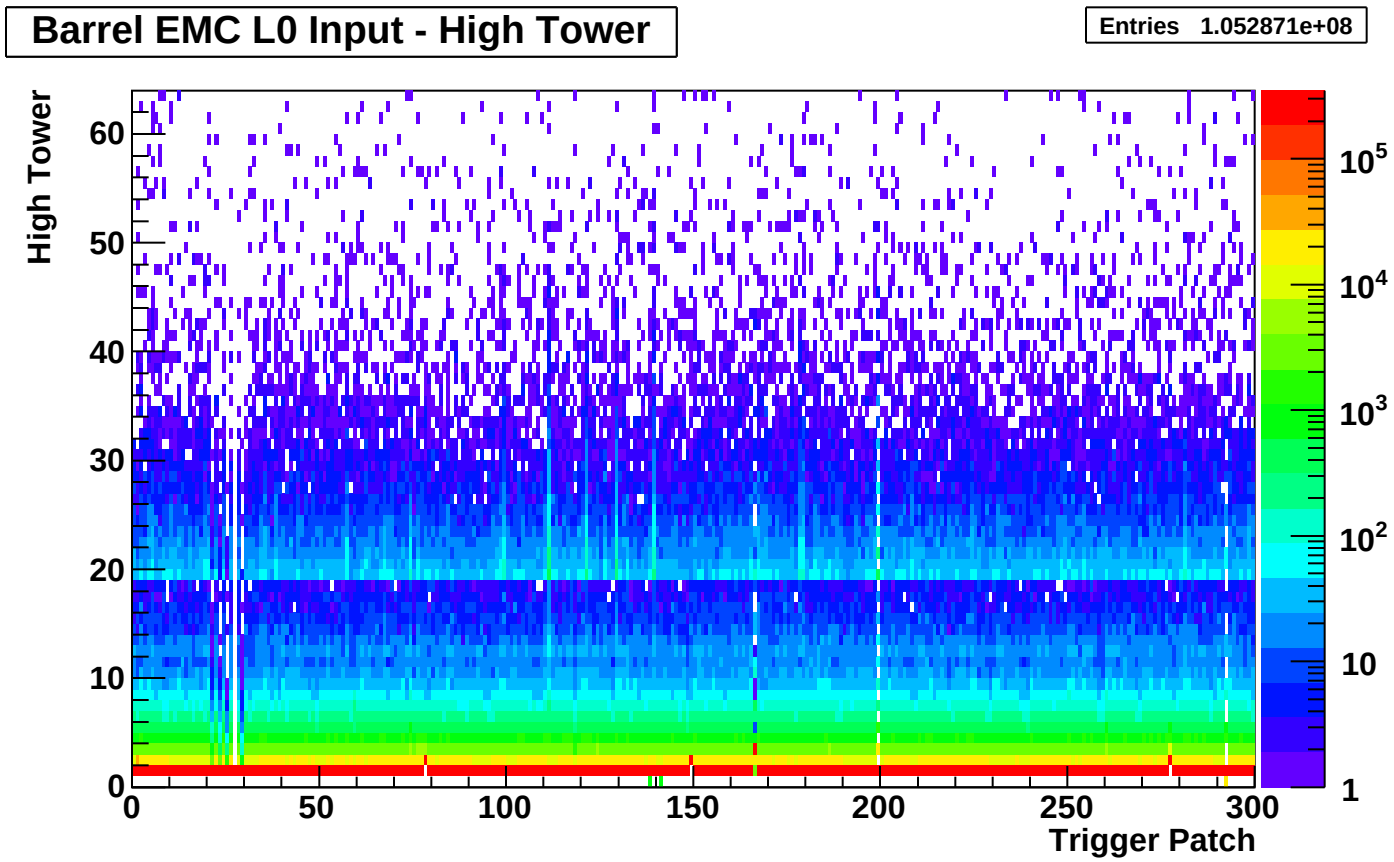

Last DSM Ch3 Input
Entries 5
LD301 Ch3 - Simulated
3 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

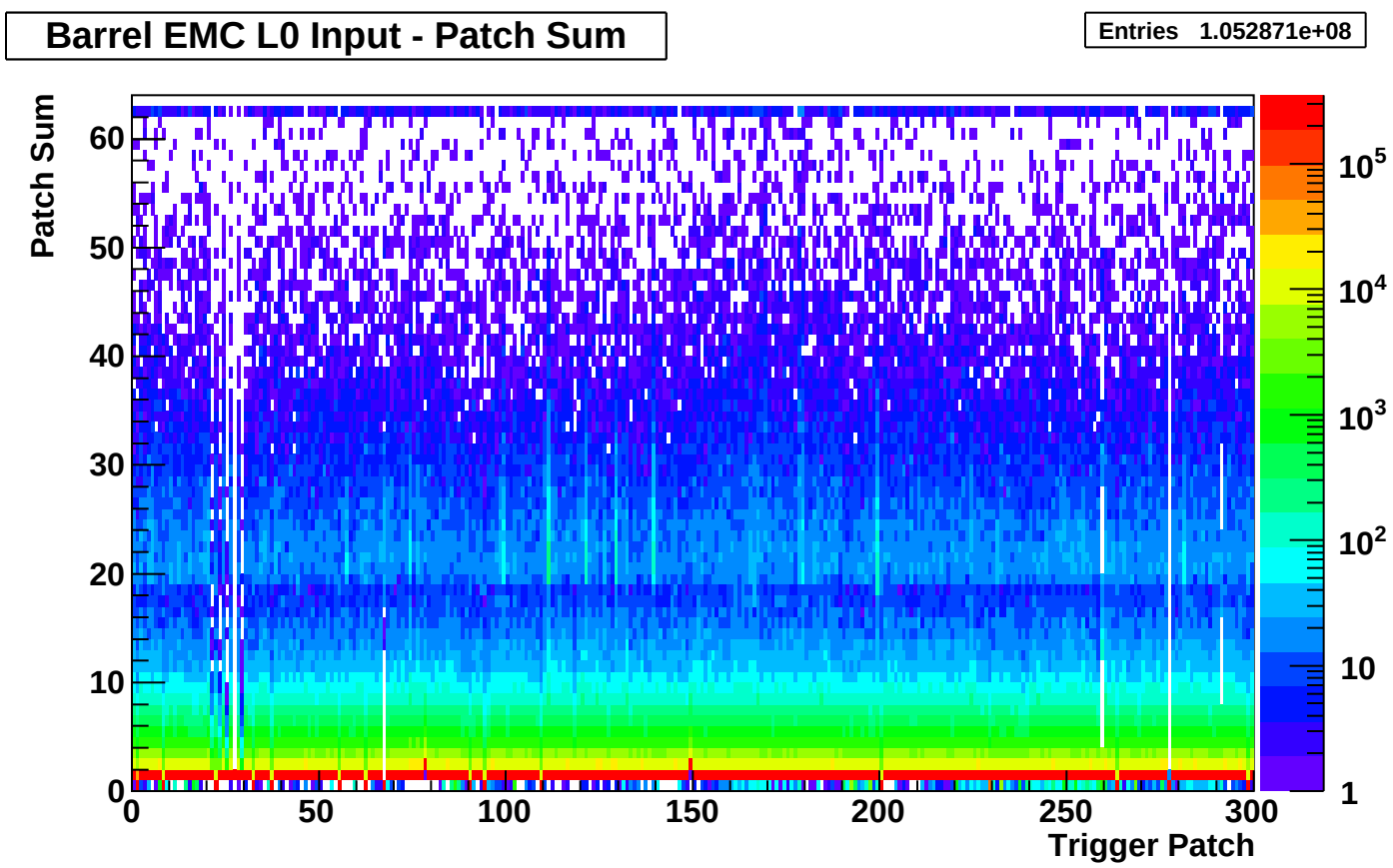

TCU bits
Entries 5615312
TCU bits
350957 350957 350957 350957 350957 350957 350957 350957 350957 350957 350957 350957 350957 350957 350957 350957
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

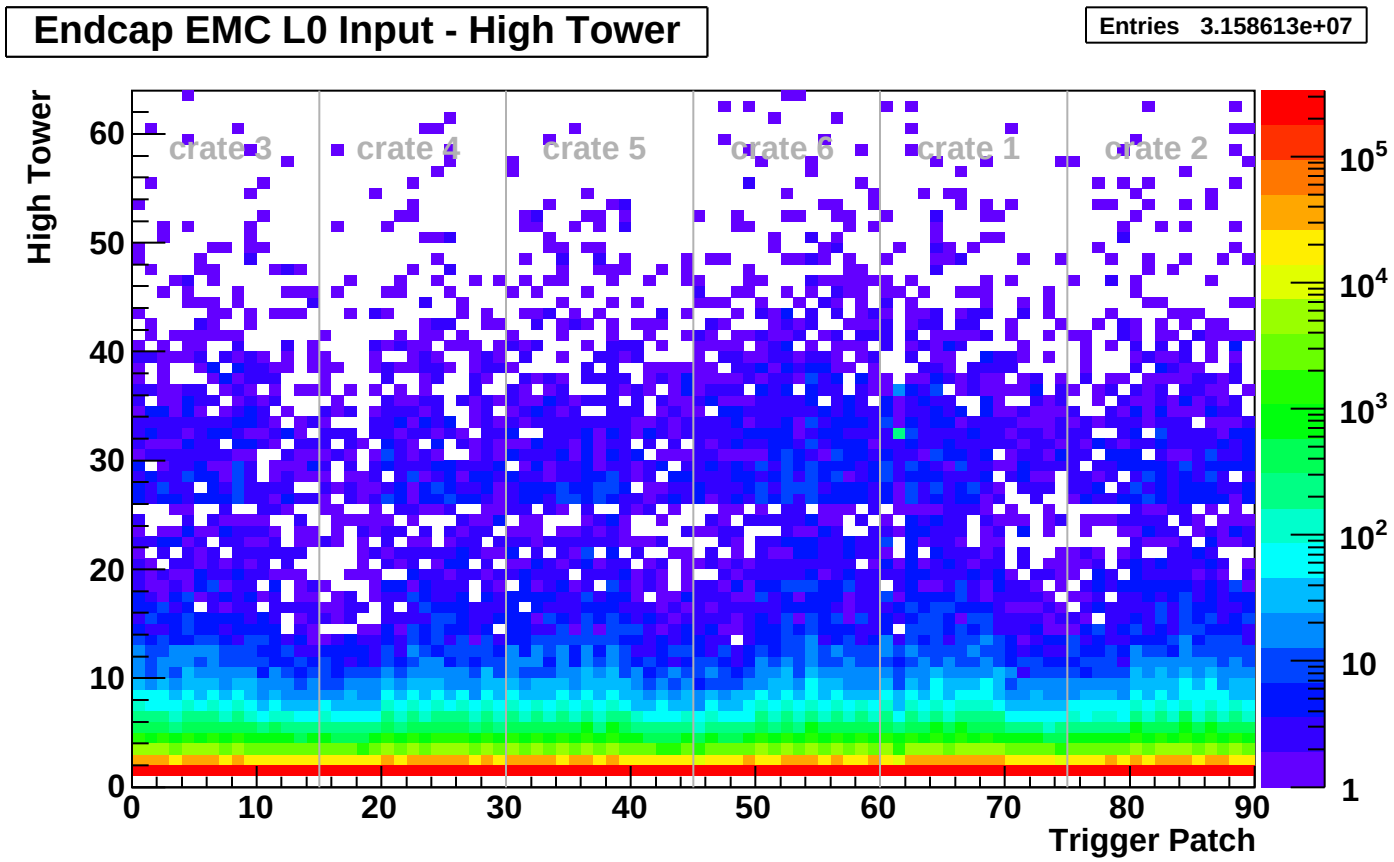

TCU bits
Entries 408497
TCU bits - Simulated
26016 59271 146863 3661 10959 160951 776
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

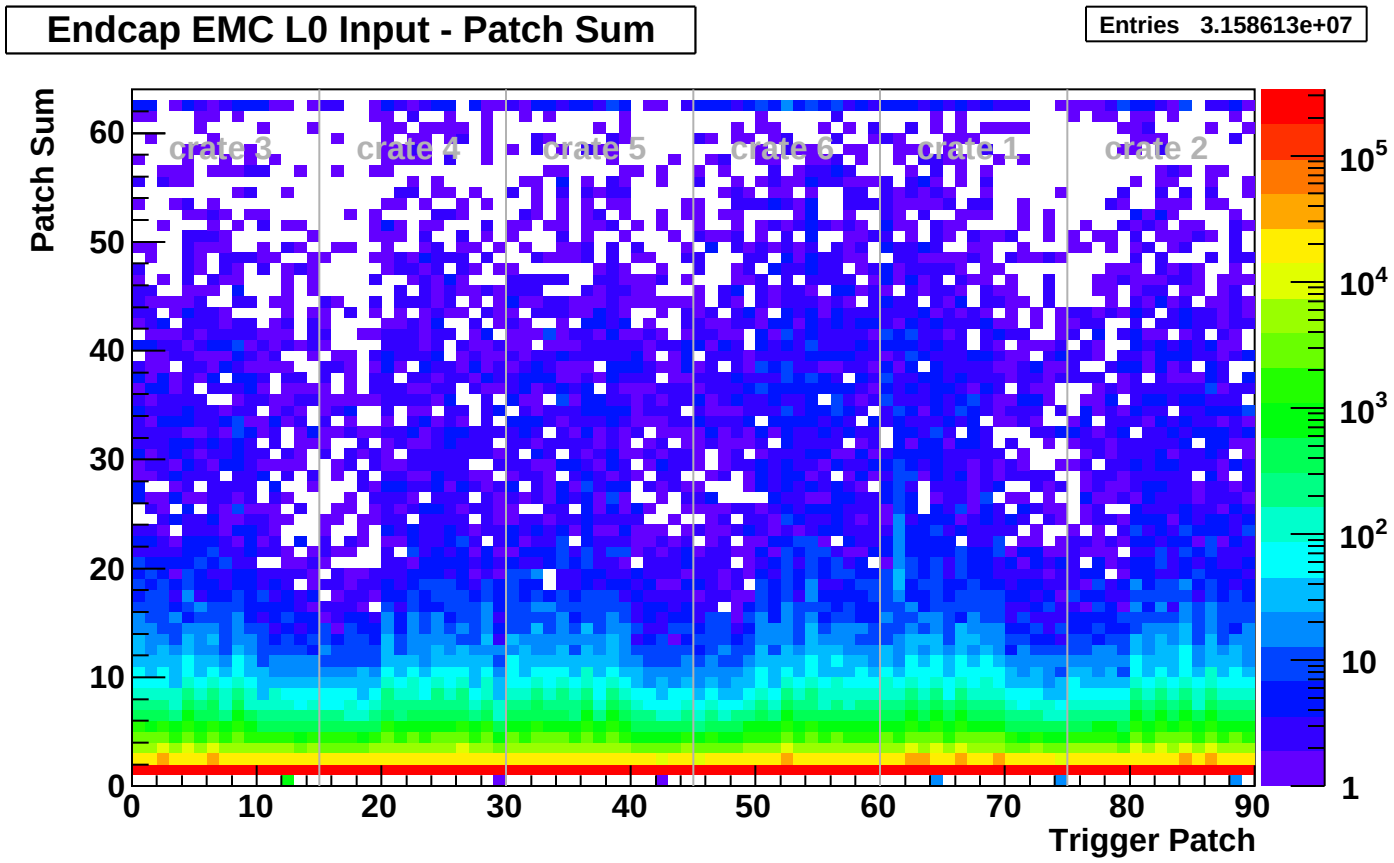

BunchId7Bit (BHT3)
Entries 4545

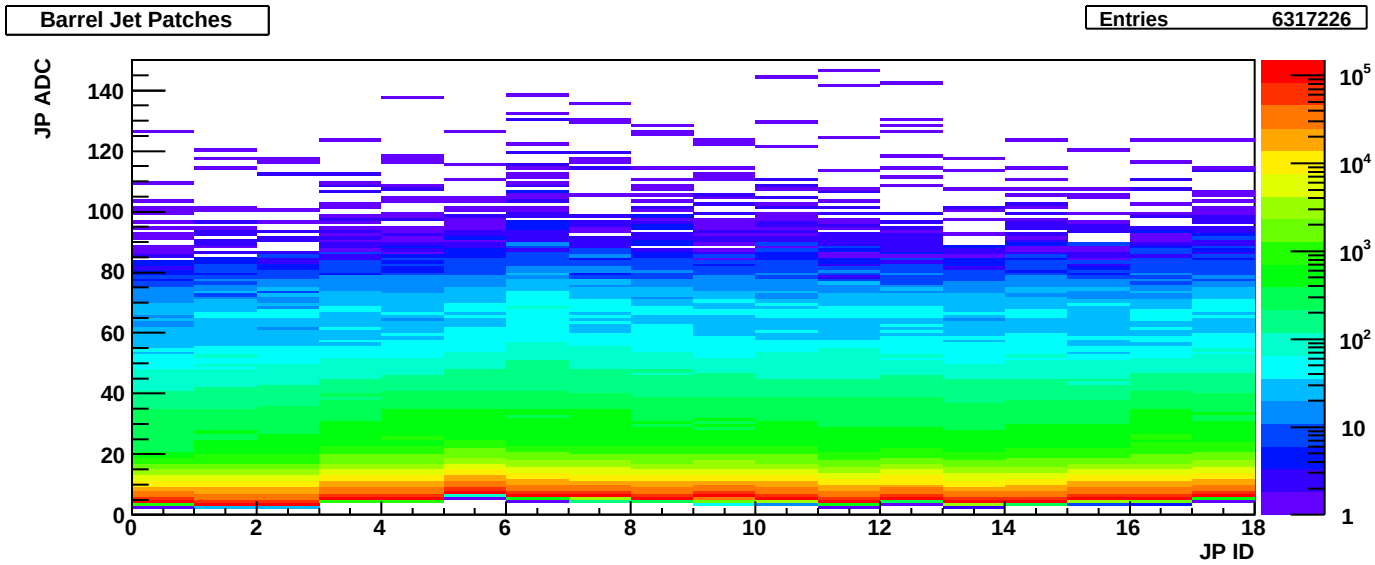

BunchId7Bit (all events)
Entries 350957

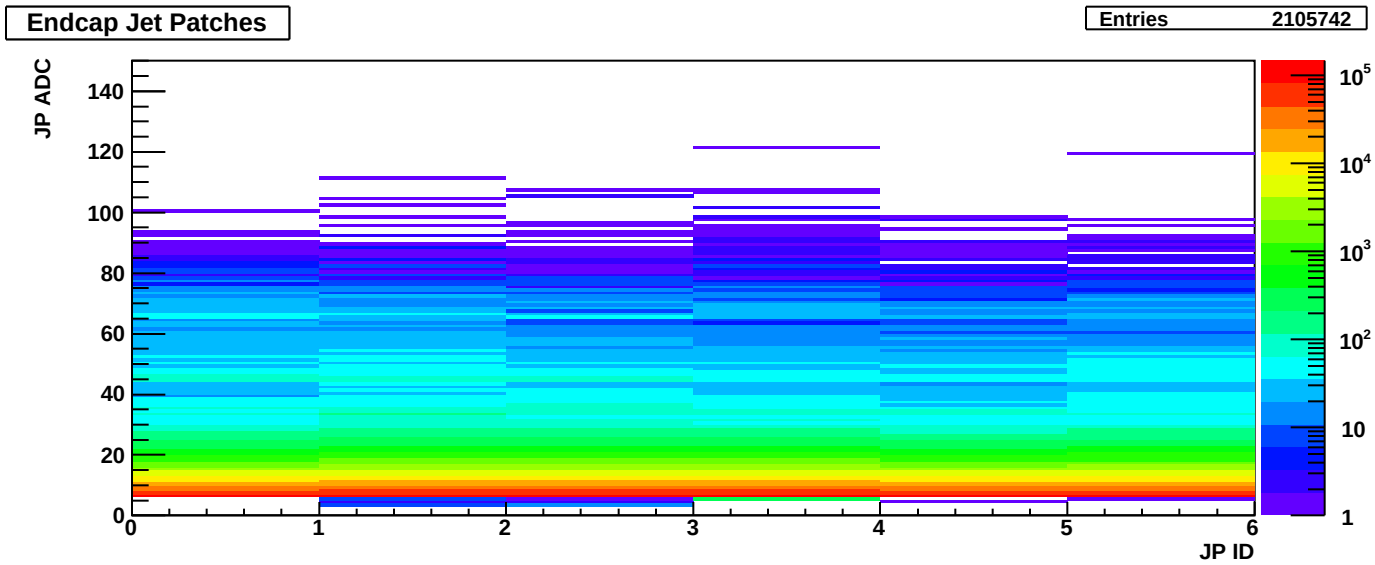

Barrel EMC L0 Input - High Tower
Entries 1.052871e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

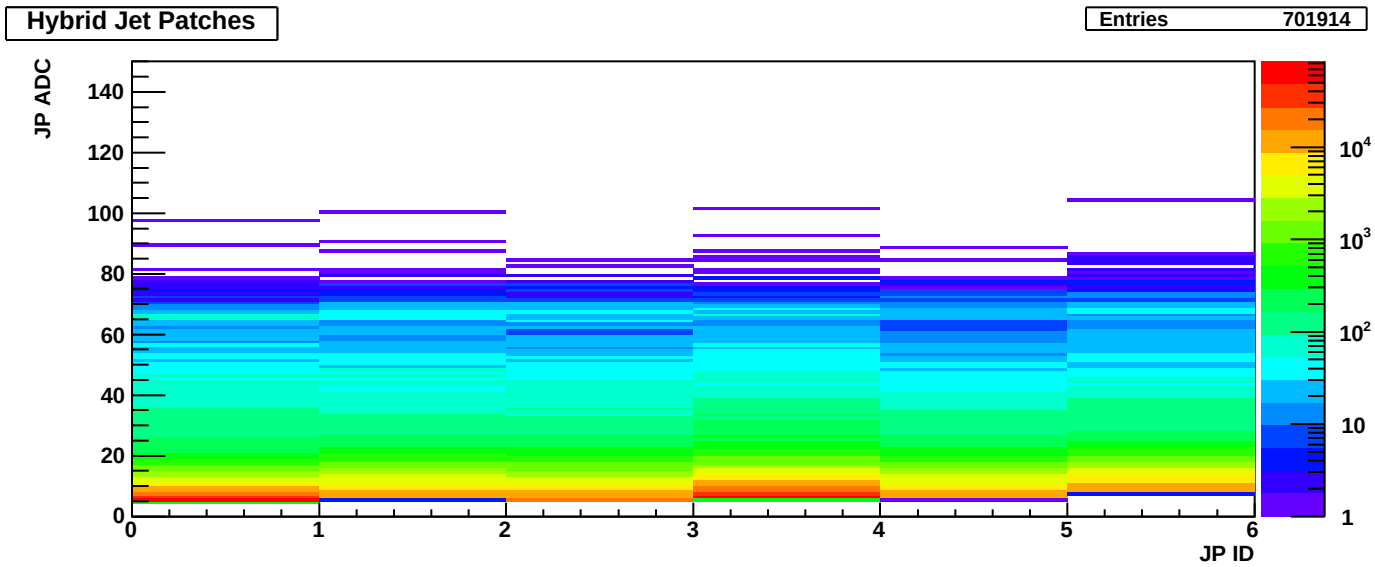

Barrel EMC L0 Input - Patch Sum
Entries 1.052871e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

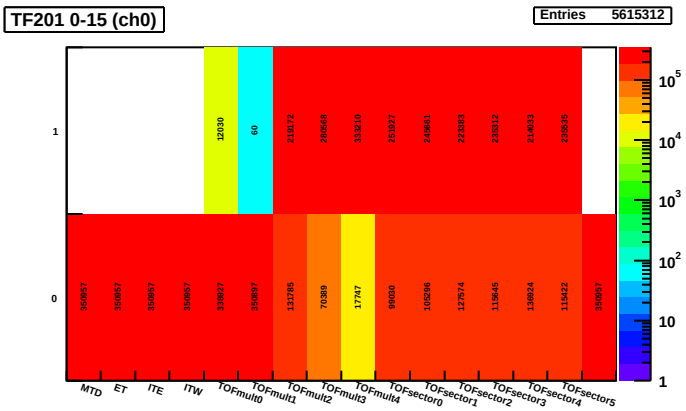

Endcap EMC L0 Input - High Tower
Entries 3.158613e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

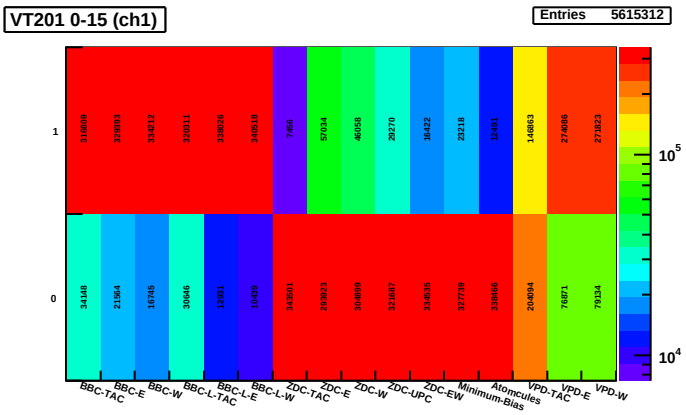

Endcap EMC L0 Input - Patch Sum
Entries 3.158613e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

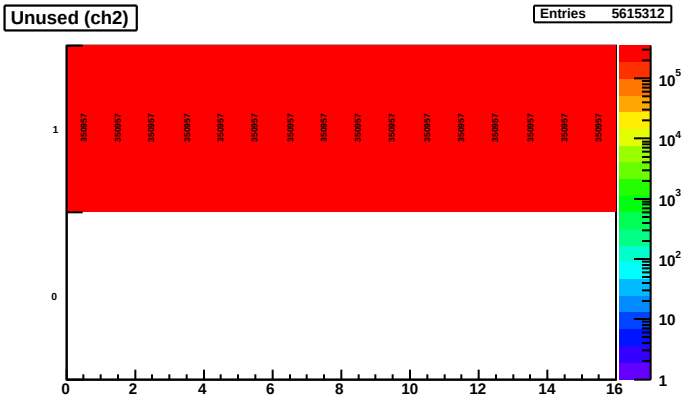

Barrel Jet Patches
Entries 6317226
JP ADC
JP ID

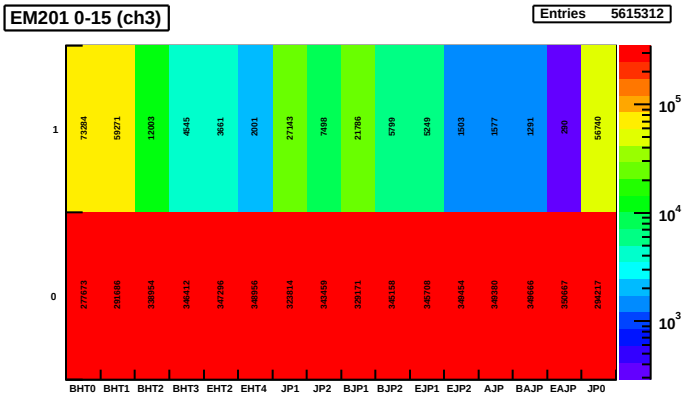

Endcap Jet Patches
Entries 2105742
JP ADC
JP ID

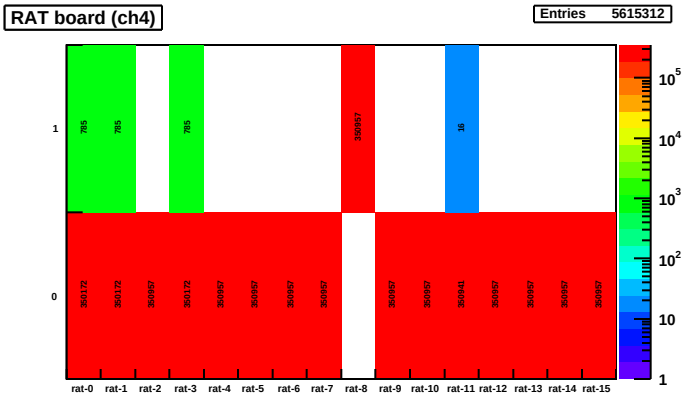

Hybrid Jet Patches
Entries 701914
JP ADC
JP ID

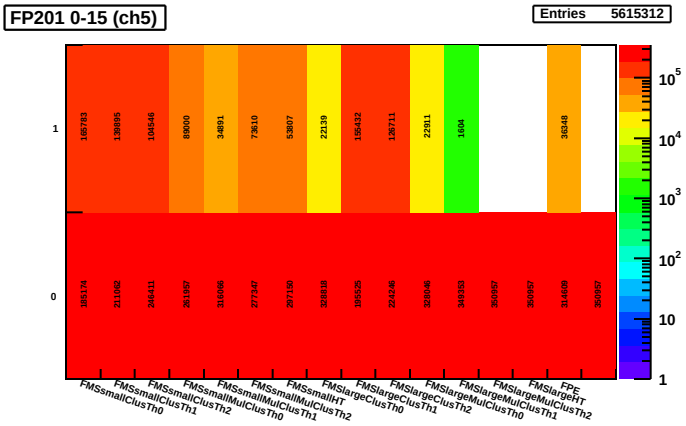

TF201 0-15 (ch0)
Entries 5615312
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

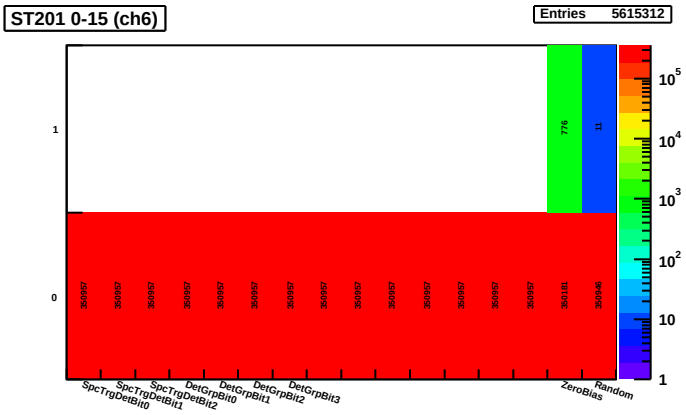

VT201 0-15 (ch1)
Entries 5615312
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

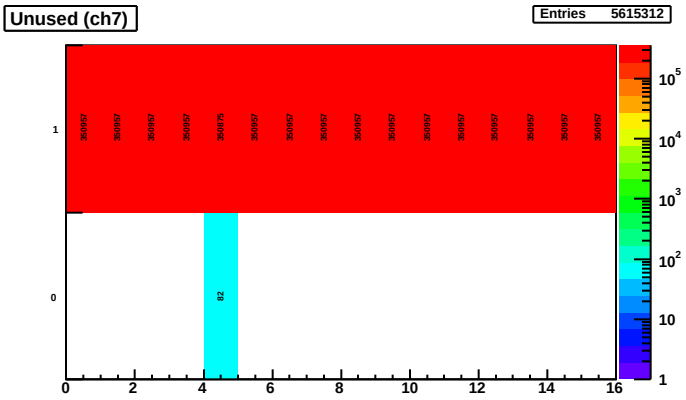

Unused (ch2)
Entries 5615312

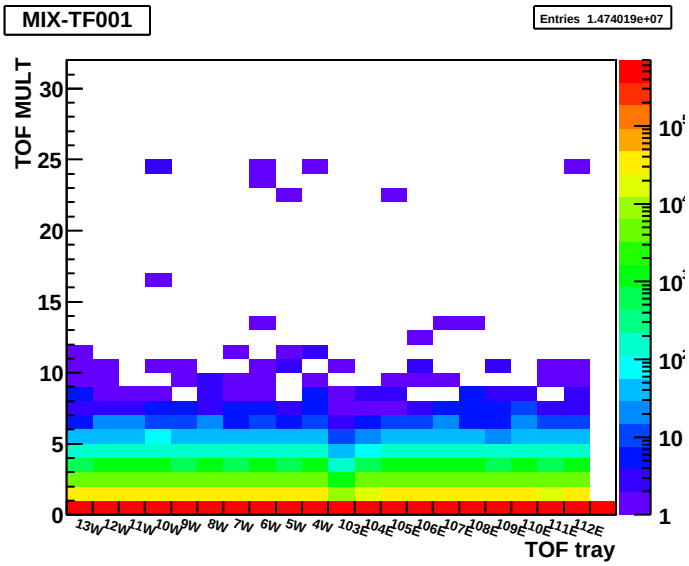

EM201 0-15 (ch3)
Entries 5615312
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

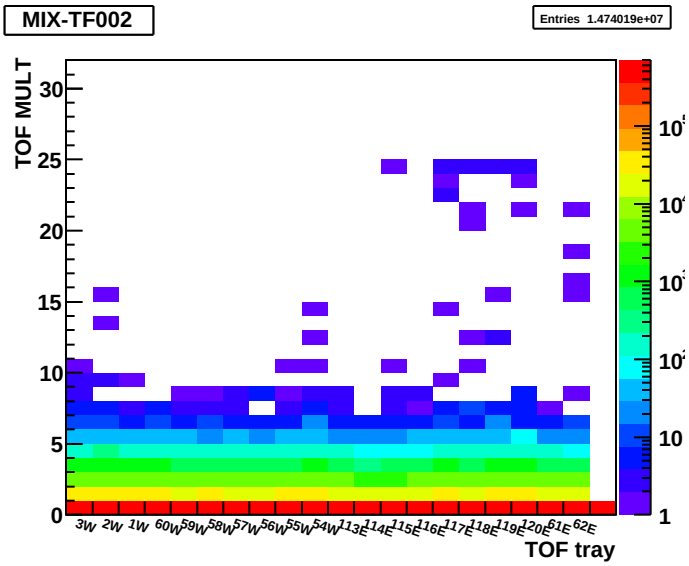

RAT board (ch4)
Entries 5615312
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

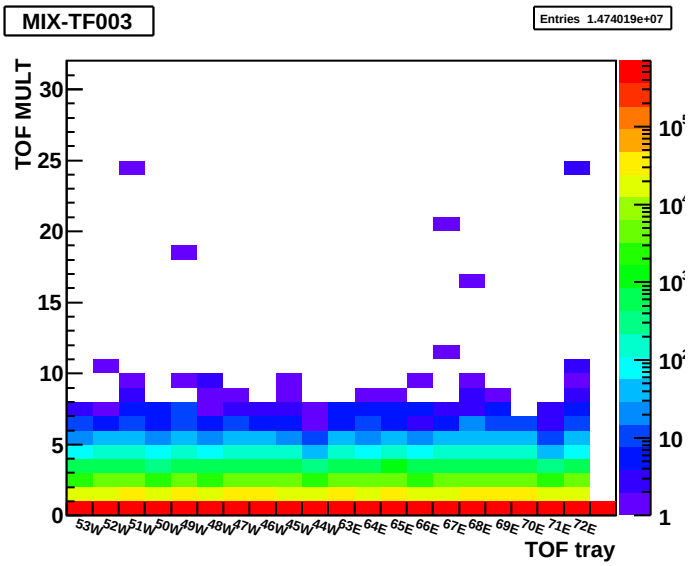

FP201 0-15 (ch5)
Entries 5615312
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

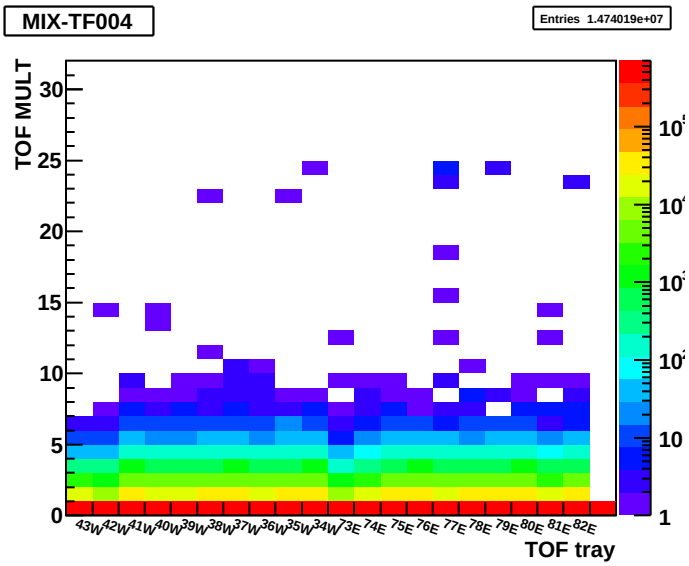

ST201 0-15 (ch6)
Entries 5615312
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

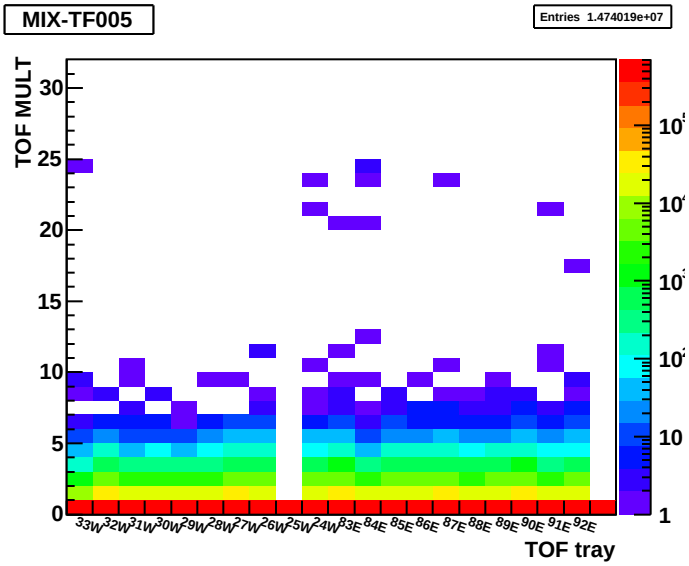

Unused (ch7)
Entries 5615312

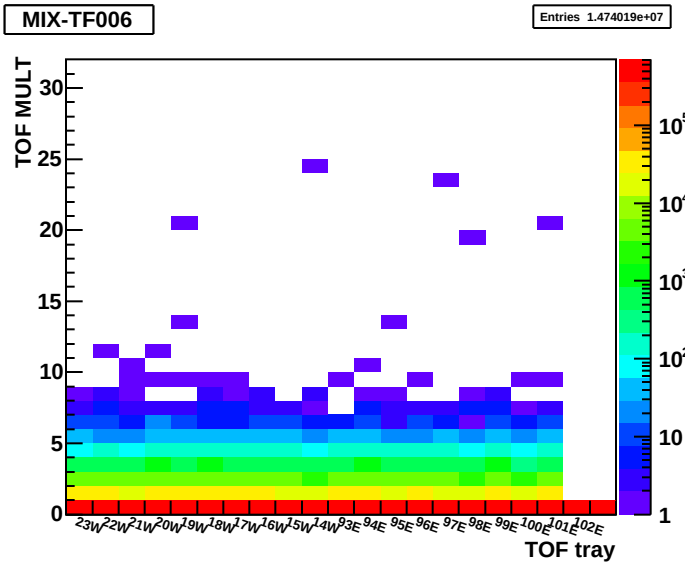

MIX-TF001
Entries 1.474019e+07
TOF MULT
TOF tray

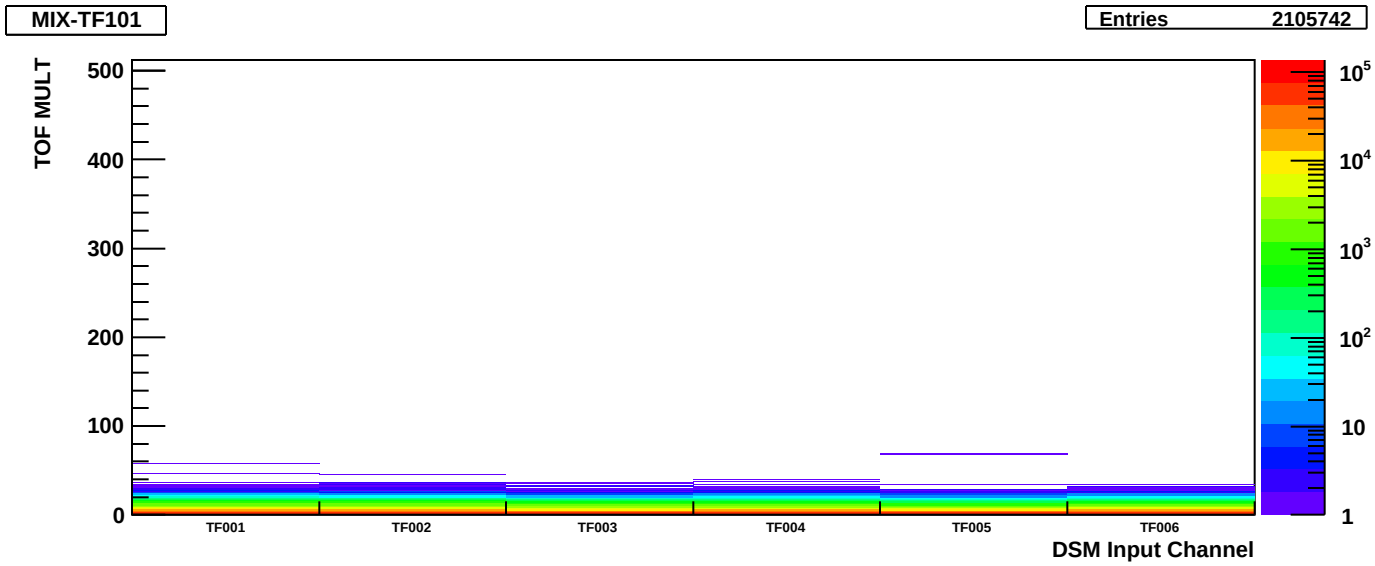

MIX-TF002
Entries 1.474019e+07
TOF MULT
TOF tray

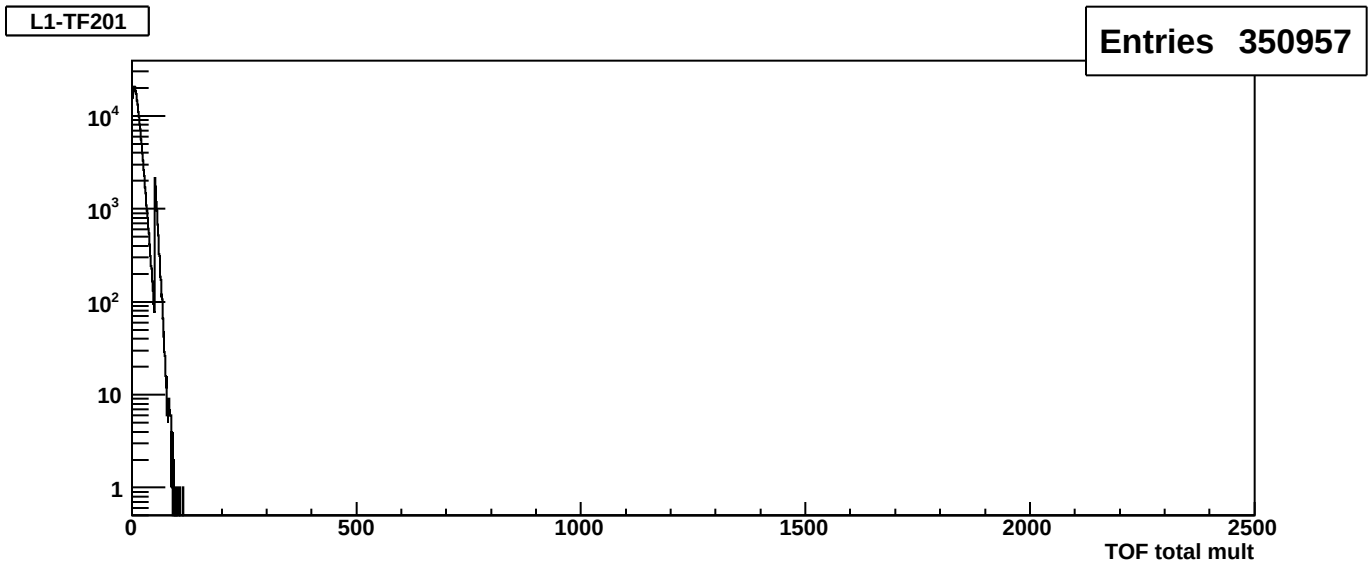

MIX-TF003
Entries 1.474019e+07
TOF MULT
TOF tray

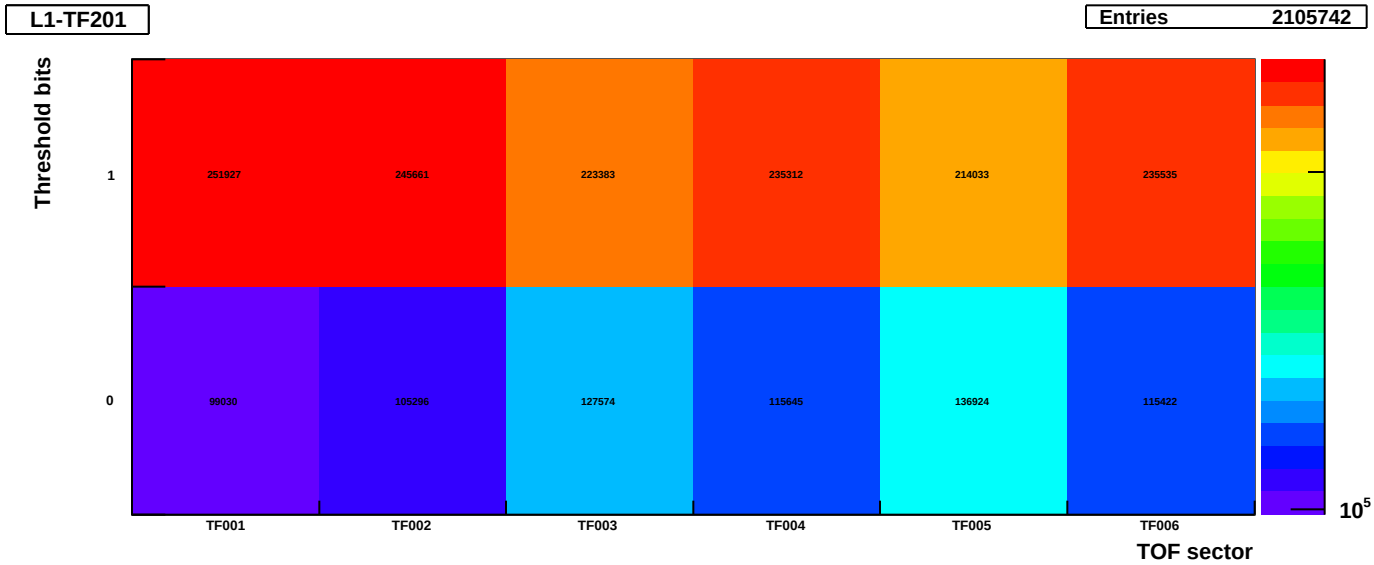

MIX-TF004
Entries 1.474019e+07
TOF MULT
TOF tray

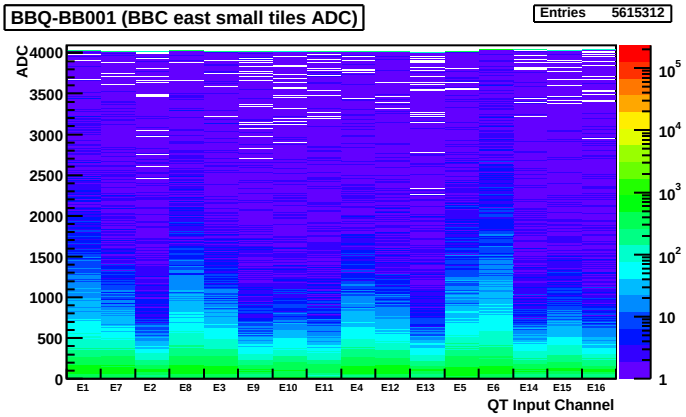

MIX-TF005
Entries 1.474019e+07
TOF MULT
TOF tray

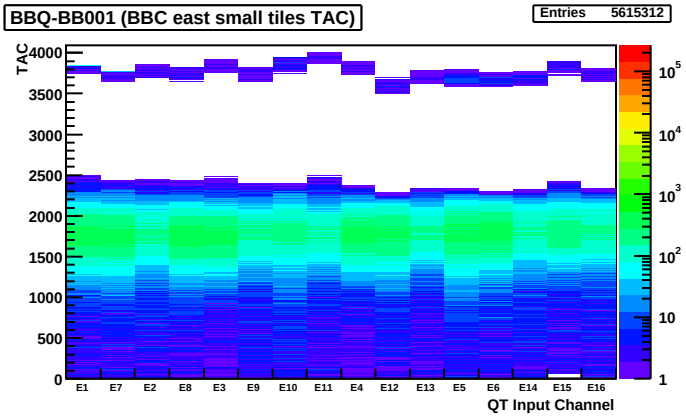

MIX-TF006
Entries 1.474019e+07
TOF MULT
TOF tray

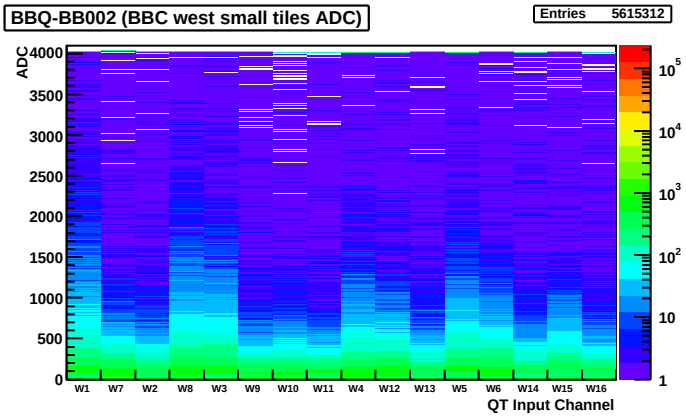

MIX-TF101
Entries 2105742
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

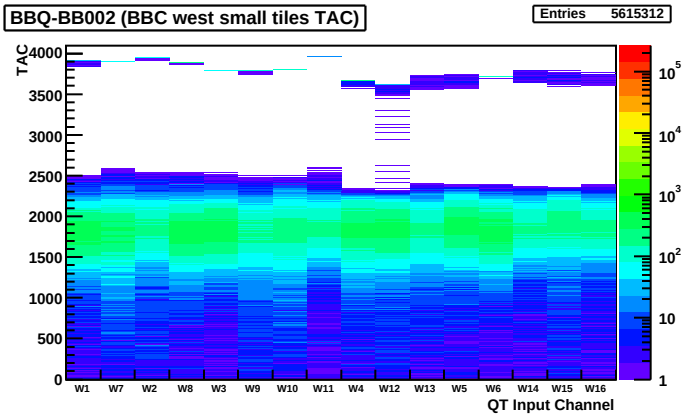

L1-TF201
Entries 350957
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

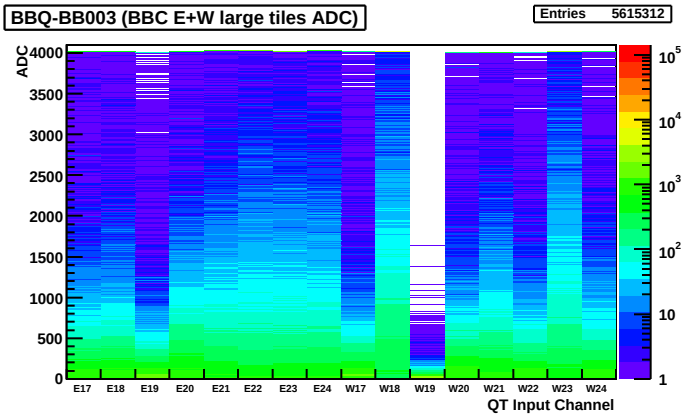

L1-TF201
Entries 2105742
Threshold bits
1
0
251927
245661
223383
235312
214033
235535
99030
105296
127574
115645
136924
115422
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

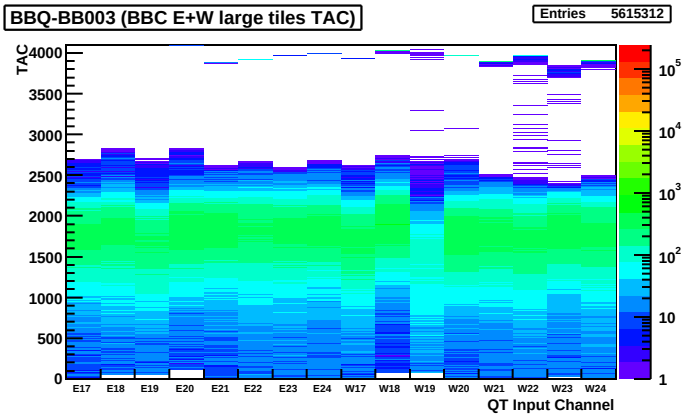

BBQ-BB001 (BBC east small tiles ADC)
Entries 5615312
ADC
QT Input Channel

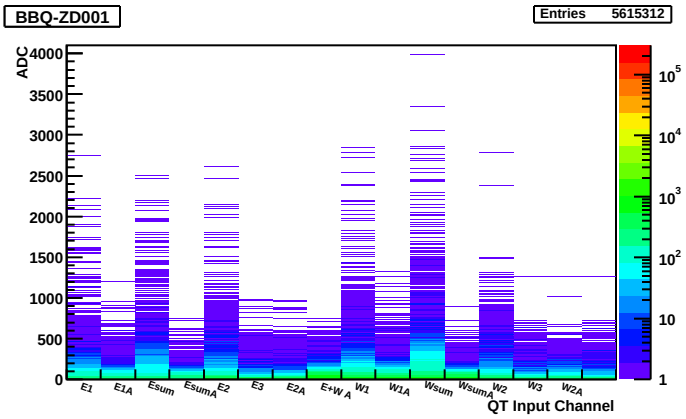

BBQ-BB001 (BBC east small tiles TAC)
Entries 5615312
TAC
QT Input Channel

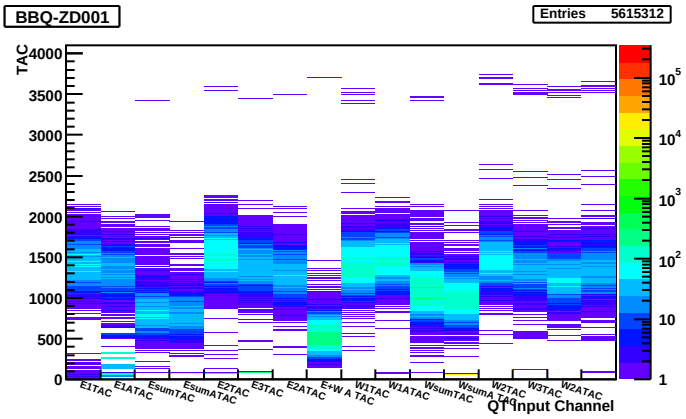

BBQ-BB002 (BBC west small tiles ADC)
Entries 5615312
ADC
QT Input Channel

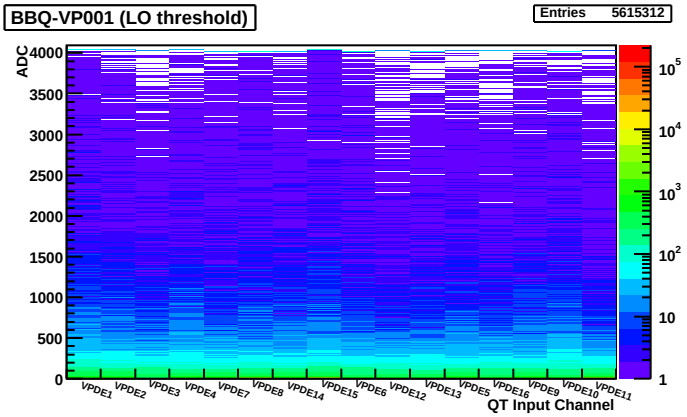

BBQ-BB002 (BBC west small tiles TAC)
Entries 5615312
TAC
QT Input Channel

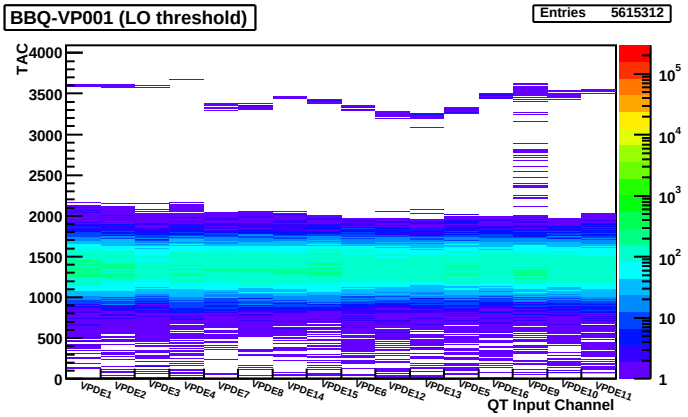

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5615312
ADC
QT Input Channel

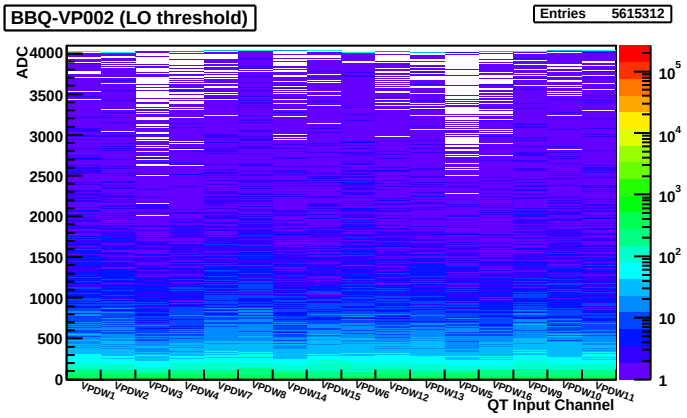

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5615312
TAC
QT Input Channel

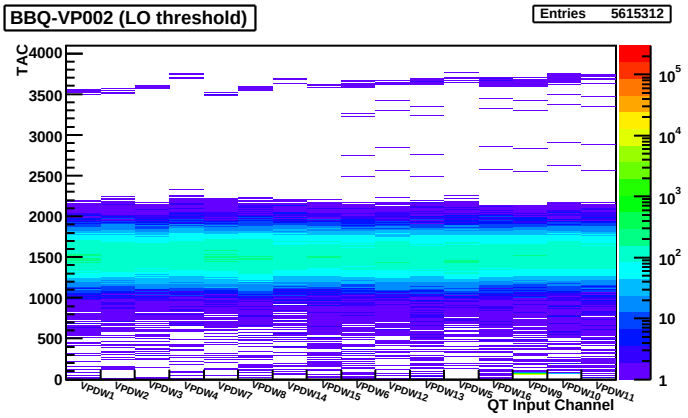

BBQ-ZD001
Entries 5615312
ADC
QT Input Channel

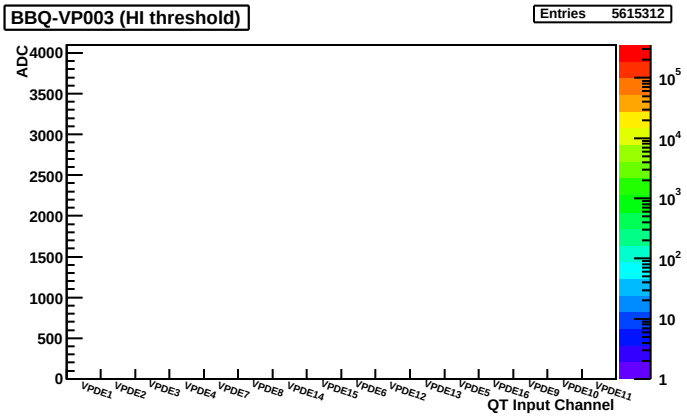

BBQ-ZD001
Entries 5615312
TAC
QT Input Channel

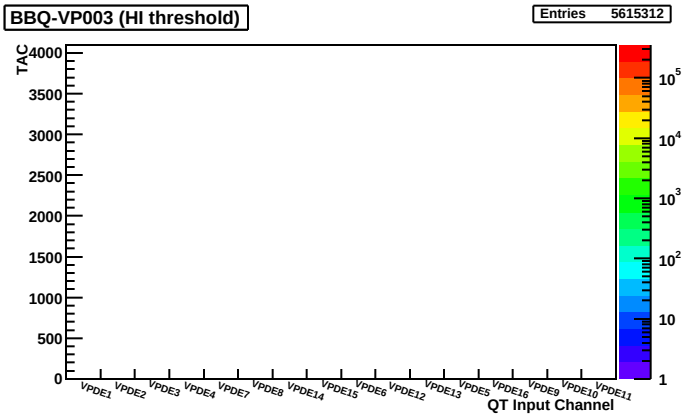

BBQ-VP001 (LO threshold)
Entries 5615312
ADC
QT Input Channel

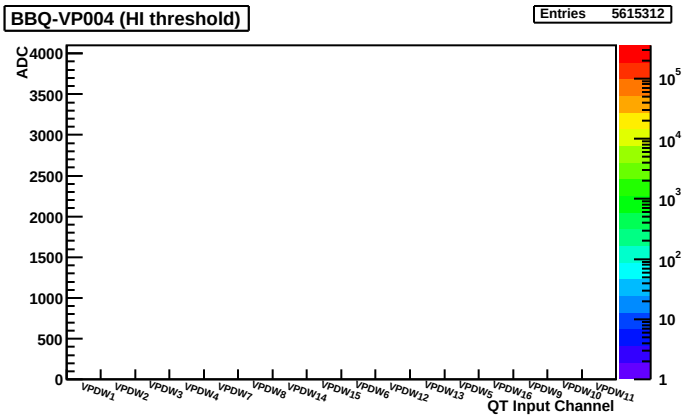

BBQ-VP001 (LO threshold)
Entries 5615312
TAC
QT Input Channel

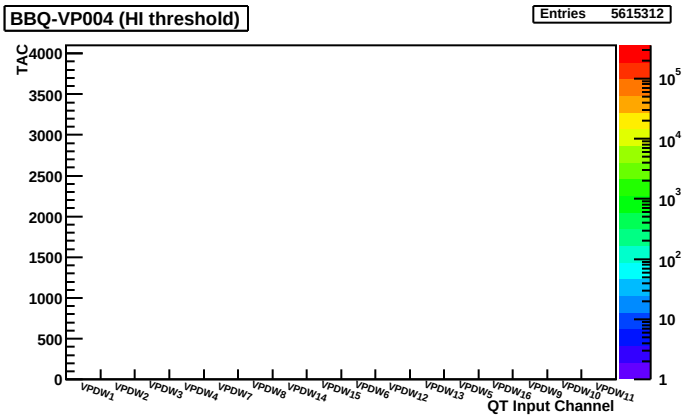

BBQ-VP002 (LO threshold)
Entries 5615312
ADC
QT Input Channel

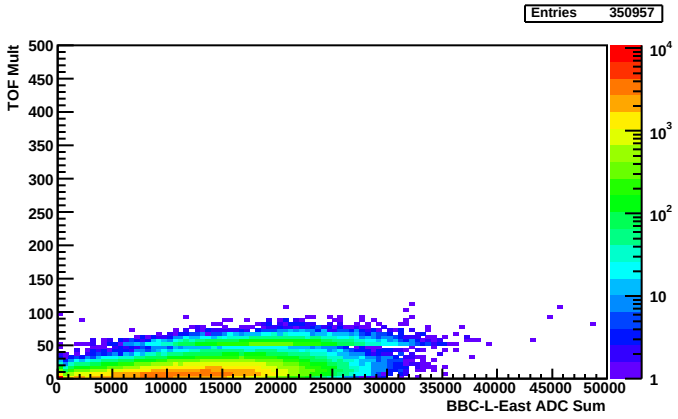

BBQ-VP002 (LO threshold)
Entries 5615312
TAC
QT Input Channel

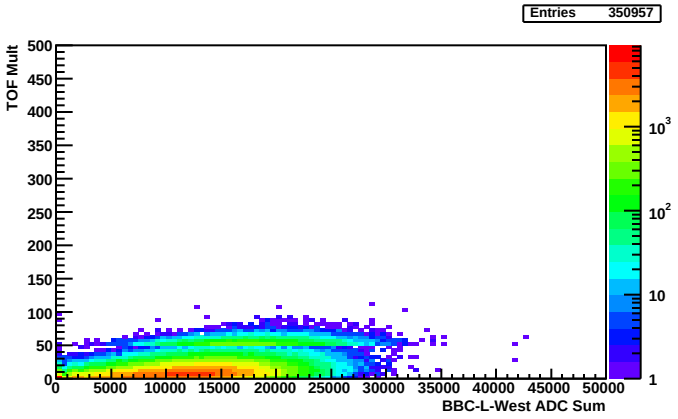

BBQ-VP003 (HI threshold)
Entries 5615312
ADC
QT Input Channel

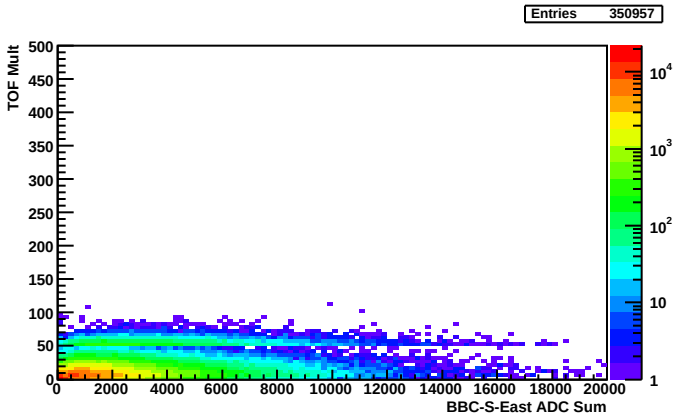

BBQ-VP003 (HI threshold)
Entries 5615312
TAC
QT Input Channel

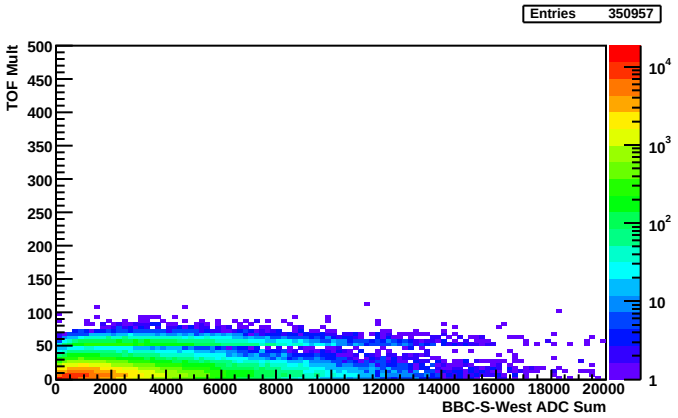

BBQ-VP004 (HI threshold)
Entries 5615312
ADC
QT Input Channel

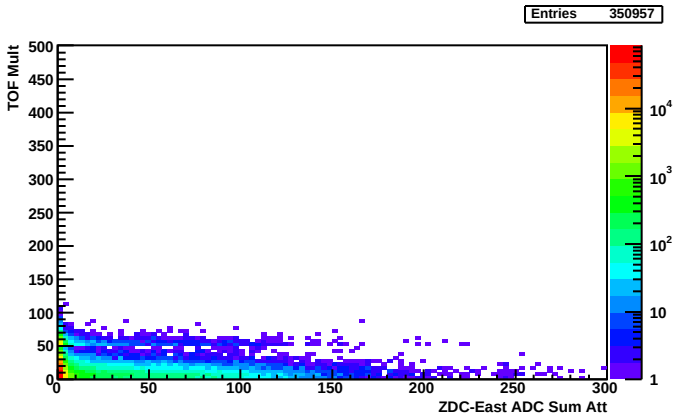

BBQ-VP004 (HI threshold)
Entries 5615312
TAC
QT Input Channel

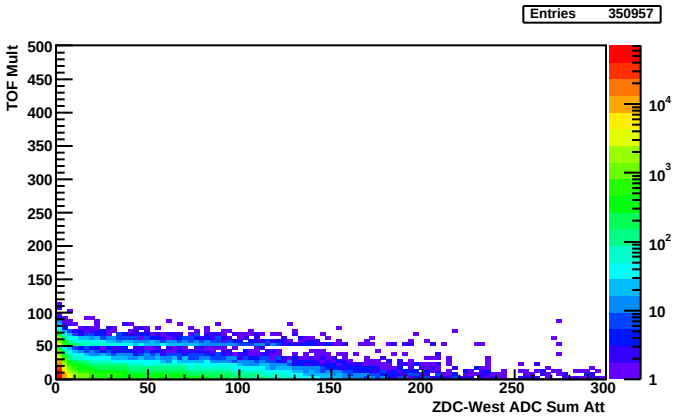

Entries 350957
TOF Mult
BBC-L-East ADC Sum

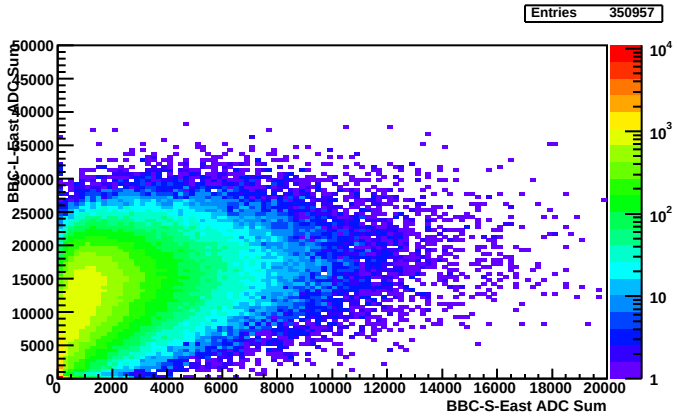

Entries 350957
TOF Mult
BBC-L-West ADC Sum

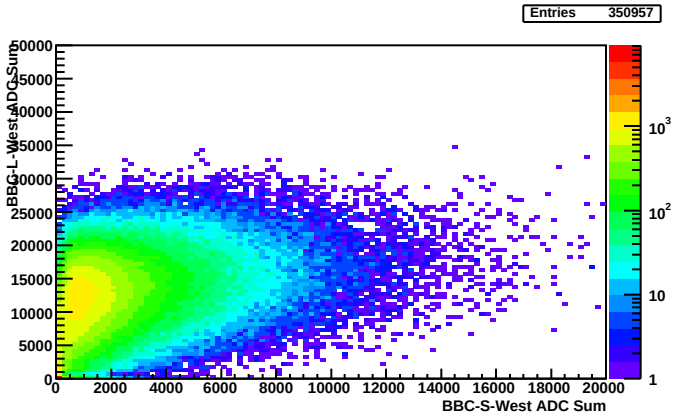

Entries 350957
TOF Mult
BBC-S-East ADC Sum

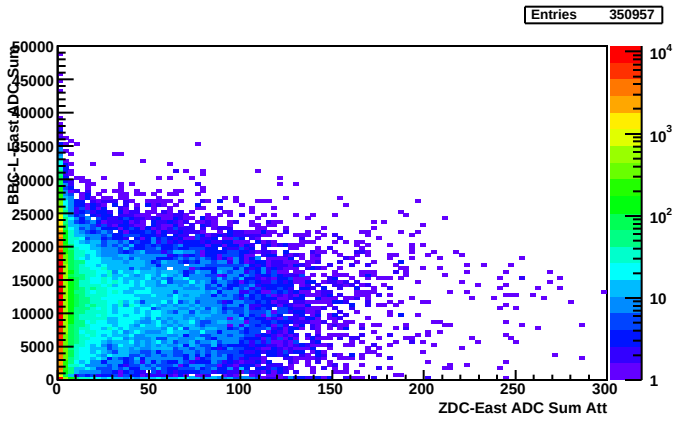

Entries 350957
TOF Mult
BBC-S-West ADC Sum

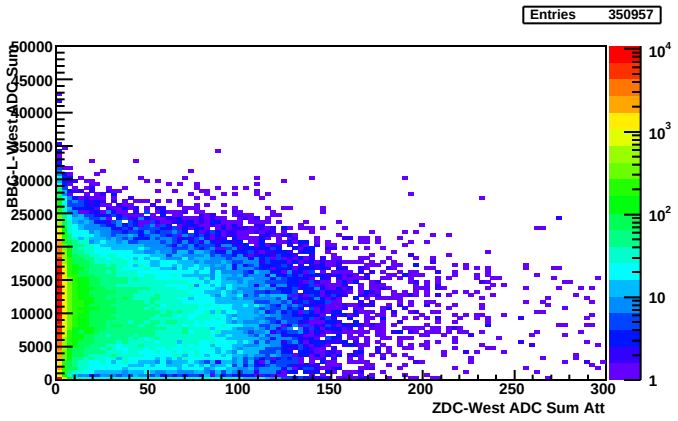

Entries 350957
TOF Mult
ZDC-East ADC Sum Att

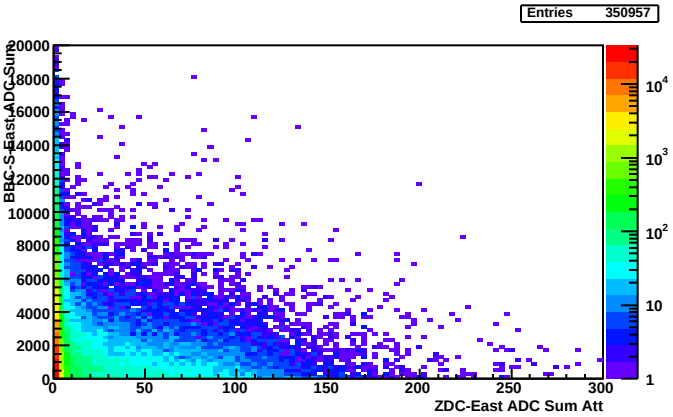

Entries 350957
TOF Mult
ZDC-West ADC Sum Att

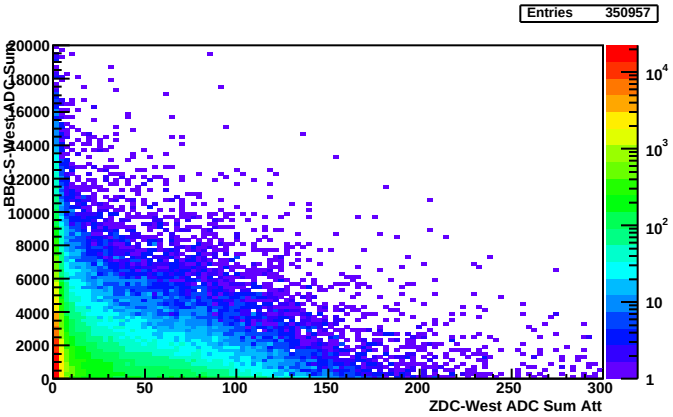

Entries 350957
BBC-L-East ADC Sum
BBC-S-East ADC Sum

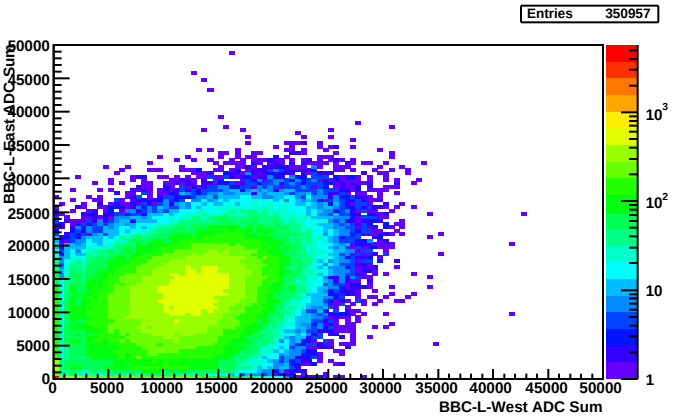

Entries 350957
BBC-L-West ADC Sum
BBC-S-West ADC Sum

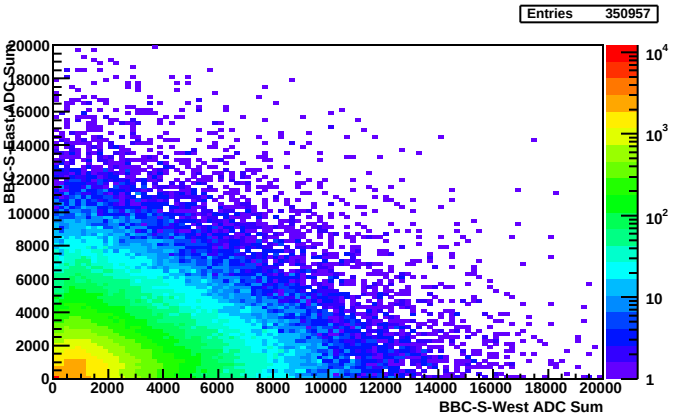

Entries 350957
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

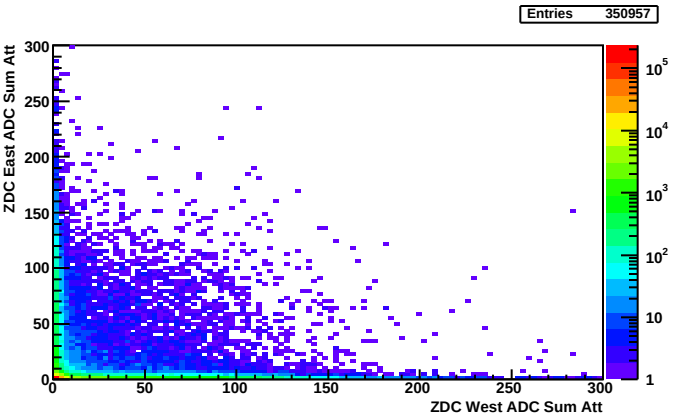

Entries 350957
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

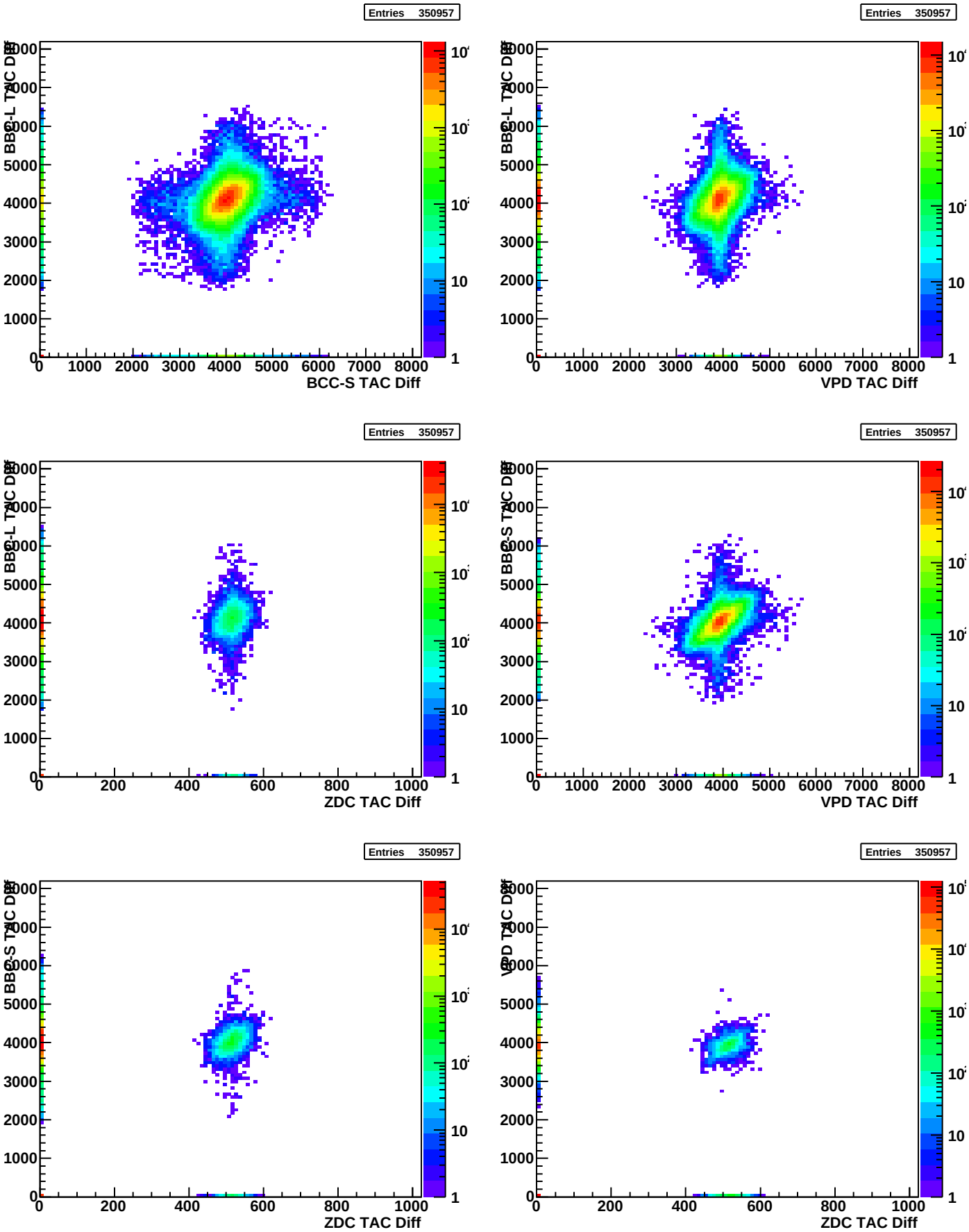

Entries 350957
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

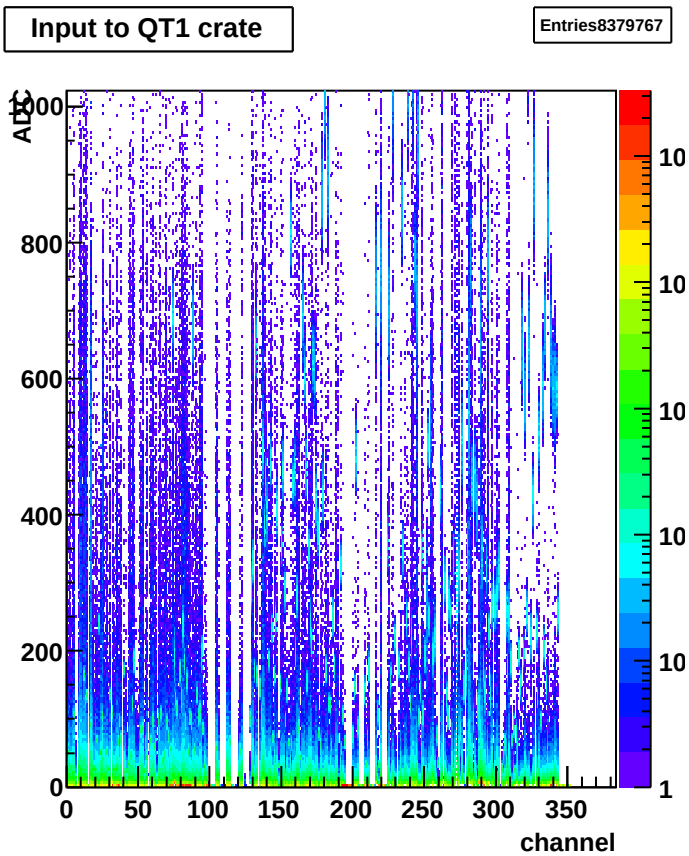

Entries 350957
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

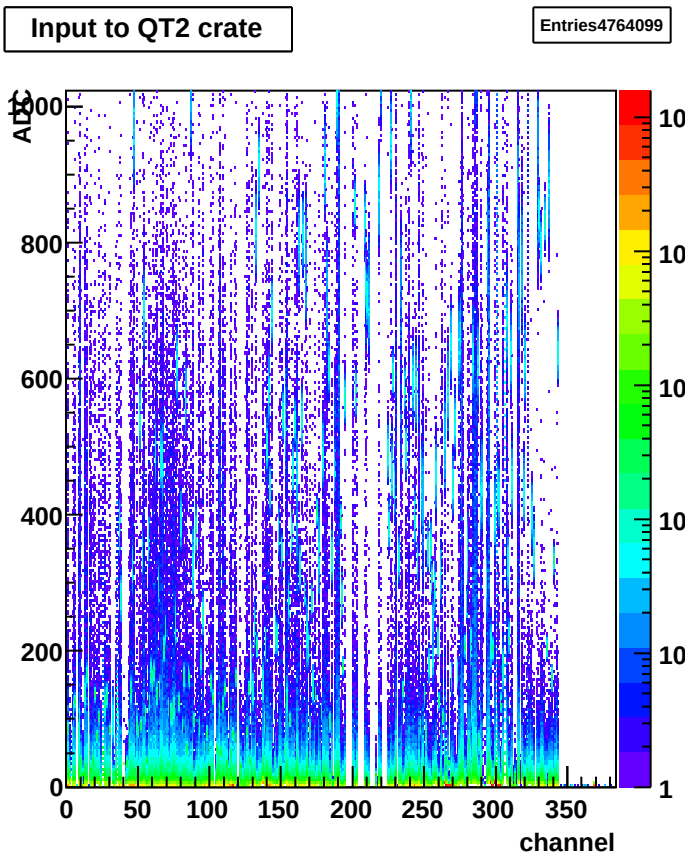

Entries 350957
BBC-L-East ADC Sum
BBC-L-West ADC Sum

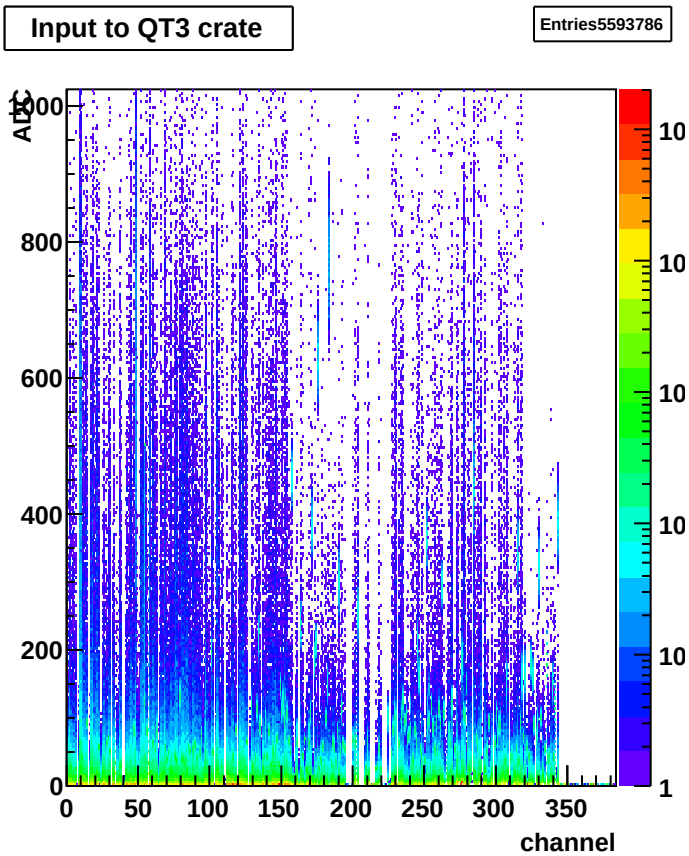

Entries 350957
BBC-S-East ADC Sum
BBC-S-West ADC Sum

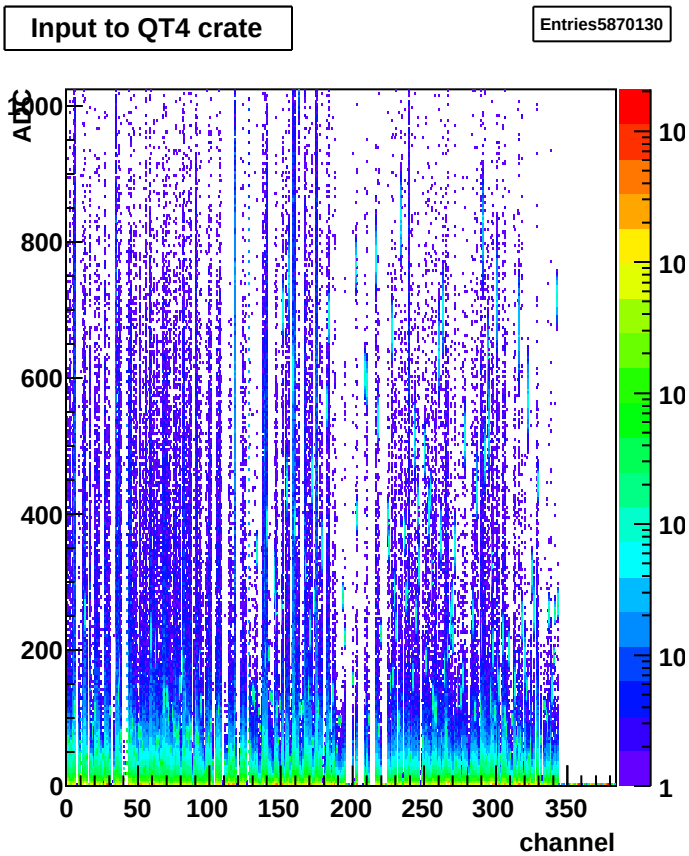

Entries 350957
ZDC East ADC Sum Att
ZDC West ADC Sum Att

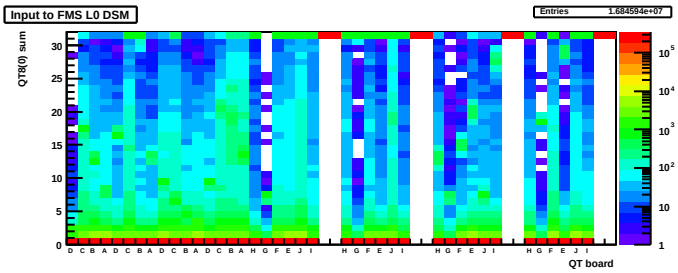

Entries 350957
BBC-L TAC Diff
BCC-S TAC Diff
Entries 350957
BBC-L TAC Diff
VPD TAC Diff
Entries 350957
BBC-L TAC Diff
ZDC TAC Diff
Entries 350957
BBC-S TAC Diff
VPD TAC Diff
Entries 350957
BBC-S TAC Diff
ZDC TAC Diff
Entries 350957
VPD TAC Diff
ZDC TAC Diff

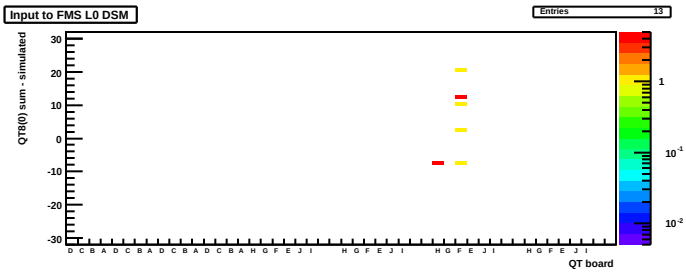

Input to QT1 crate
Entries8379767
ADC
channel

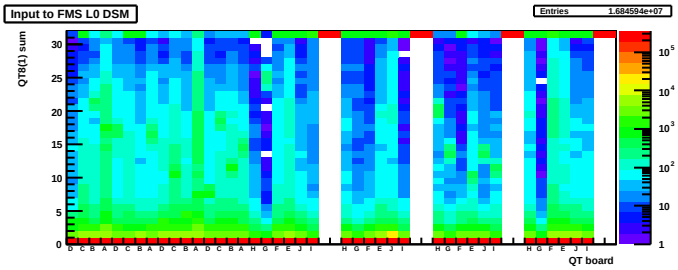

Input to QT2 crate
Entries4764099
ADC
channel

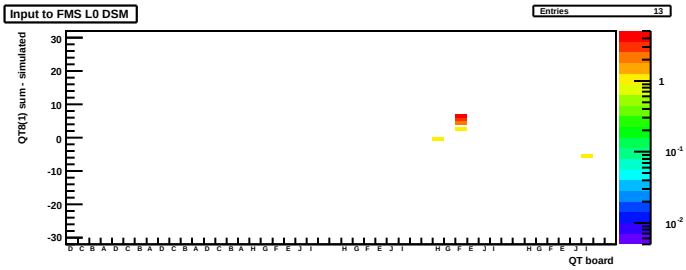

Input to QT3 crate
Entries5593786
ADC
channel

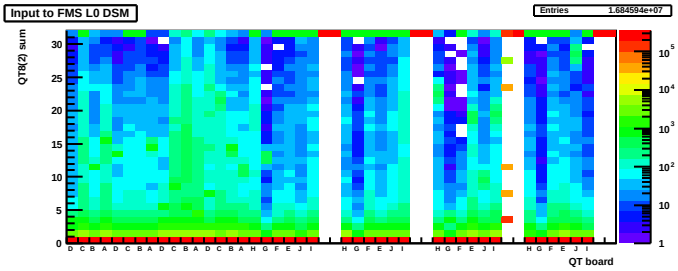

Input to QT4 crate
Entries5870130
ADC
channel

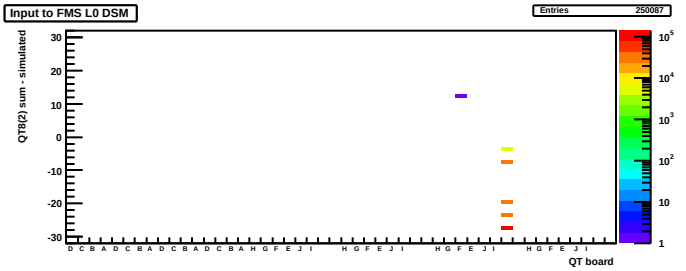

Input to FMS L0 DSM
Entries 1.684594e+07
QT8(0) sum
QT board

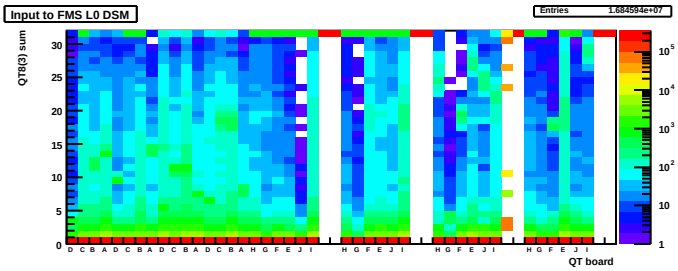

Input to FMS L0 DSM
Entries 13
QT8(0) sum - simulated
QT board

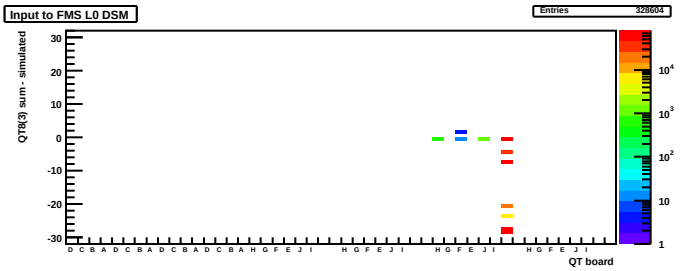

Input to FMS L0 DSM
Entries 1.684594e+07
QT8(1) sum
QT board

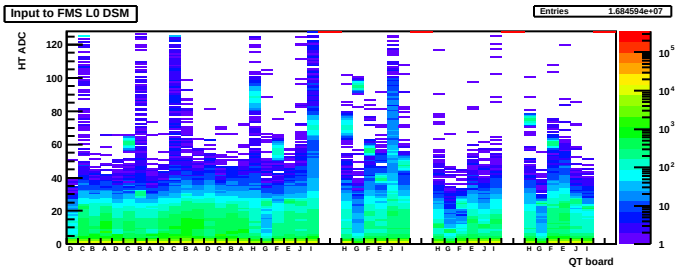

Input to FMS L0 DSM
Entries 13
QT8(1) sum - simulated
QT board

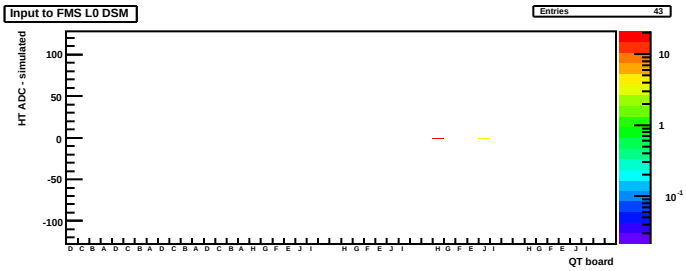

Input to FMS L0 DSM
Entries 1.684594e+07
QT8(2) sum
QT board

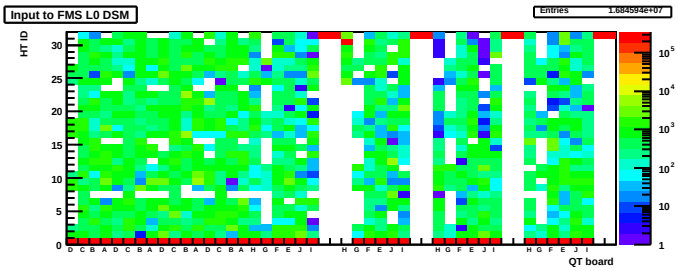

Input to FMS L0 DSM
Entries 250087
QT8(2) sum - simulated
QT board

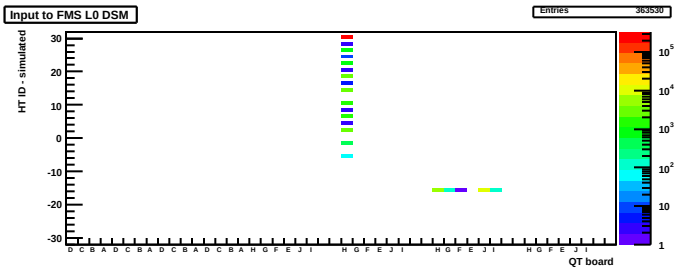

Input to FMS L0 DSM
Entries 1.684594e+07
QT8(3) sum
QT board

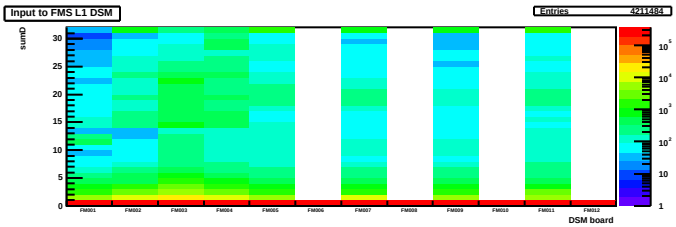

Input to FMS L0 DSM
Entries 328604
QT8(3) sum - simulated
QT board

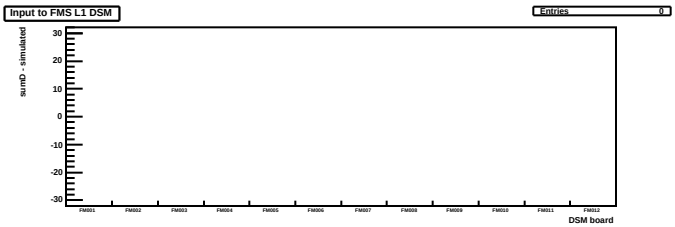

Input to FMS L0 DSM
Entries 1.684594e+07
HT ADC
QT board

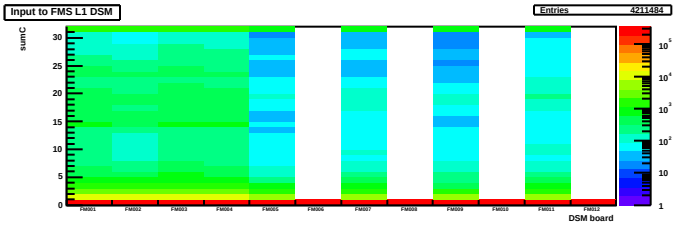

Input to FMS L0 DSM
Entries 43
HT ADC - simulated
QT board

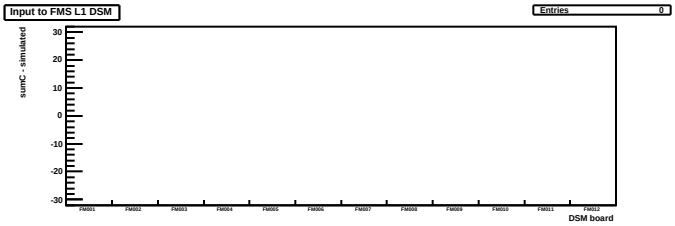

Input to FMS L0 DSM
Entries 1.684594e+07
HT ID
QT board

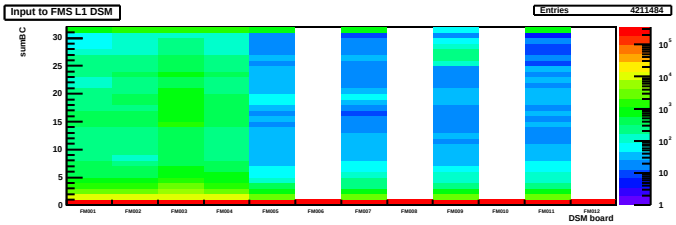

Input to FMS L0 DSM
Entries 363530
HT ID - simulated
QT board

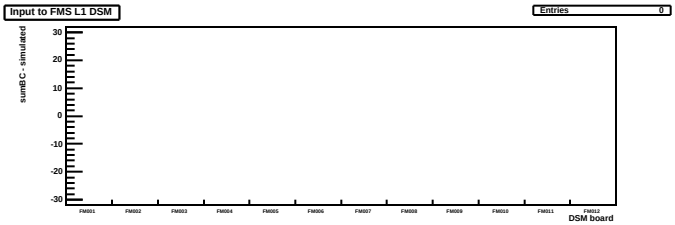

Input to FMS L1 DSM
Entries 4211484
sumD
DSM board

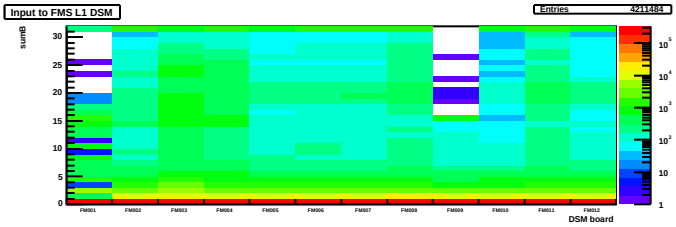

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

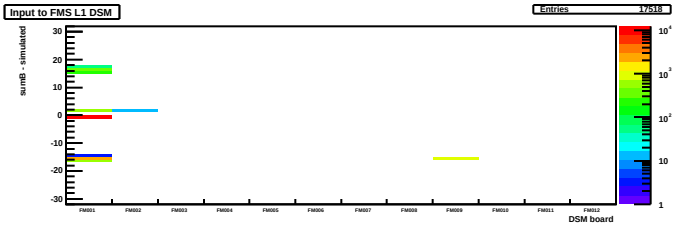

Input to FMS L1 DSM
Entries 4211484
sumC
DSM board

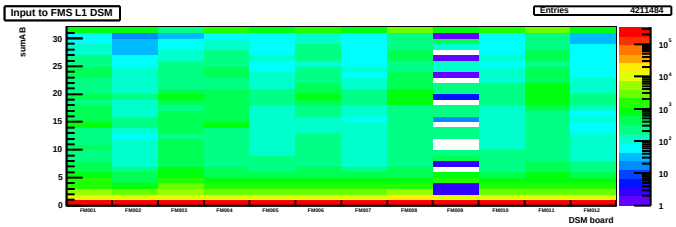

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

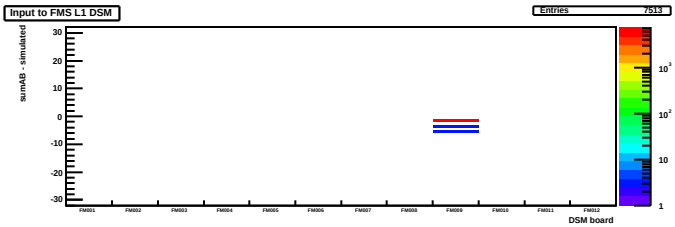

Input to FMS L1 DSM
Entries 4211484
sumBC
DSM board

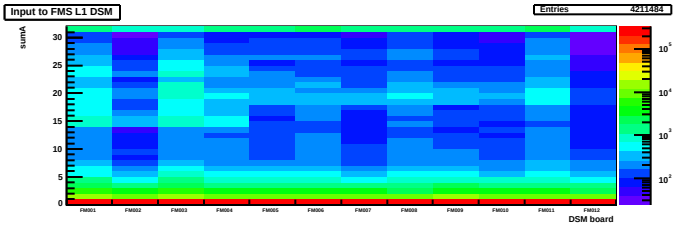

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

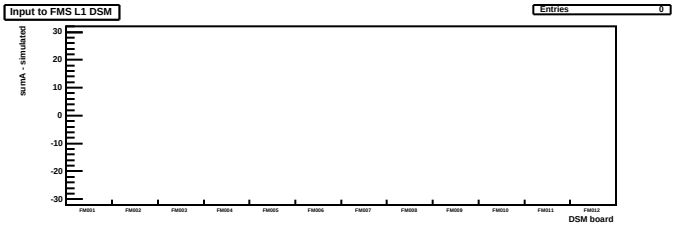

Input to FMS L1 DSM
Entries 4211484
sumB
DSM board

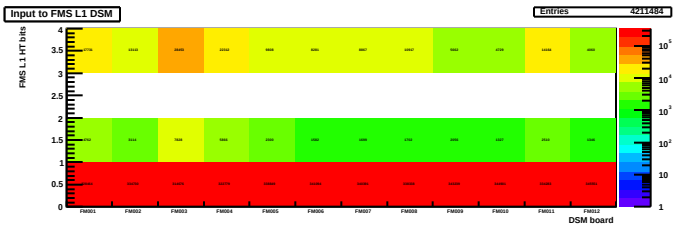

Input to FMS L1 DSM
Entries 17518
sumB - simulated
DSM board

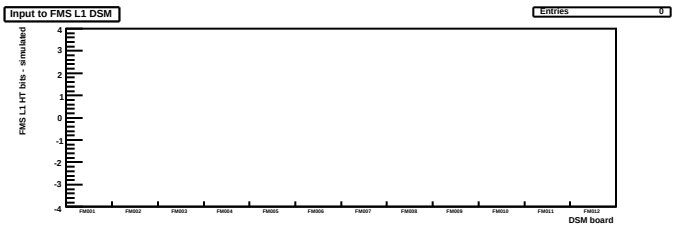

Input to FMS L1 DSM
Entries 4211484
sumAB
DSM board

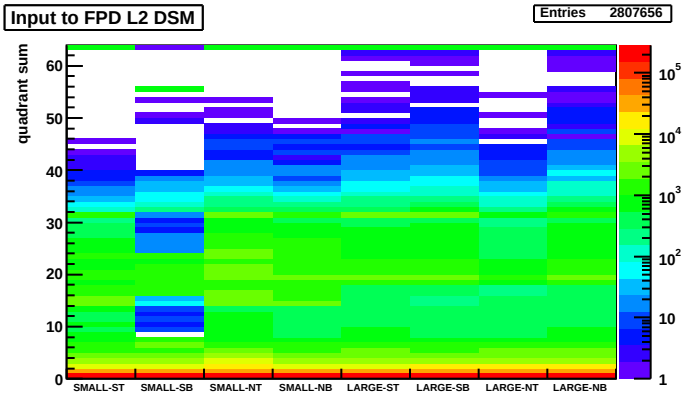

Input to FMS L1 DSM
Entries 7513
sumAB - simulated
DSM board

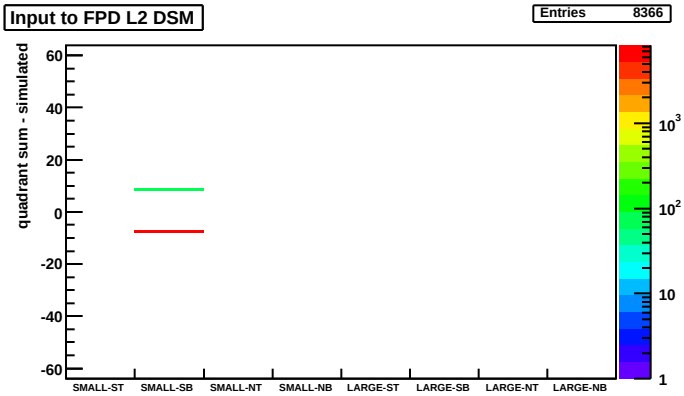

Input to FMS L1 DSM
Entries 4211484
sumA
DSM board

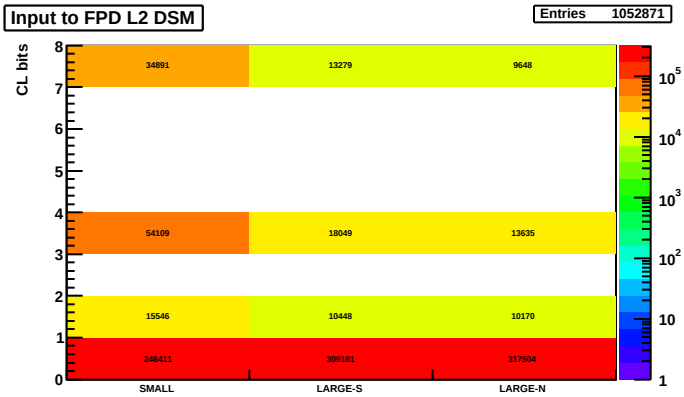

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

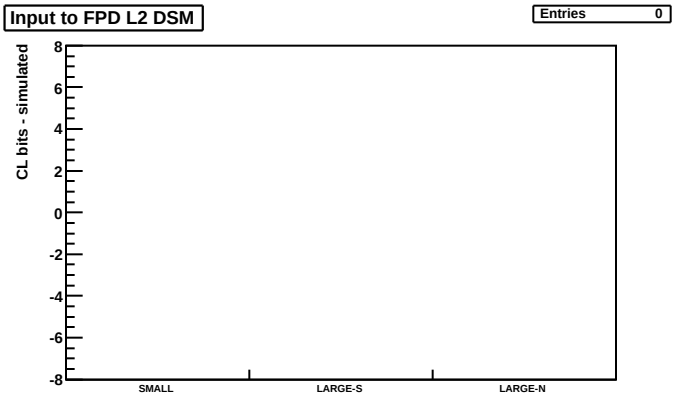

Input to FMS L1 DSM
Entries 4211484
FMS L1 HT bits
DSM board

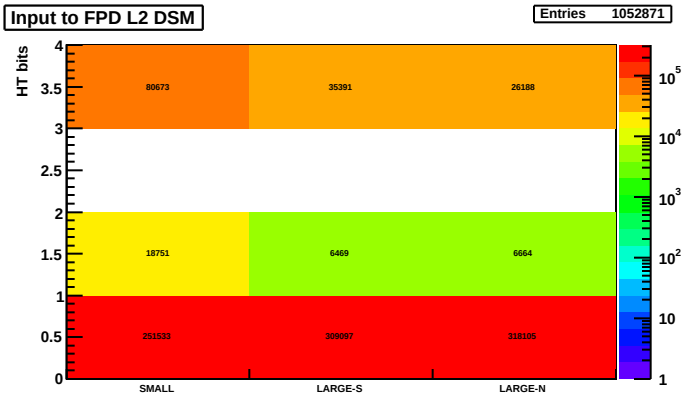

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

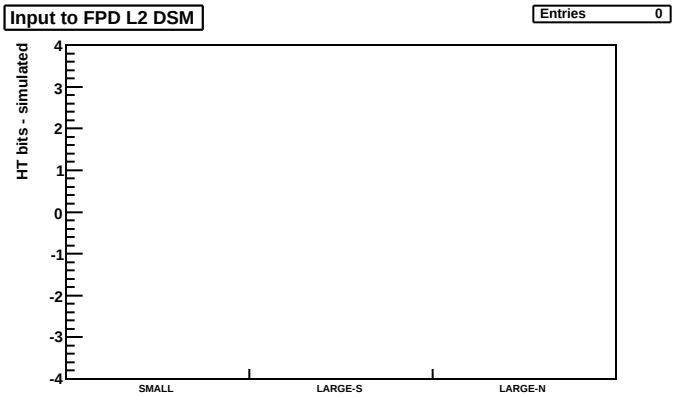

Input to FPD L2 DSM
Entries 2807656
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

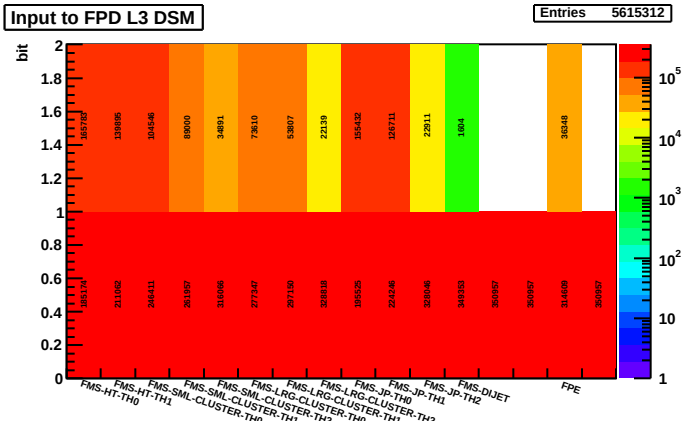

Input to FPD L2 DSM
Entries 8366
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

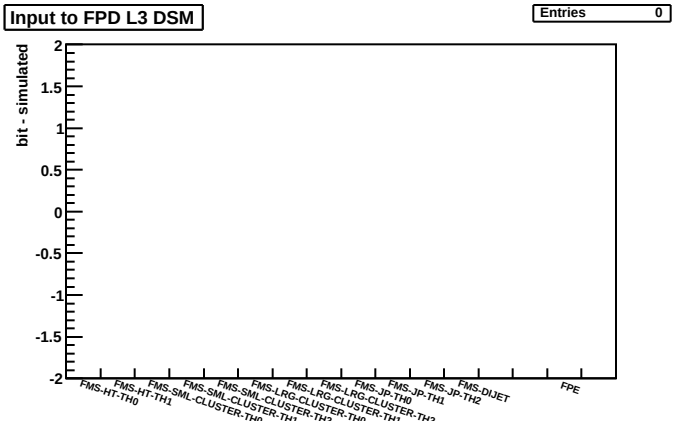

Input to FPD L2 DSM
Entries 1052871
CL bits
34891
13279
9648
54109
18049
13635
15546
10448
10170
246411
309181
317504
SMALL
LARGE-S
LARGE-N

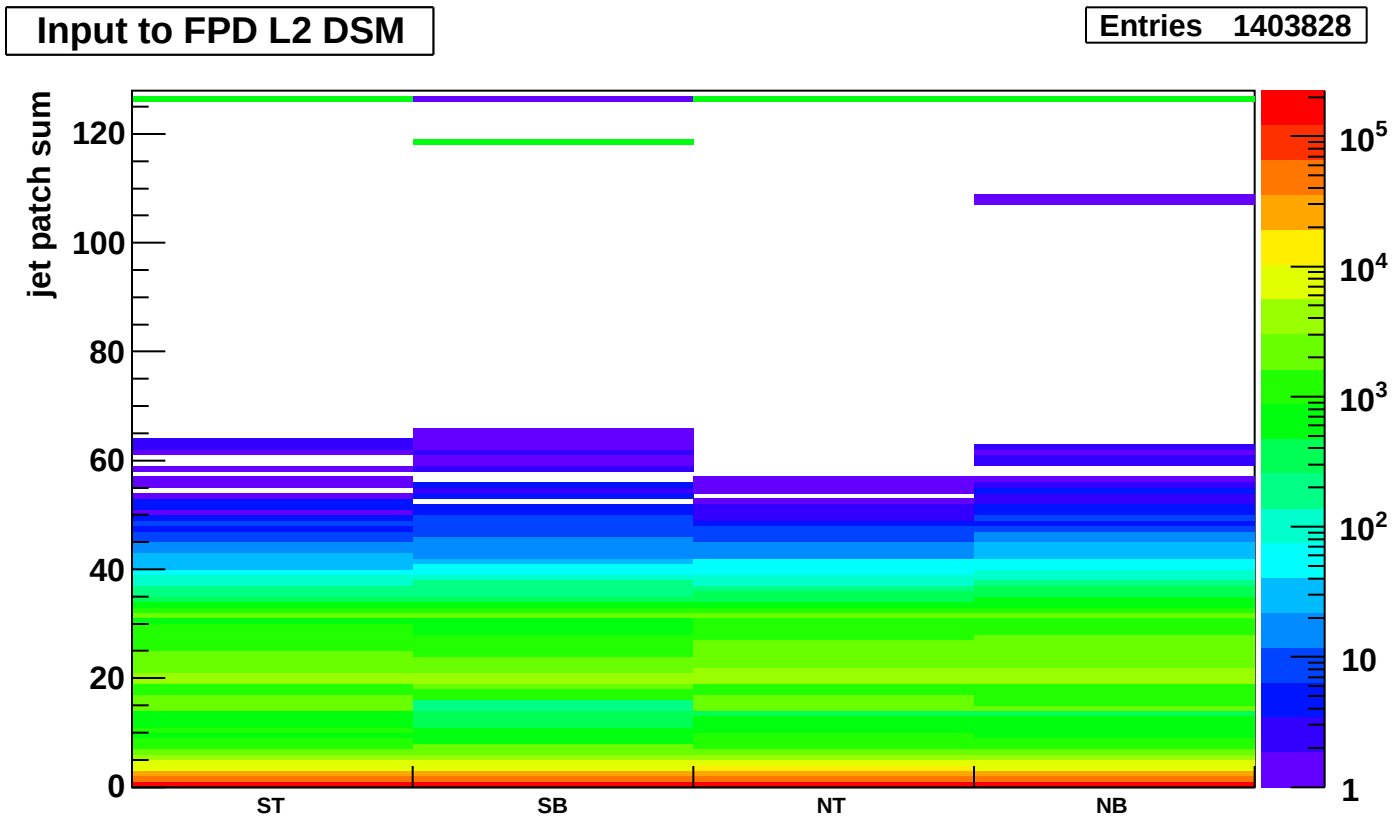

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

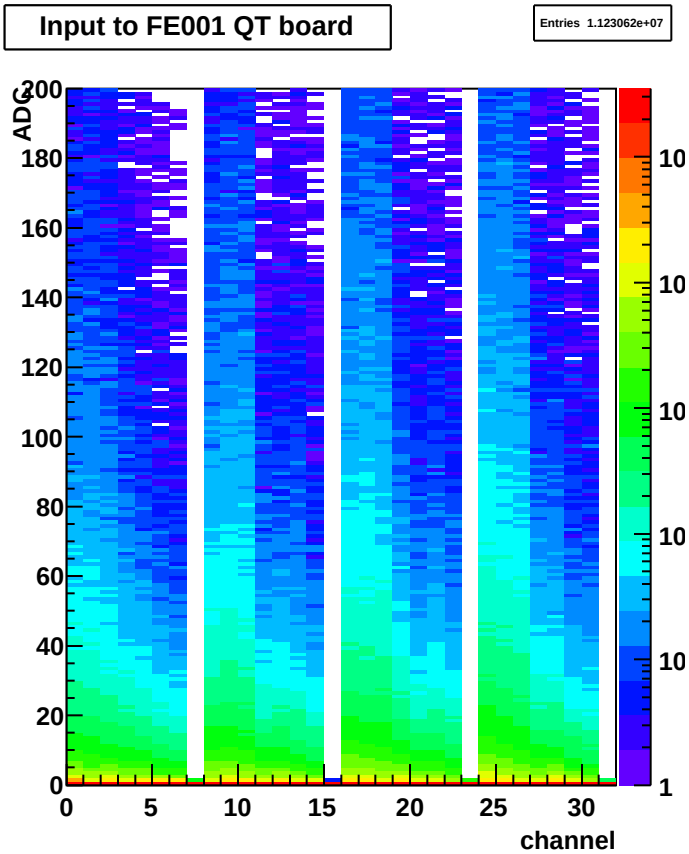

Input to FPD L2 DSM
Entries 1052871
HT bits
80673
35391
26188
18751
6469
6664
251533
309097
318105
SMALL
LARGE-S
LARGE-N

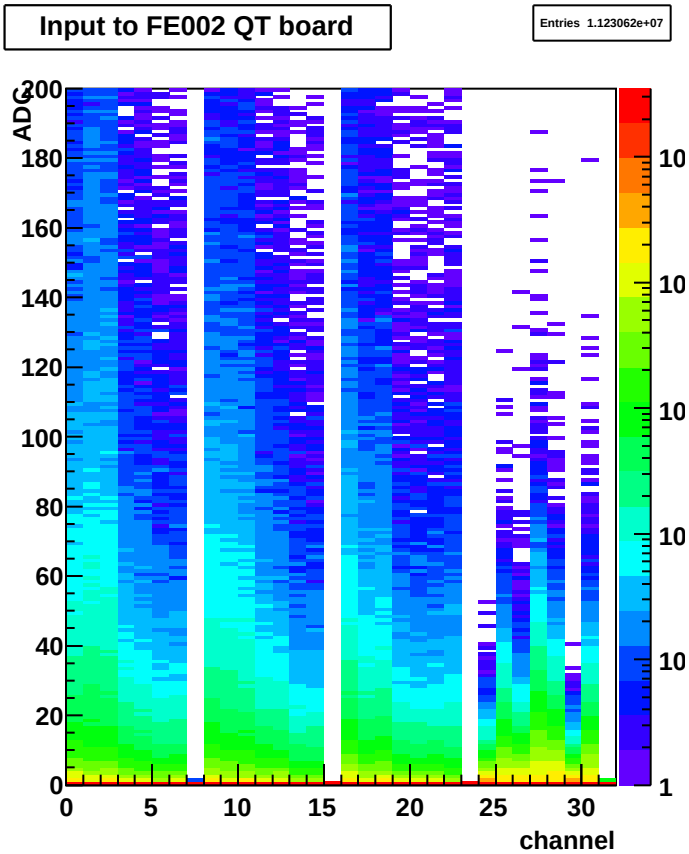

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

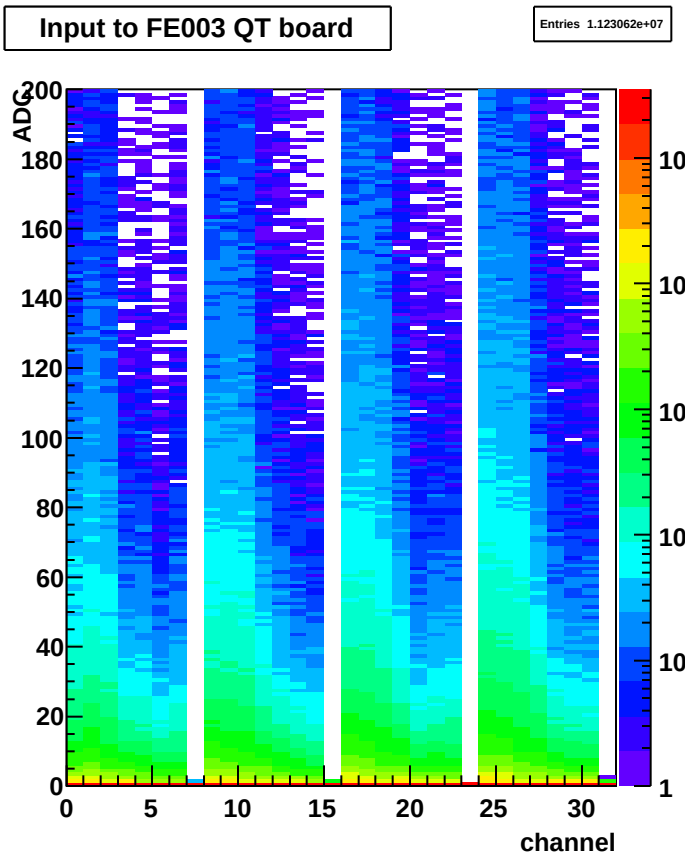

Input to FPD L3 DSM
Entries 5615312
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

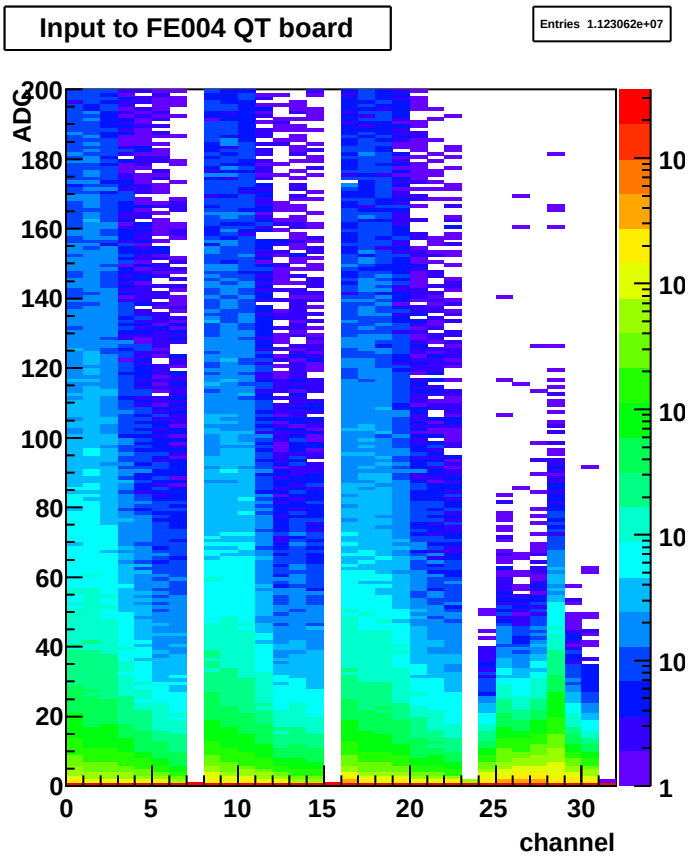

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

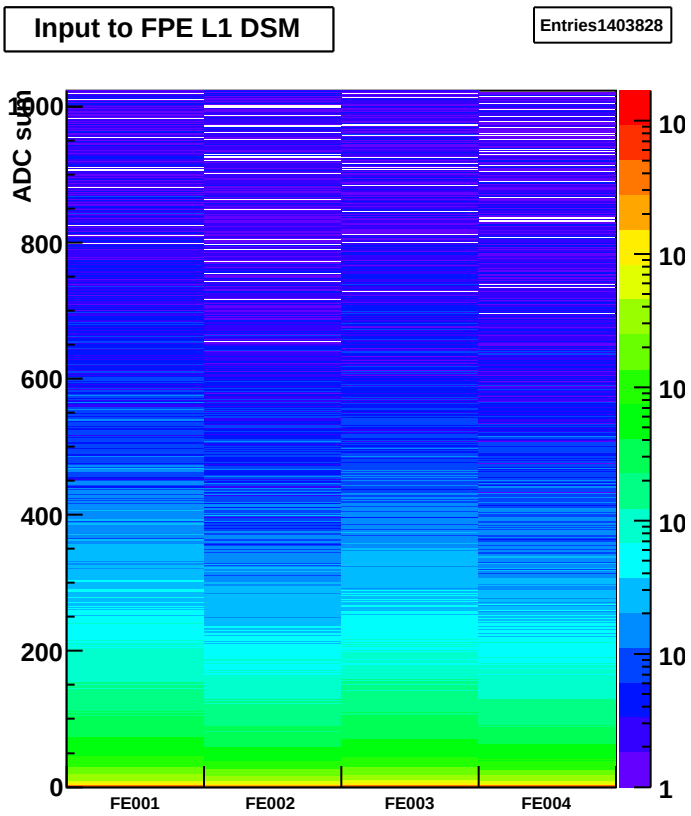

Input to FPD L2 DSM
Entries 1403828
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

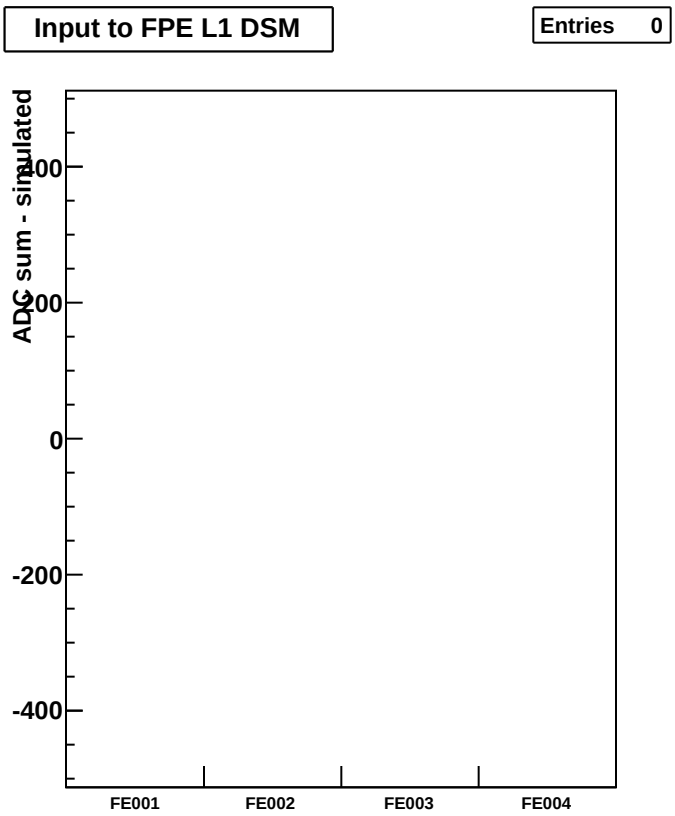

Input to FE001 QT board
Entries 1.123062e+07
ADC
channel

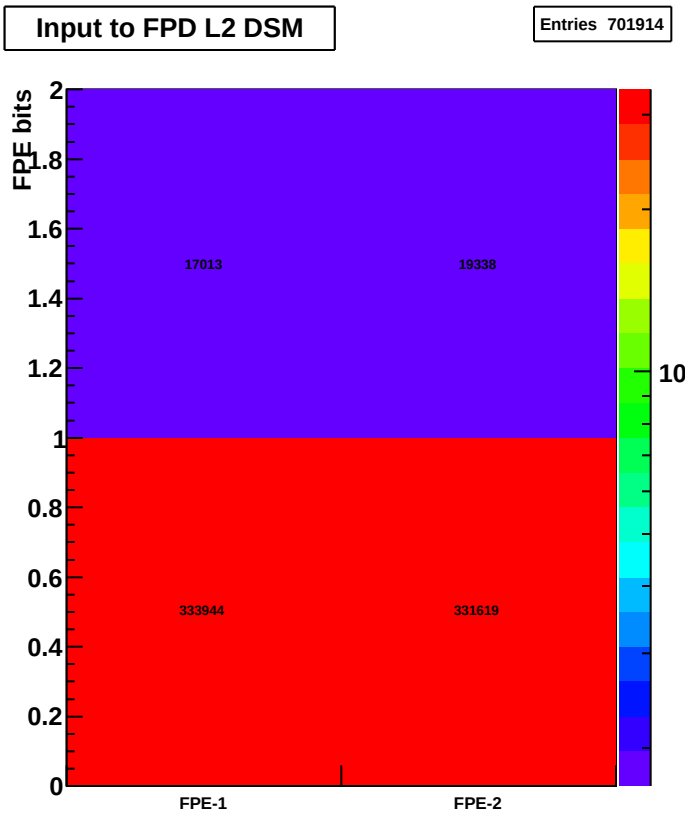

Input to FE002 QT board
Entries 1.123062e+07
ADC
channel

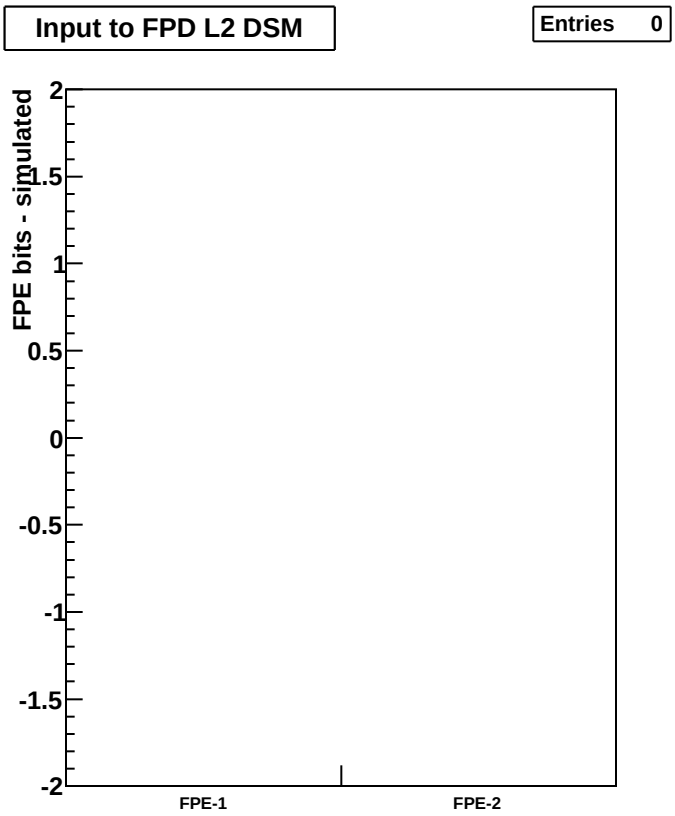

Input to FE003 QT board
Entries 1.123062e+07
ADC
channel


Input to FE004 QT board
Entries 1.123062e+07
ADC
channel


Input to FPE L1 DSM
Entries1403828
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 701914
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
17013
19338
333944
331619
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2