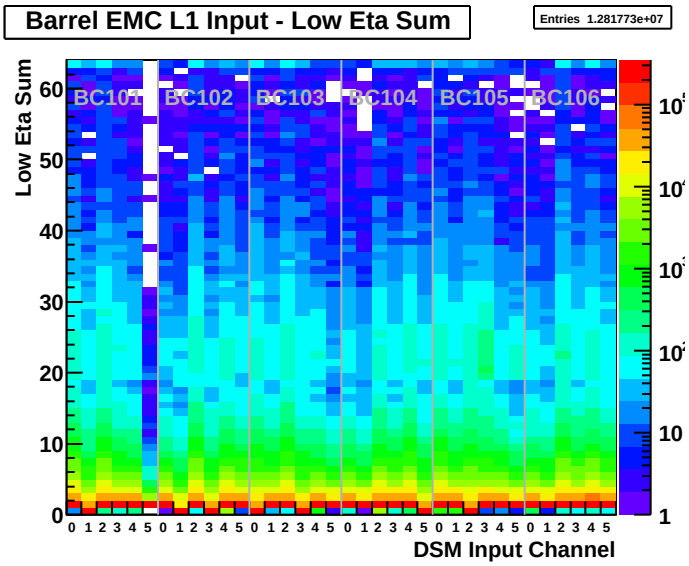
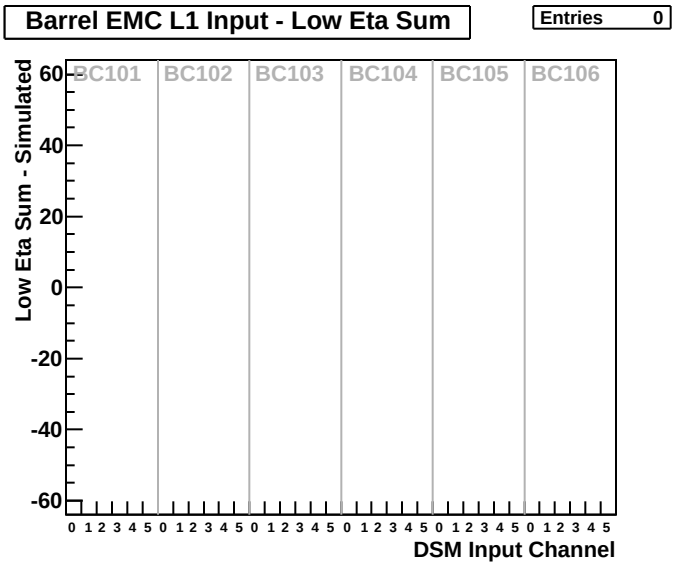
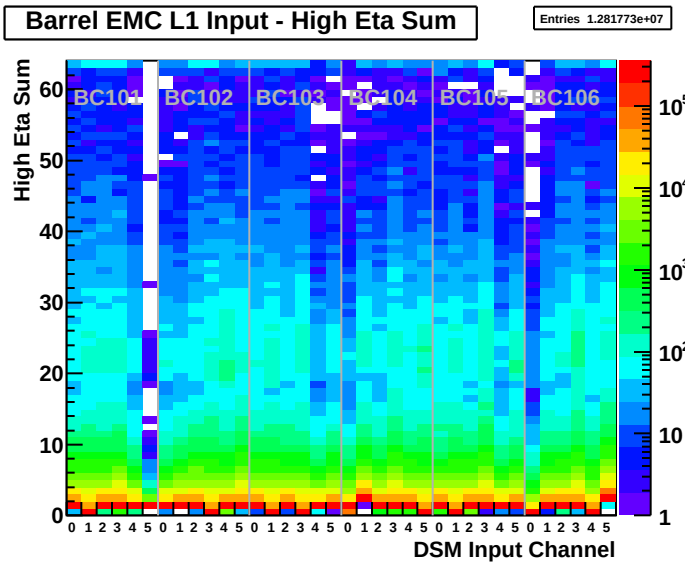
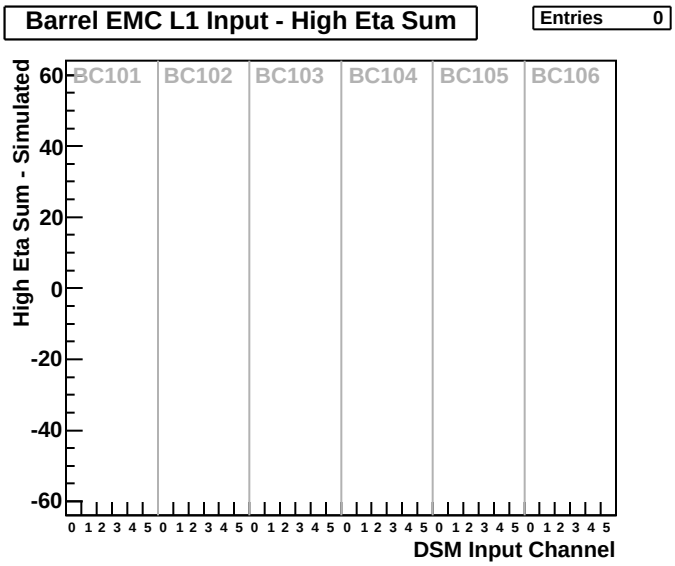
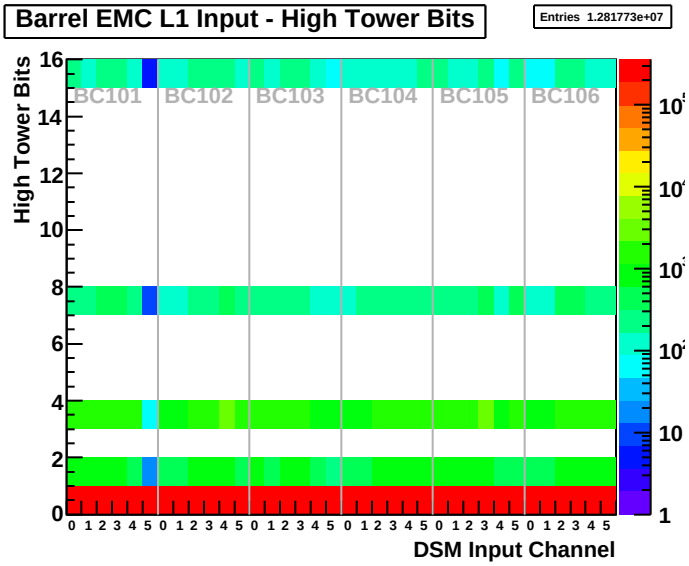
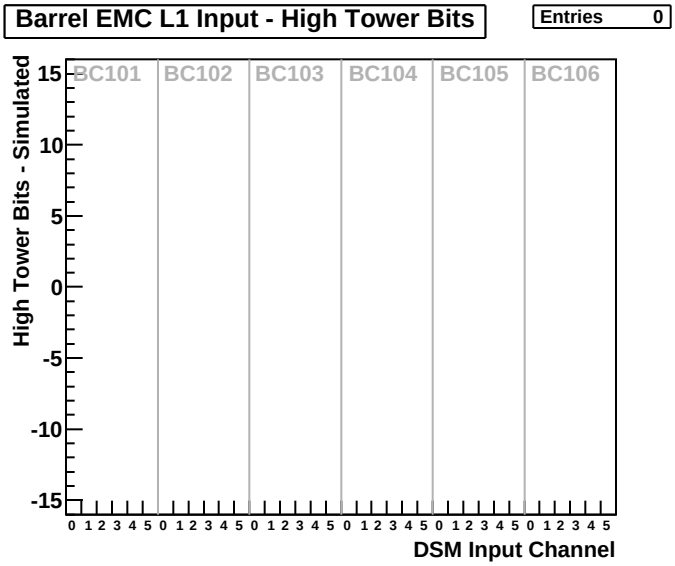

Barrel EMC L1 Input - Low Eta Sum
Entries 1.281773e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

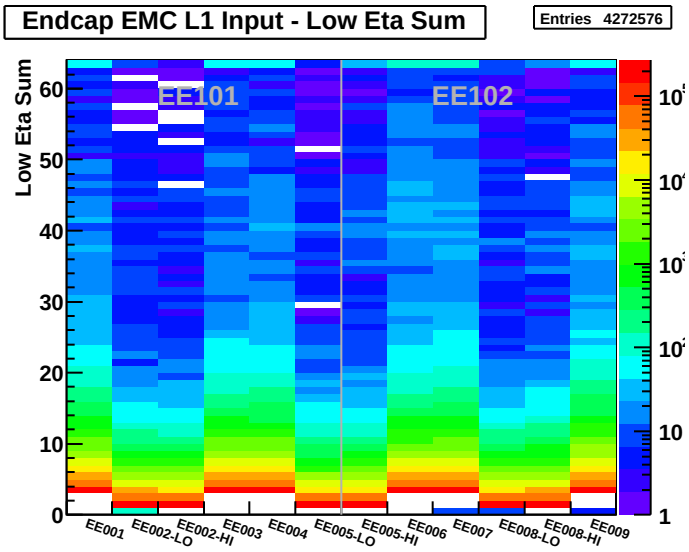

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

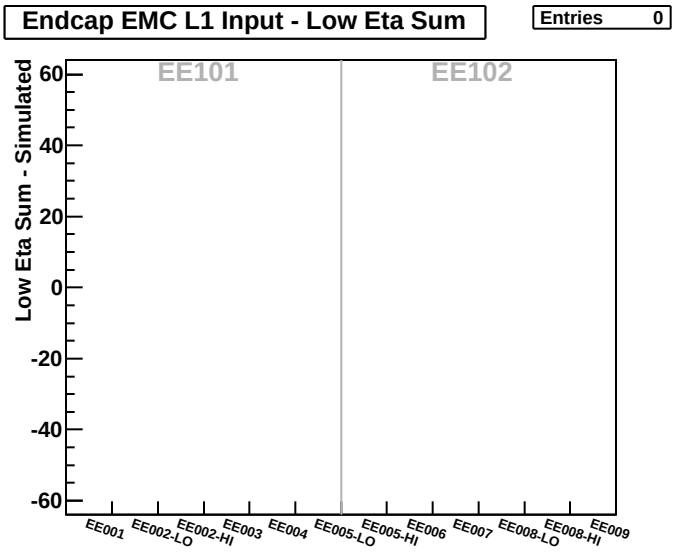

Barrel EMC L1 Input - High Eta Sum
Entries 1.281773e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

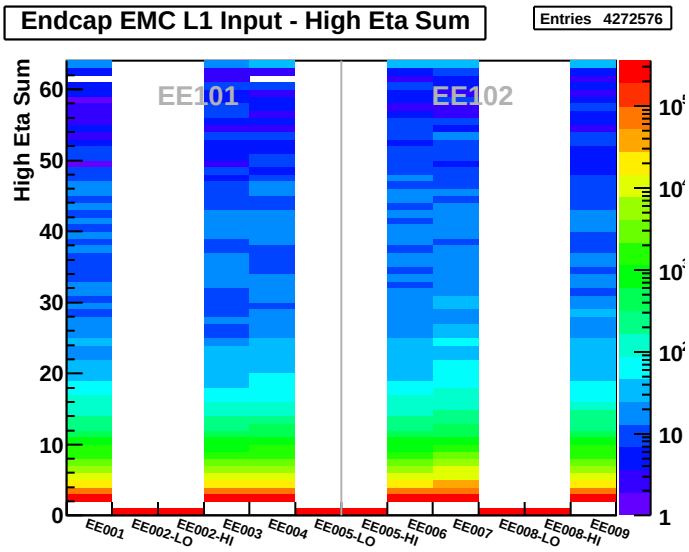

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

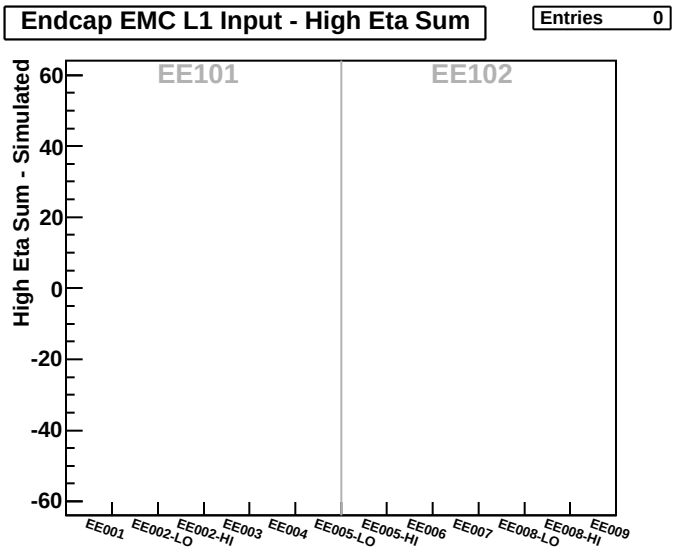

Barrel EMC L1 Input - High Tower Bits
Entries 1.281773e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

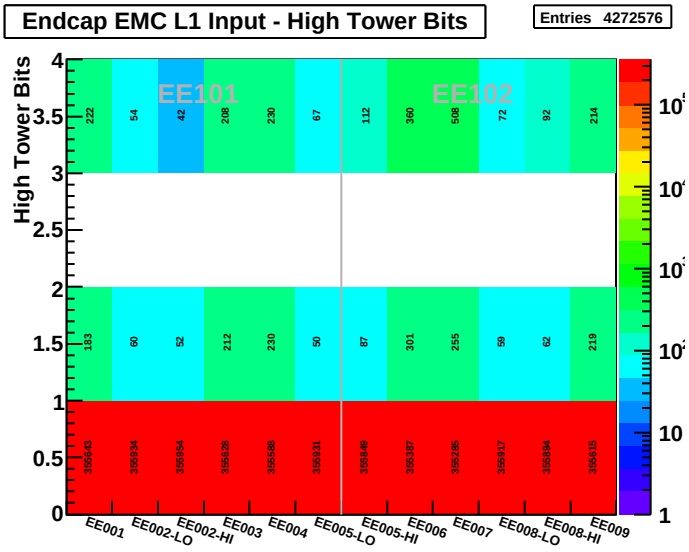

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

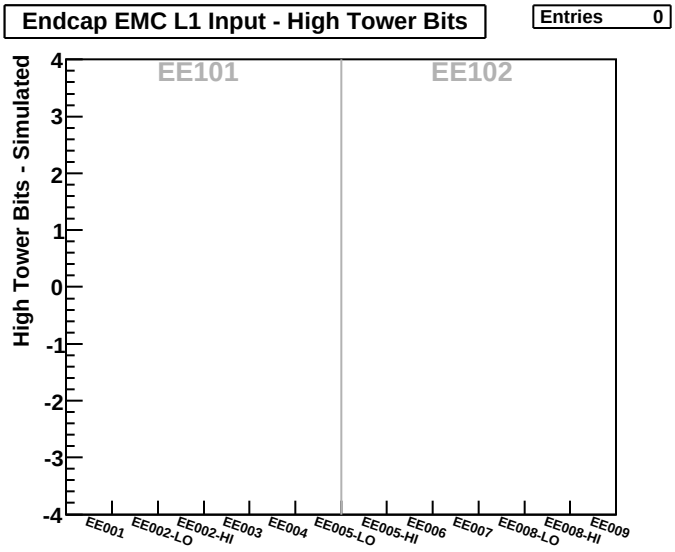

Endcap EMC L1 Input - Low Eta Sum
Entries 4272576
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

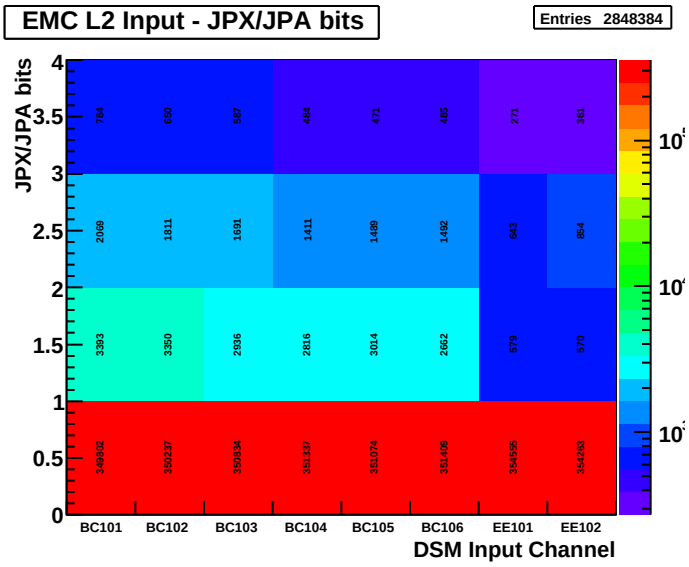

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

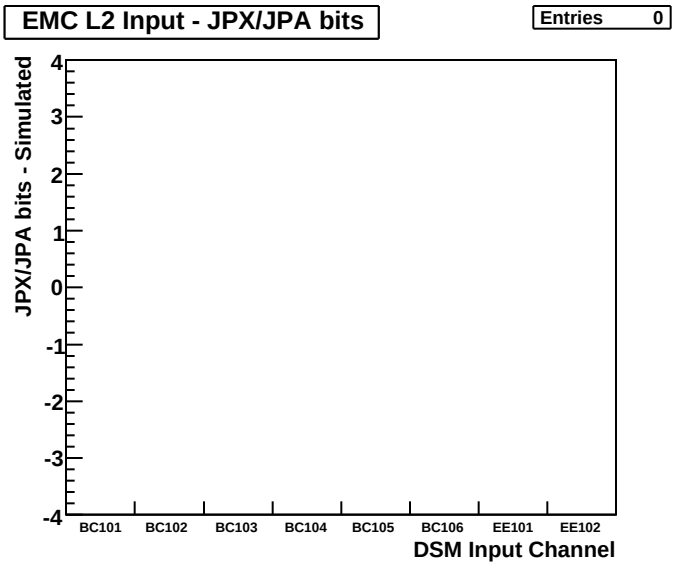

Endcap EMC L1 Input - High Eta Sum
Entries 4272576
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

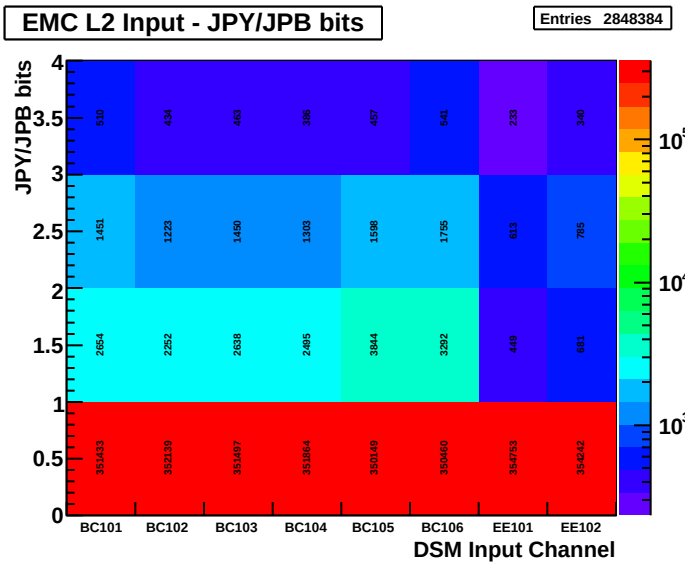

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

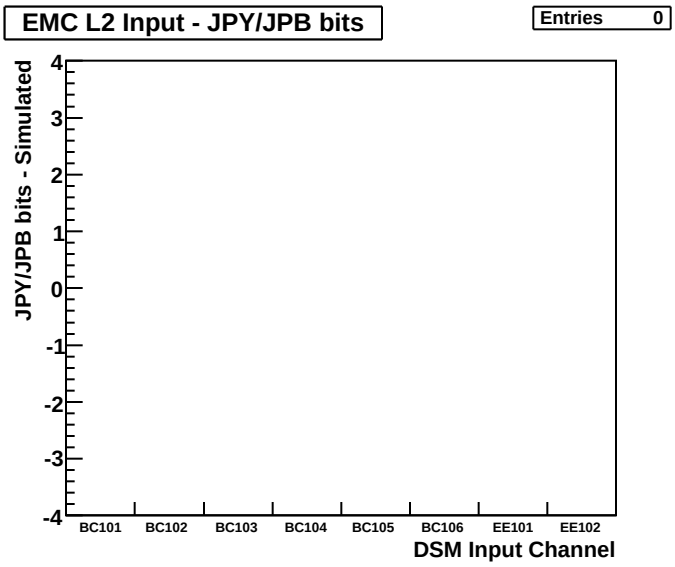

Endcap EMC L1 Input - High Tower Bits
Entries 4272576
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

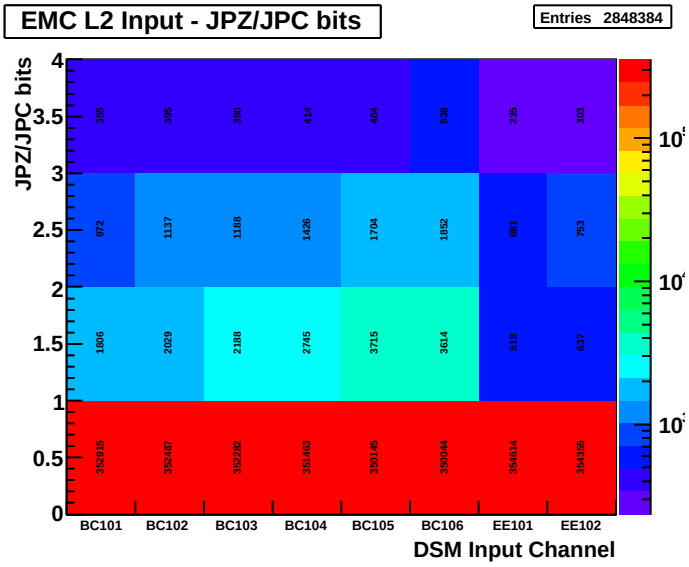

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

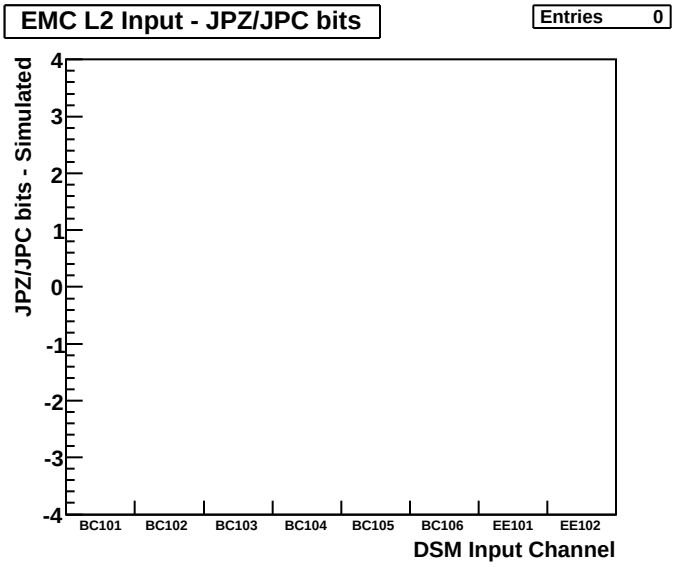

EMC L2 Input - JPX/JPA bits
Entries 2848384
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
784 690 587 484 471 485 271 361
2069 1811 1691 1411 1489 1492 643 854
3393 3350 2936 2816 3014 2662 576 570
348602 350237 350834 351337 351074 351409 354595 354263

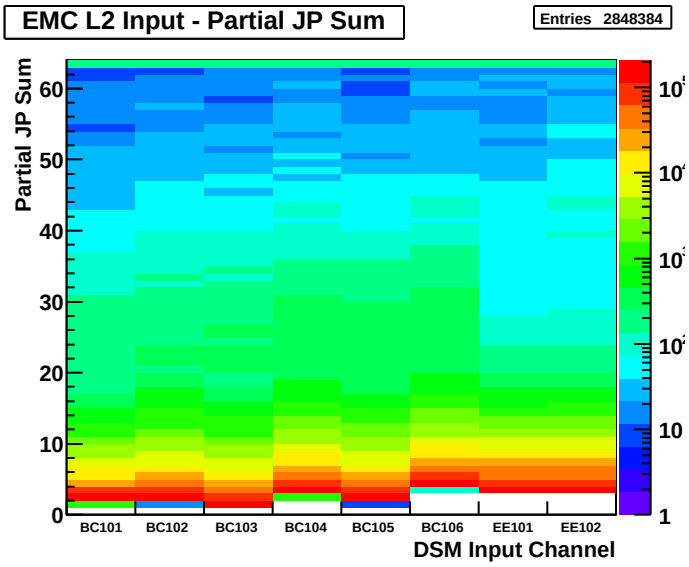

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

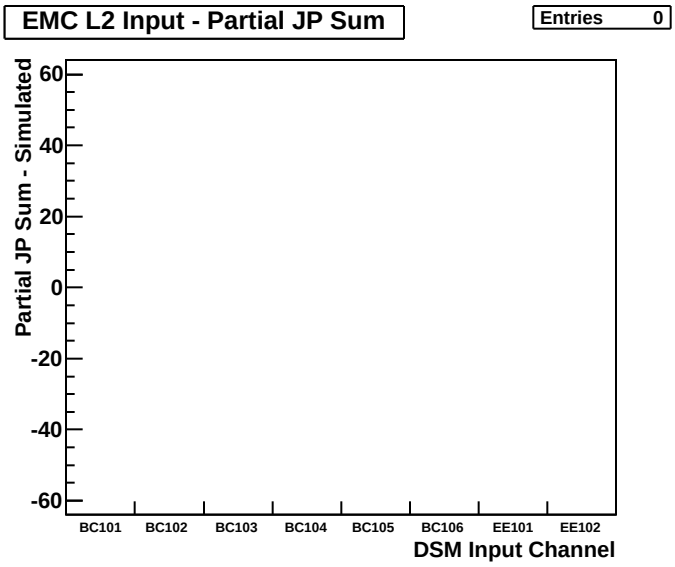

EMC L2 Input - JPY/JPB bits
Entries 2848384
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
510 434 468 386 457 541 233 340
1451 1223 1450 1303 1598 1755 613 785
2654 2252 2638 2495 3044 3292 449 631
351433 352139 351497 351864 350149 350460 354753 354242

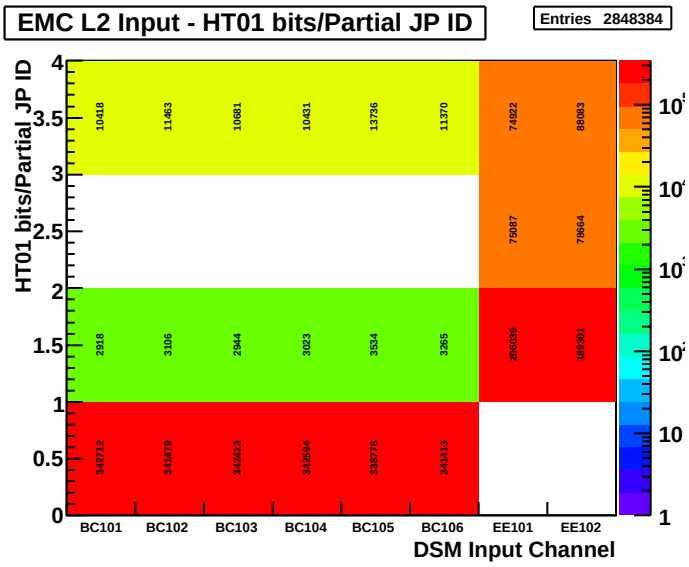

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

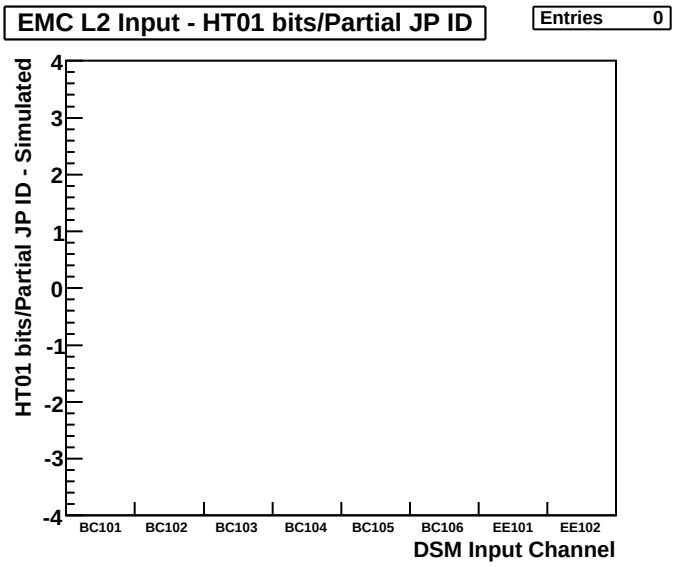

EMC L2 Input - JPZ/JPC bits
Entries 2848384
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
355 395 390 414 484 538 235 303
972 1137 1188 1426 1704 1852 631 753
1806 2029 2188 2745 3715 3614 518 637
352915 352487 352282 351463 350145 350044 354614 354355

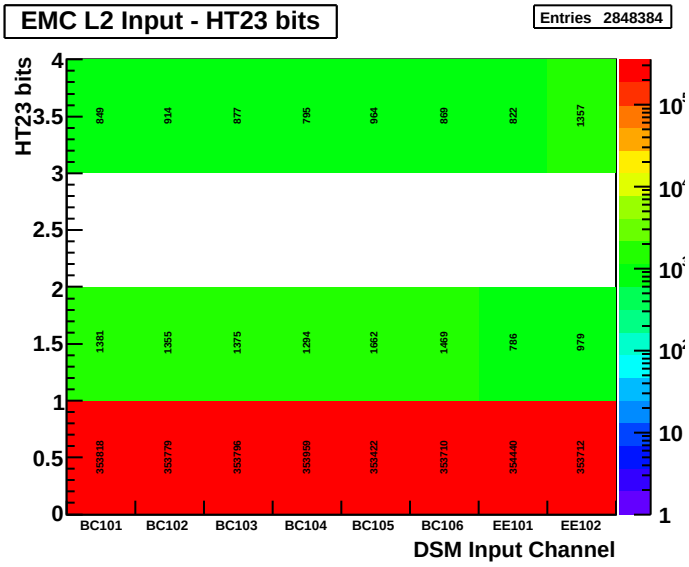

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

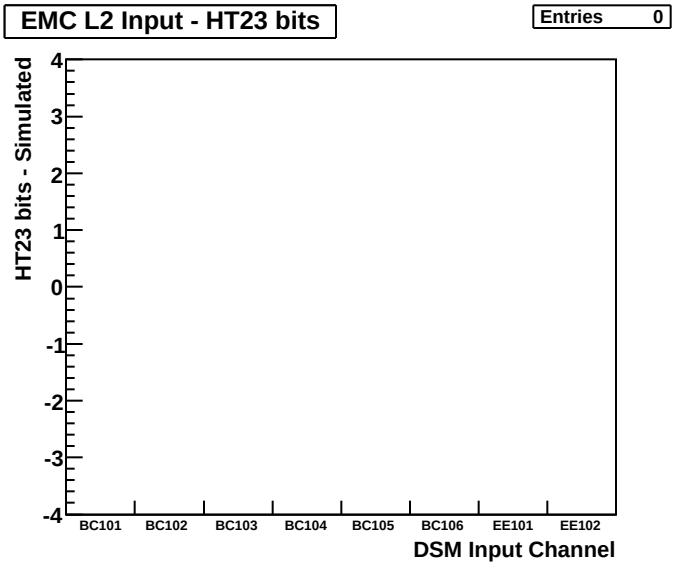

EMC L2 Input - Partial JP Sum
Entries 2848384
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

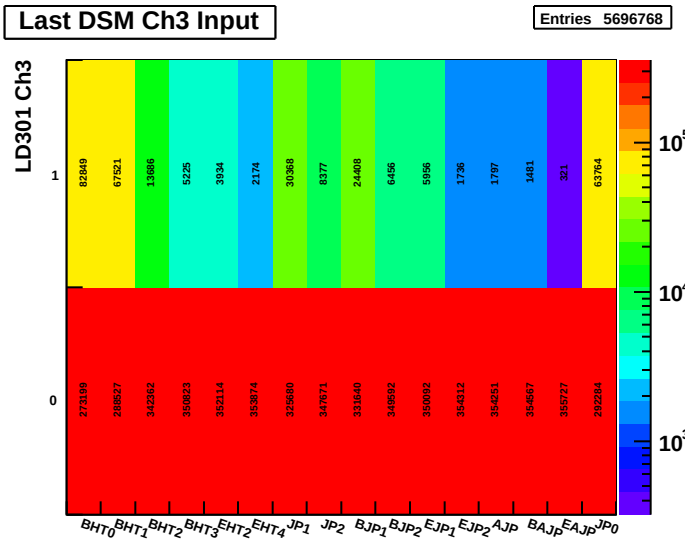

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

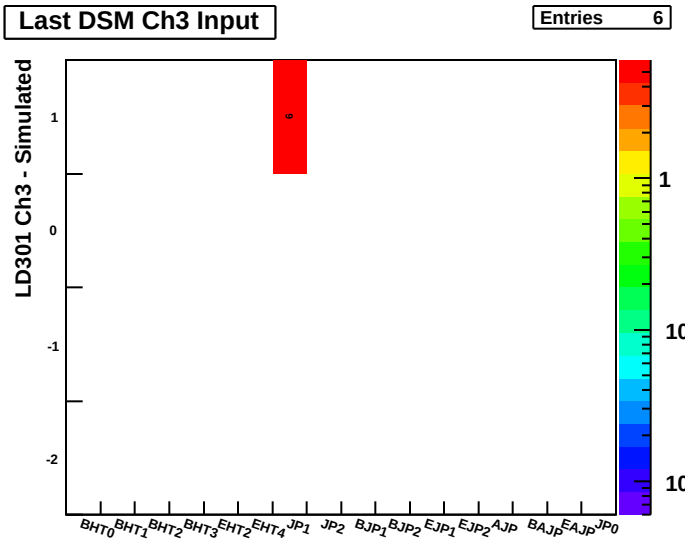

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2848384
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

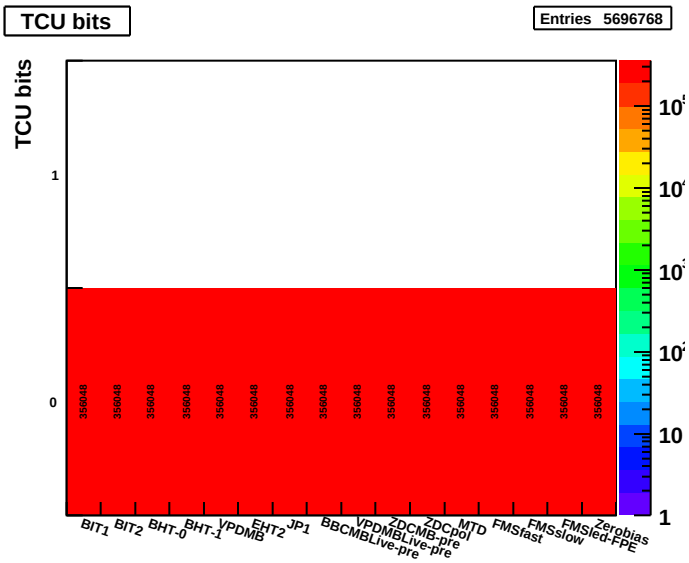

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

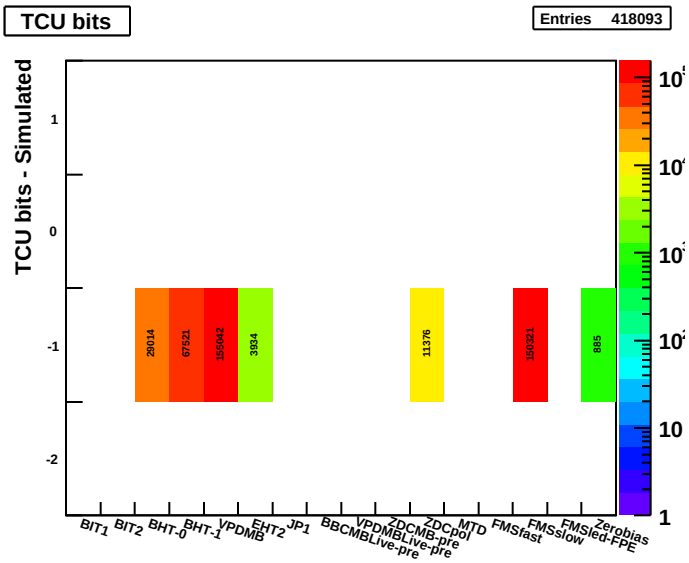

EMC L2 Input - HT23 bits
Entries 2848384
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

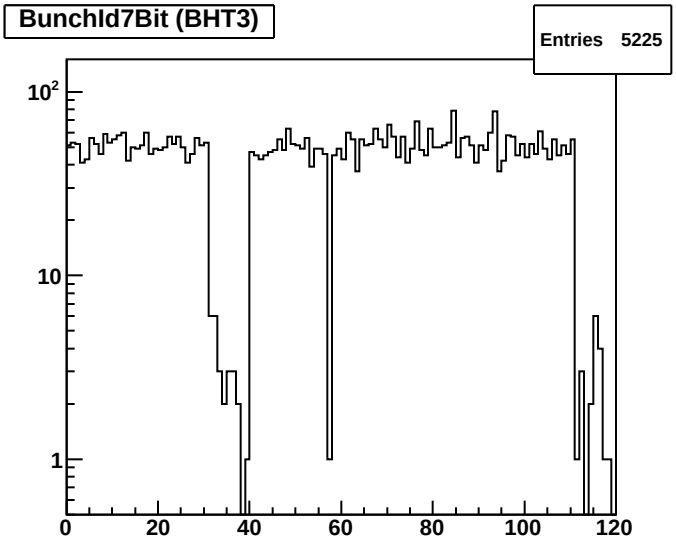

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

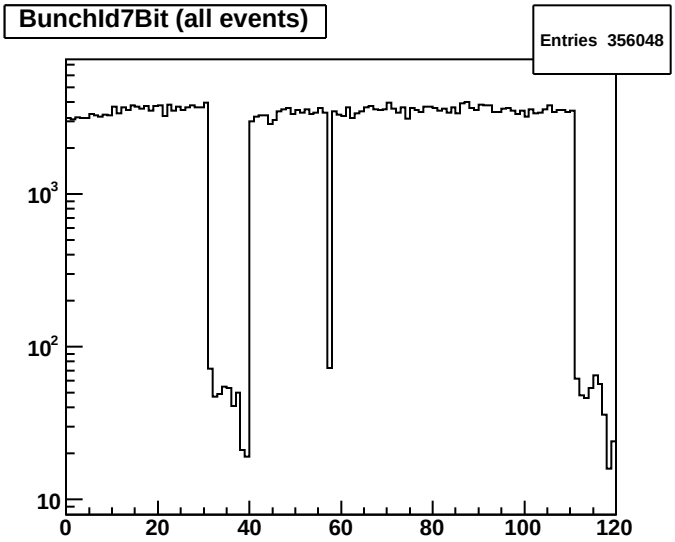

Last DSM Ch3 Input
Entries 5696768
LD301 Ch3
82849 67521 13686 5225 3934 2174 30368 8377 24408 6456 5956 1736 1797 1481 321 63764
273189 288527 342362 350823 352114 353874 325680 347671 331640 348532 350092 354312 354251 354567 355727 292284
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

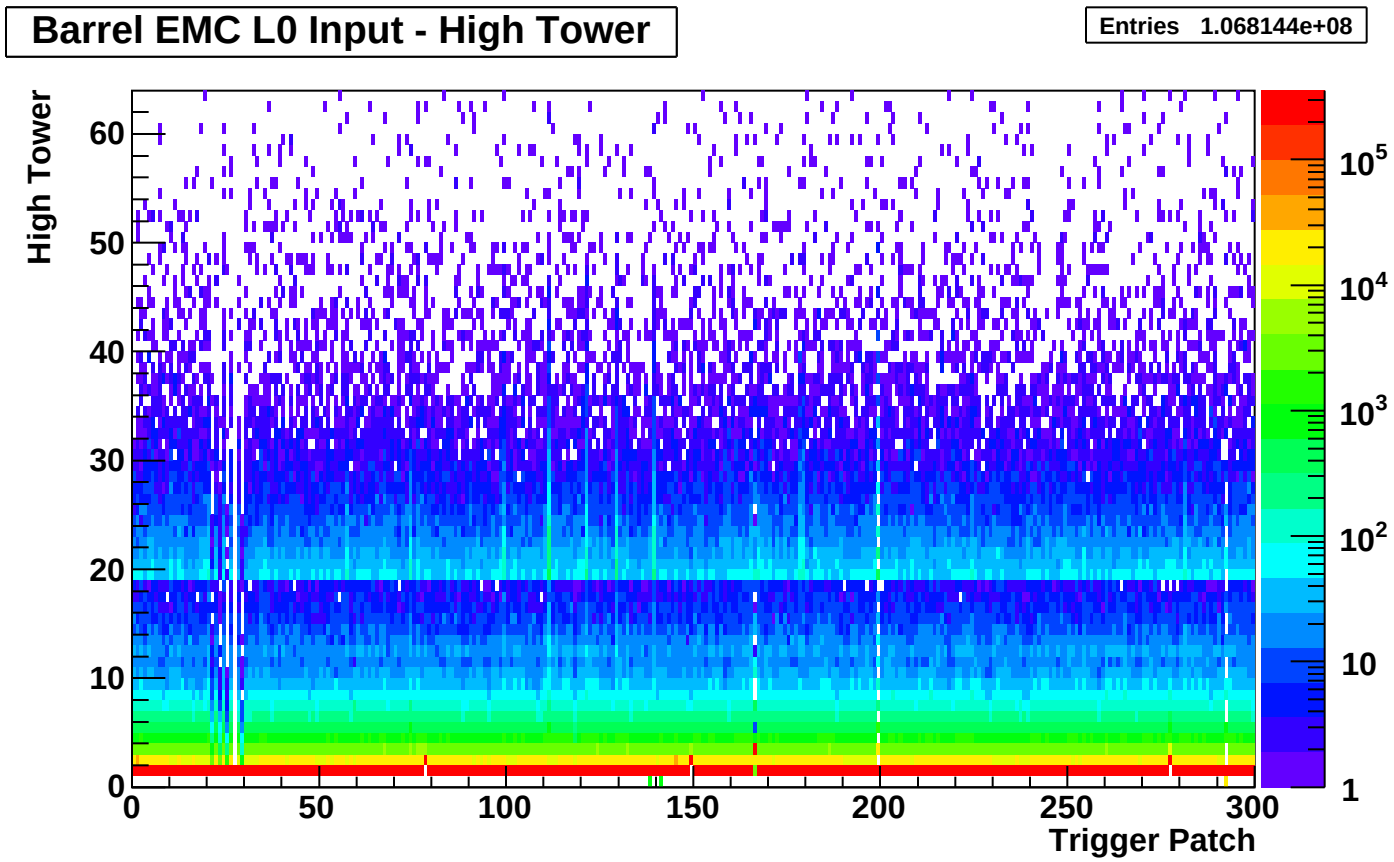

Last DSM Ch3 Input
Entries 6
LD301 Ch3 - Simulated
6
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

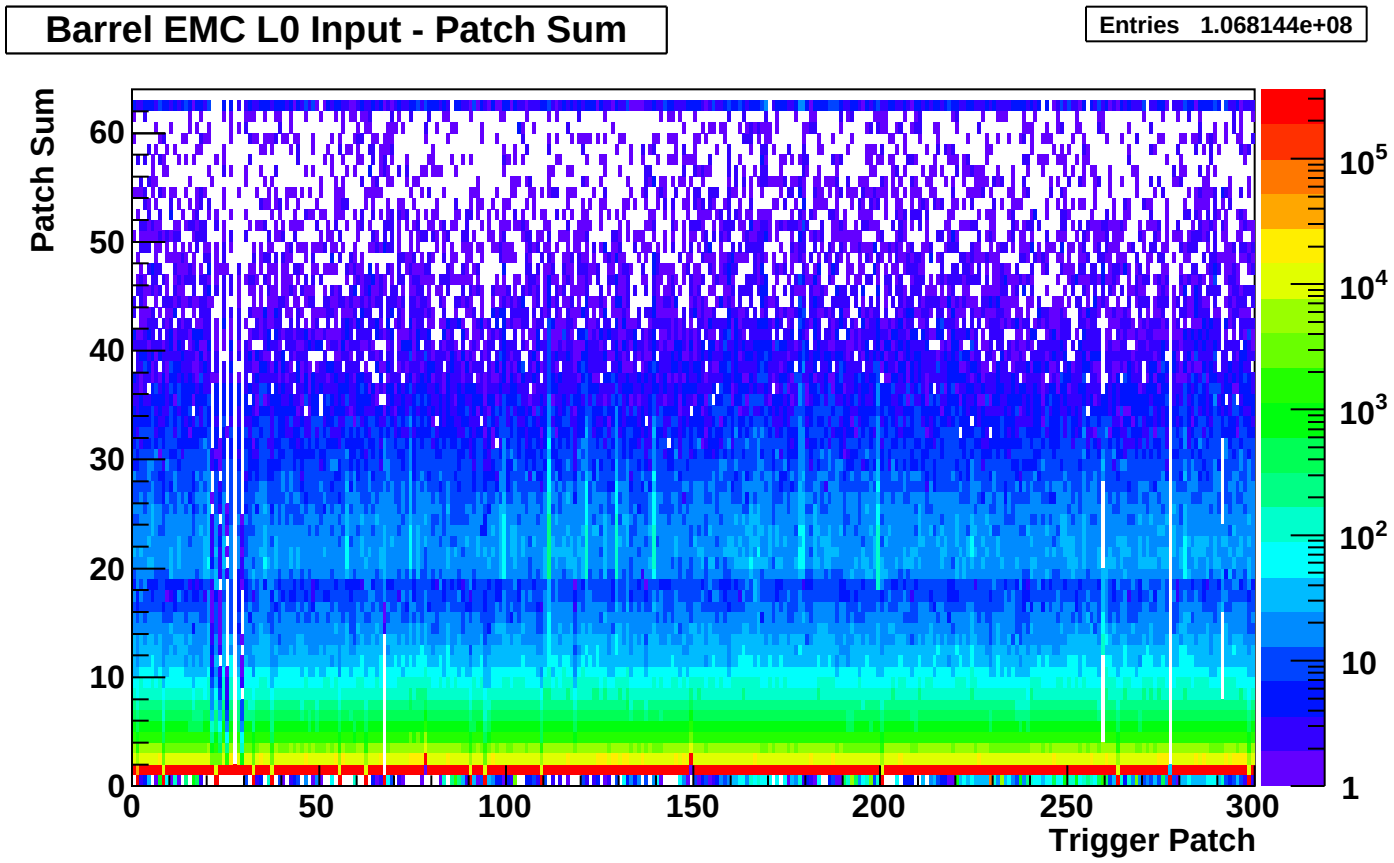

TCU bits
Entries 5696768
TCU bits
356048 356048 356048 356048 356048 356048 356048 356048 356048 356048 356048 356048 356048 356048 356048 356048
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

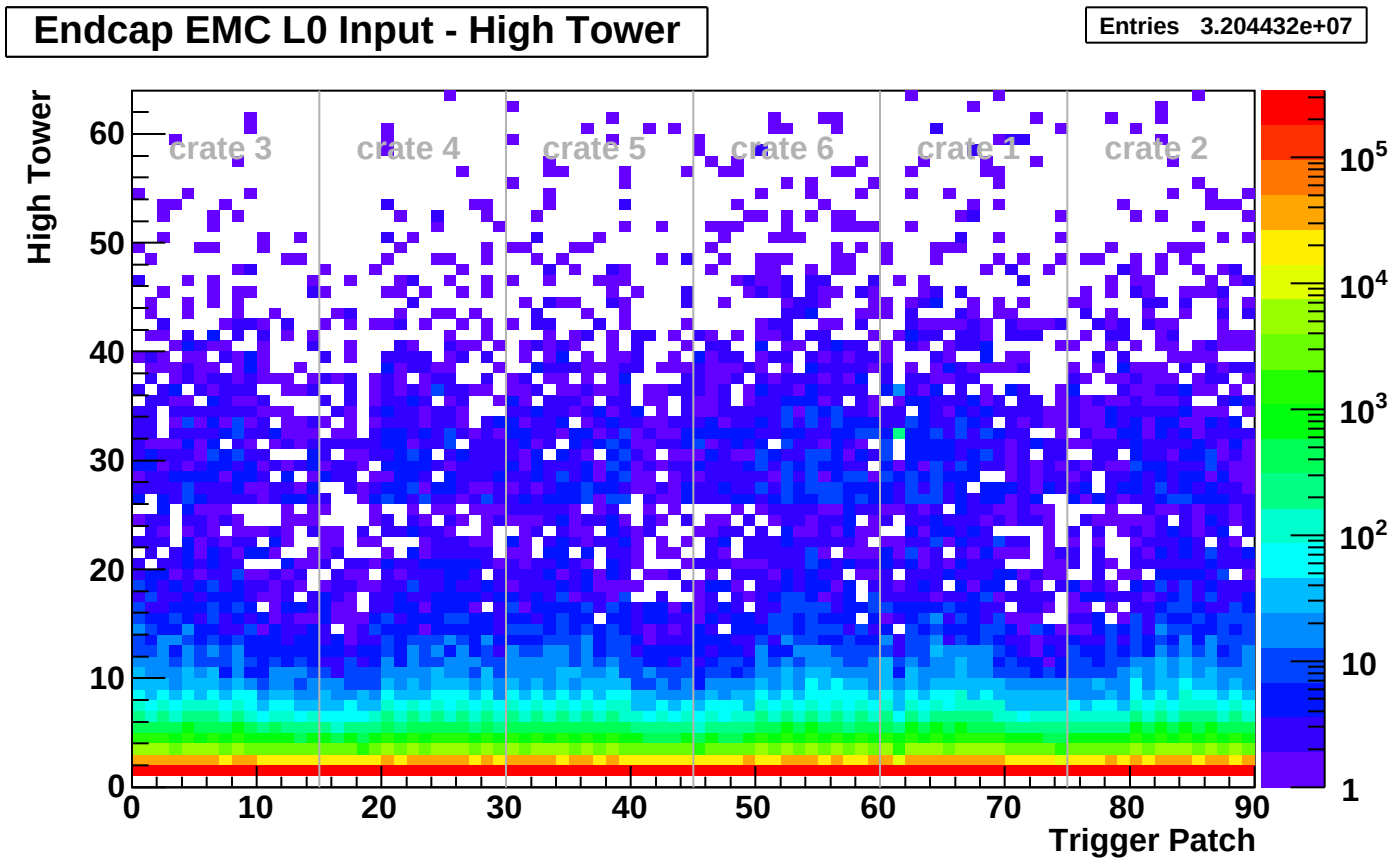

TCU bits
Entries 418093
TCU bits - Simulated
29014 67521 155042 3934 11376 150321 885
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

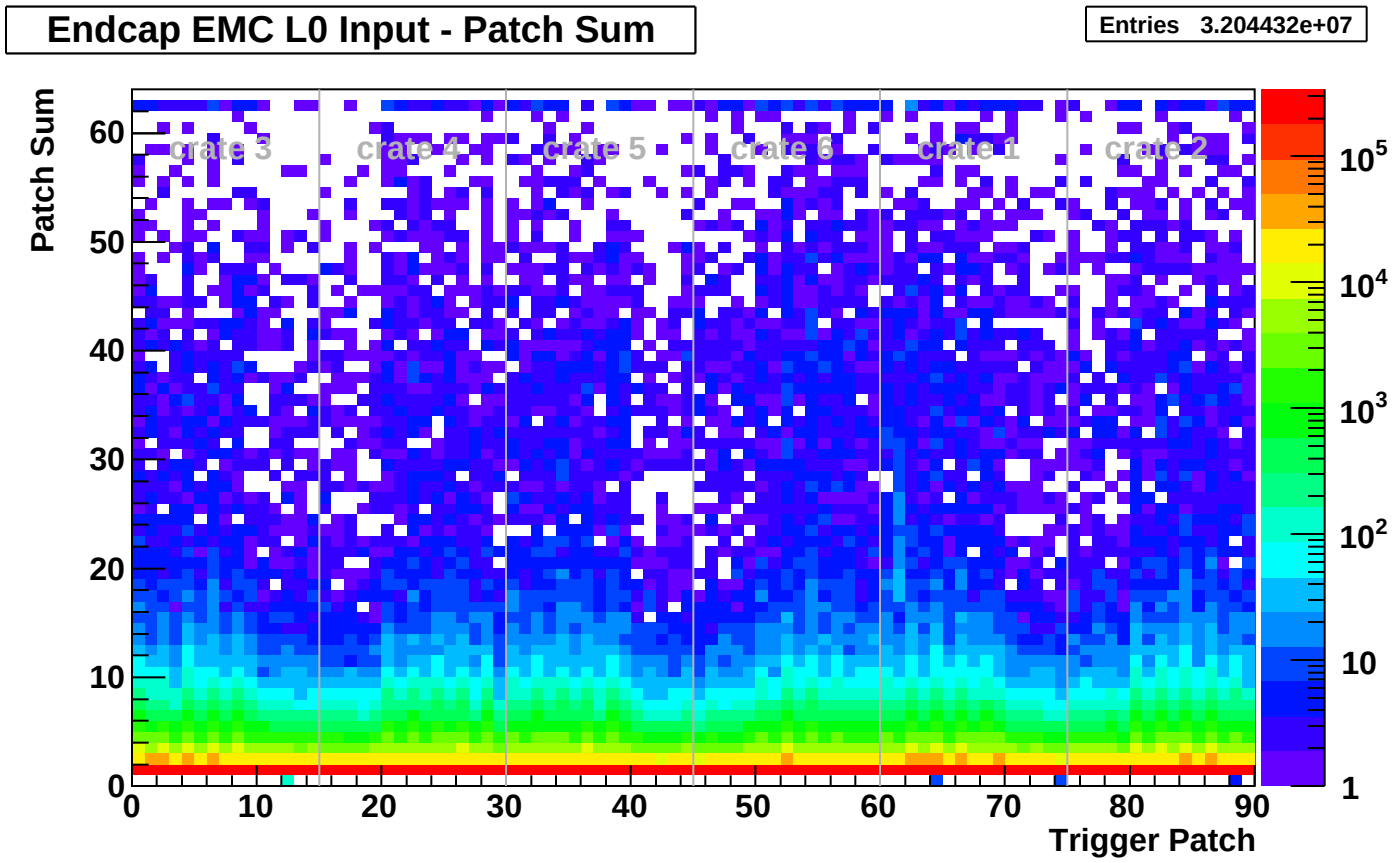

BunchId7Bit (BHT3)
Entries 5225

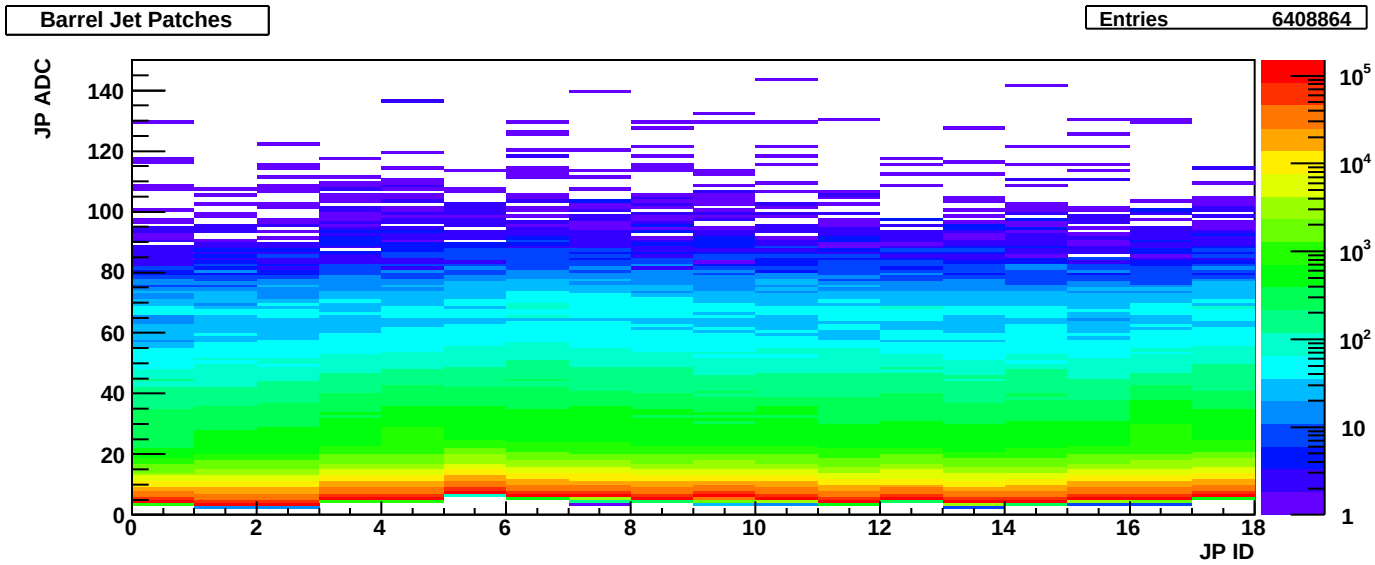

BunchId7Bit (all events)
Entries 356048

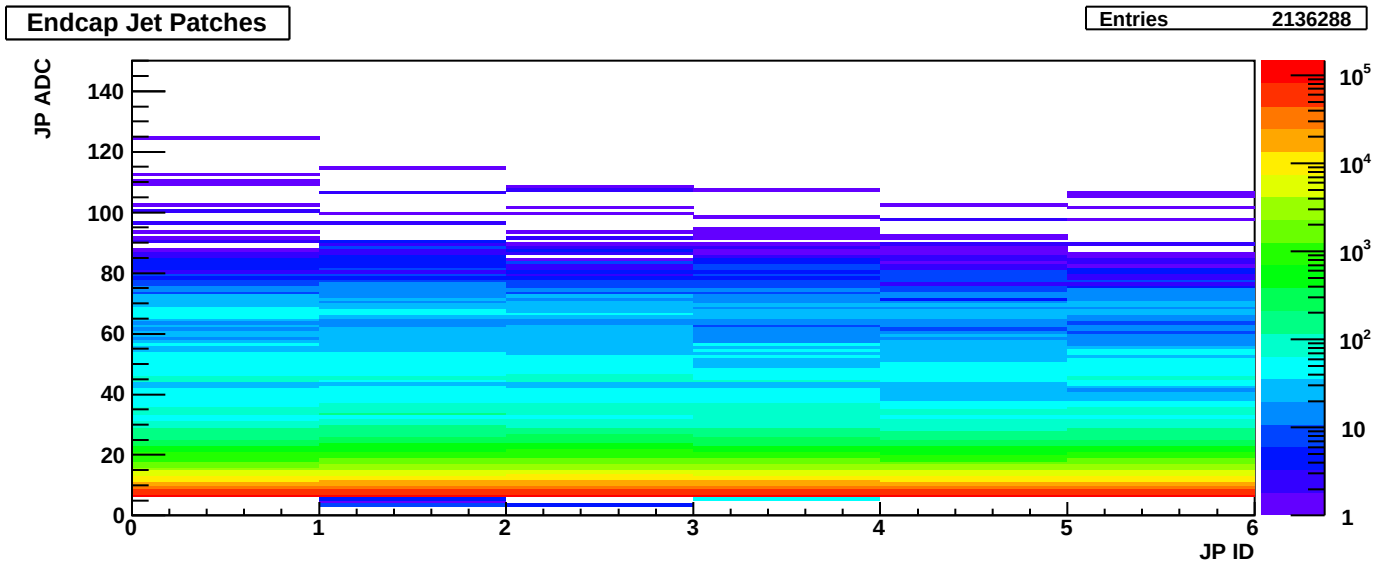

Barrel EMC L0 Input - High Tower
Entries 1.068144e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

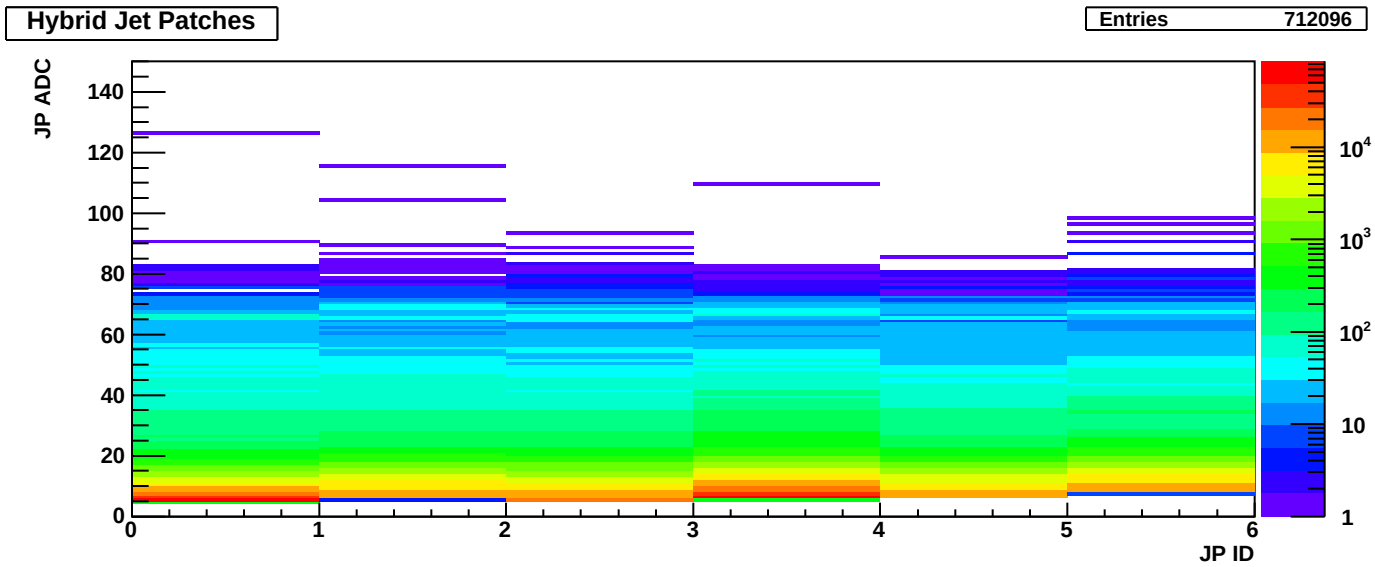

Barrel EMC L0 Input - Patch Sum
Entries 1.068144e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

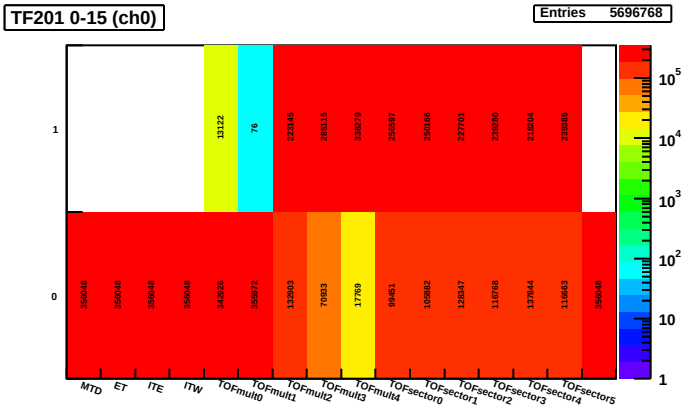

Endcap EMC L0 Input - High Tower
Entries 3.204432e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

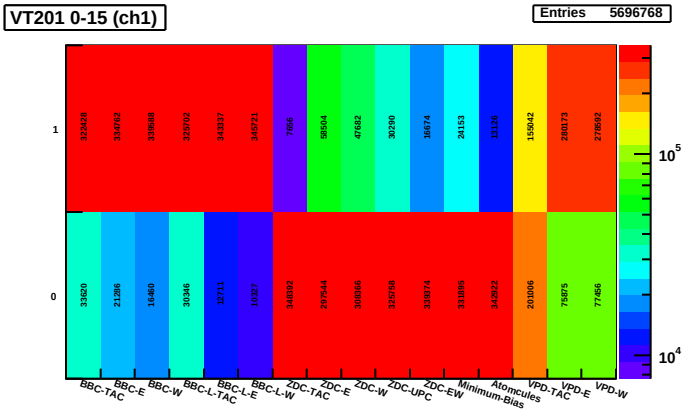

Endcap EMC L0 Input - Patch Sum
Entries 3.204432e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

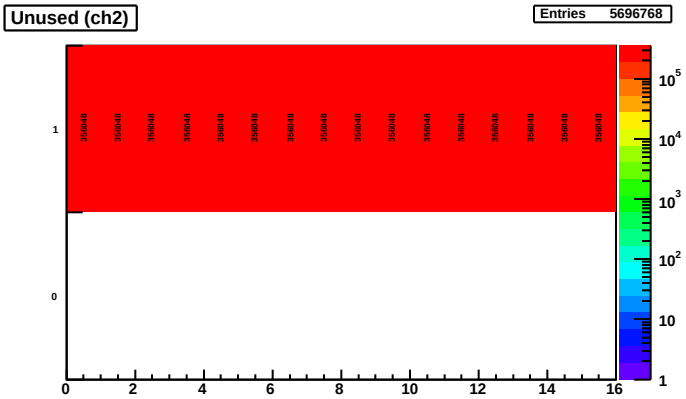

Barrel Jet Patches
Entries 6408864
JP ADC
JP ID

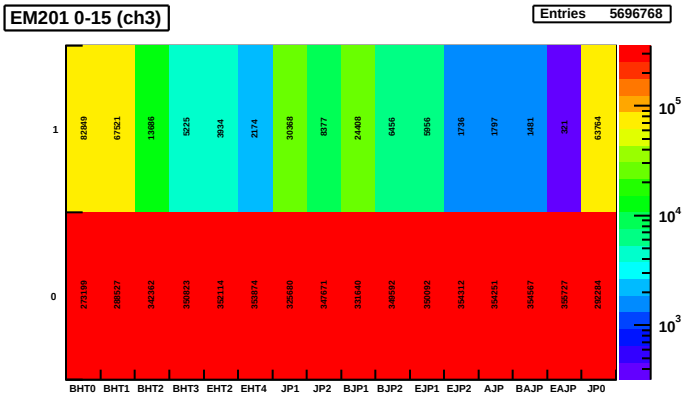

Endcap Jet Patches
Entries 2136288
JP ADC
JP ID

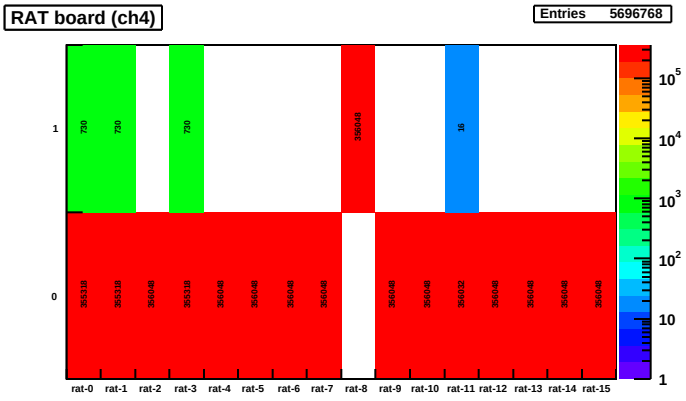

Hybrid Jet Patches
Entries 712096
JP ADC
JP ID

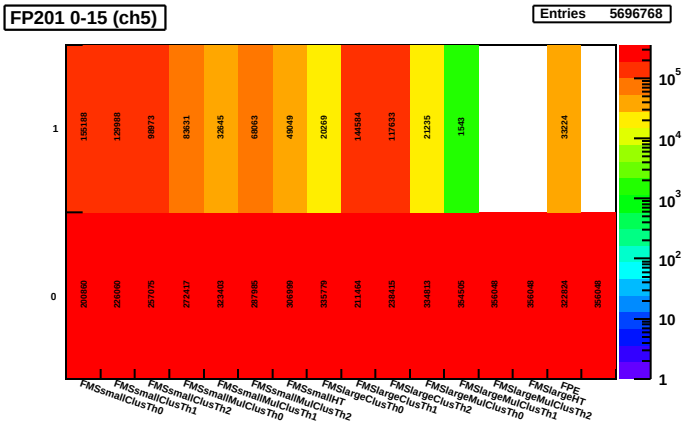

TF201 0-15 (ch0)
Entries 5696768
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

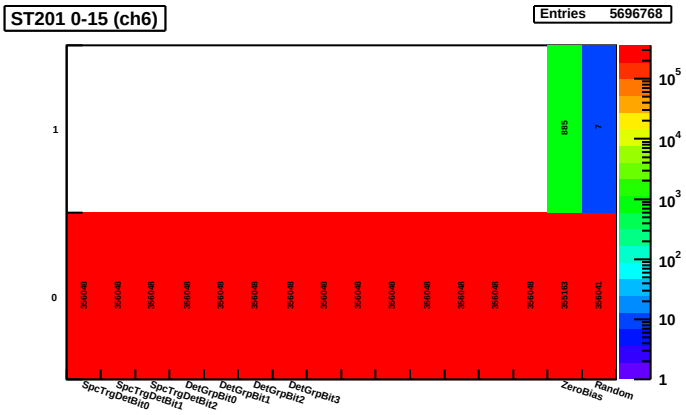

VT201 0-15 (ch1)
Entries 5696768
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

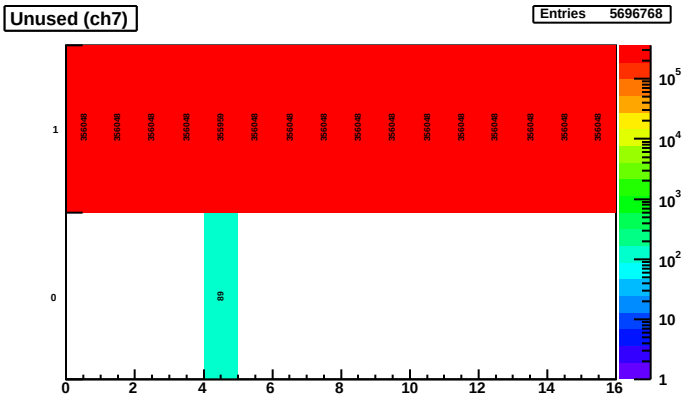

Unused (ch2)
Entries 5696768

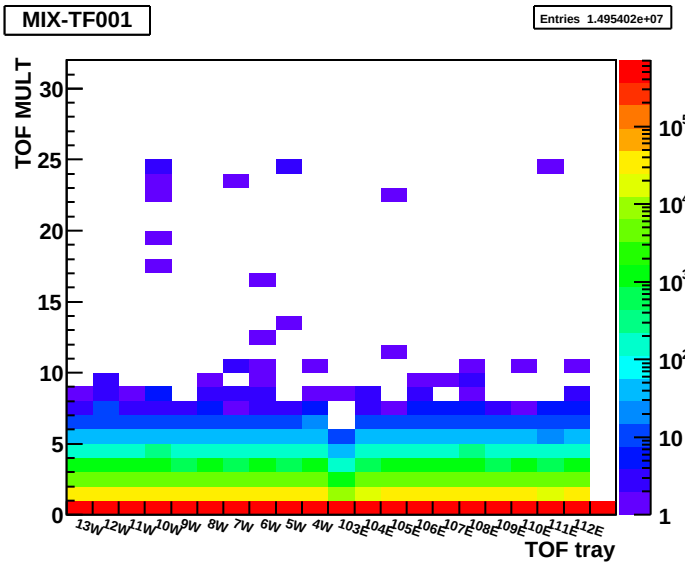

EM201 0-15 (ch3)
Entries 5696768
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

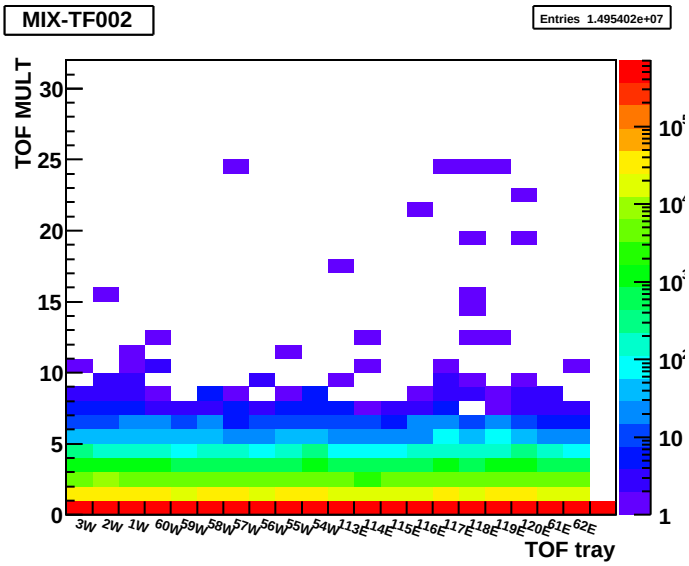

RAT board (ch4)
Entries 5696768
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

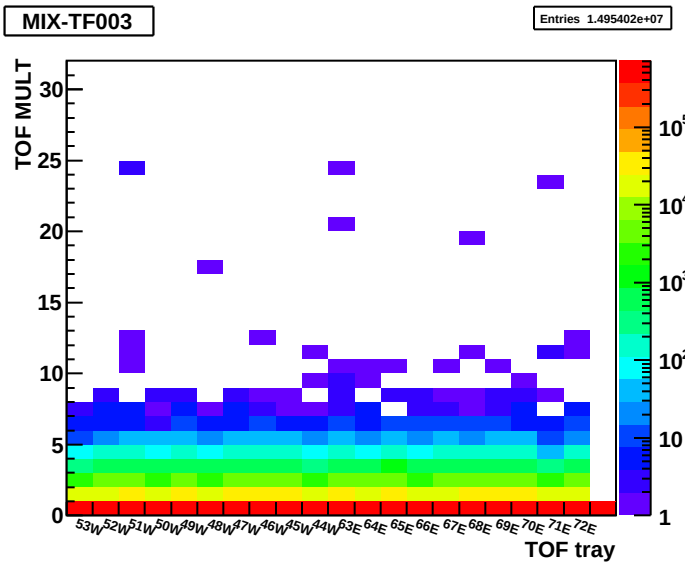

FP201 0-15 (ch5)
Entries 5696768
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

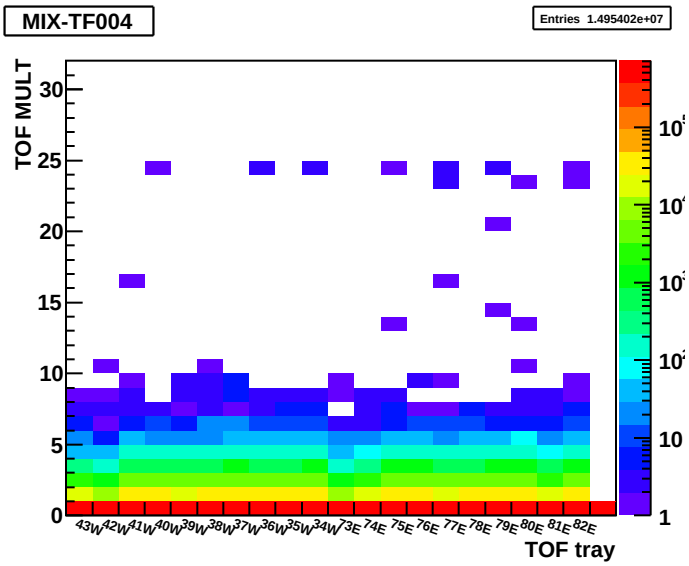

ST201 0-15 (ch6)
Entries 5696768
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

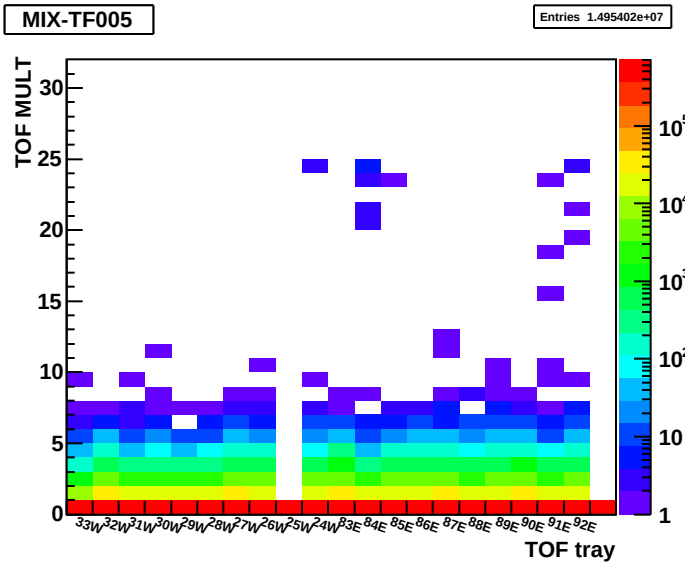

Unused (ch7)
Entries 5696768

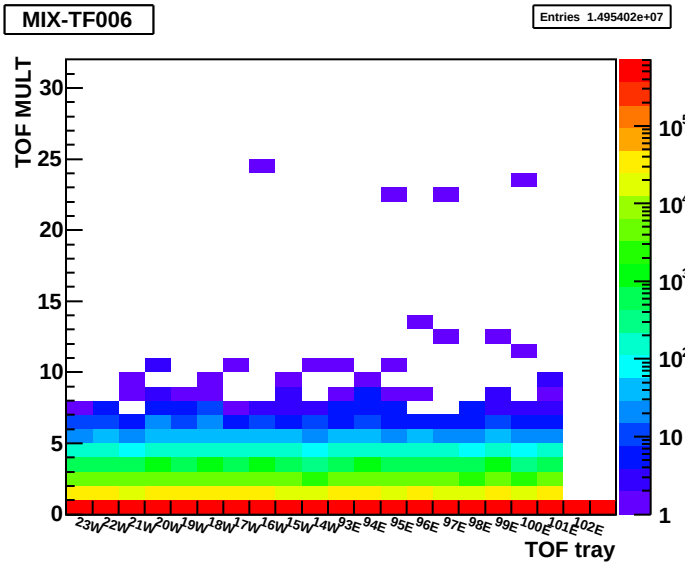

MIX-TF001
Entries 1.495402e+07
TOF MULT
TOF tray

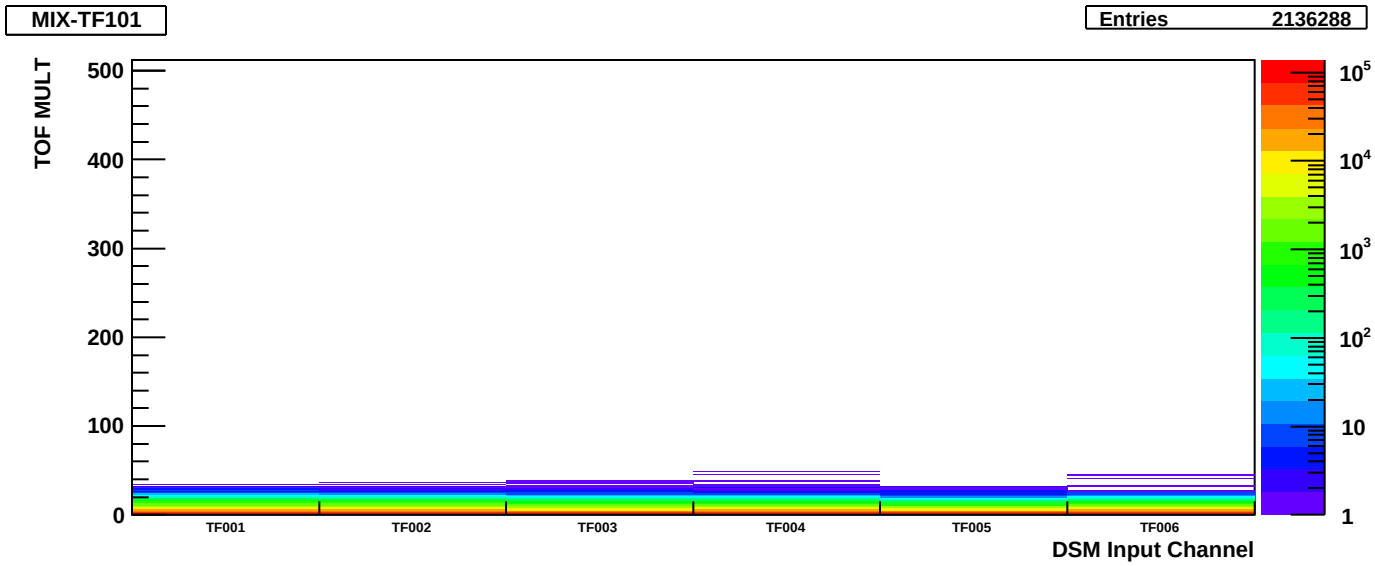

MIX-TF002
Entries 1.495402e+07
TOF MULT
TOF tray

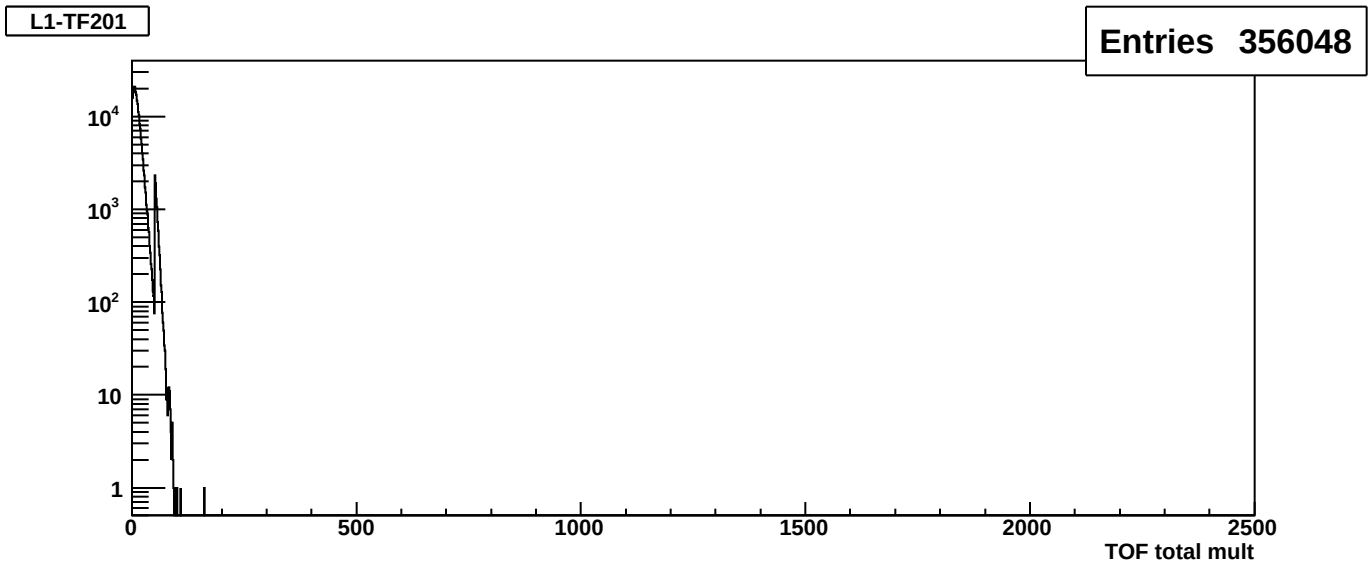

MIX-TF003
Entries 1.495402e+07
TOF MULT
TOF tray

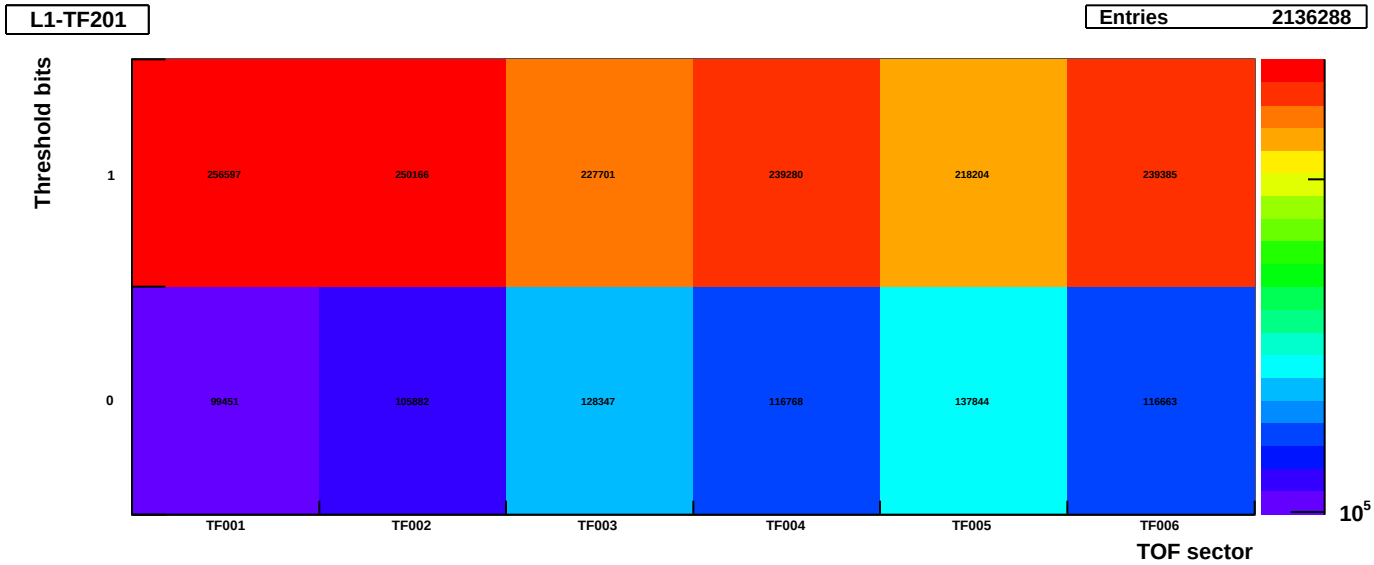

MIX-TF004
Entries 1.495402e+07
TOF MULT
TOF tray

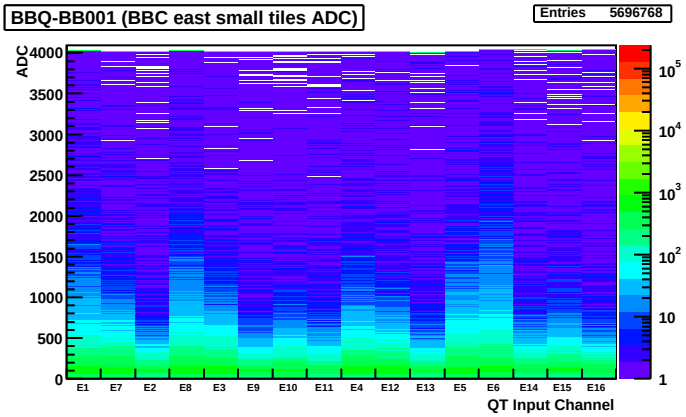

MIX-TF005
Entries 1.495402e+07
TOF MULT
TOF tray

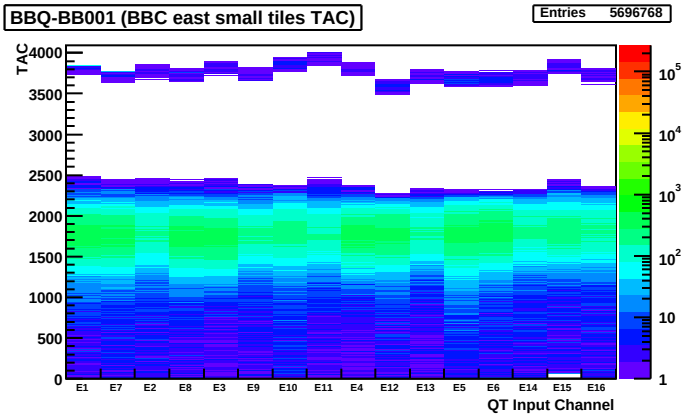

MIX-TF006
Entries 1.495402e+07
TOF MULT
TOF tray

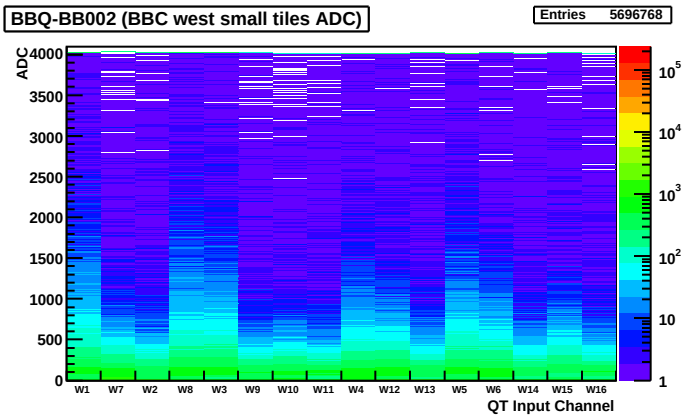

MIX-TF101
Entries 2136288
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

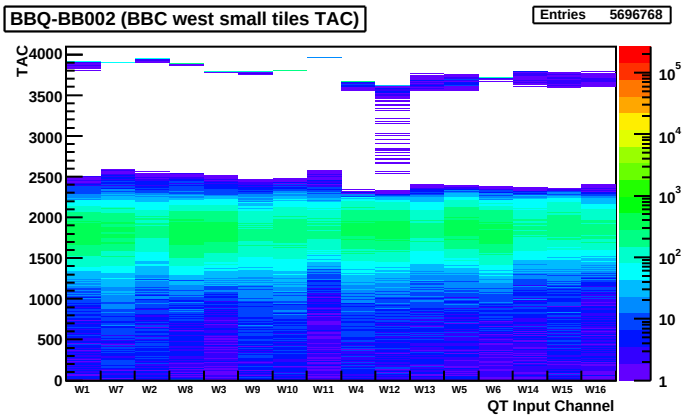

L1-TF201
Entries 356048
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

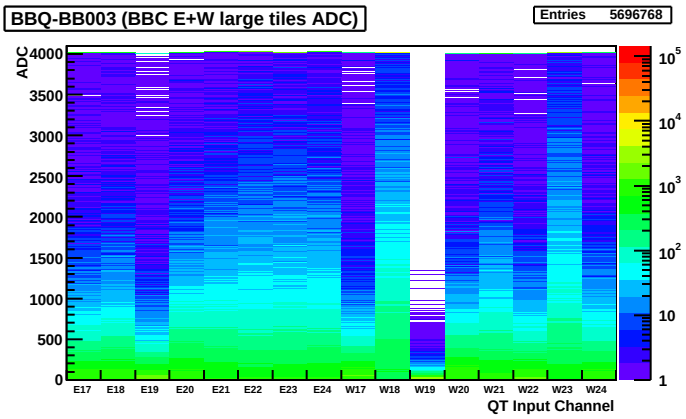

L1-TF201
Entries 2136288
Threshold bits
1
0
256597
250166
227701
239280
218204
239385
99451
105882
128347
116768
137844
116663
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

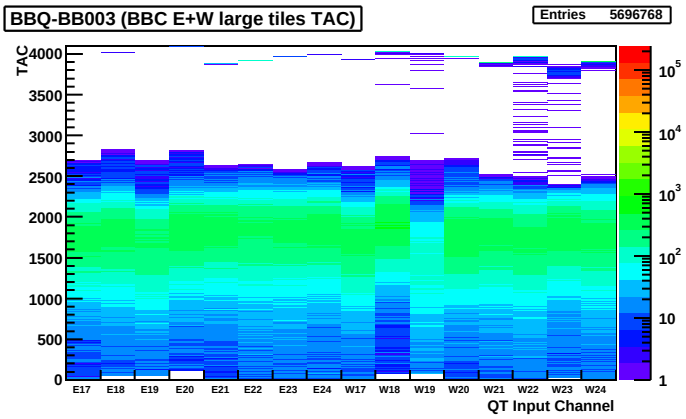

BBQ-BB001 (BBC east small tiles ADC)
Entries 5696768
ADC
QT Input Channel

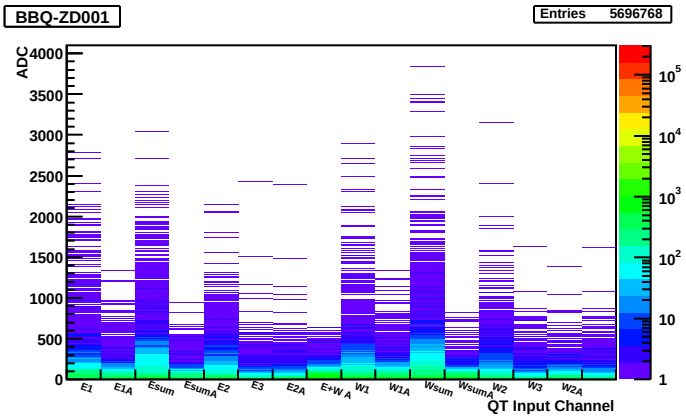

BBQ-BB001 (BBC east small tiles TAC)
Entries 5696768
TAC
QT Input Channel

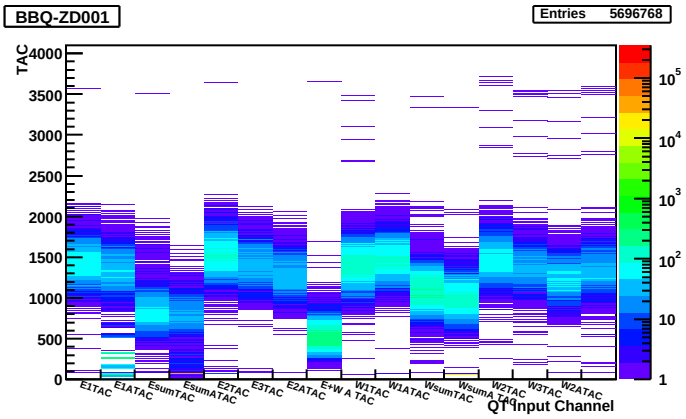

BBQ-BB002 (BBC west small tiles ADC)
Entries 5696768
ADC
QT Input Channel

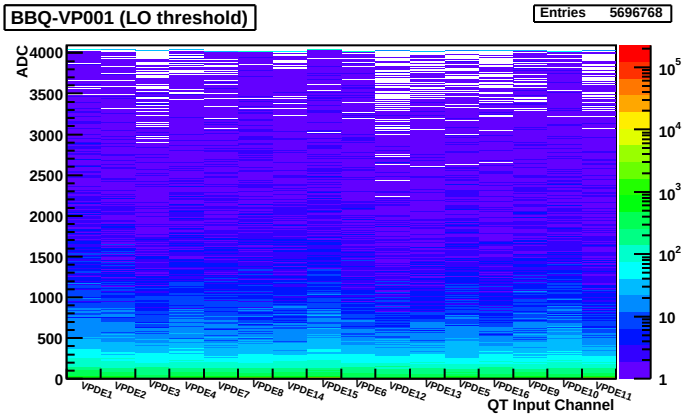

BBQ-BB002 (BBC west small tiles TAC)
Entries 5696768
TAC
QT Input Channel

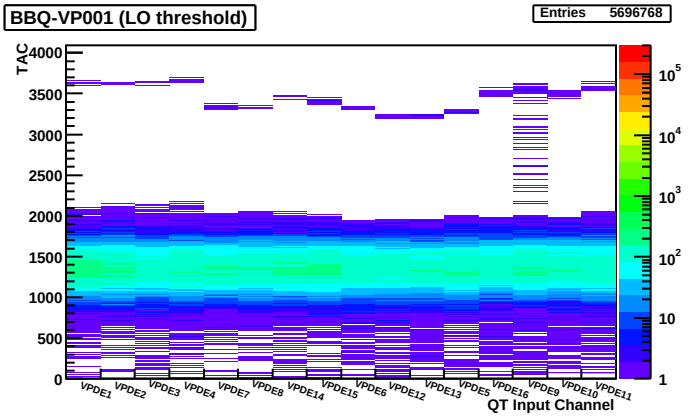

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5696768
ADC
QT Input Channel

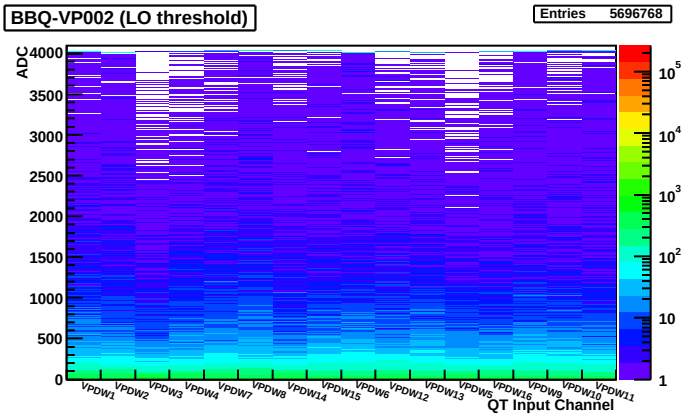

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5696768
TAC
QT Input Channel

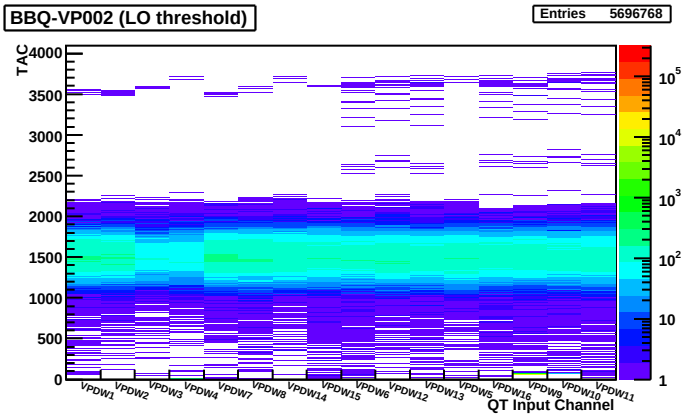

BBQ-ZD001
Entries 5696768
ADC
QT Input Channel

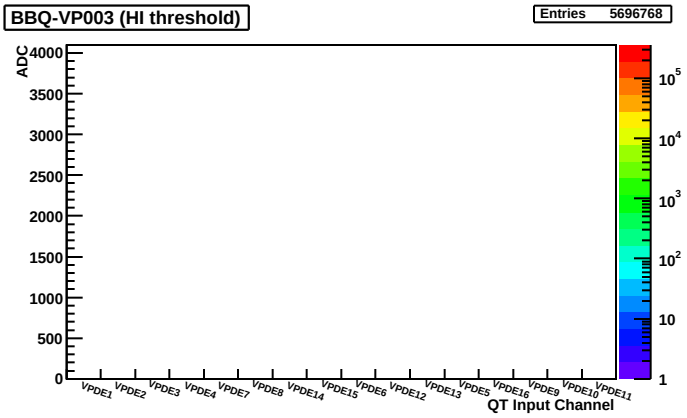

BBQ-ZD001
Entries 5696768
TAC
QT Input Channel

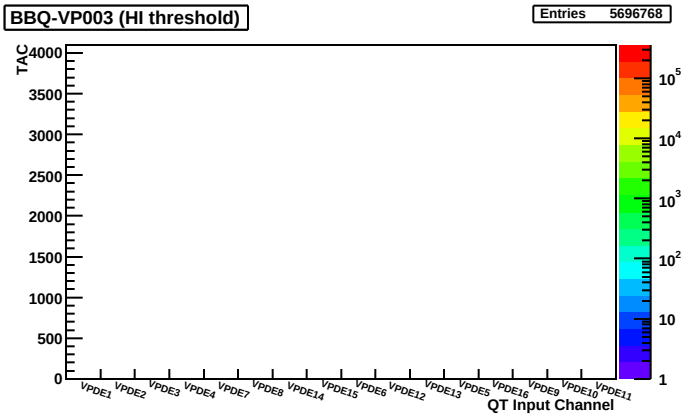

BBQ-VP001 (LO threshold)
Entries 5696768
ADC
QT Input Channel

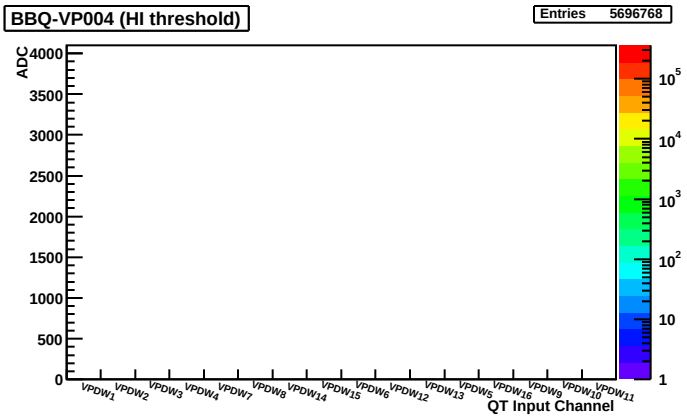

BBQ-VP001 (LO threshold)
Entries 5696768
TAC
QT Input Channel

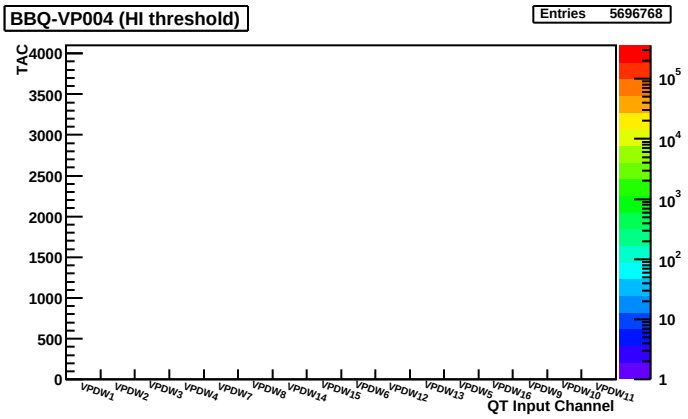

BBQ-VP002 (LO threshold)
Entries 5696768
ADC
QT Input Channel

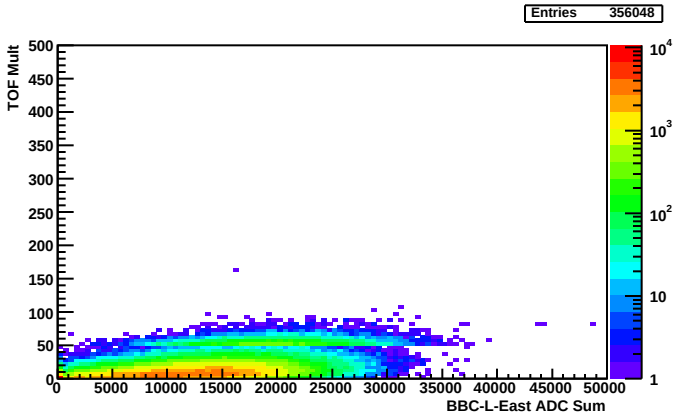

BBQ-VP002 (LO threshold)
Entries 5696768
TAC
QT Input Channel

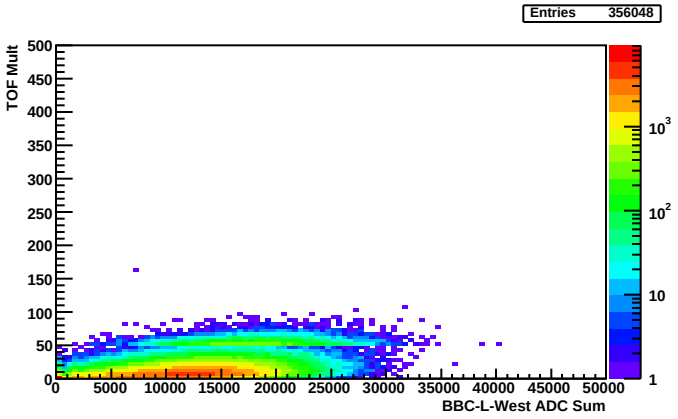

BBQ-VP003 (HI threshold)
Entries 5696768
ADC
QT Input Channel

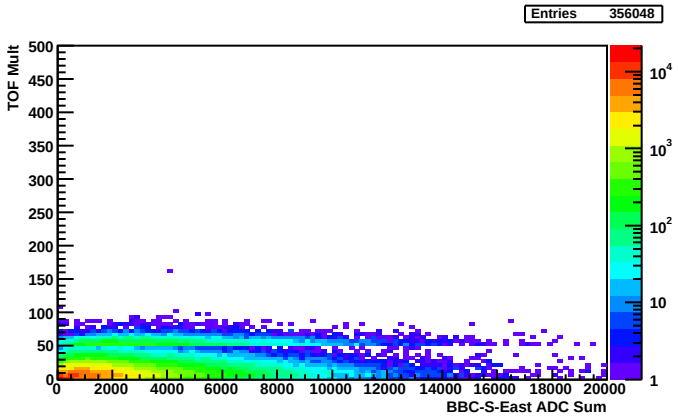

BBQ-VP003 (HI threshold)
Entries 5696768
TAC
QT Input Channel

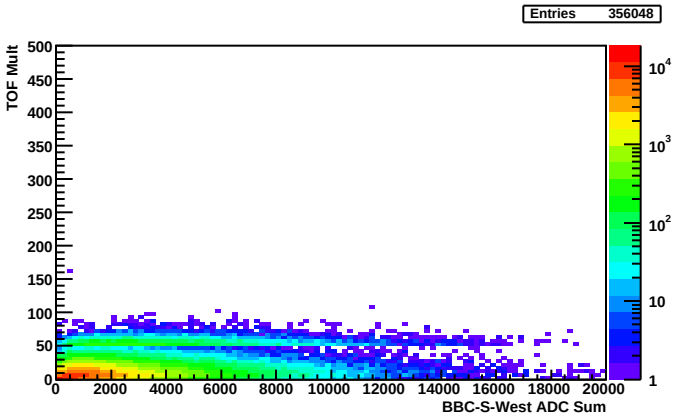

BBQ-VP004 (HI threshold)
Entries 5696768
ADC
QT Input Channel

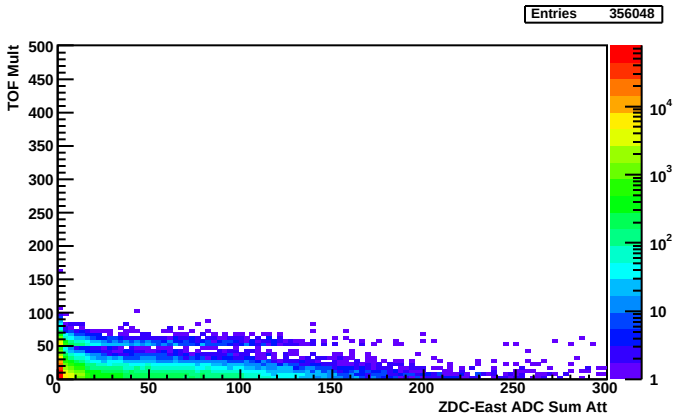

BBQ-VP004 (HI threshold)
Entries 5696768
TAC
QT Input Channel

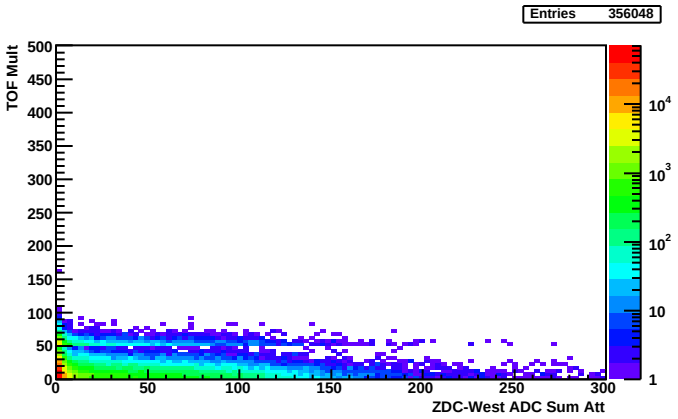

Entries 356048
TOF Mult
BBC-L-East ADC Sum

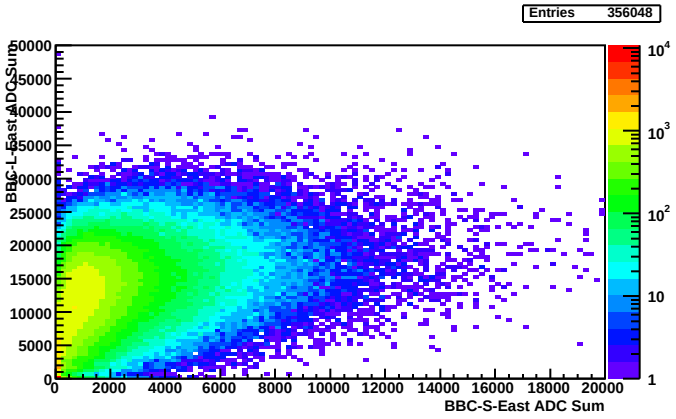

Entries 356048
TOF Mult
BBC-L-West ADC Sum

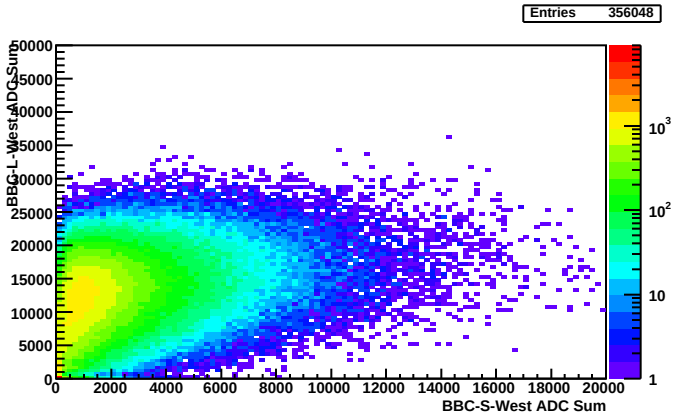

Entries 356048
TOF Mult
BBC-S-East ADC Sum

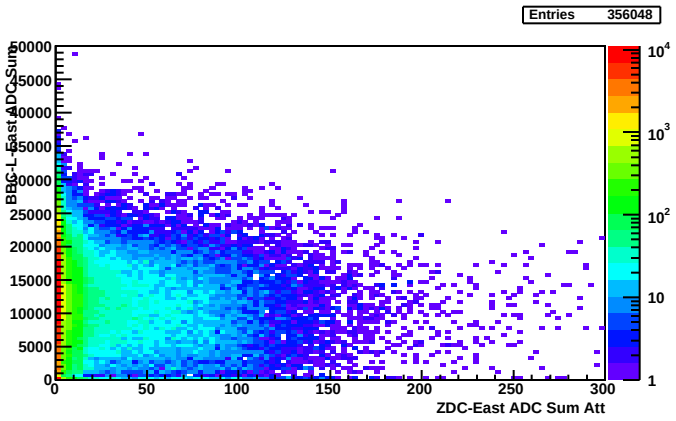

Entries 356048
TOF Mult
BBC-S-West ADC Sum

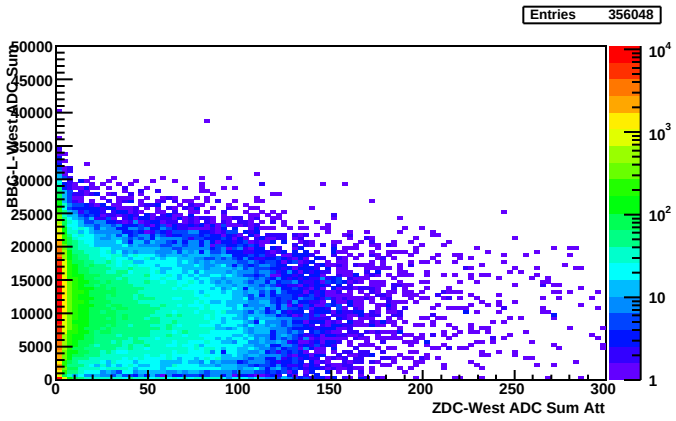

Entries 356048
TOF Mult
ZDC-East ADC Sum Att

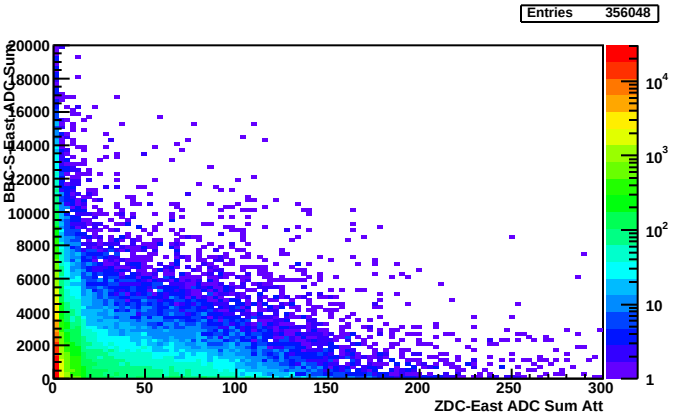

Entries 356048
TOF Mult
ZDC-West ADC Sum Att

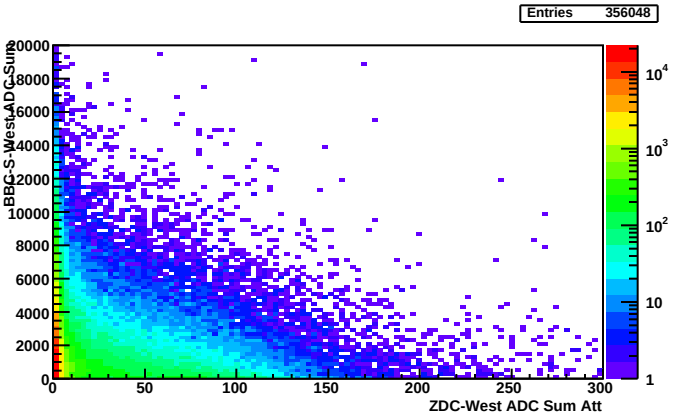

Entries 356048
BBC-L-East ADC Sum
BBC-S-East ADC Sum

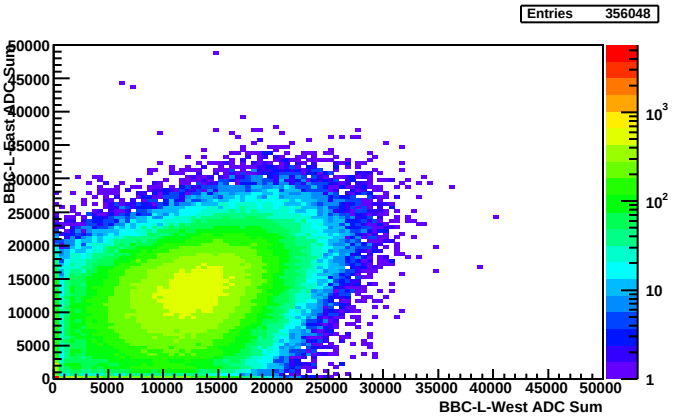

Entries 356048
BBC-L-West ADC Sum
BBC-S-West ADC Sum

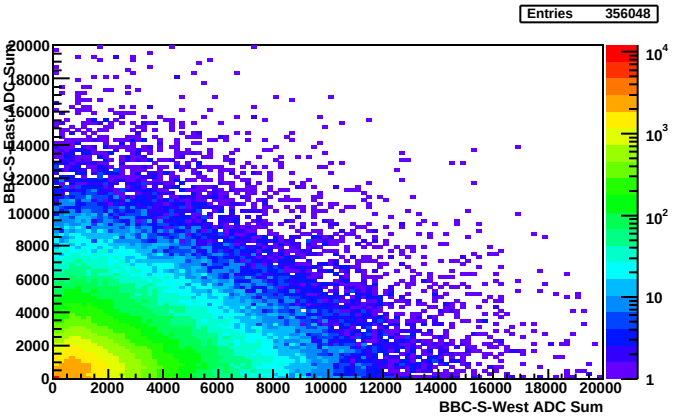

Entries 356048
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

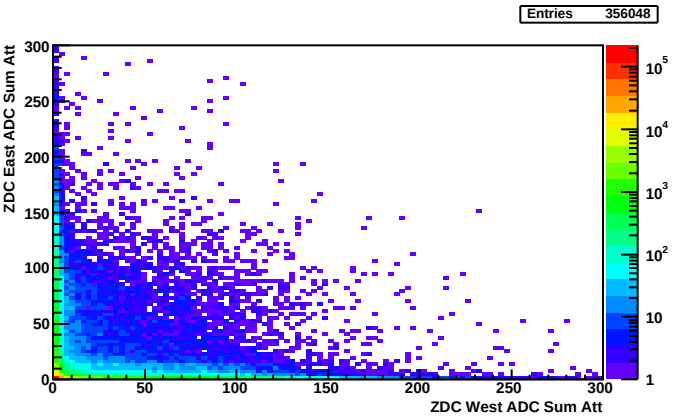

Entries 356048
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

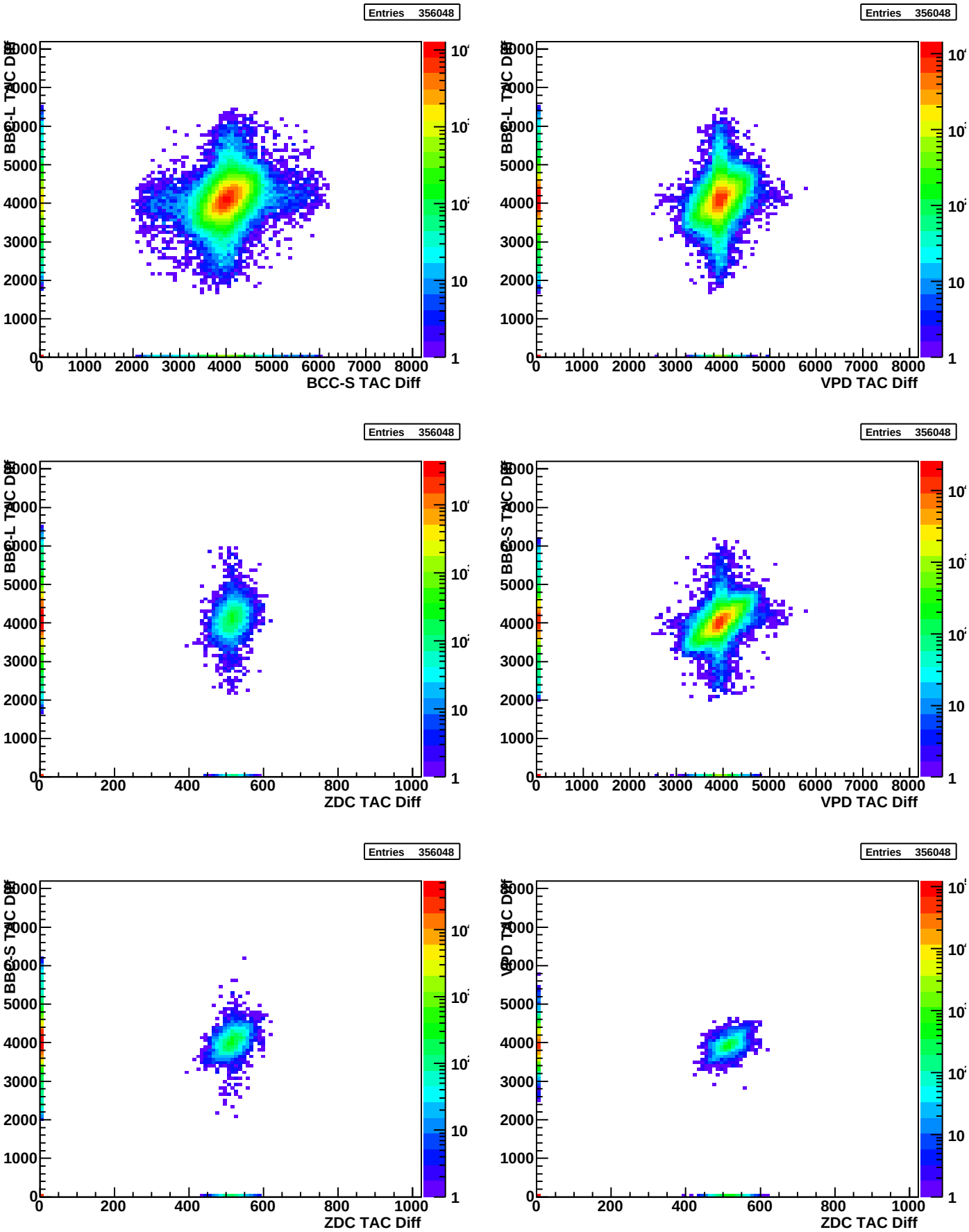

Entries 356048
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

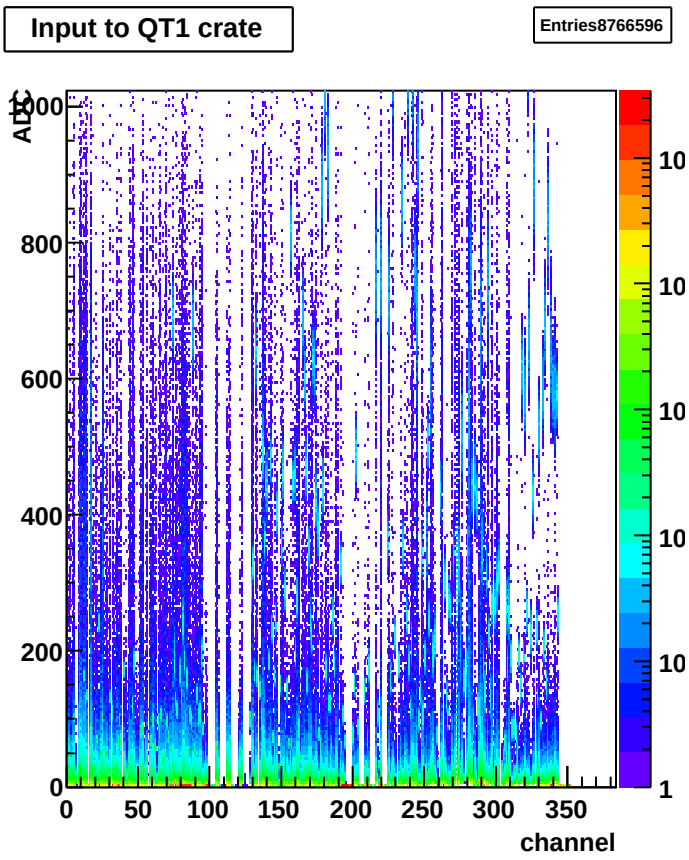

Entries 356048
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

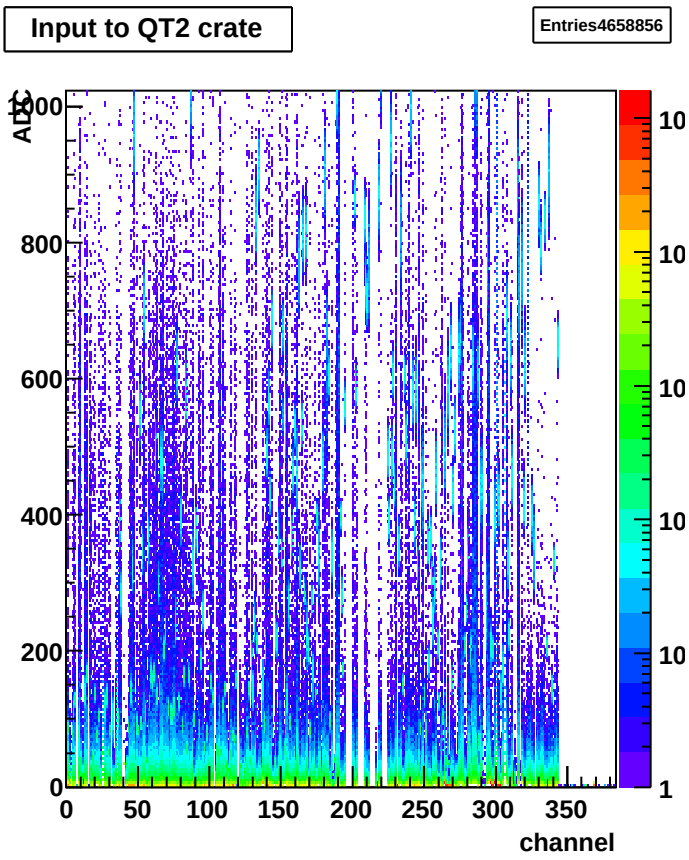

Entries 356048
BBC-L-East ADC Sum
BBC-L-West ADC Sum

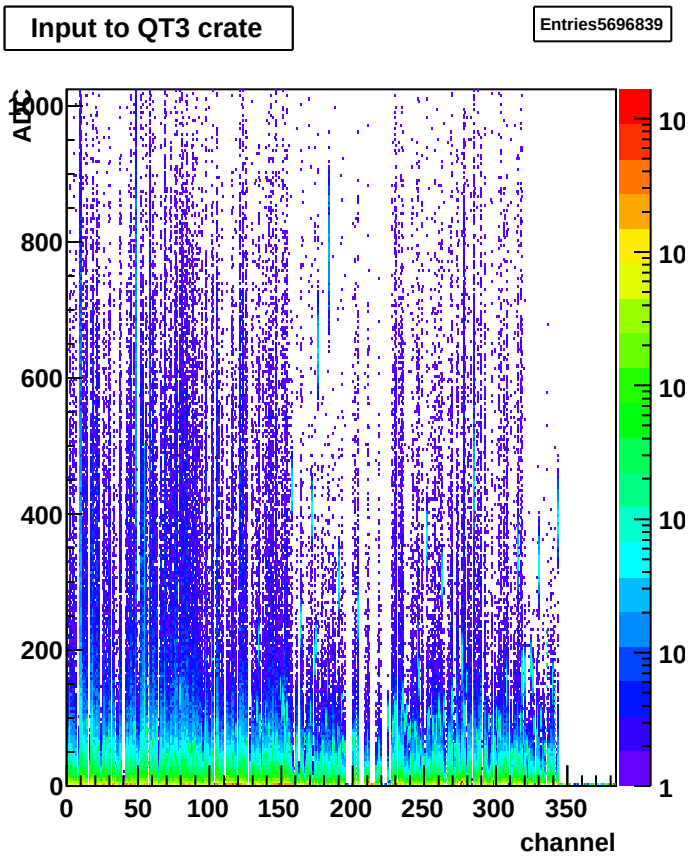

Entries 356048
BBC-S-East ADC Sum
BBC-S-West ADC Sum

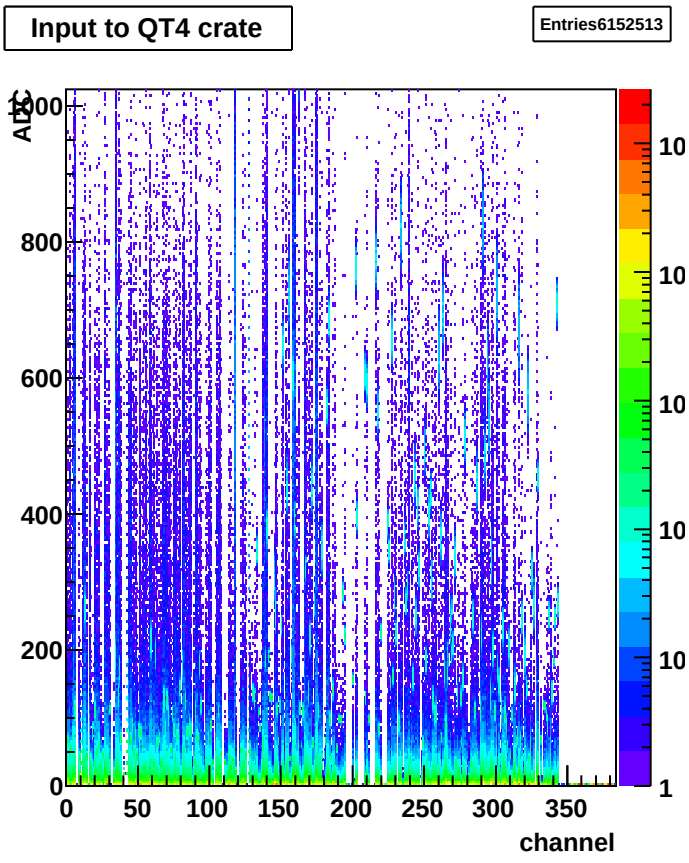

Entries 356048
ZDC East ADC Sum Att
ZDC West ADC Sum Att

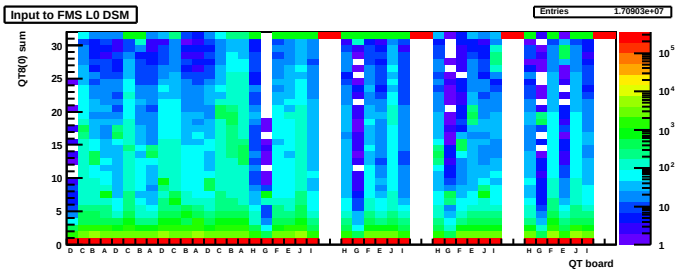

Entries 356048
BBC-L TAC Diff
BCC-S TAC Diff
Entries 356048
BBC-L TAC Diff
VPD TAC Diff
Entries 356048
BBC-L TAC Diff
ZDC TAC Diff
Entries 356048
BBC-S TAC Diff
VPD TAC Diff
Entries 356048
BBC-S TAC Diff
ZDC TAC Diff
Entries 356048
VPD TAC Diff
ZDC TAC Diff

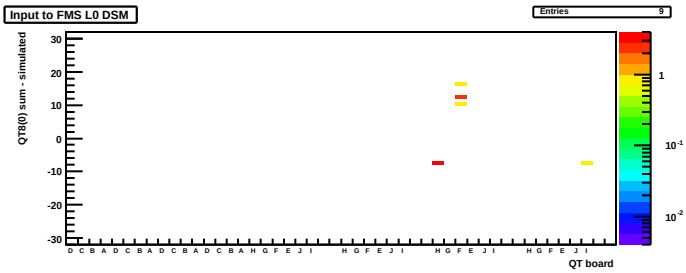

Input to QT1 crate
Entries8766596
ADC
channel

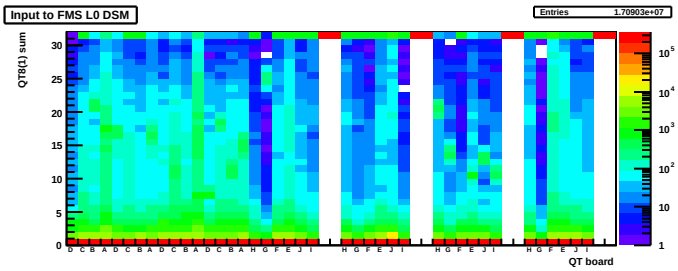

Input to QT2 crate
Entries4658856
ADC
channel

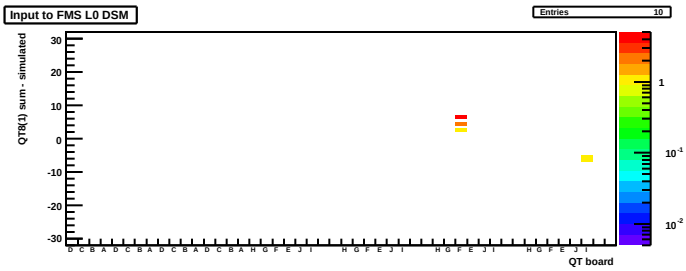

Input to QT3 crate
Entries5696839
ADC
channel

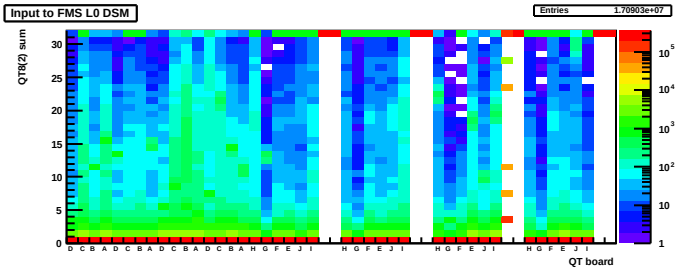

Input to QT4 crate
Entries6152513
ADC
channel

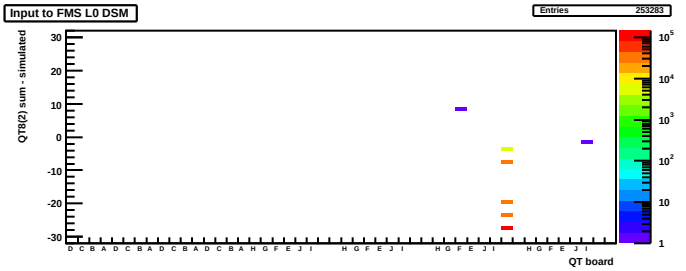

Input to FMS L0 DSM
Entries 1.70903e+07
QT8(0) sum
QT board

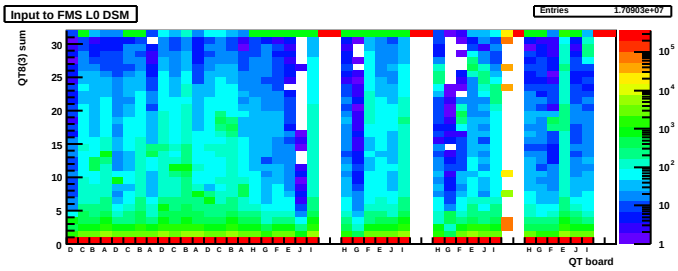

Input to FMS L0 DSM
Entries 9
QT8(0) sum - simulated
QT board

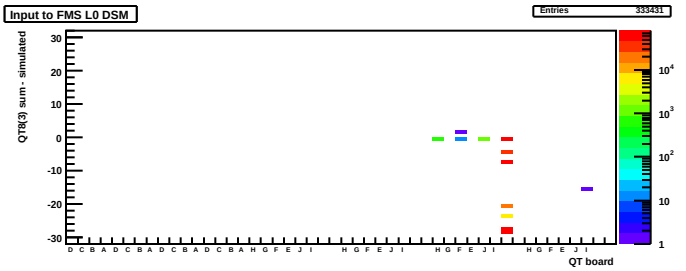

Input to FMS L0 DSM
Entries 1.70903e+07
QT8(1) sum
QT board

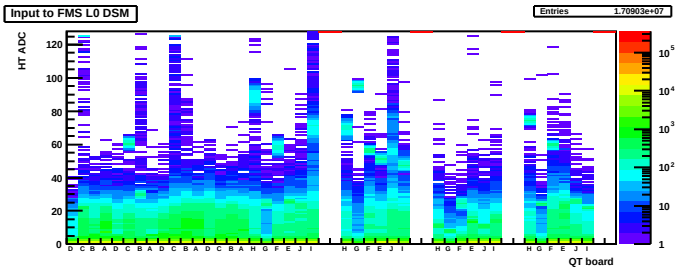

Input to FMS L0 DSM
Entries 10
QT8(1) sum - simulated
QT board

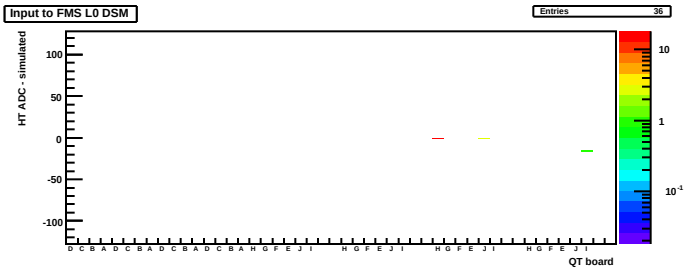

Input to FMS L0 DSM
Entries 1.70903e+07
QT8(2) sum
QT board

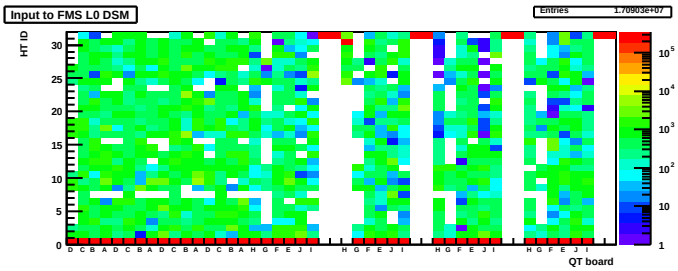

Input to FMS L0 DSM
Entries 253283
QT8(2) sum - simulated
QT board

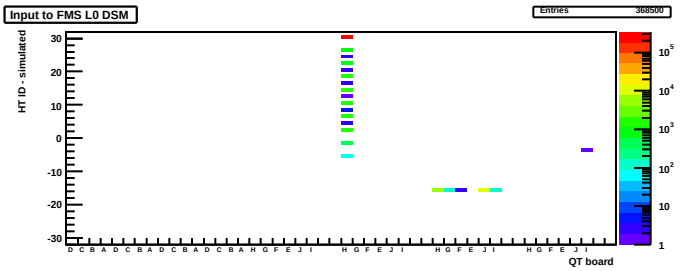

Input to FMS L0 DSM
Entries 1.70903e+07
QT8(3) sum
QT board

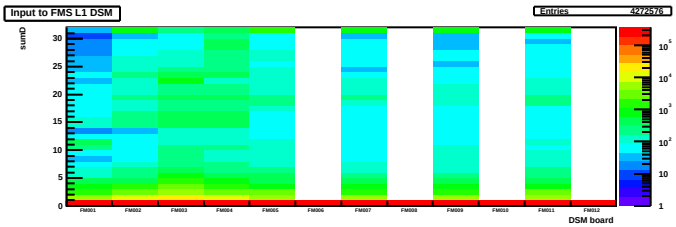

Input to FMS L0 DSM
Entries 333431
QT8(3) sum - simulated
QT board

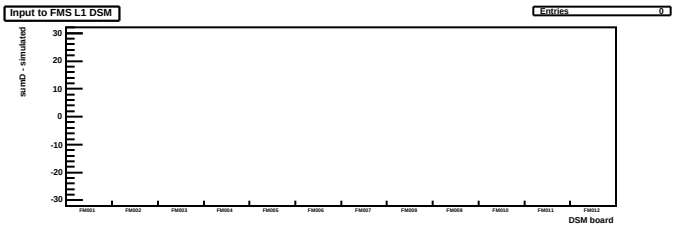

Input to FMS L0 DSM
Entries 1.70903e+07
HT ADC
QT board

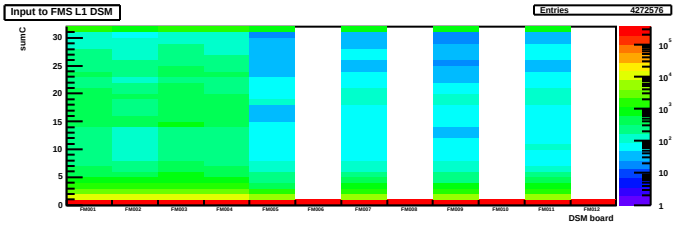

Input to FMS L0 DSM
Entries 36
HT ADC - simulated
QT board

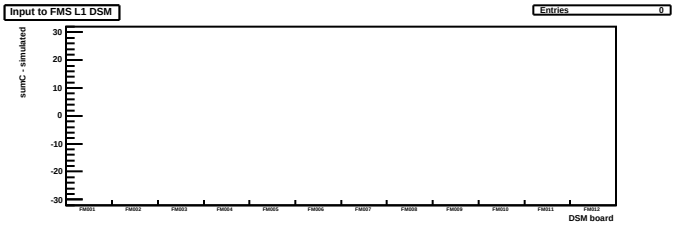

Input to FMS L0 DSM
Entries 1.70903e+07
HT ID
QT board

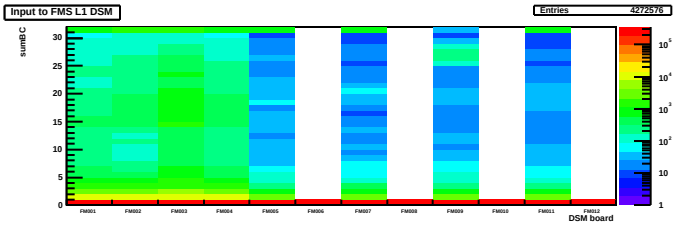

Input to FMS L0 DSM
Entries 368500
HT ID - simulated
QT board

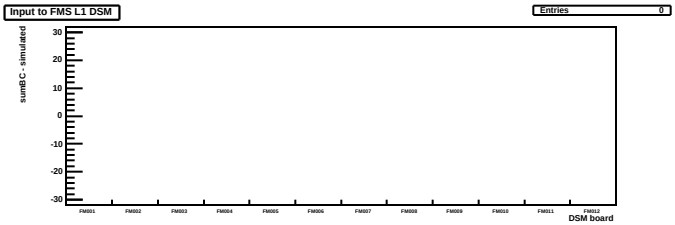

Input to FMS L1 DSM
Entries 4272576
sumD
DSM board

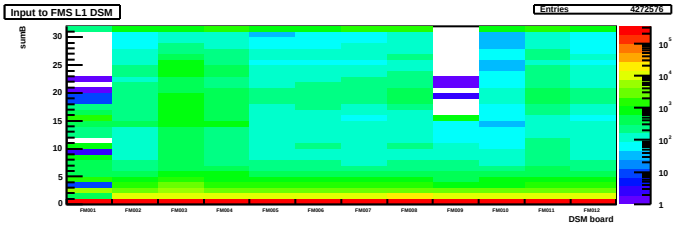

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

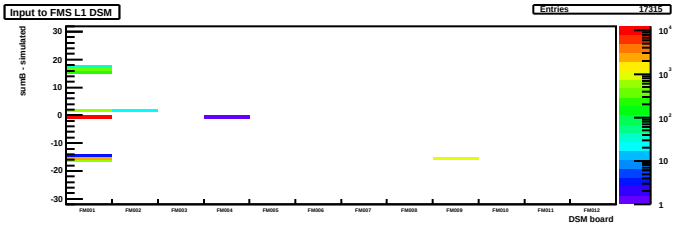

Input to FMS L1 DSM
Entries 4272576
sumC
DSM board

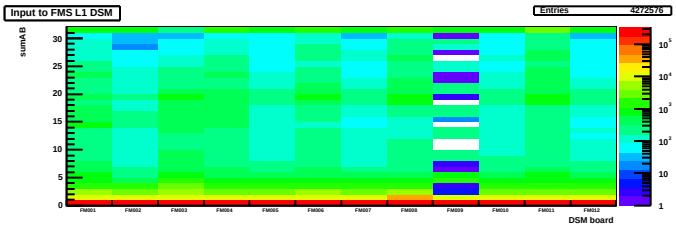

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

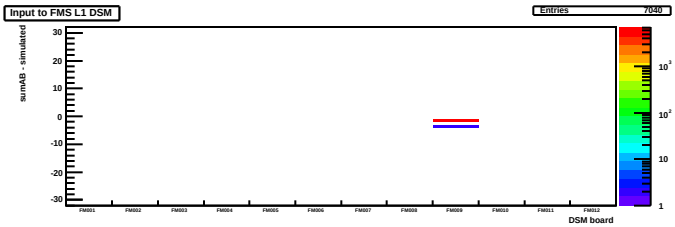

Input to FMS L1 DSM
Entries 4272576
sumBC
DSM board

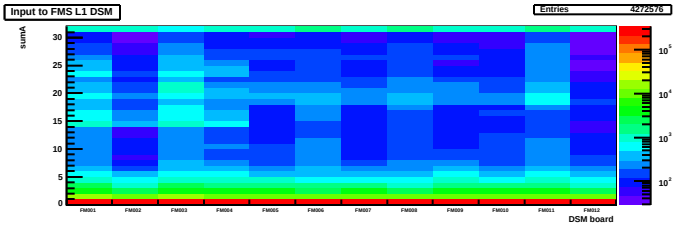

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

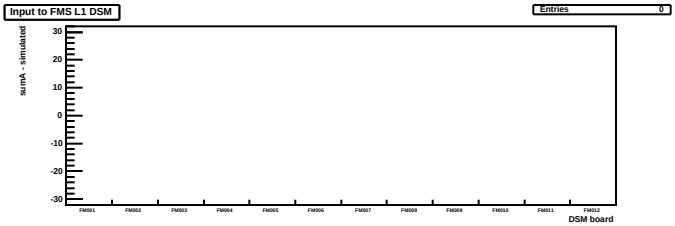

Input to FMS L1 DSM
Entries 4272576
sumB
DSM board

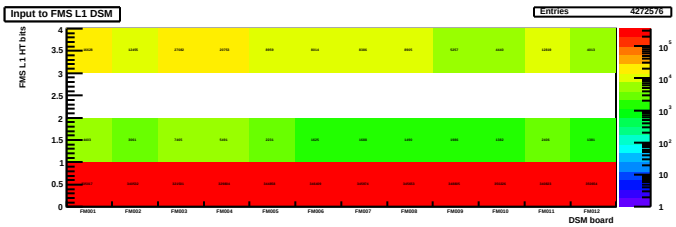

Input to FMS L1 DSM
Entries 17315
sumB - simulated
DSM board

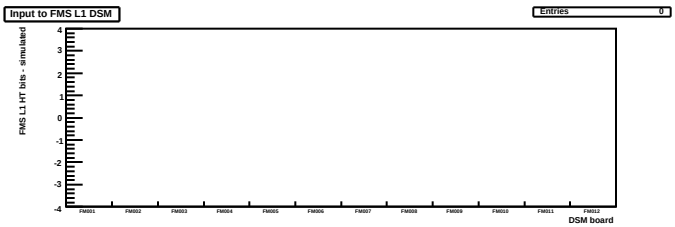

Input to FMS L1 DSM
Entries 4272576
sumAB
DSM board

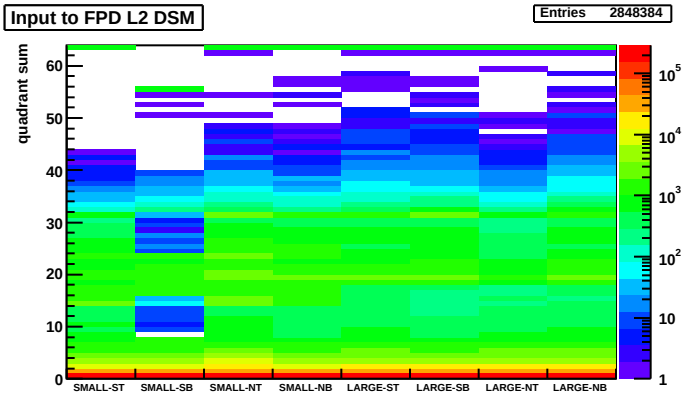

Input to FMS L1 DSM
Entries 7040
sumAB - simulated
DSM board

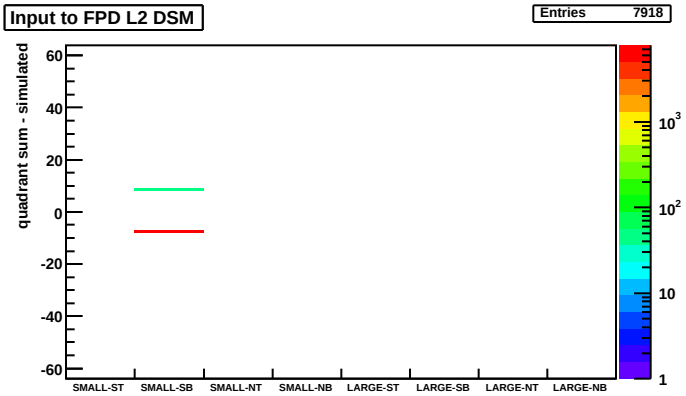

Input to FMS L1 DSM
Entries 4272576
sumA
DSM board

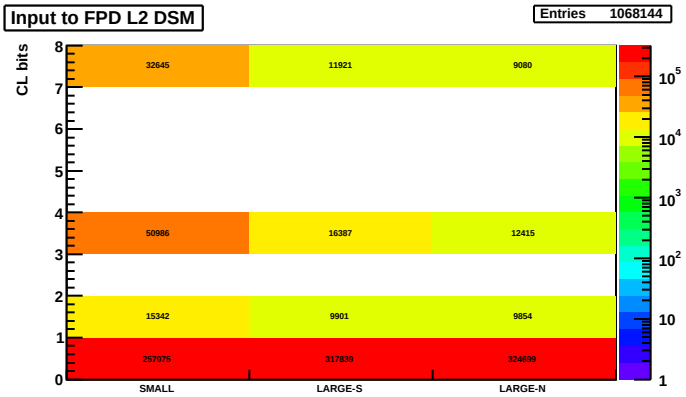

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

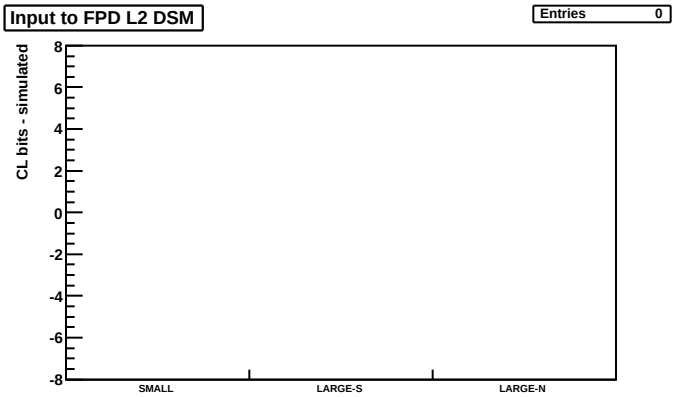

Input to FMS L1 DSM
Entries 4272576
FMS L1 HT bits
DSM board

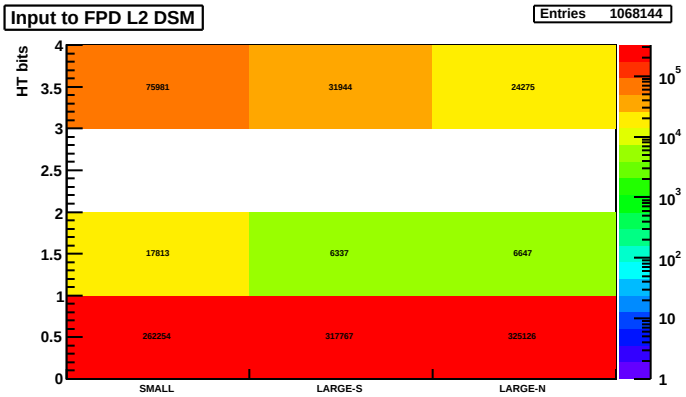

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

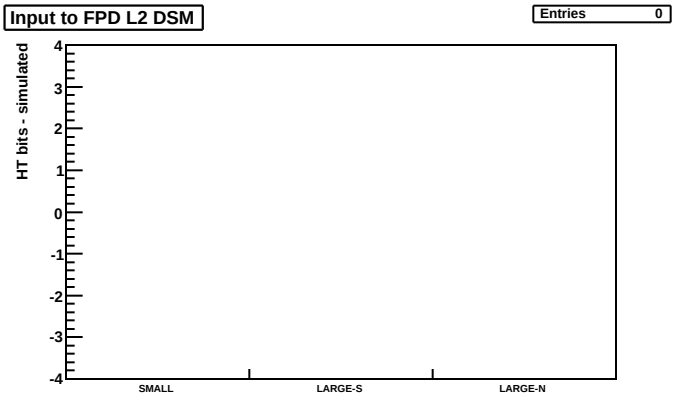

Input to FPD L2 DSM
Entries 2848384
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

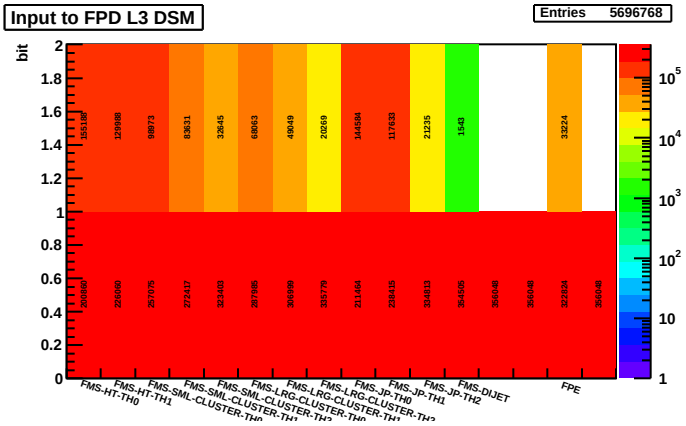

Input to FPD L2 DSM
Entries 7918
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

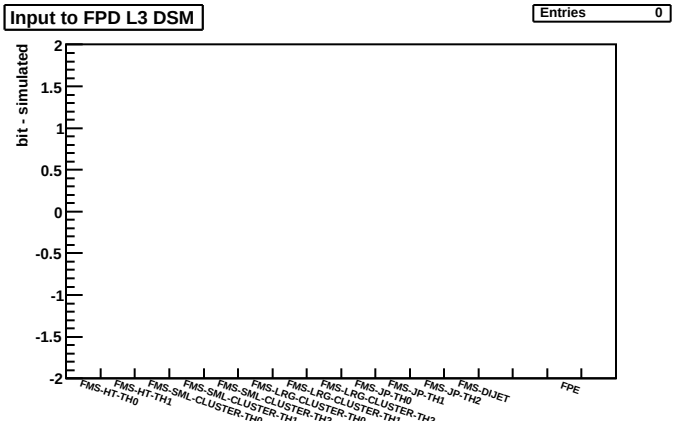

Input to FPD L2 DSM
Entries 1068144
CL bits
32645
11921
9080
50986
16387
12415
15342
9901
9854
257075
317839
324699
SMALL
LARGE-S
LARGE-N

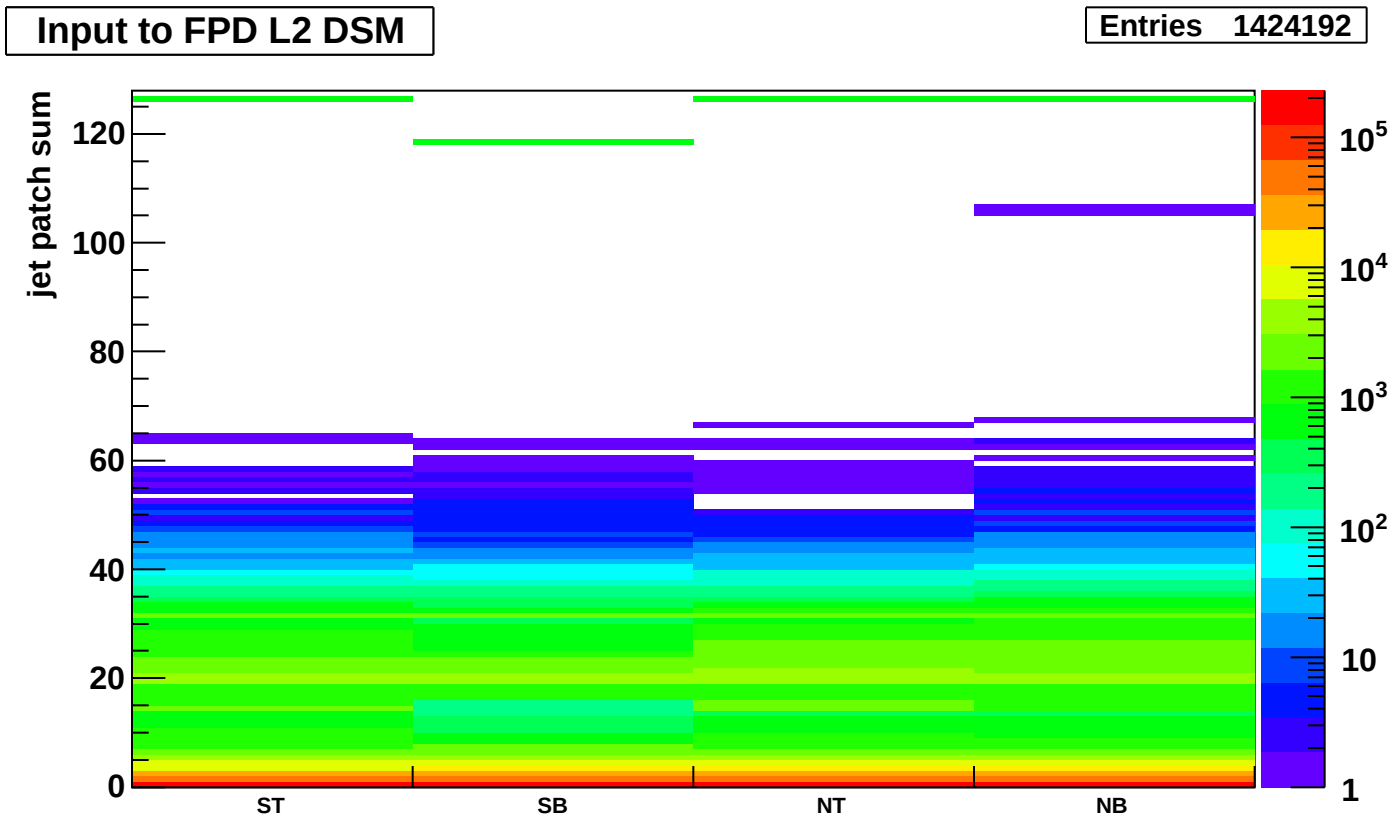

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

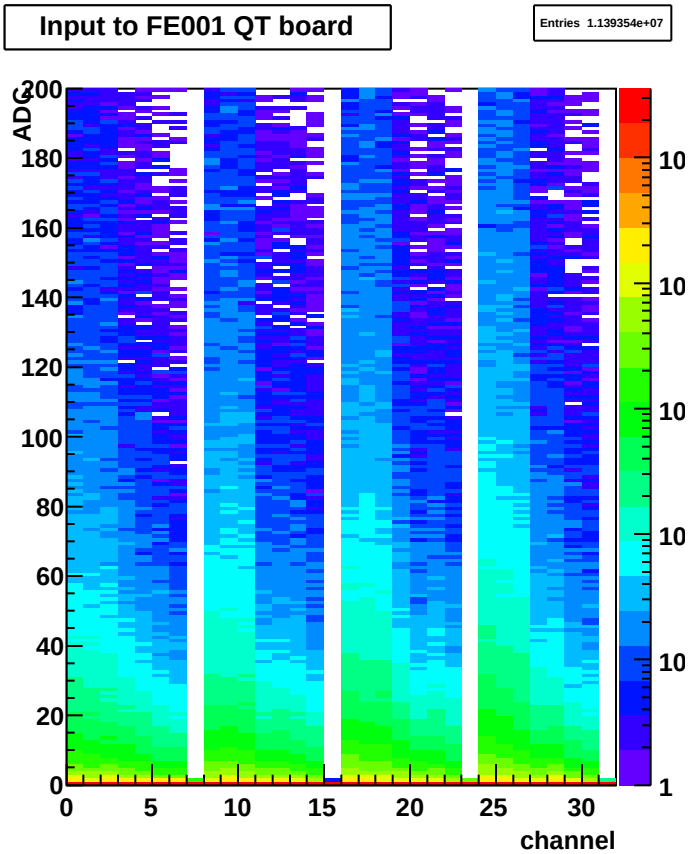

Input to FPD L2 DSM
Entries 1068144
HT bits
75981
31944
24275
17813
6337
6647
262254
317767
325126
SMALL
LARGE-S
LARGE-N

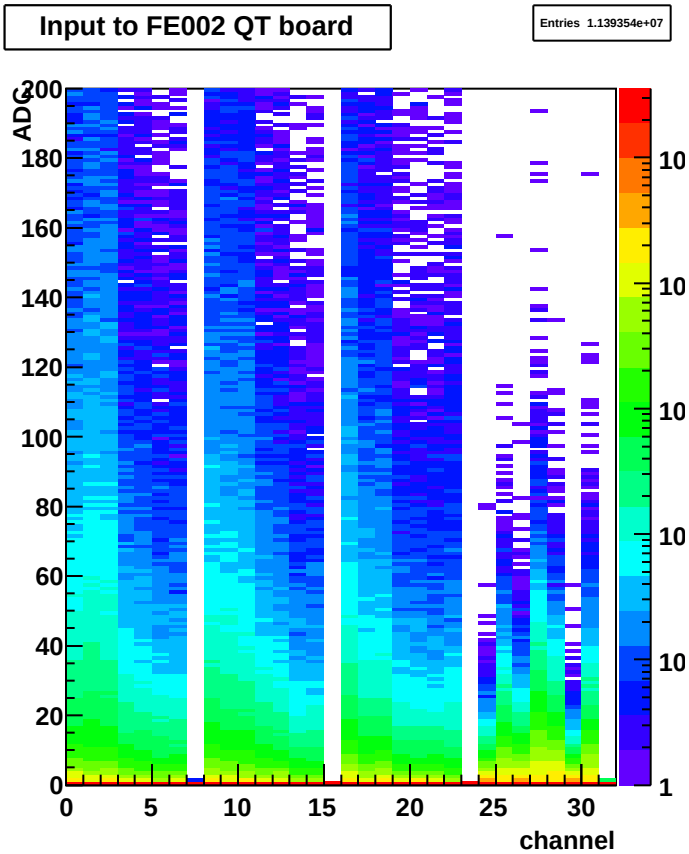

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

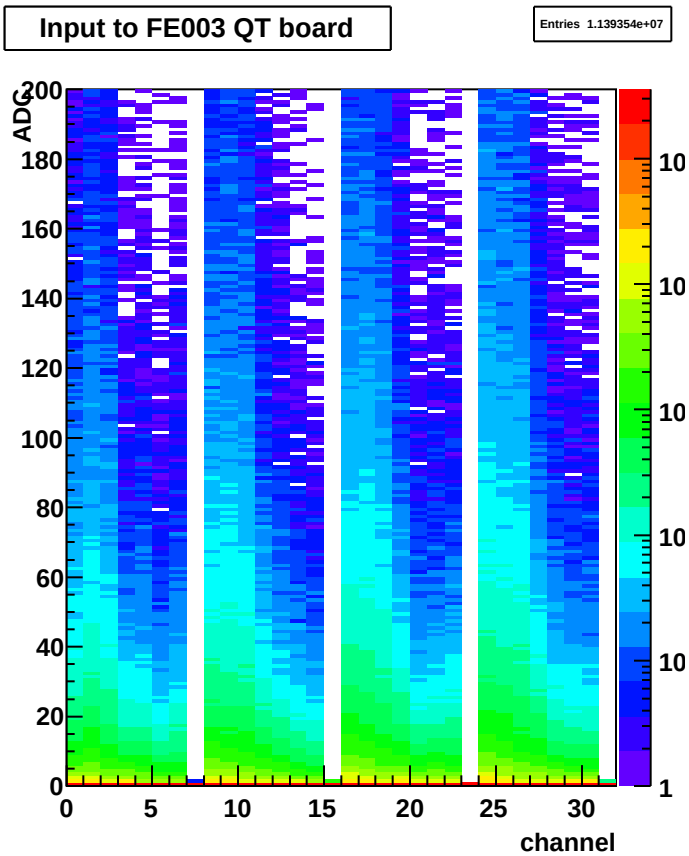

Input to FPD L3 DSM
Entries 5696768
bit
155187
129988
99973
83631
32645
68063
49049
20269
144584
117633
21235
1543
33224
220080
226060
257075
272417
322403
297985
306999
335779
211464
228435
334813
354505
356048
356048
322824
356048
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

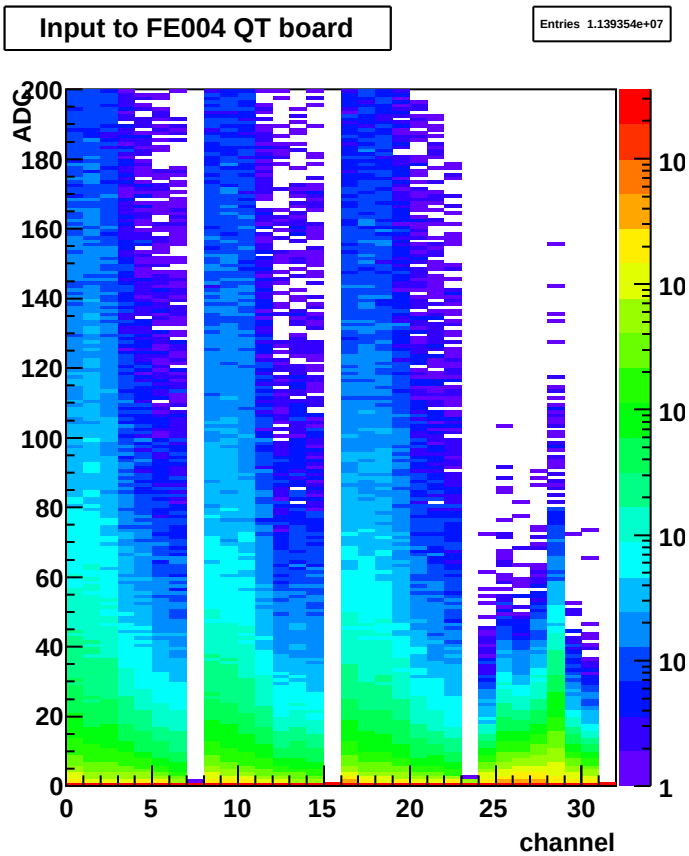

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

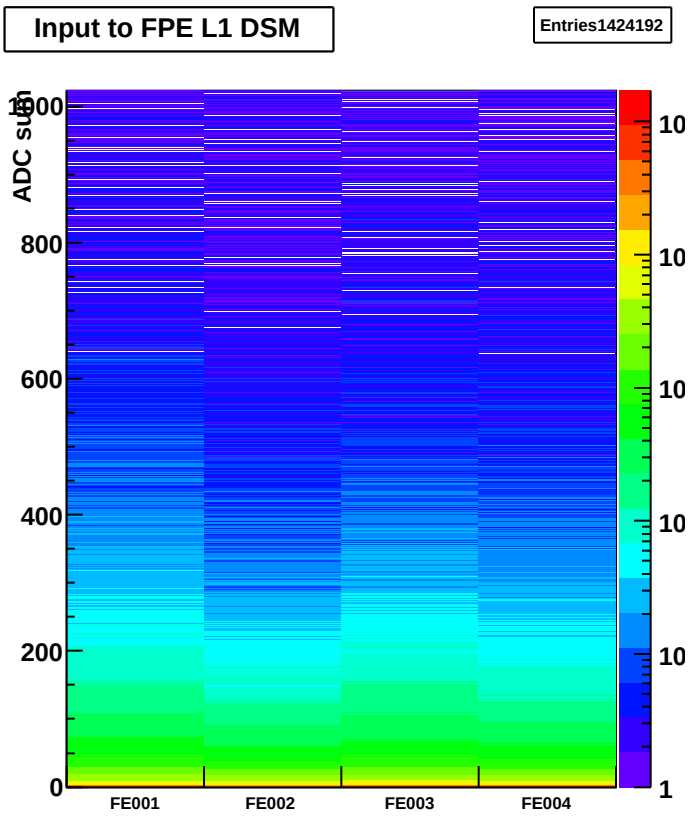

Input to FPD L2 DSM
Entries 1424192
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

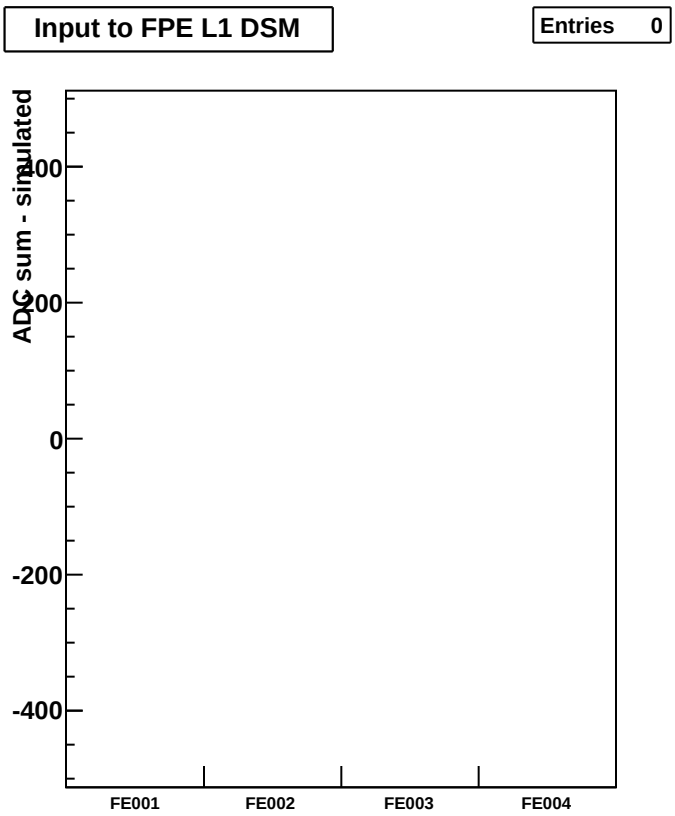

Input to FE001 QT board
Entries 1.139354e+07
ADC
channel

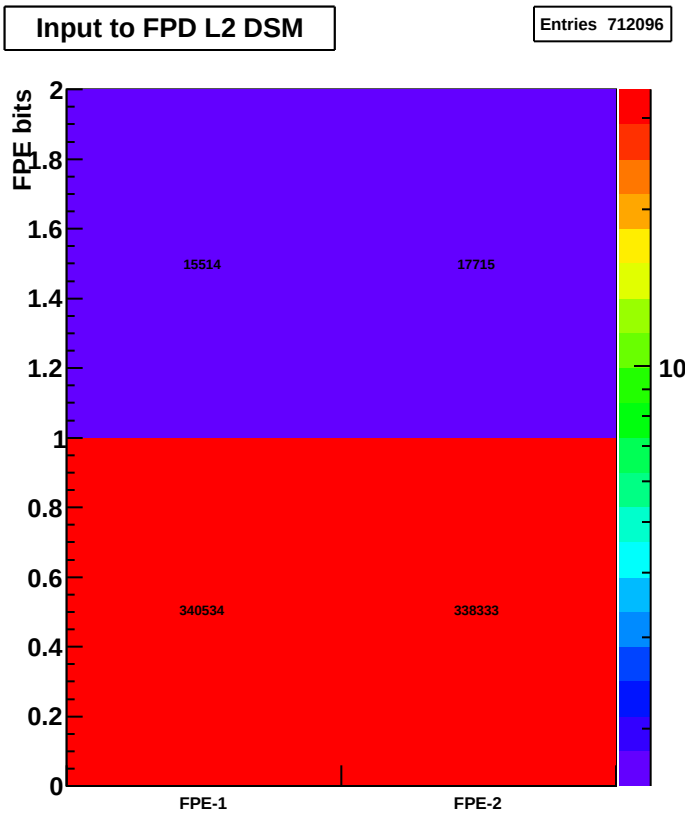

Input to FE002 QT board
Entries 1.139354e+07
ADC
channel

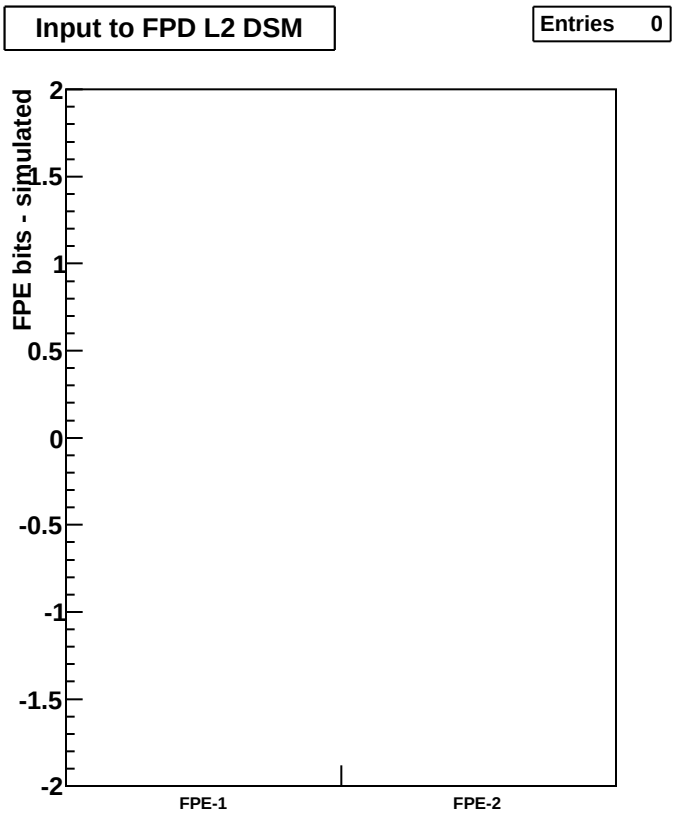

Input to FE003 QT board
Entries 1.139354e+07
ADC
channel


Input to FE004 QT board
Entries 1.139354e+07
ADC
channel


Input to FPE L1 DSM
Entries1424192
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 712096
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
15514
17715
340534
338333
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2