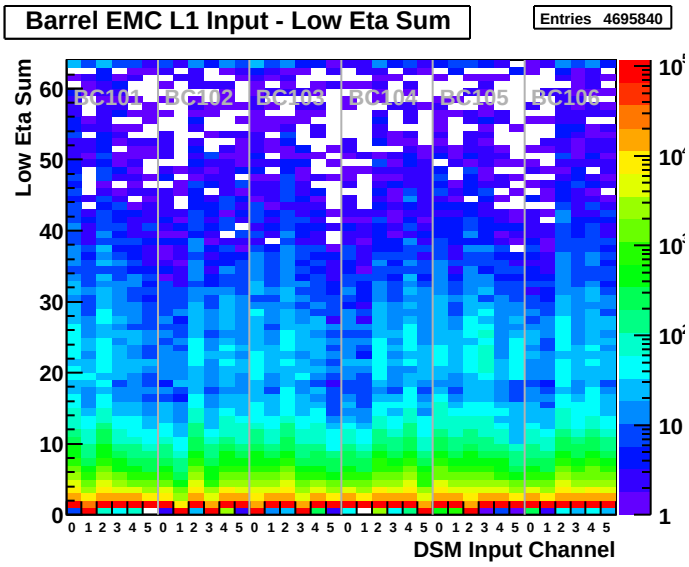
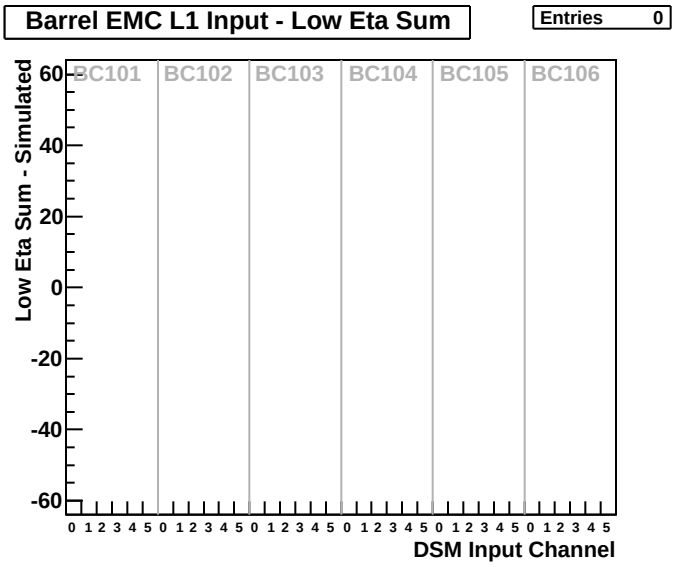
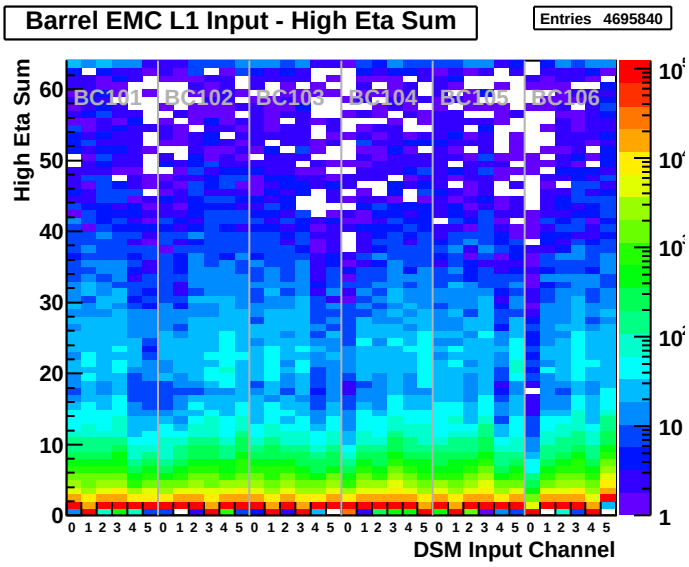
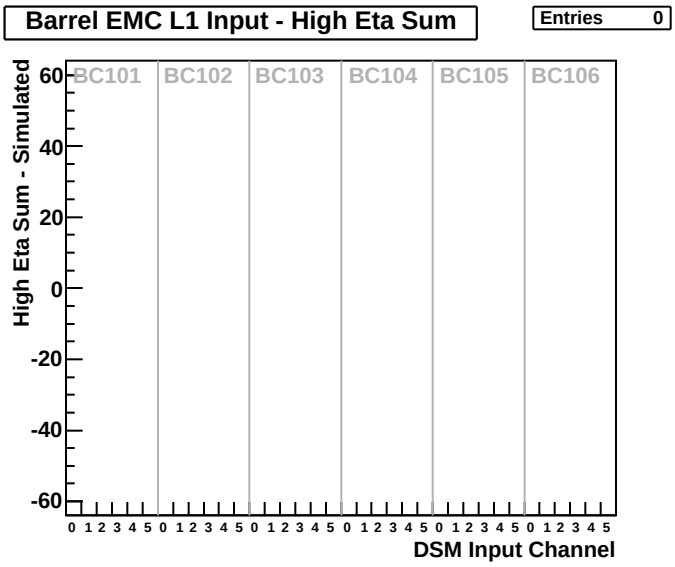
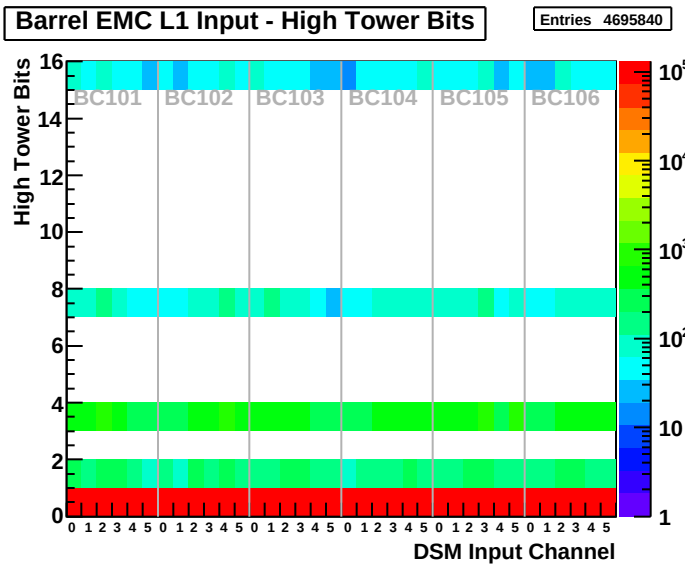
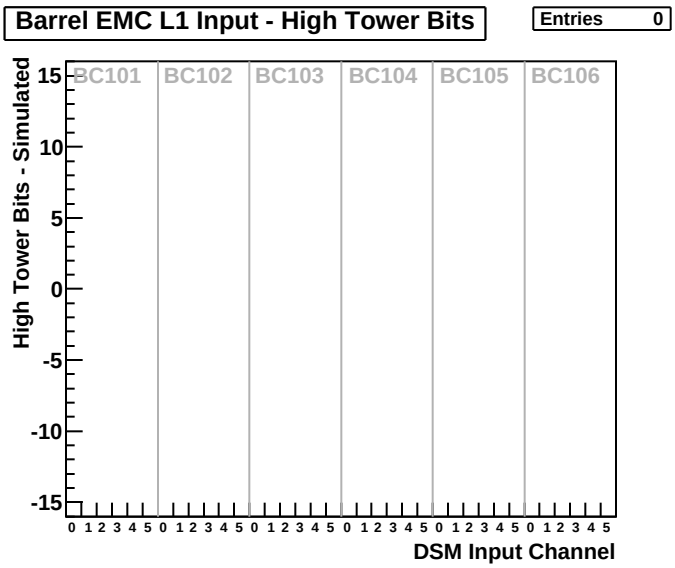

Barrel EMC L1 Input - Low Eta Sum
Entries 4695840
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

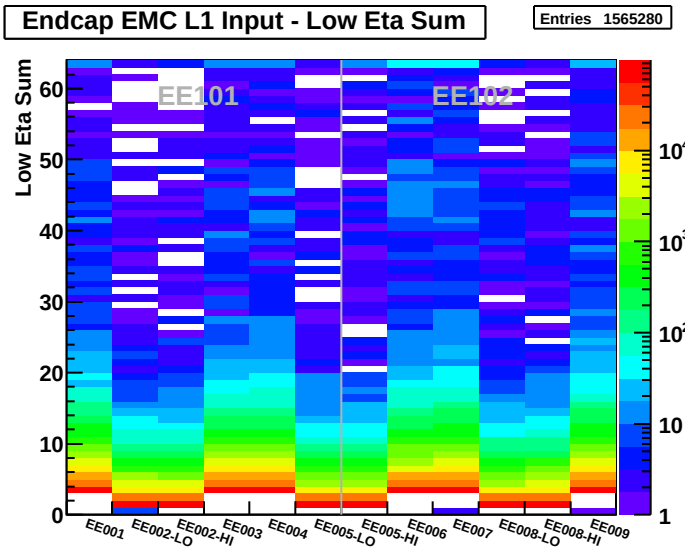

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

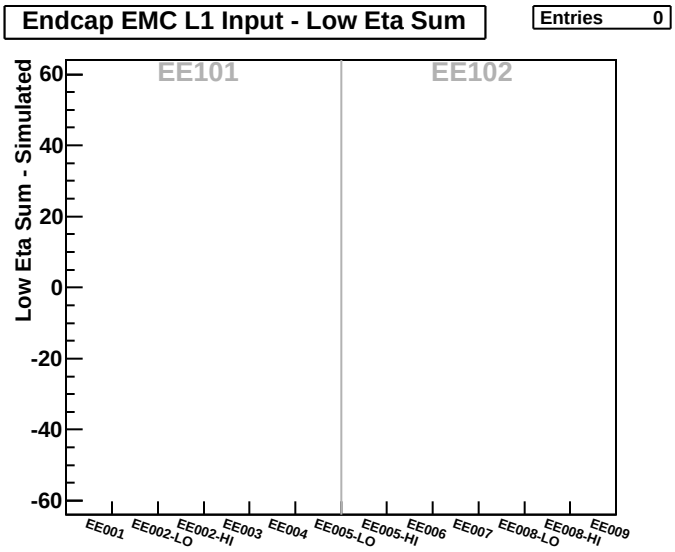

Barrel EMC L1 Input - High Eta Sum
Entries 4695840
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

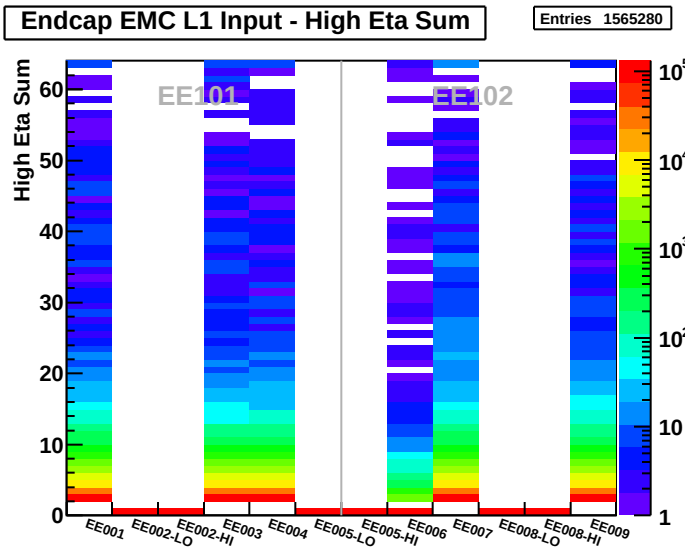

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

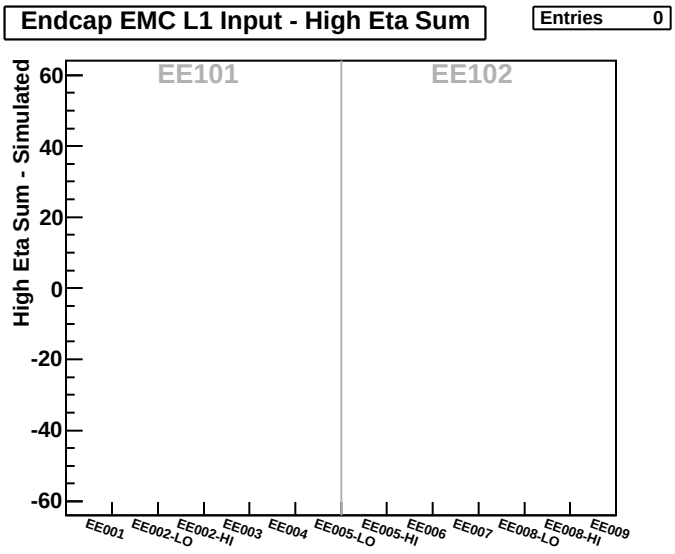

Barrel EMC L1 Input - High Tower Bits
Entries 4695840
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

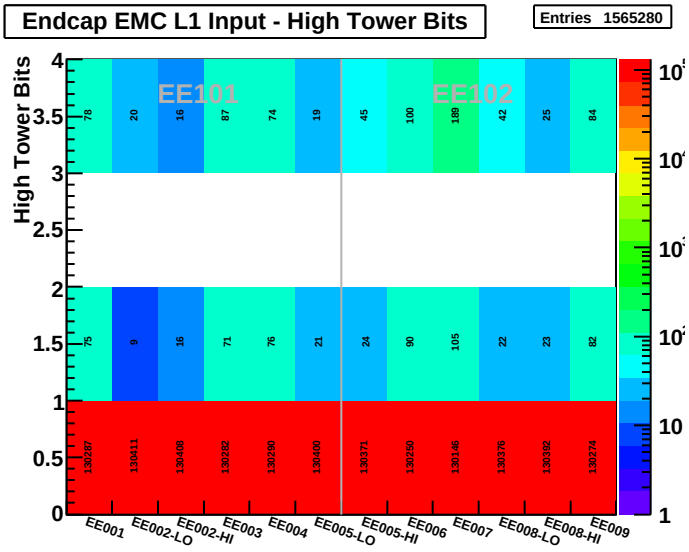

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

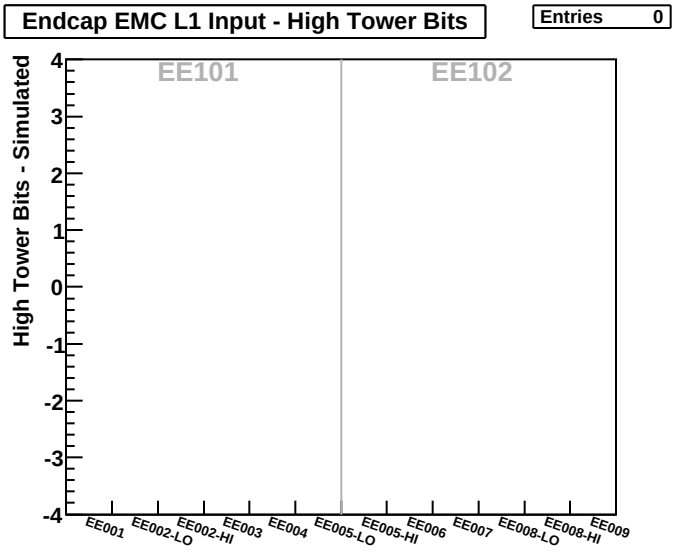

Endcap EMC L1 Input - Low Eta Sum
Entries 1565280
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

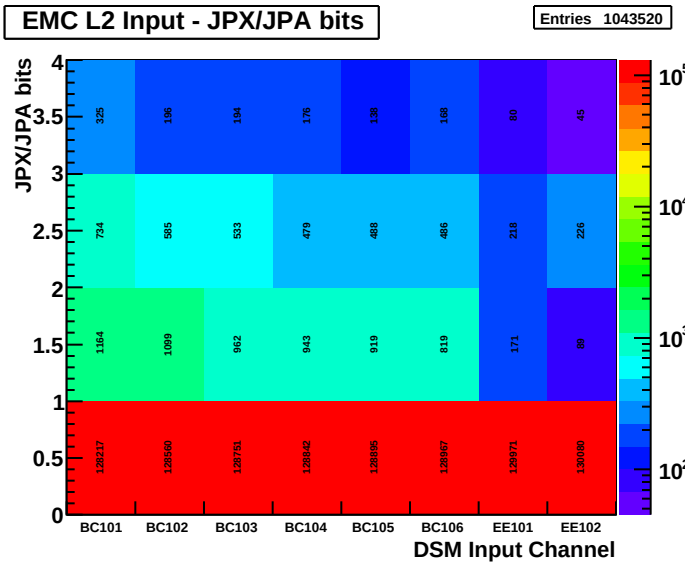

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

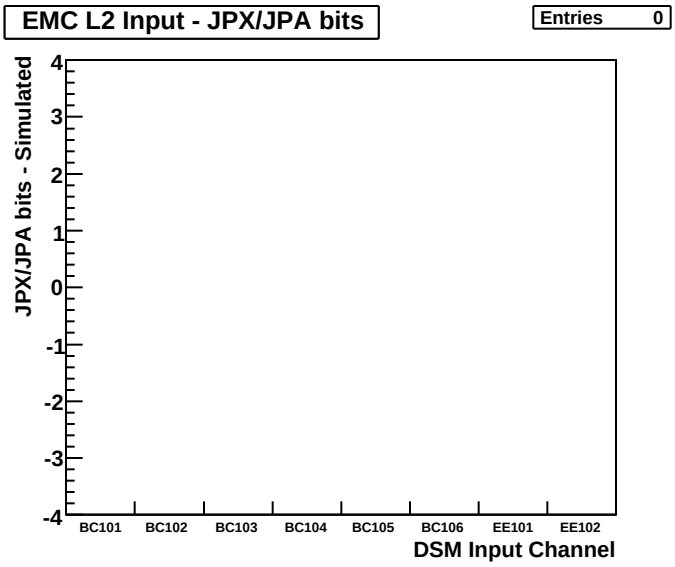

Endcap EMC L1 Input - High Eta Sum
Entries 1565280
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

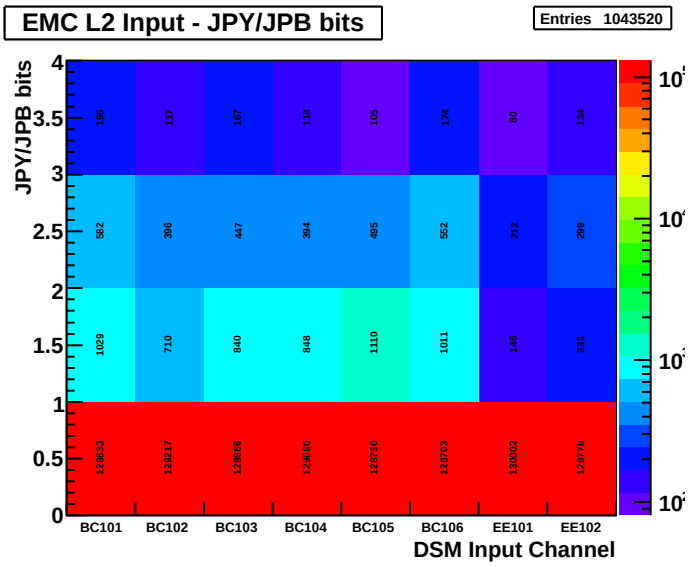

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

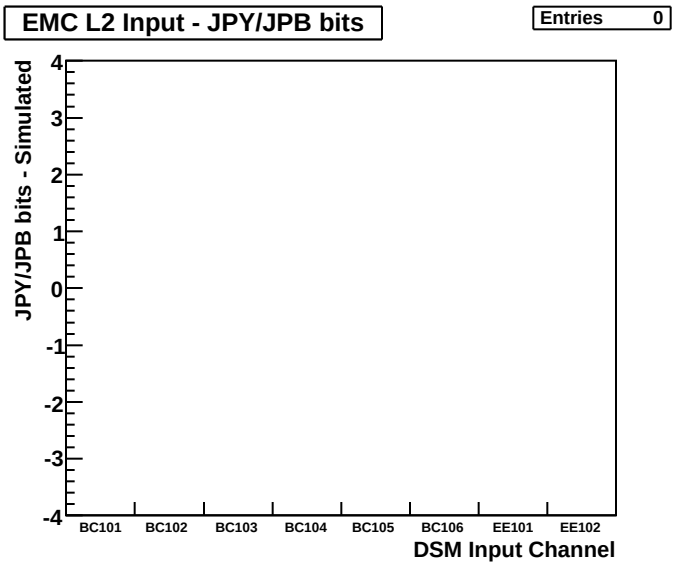

Endcap EMC L1 Input - High Tower Bits
Entries 1565280
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

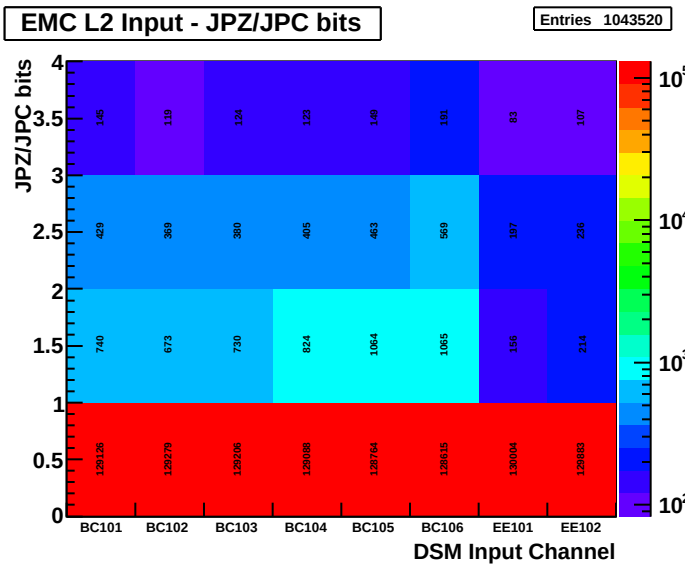

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

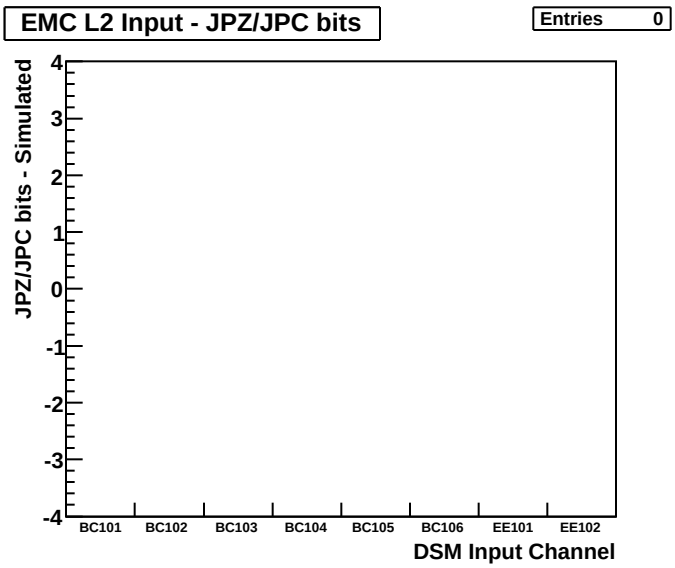

EMC L2 Input - JPX/JPA bits
Entries 1043520
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
325 196 194 176 138 168 80 45
734 585 533 479 488 486 218 226
1164 1099 962 943 919 819 171 89
128217 128560 128751 128842 128895 128967 129971 130080

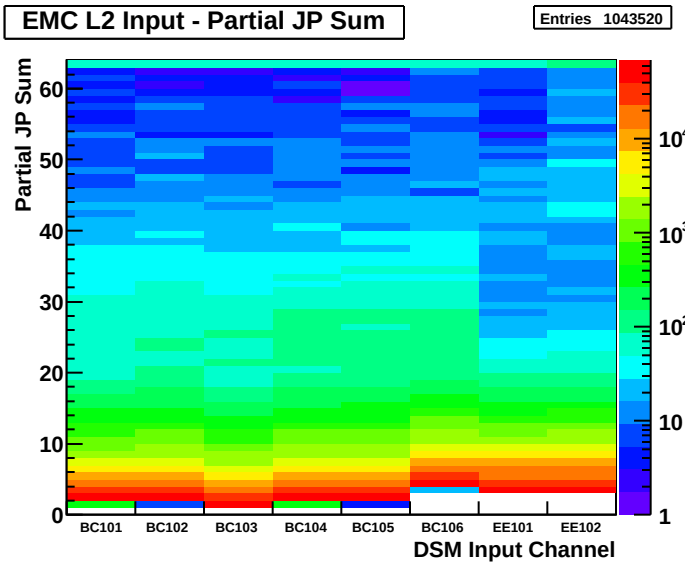

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

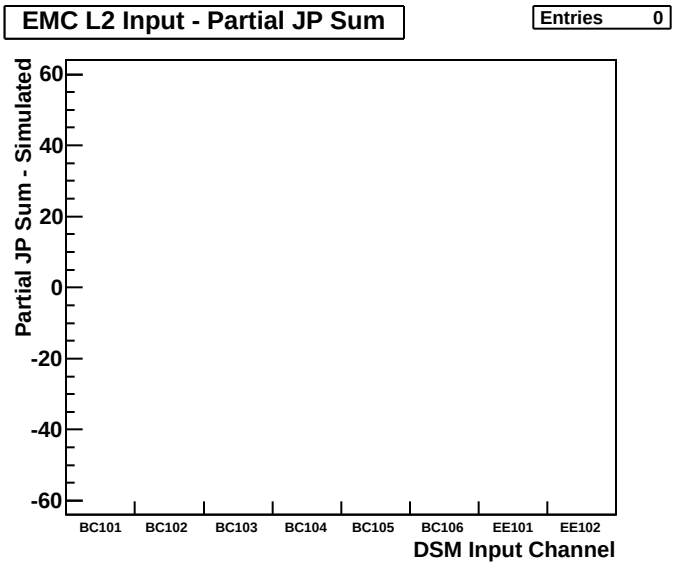

EMC L2 Input - JPY/JPB bits
Entries 1043520
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
156 117 167 118 105 174 80 134
582 396 447 394 495 552 212 299
1029 710 840 848 1110 1011 146 231
128633 128217 128086 129080 128730 128703 130002 129776

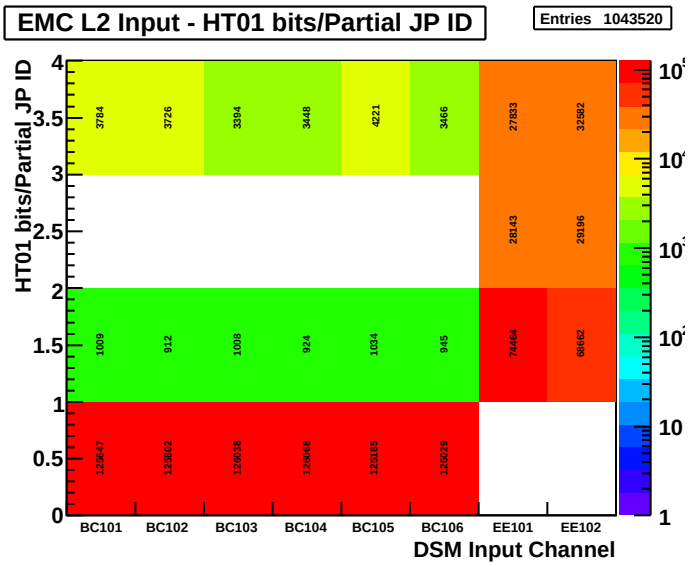

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

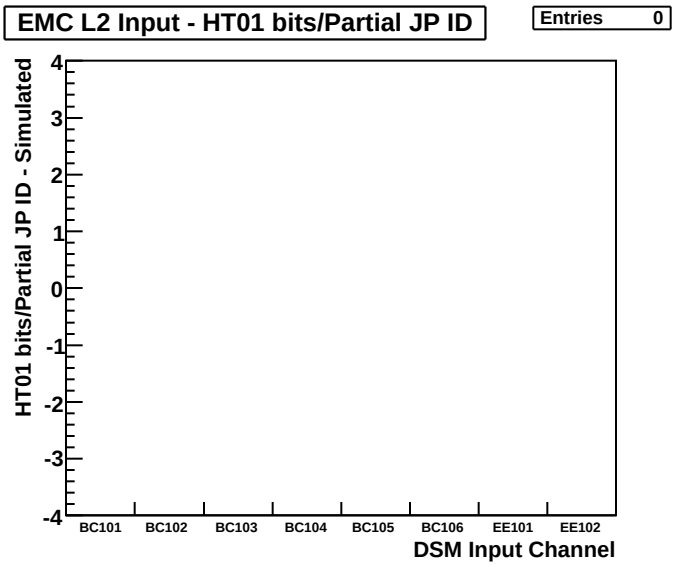

EMC L2 Input - JPZ/JPC bits
Entries 1043520
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
145 119 124 123 149 191 83 107
429 369 380 405 463 569 197 236
740 673 730 824 1064 1065 156 214
129126 129279 129206 129088 128764 128615 130004 129833

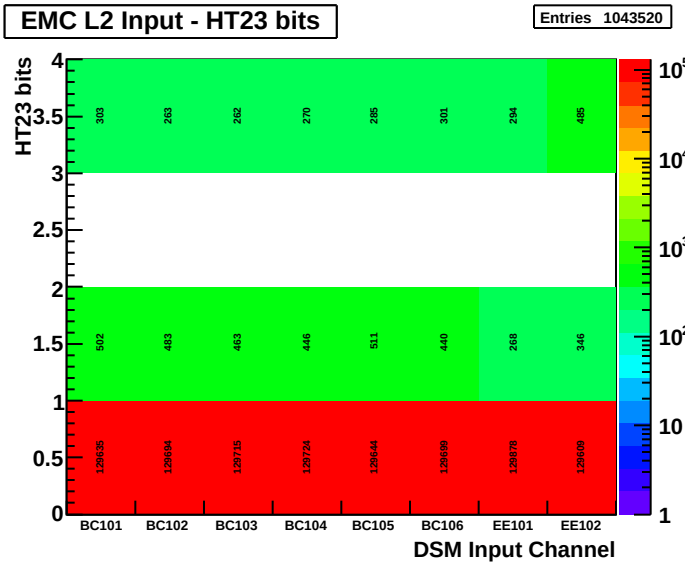

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

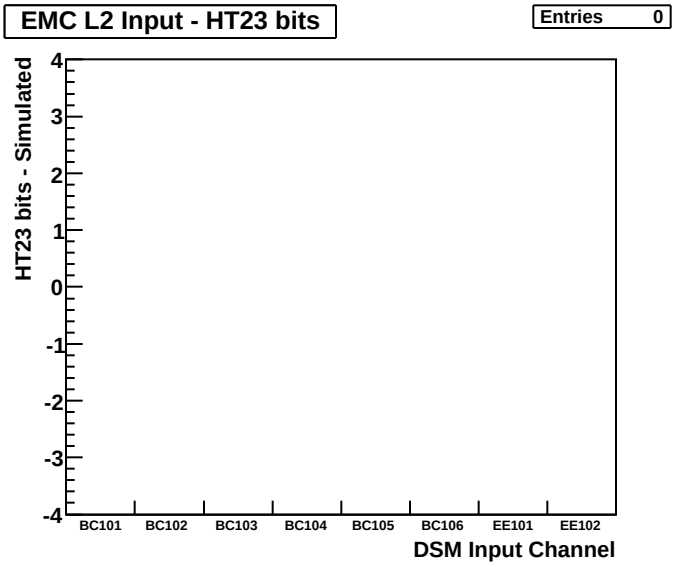

EMC L2 Input - Partial JP Sum
Entries 1043520
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

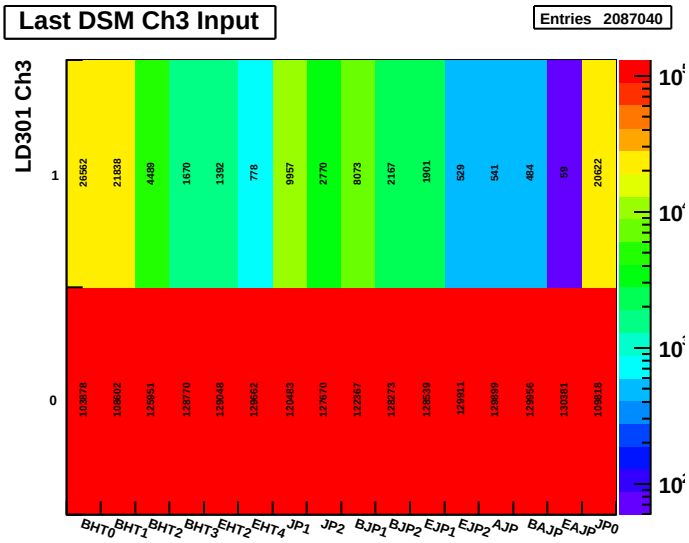

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

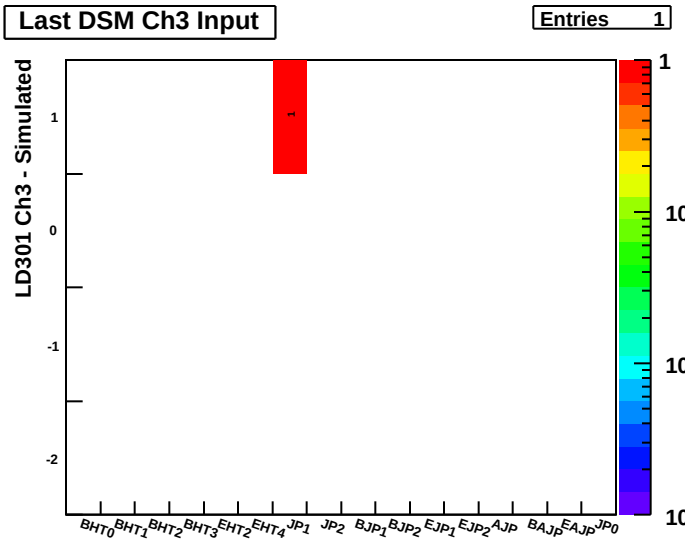

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1043520
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

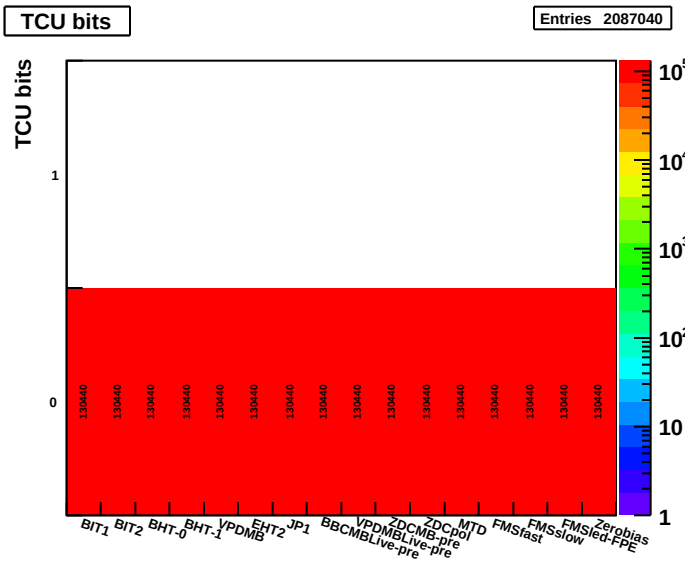

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

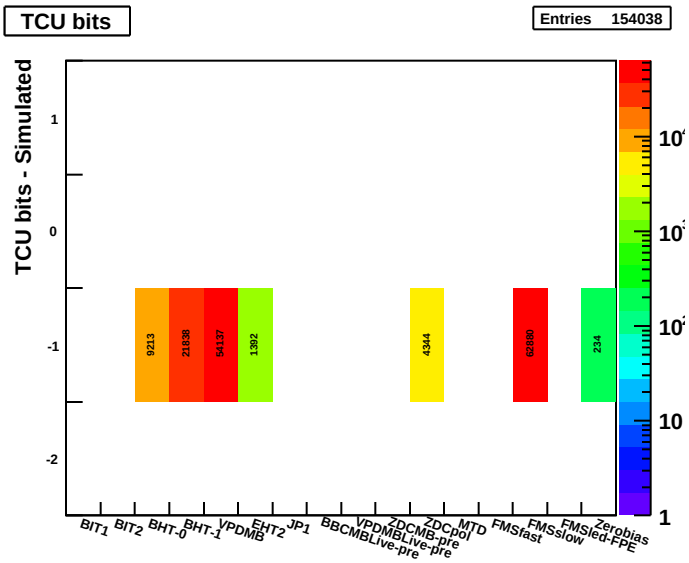

EMC L2 Input - HT23 bits
Entries 1043520
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

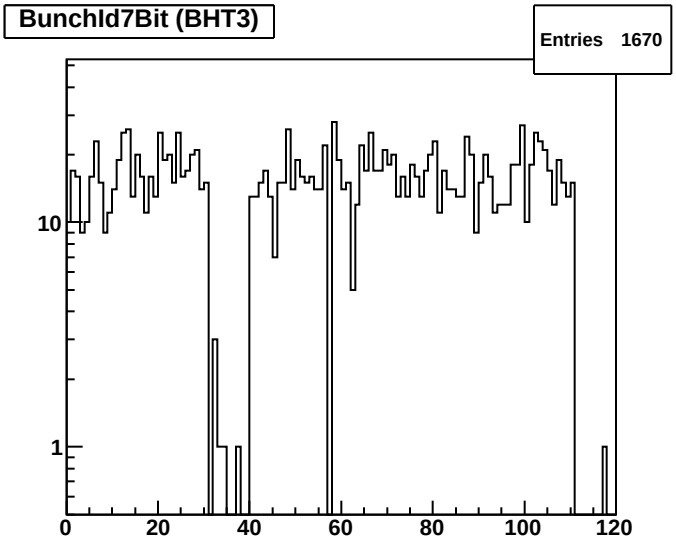

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

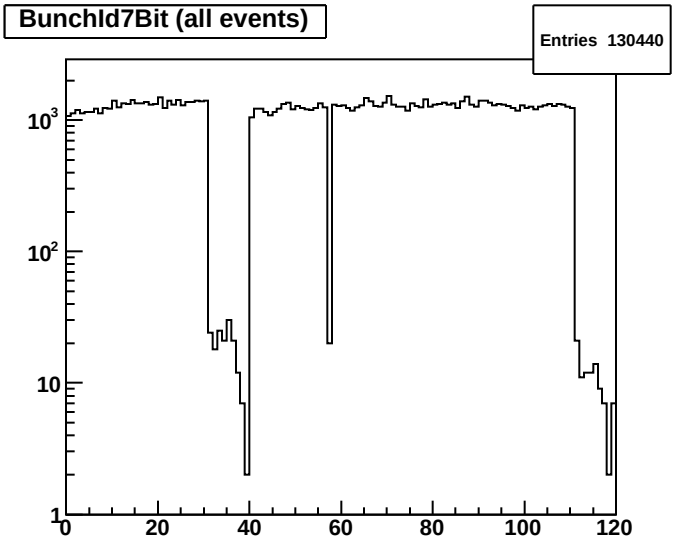

Last DSM Ch3 Input
Entries 2087040
LD301 Ch3
26562 21838 4489 1670 1392 778 9957 2770 8073 2167 1901 529 541 484 59 20622
103878 108602 125951 128770 129048 126662 120483 127670 122367 128273 128539 129911 128999 129956 130381 109818
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

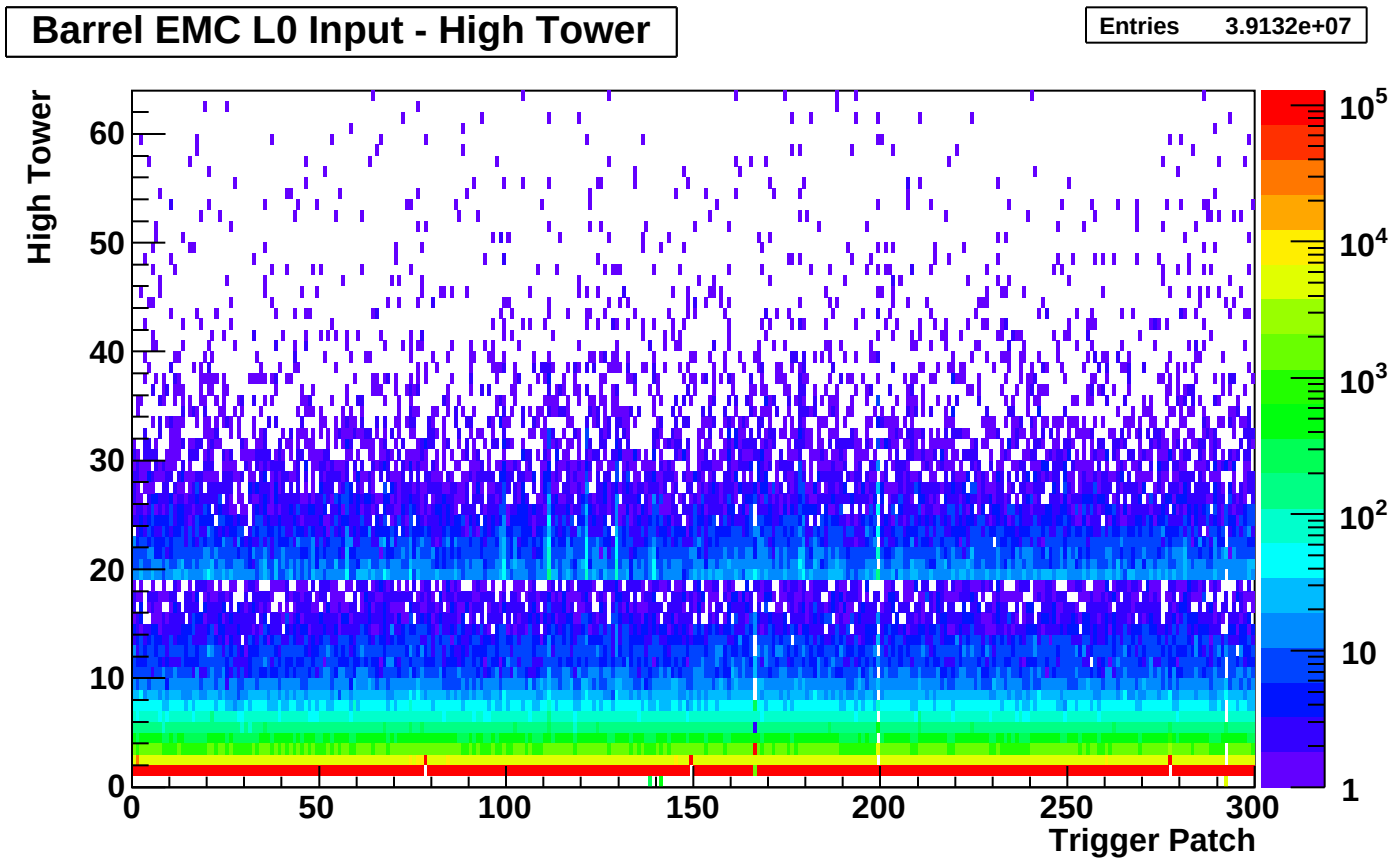

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

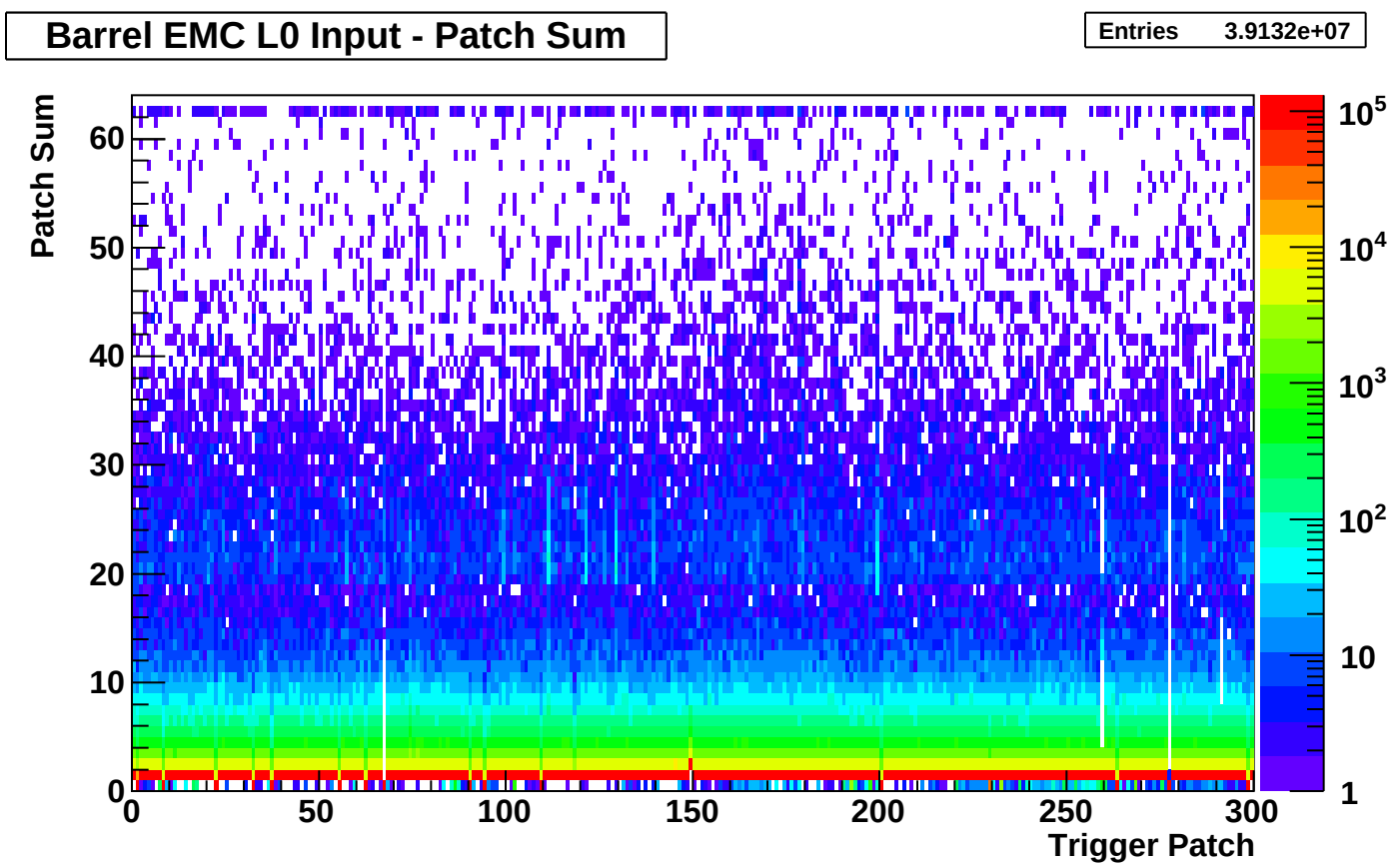

TCU bits
Entries 2087040
TCU bits
130440 130440 130440 130440 130440 130440 130440 130440 130440 130440 130440 130440 130440 130440 130440 130440
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

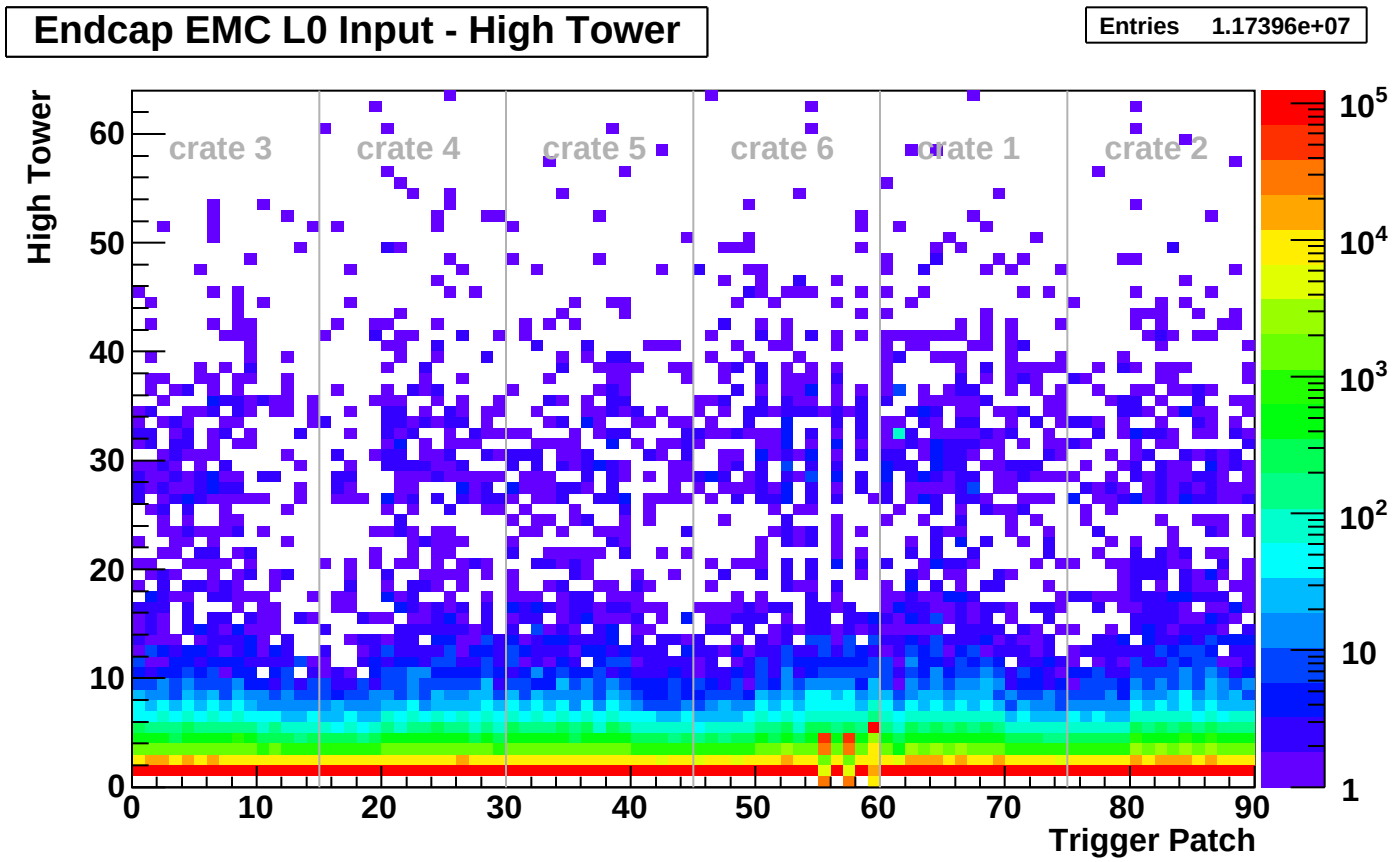

TCU bits
Entries 154038
TCU bits - Simulated
9213 21838 54137 1392 4344 62880 234
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

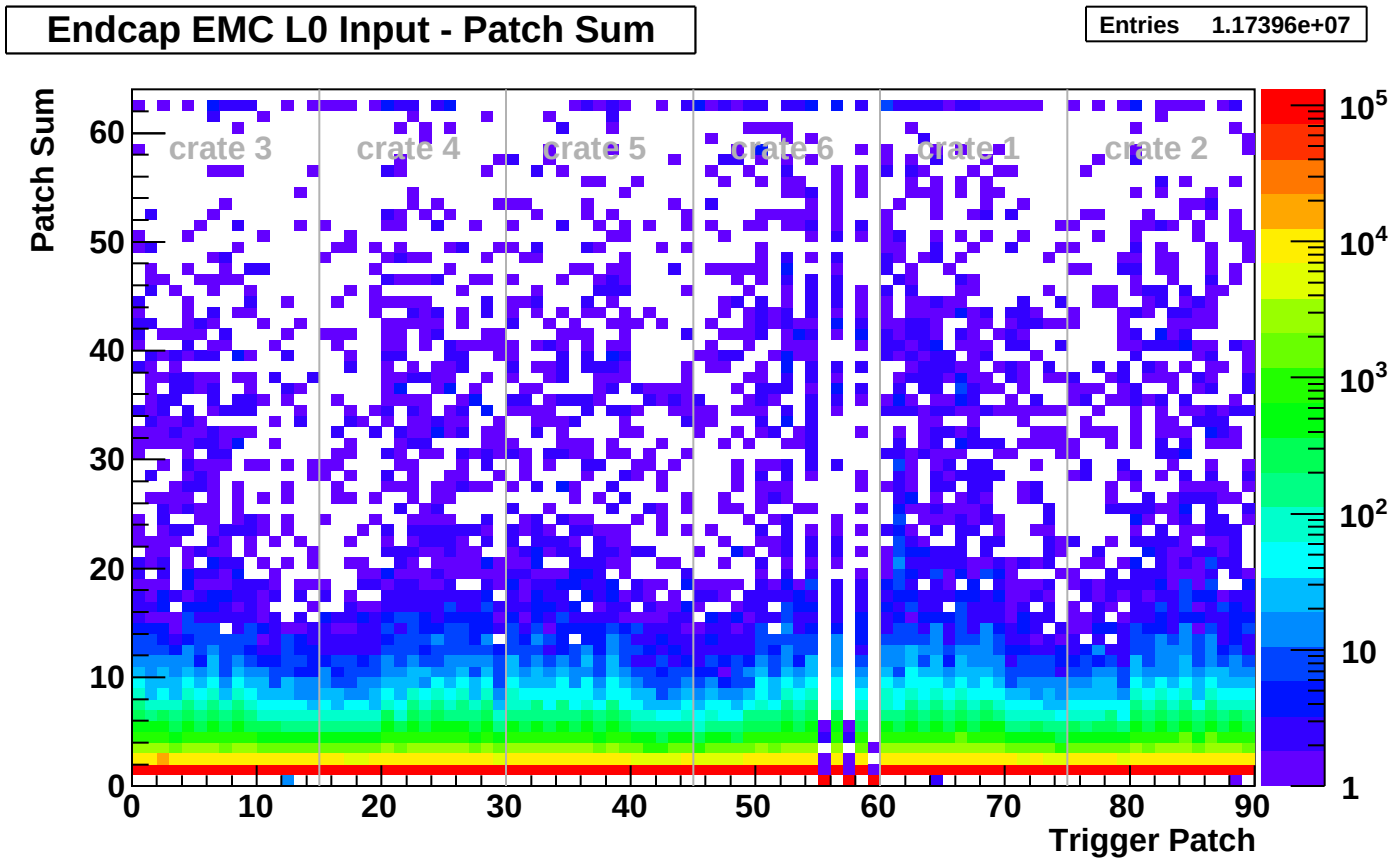

BunchId7Bit (BHT3)
Entries 1670

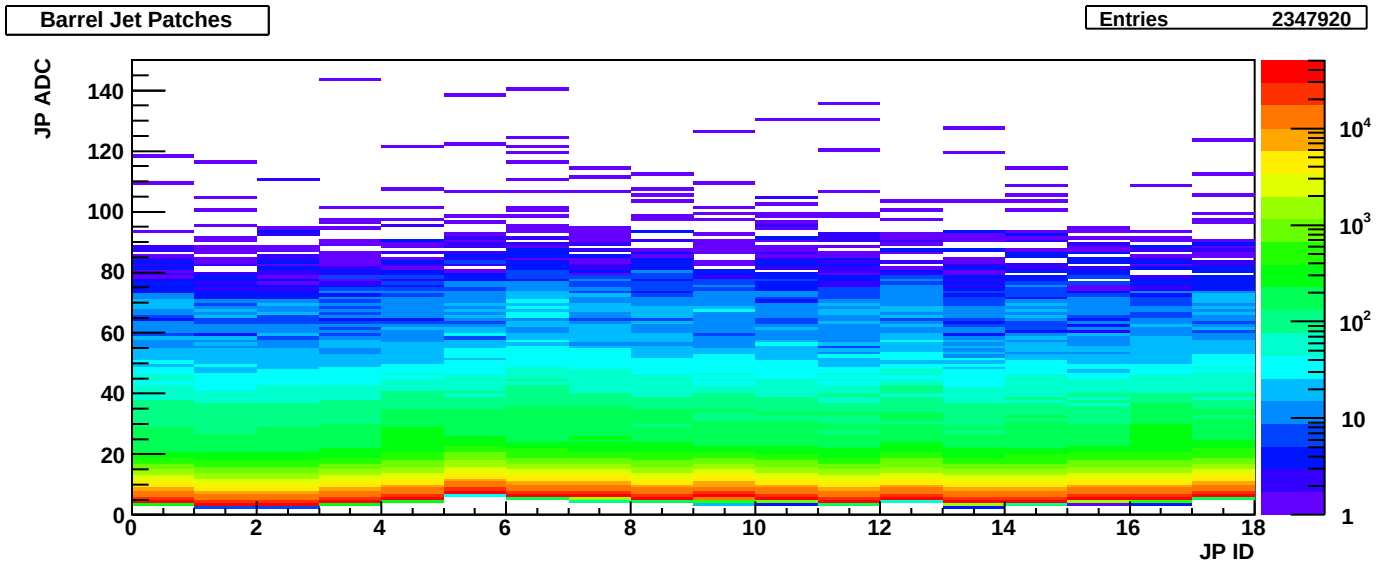

BunchId7Bit (all events)
Entries 130440

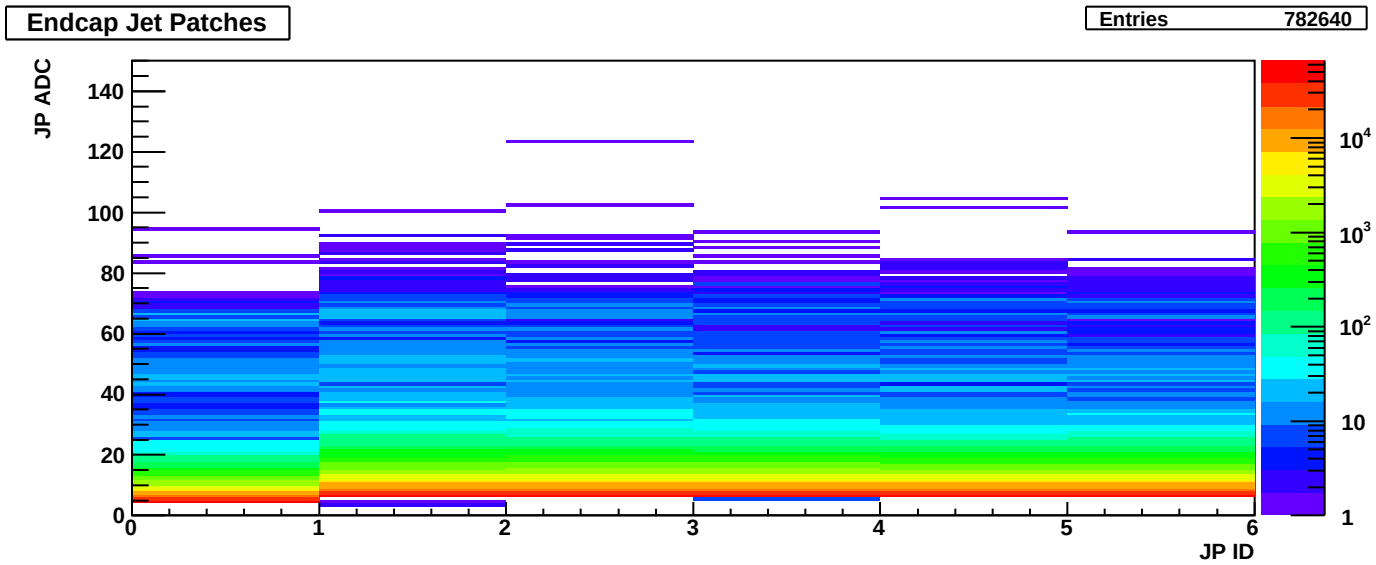

Barrel EMC L0 Input - High Tower
Entries 3.9132e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

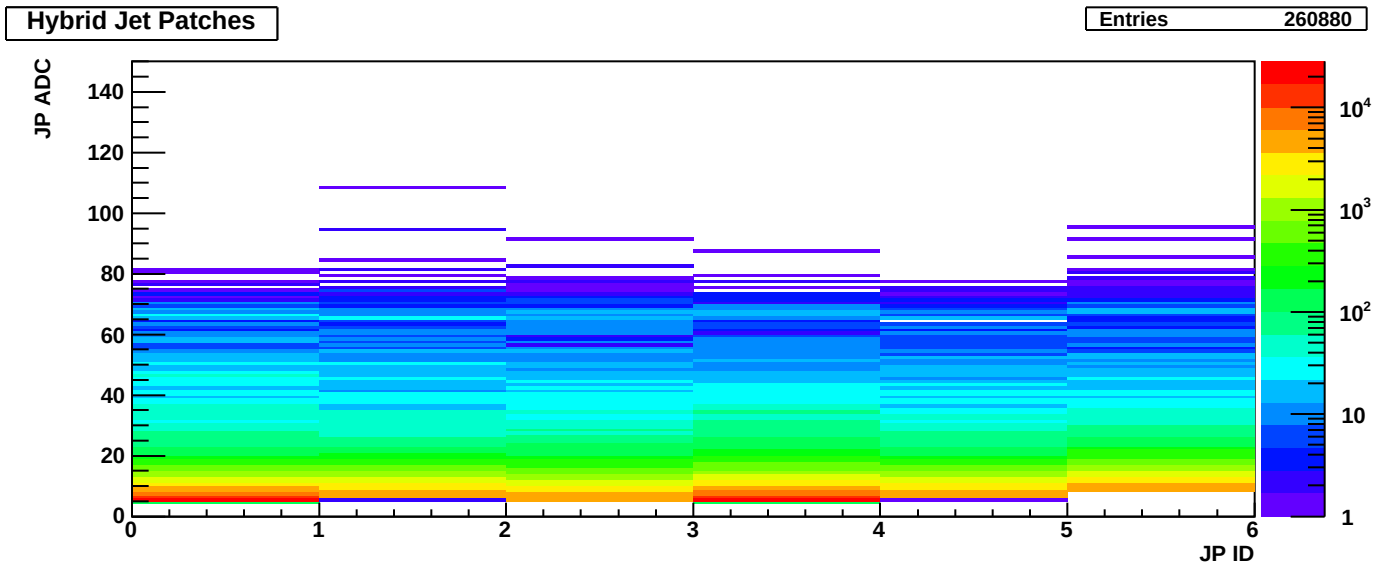

Barrel EMC L0 Input - Patch Sum
Entries 3.9132e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

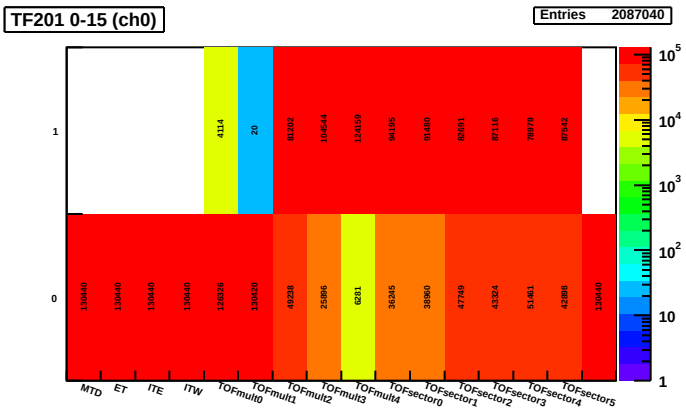

Endcap EMC L0 Input - High Tower
Entries 1.17396e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

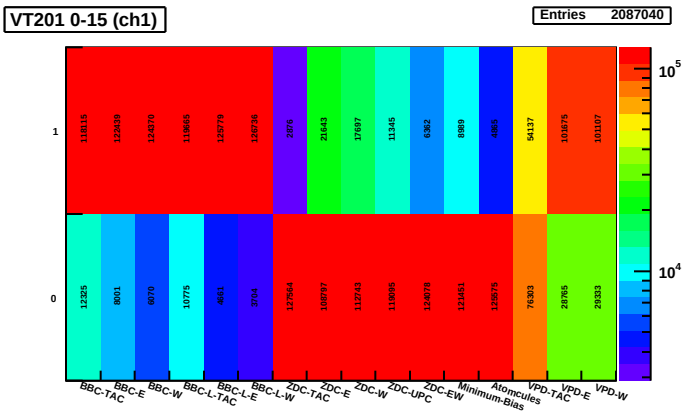

Endcap EMC L0 Input - Patch Sum
Entries 1.17396e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

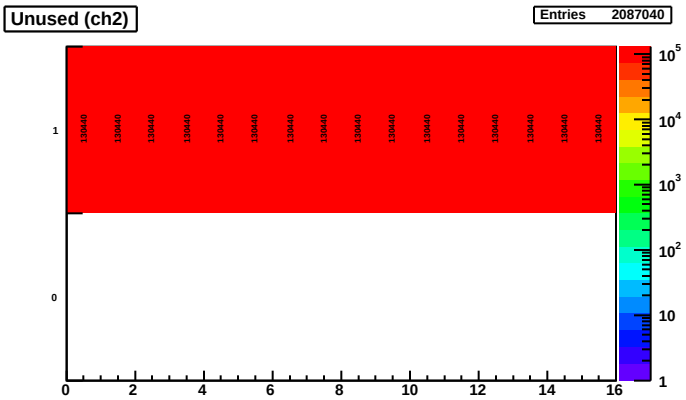

Barrel Jet Patches
Entries 2347920
JP ADC
JP ID

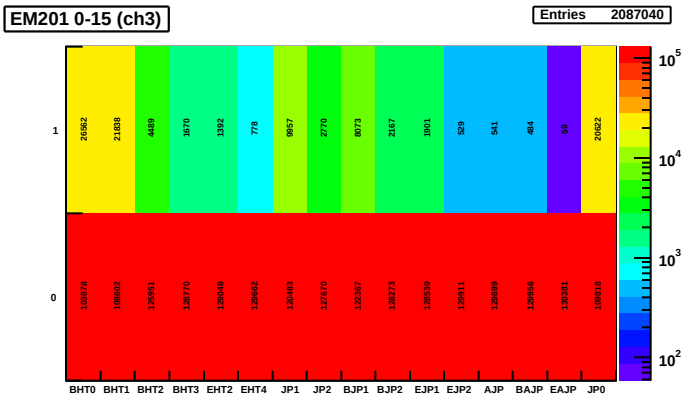

Endcap Jet Patches
Entries 782640
JP ADC
JP ID

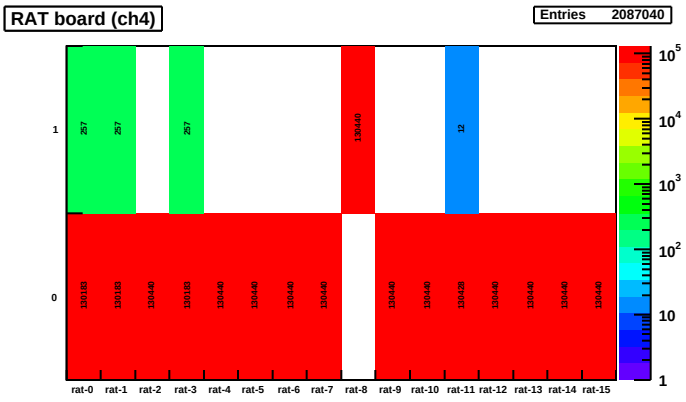

Hybrid Jet Patches
Entries 260880
JP ADC
JP ID

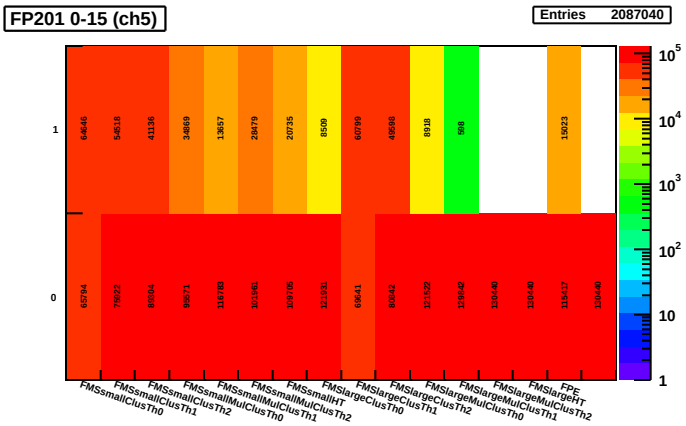

TF201 0-15 (ch0)
Entries 2087040
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

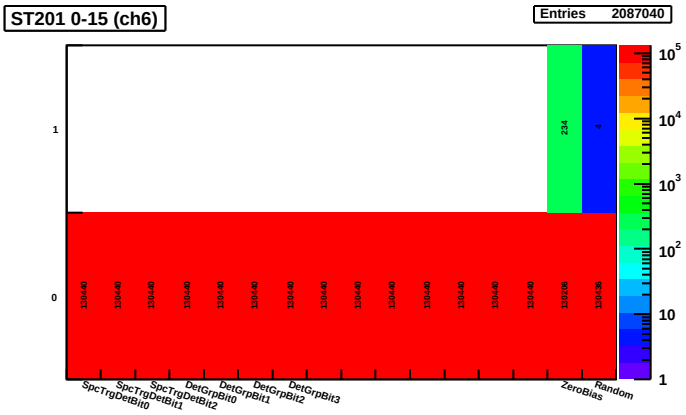

VT201 0-15 (ch1)
Entries 2087040
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

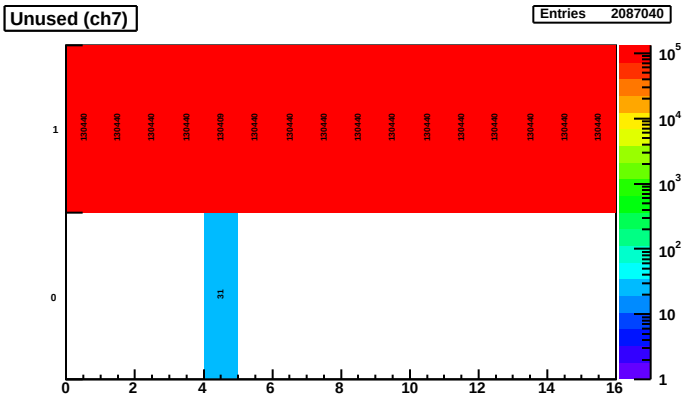

Unused (ch2)
Entries 2087040

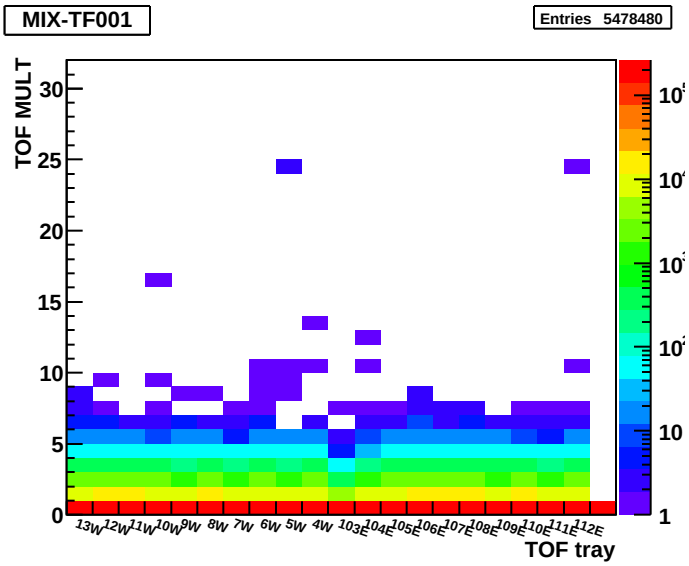

EM201 0-15 (ch3)
Entries 2087040
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

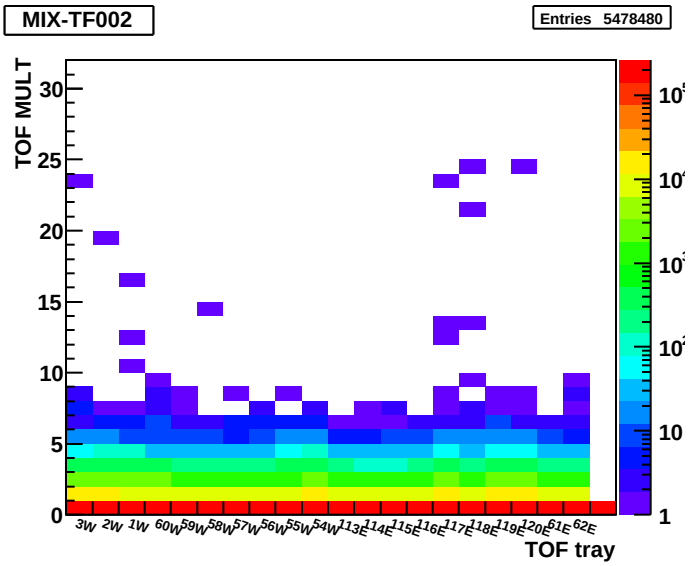

RAT board (ch4)
Entries 2087040
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

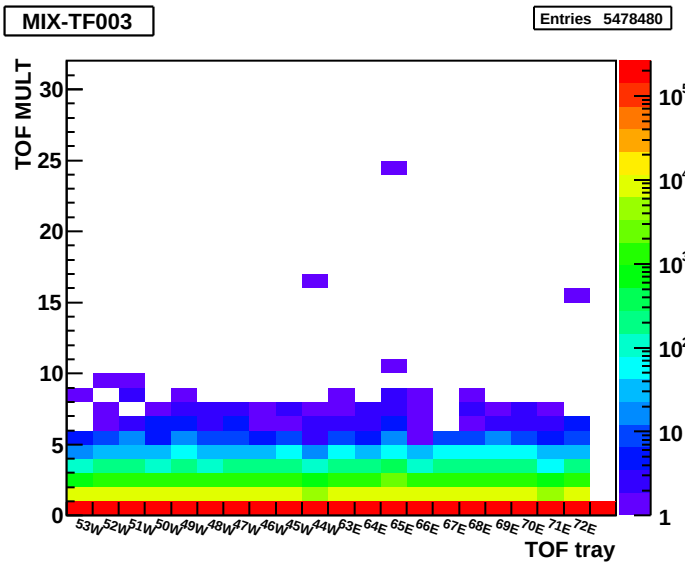

FP201 0-15 (ch5)
Entries 2087040
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

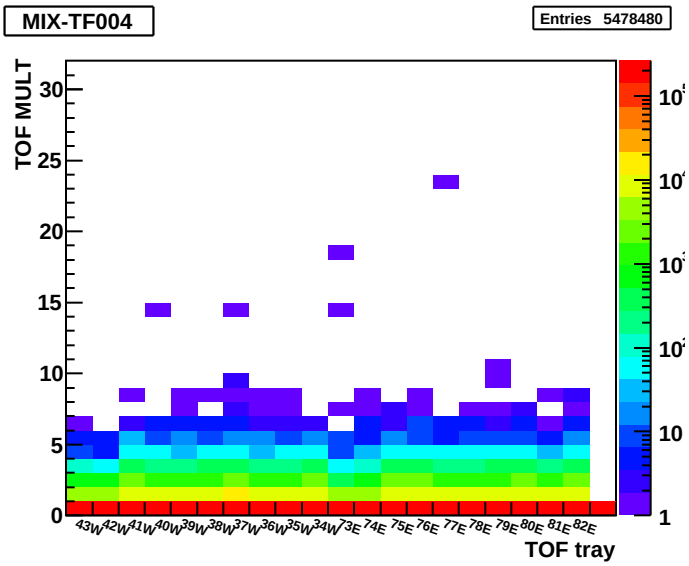

ST201 0-15 (ch6)
Entries 2087040
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

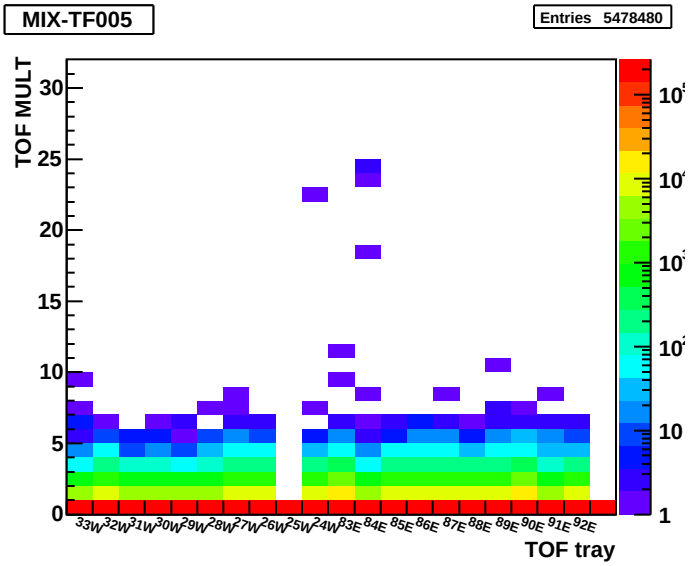

Unused (ch7)
Entries 2087040

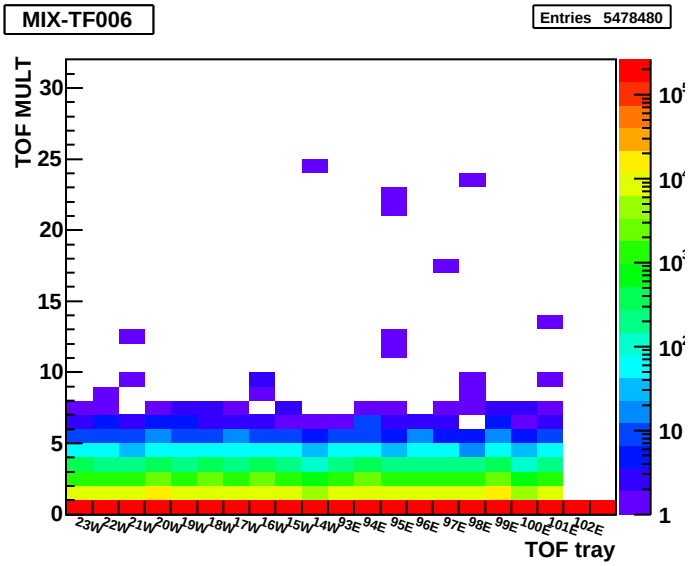

MIX-TF001
Entries 5478480
TOF MULT
TOF tray

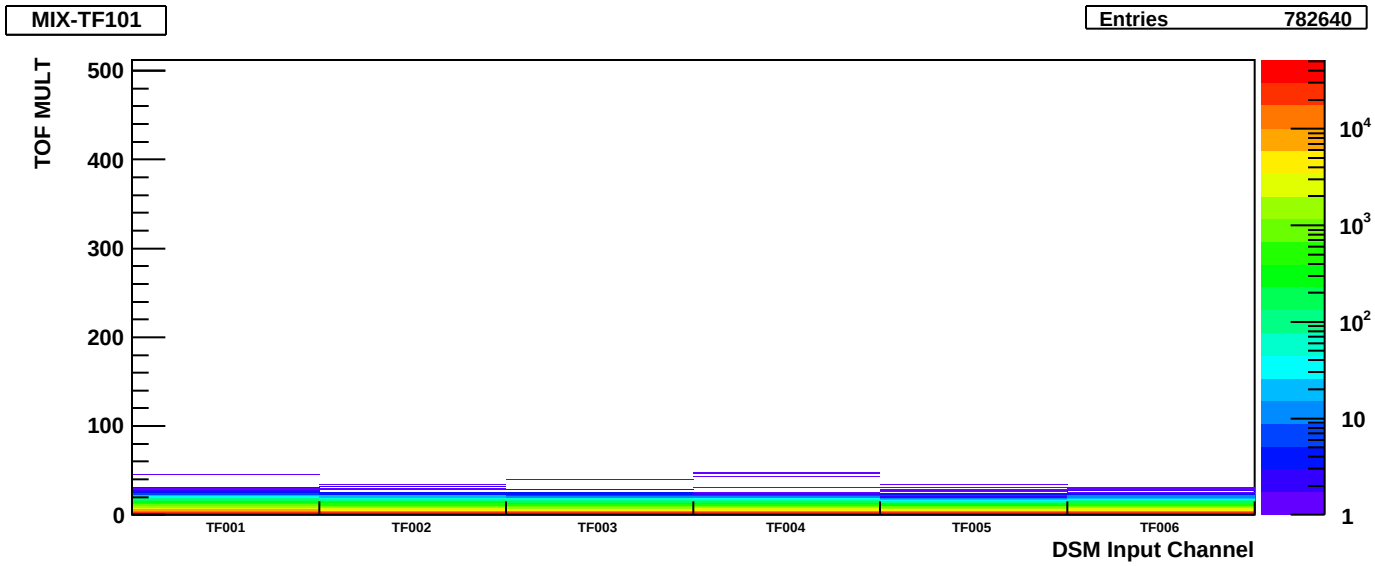

MIX-TF002
Entries 5478480
TOF MULT
TOF tray

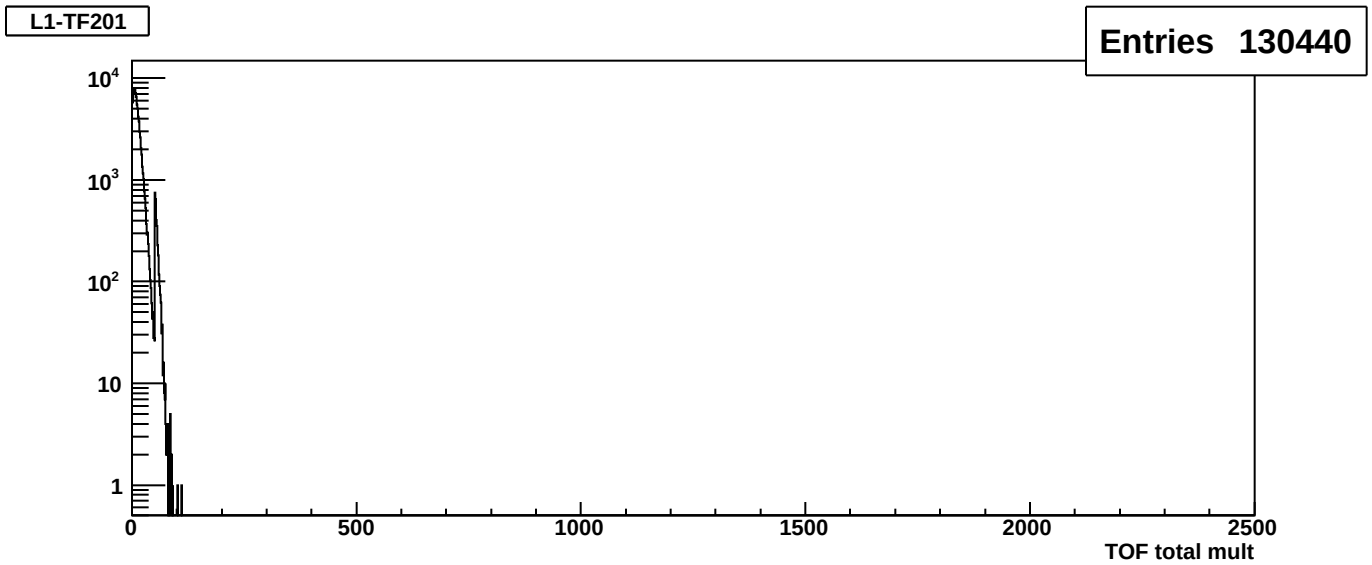

MIX-TF003
Entries 5478480
TOF MULT
TOF tray

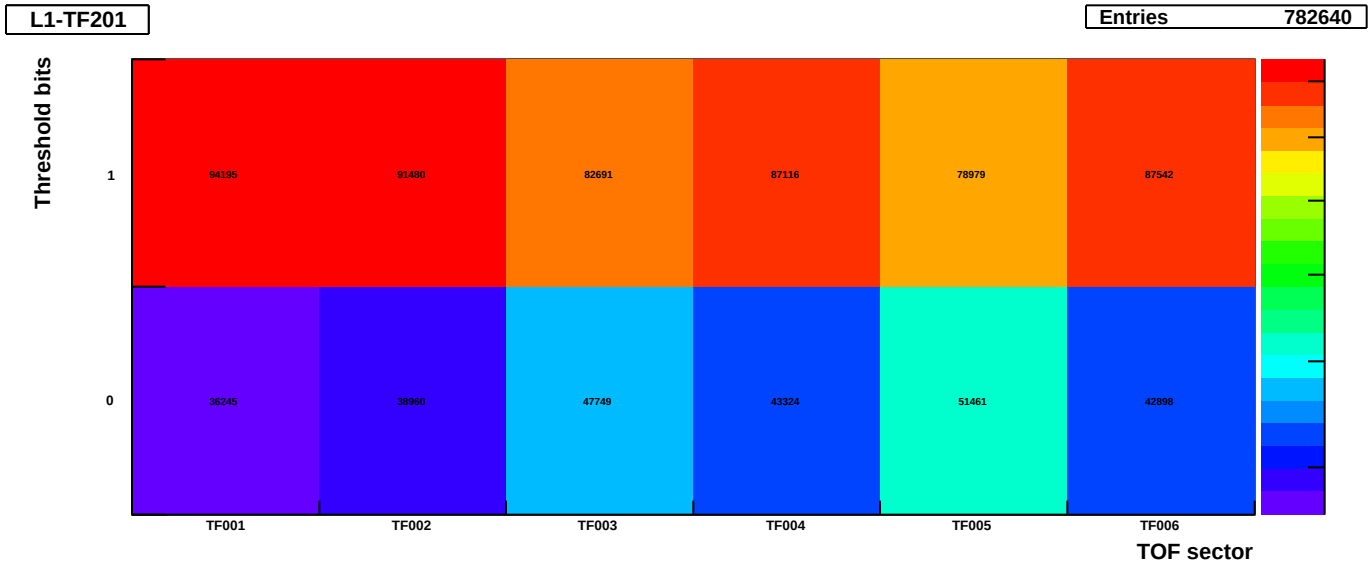

MIX-TF004
Entries 5478480
TOF MULT
TOF tray

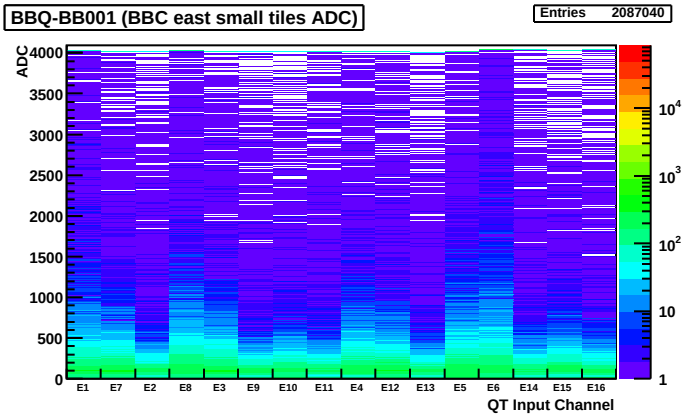

MIX-TF005
Entries 5478480
TOF MULT
TOF tray

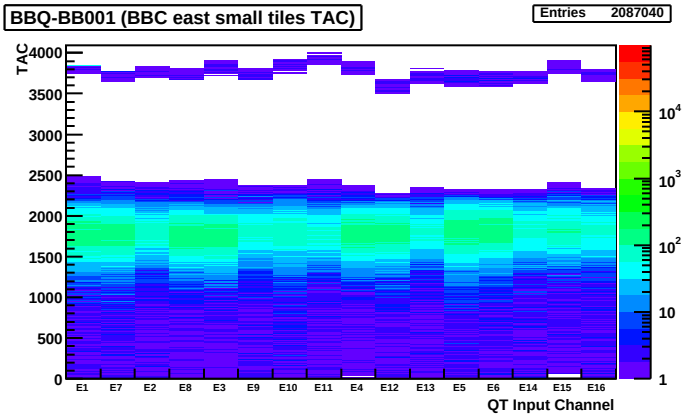

MIX-TF006
Entries 5478480
TOF MULT
TOF tray

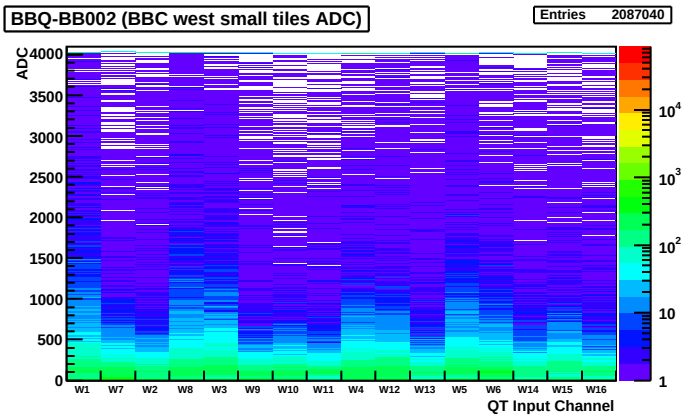

MIX-TF101
Entries 782640
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

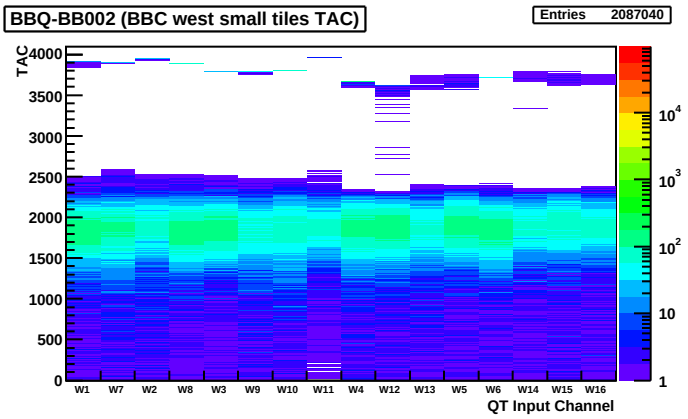

L1-TF201
Entries 130440
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

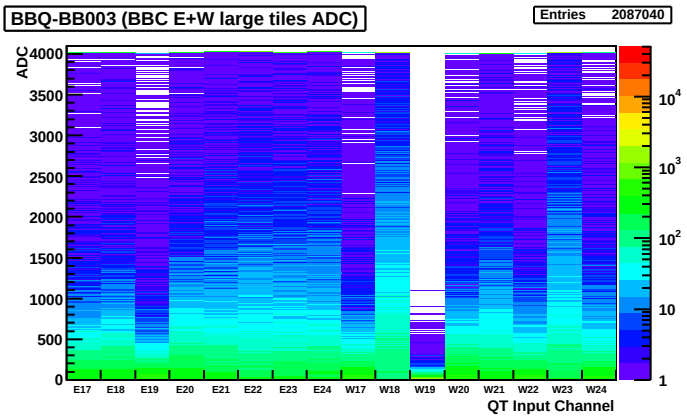

L1-TF201
Entries 782640
Threshold bits
1
0
94195
91480
82691
87116
78979
87542
36245
38960
47749
43324
51461
42898
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

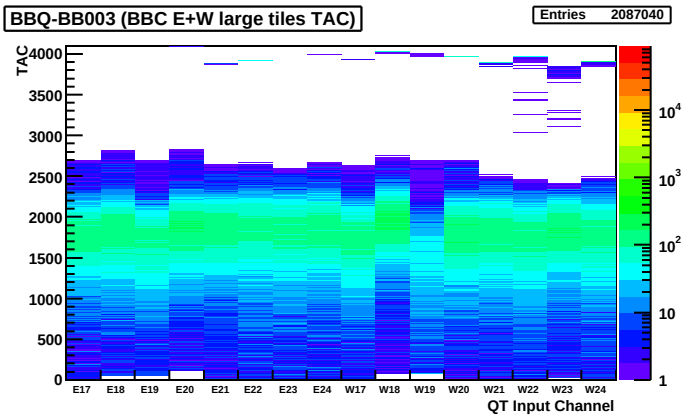

BBQ-BB001 (BBC east small tiles ADC)
Entries 2087040
ADC
QT Input Channel

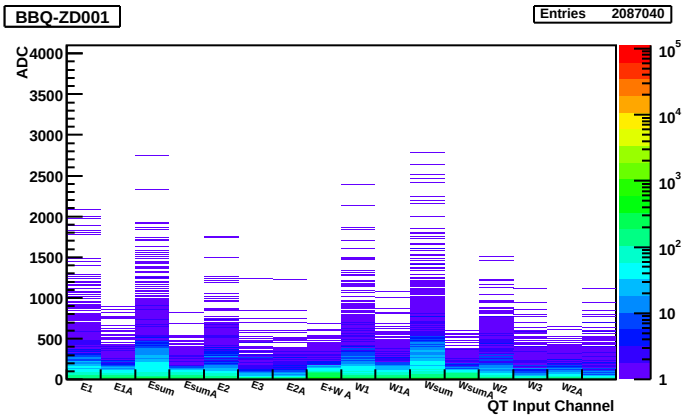

BBQ-BB001 (BBC east small tiles TAC)
Entries 2087040
TAC
QT Input Channel

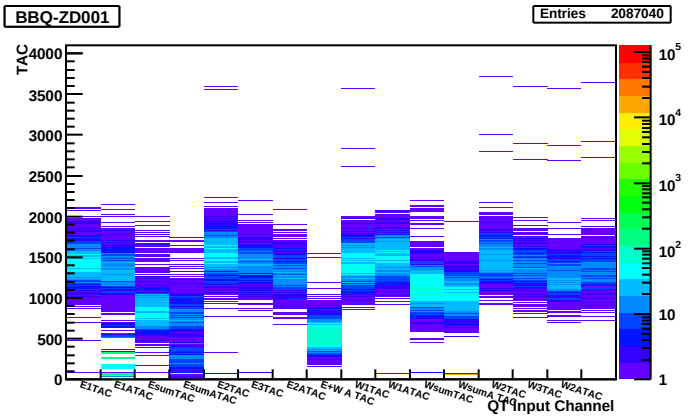

BBQ-BB002 (BBC west small tiles ADC)
Entries 2087040
ADC
QT Input Channel

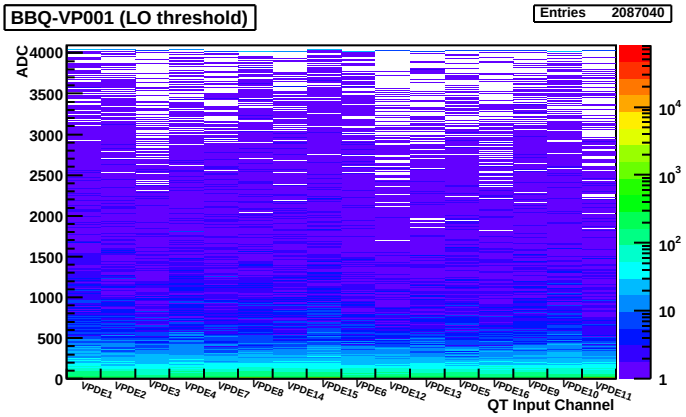

BBQ-BB002 (BBC west small tiles TAC)
Entries 2087040
TAC
QT Input Channel

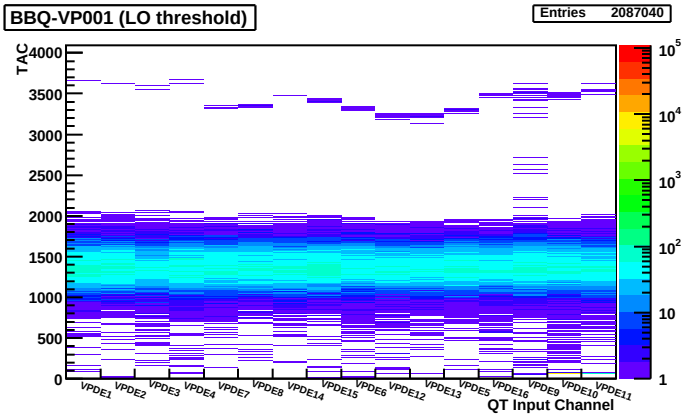

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2087040
ADC
QT Input Channel

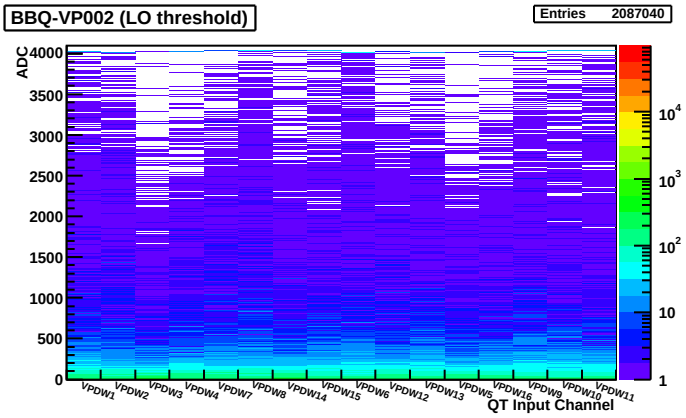

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2087040
TAC
QT Input Channel

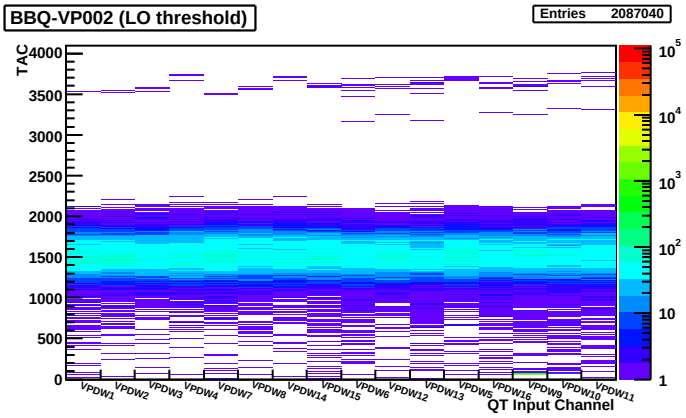

BBQ-ZD001
Entries 2087040
ADC
QT Input Channel

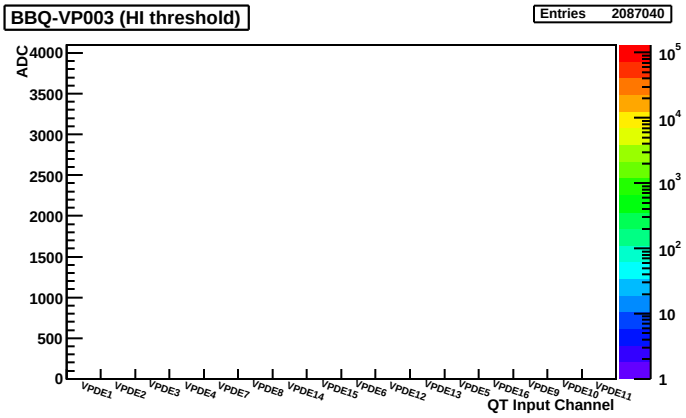

BBQ-ZD001
Entries 2087040
TAC
QT Input Channel

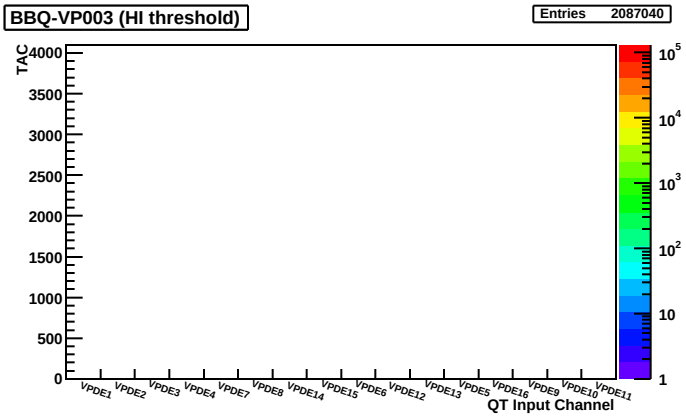

BBQ-VP001 (LO threshold)
Entries 2087040
ADC
QT Input Channel

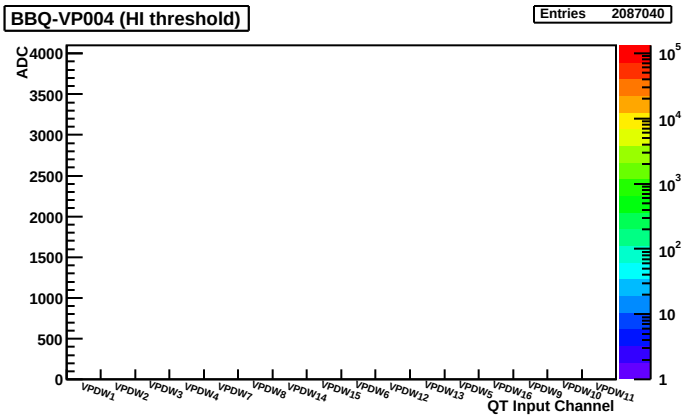

BBQ-VP001 (LO threshold)
Entries 2087040
TAC
QT Input Channel

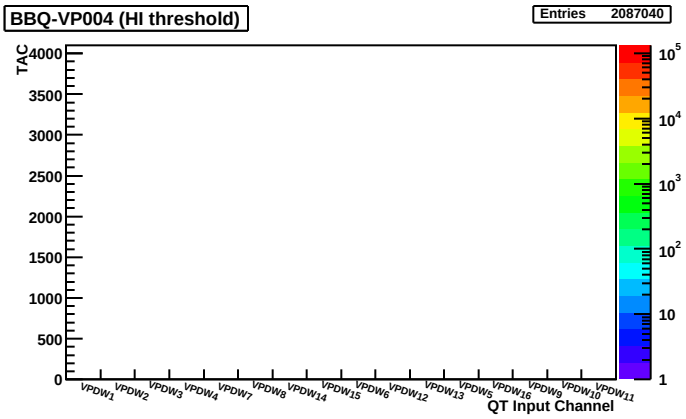

BBQ-VP002 (LO threshold)
Entries 2087040
ADC
QT Input Channel

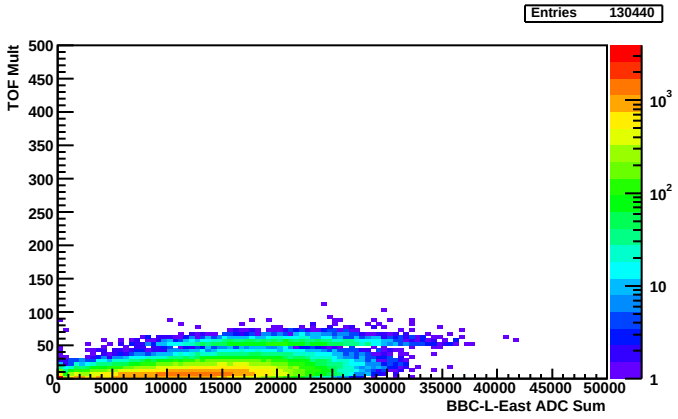

BBQ-VP002 (LO threshold)
Entries 2087040
TAC
QT Input Channel

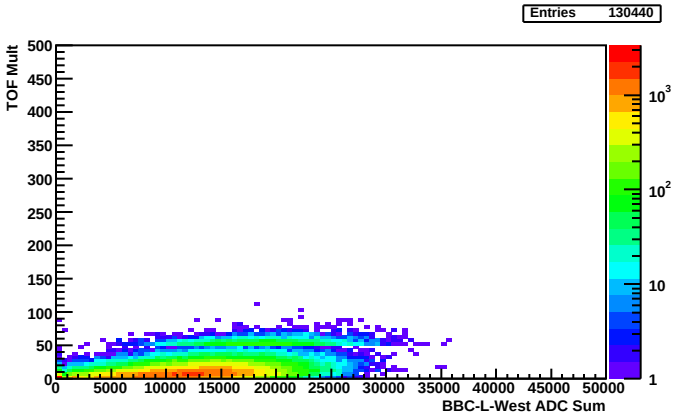

BBQ-VP003 (HI threshold)
Entries 2087040
ADC
QT Input Channel

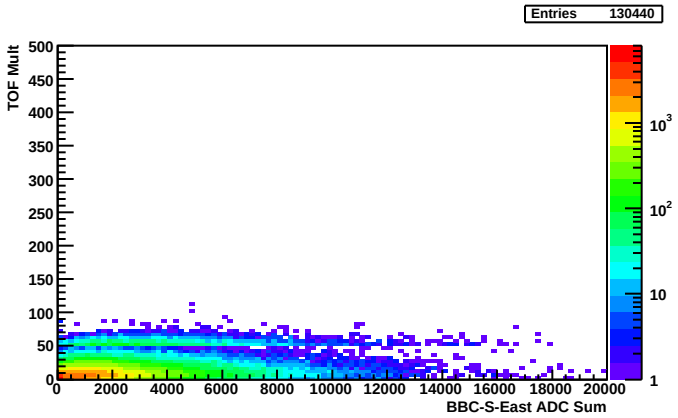

BBQ-VP003 (HI threshold)
Entries 2087040
TAC
QT Input Channel

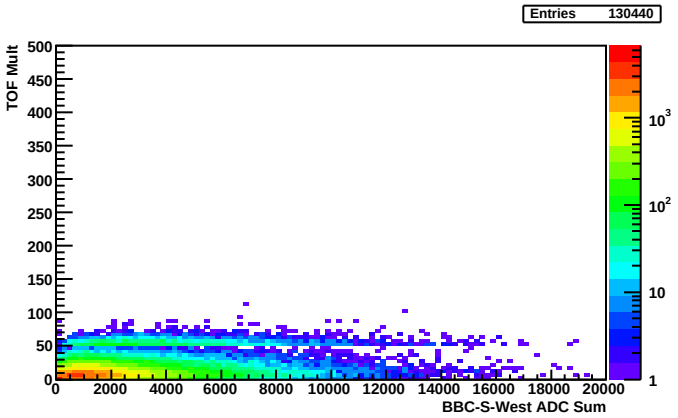

BBQ-VP004 (HI threshold)
Entries 2087040
ADC
QT Input Channel

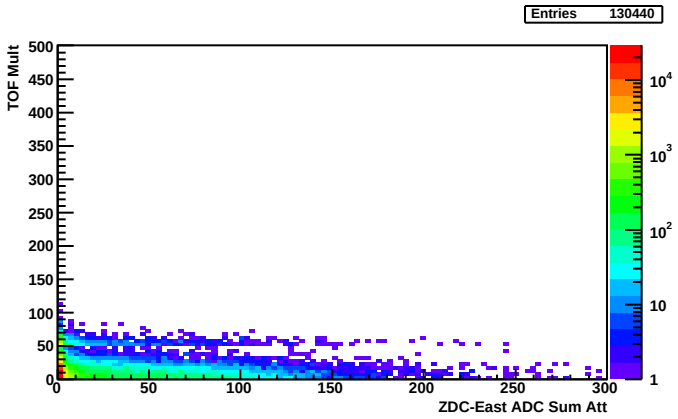

BBQ-VP004 (HI threshold)
Entries 2087040
TAC
QT Input Channel

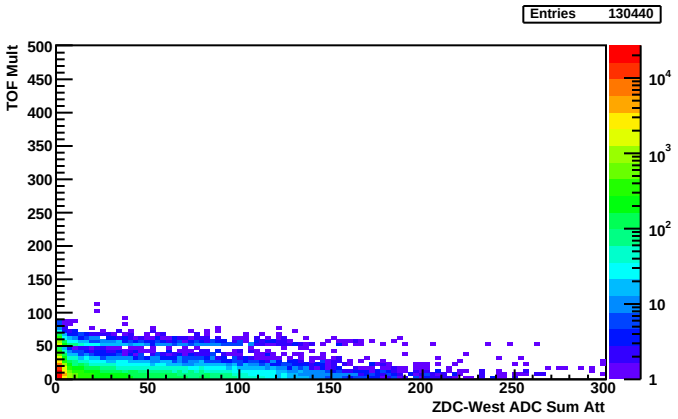

Entries 130440
TOF Mult
BBC-L-East ADC Sum

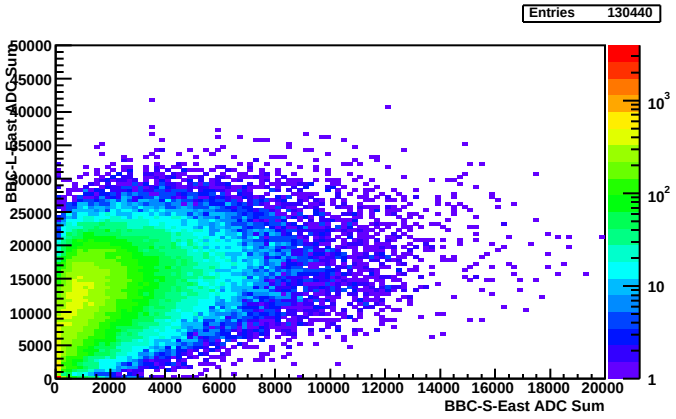

Entries 130440
TOF Mult
BBC-L-West ADC Sum

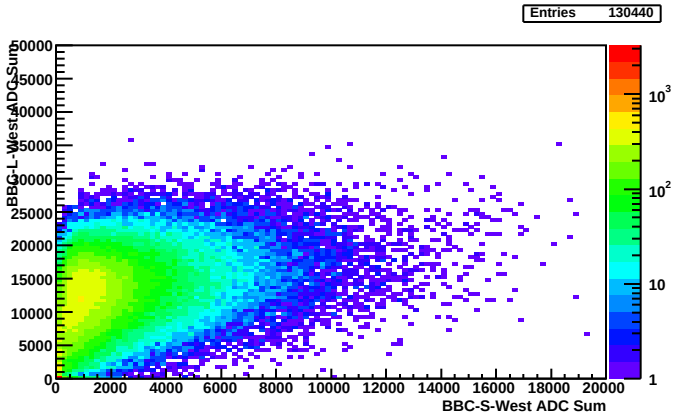

Entries 130440
TOF Mult
BBC-S-East ADC Sum

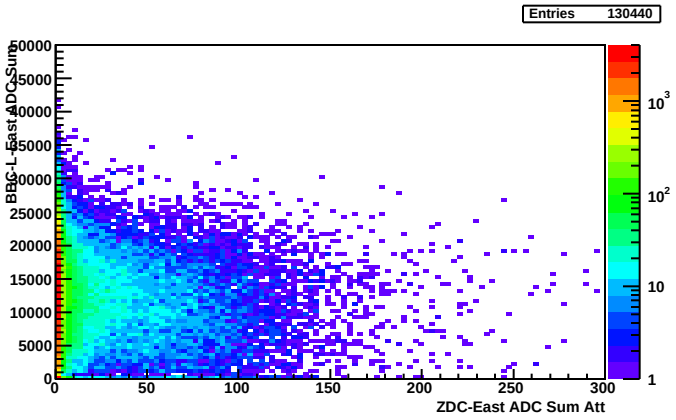

Entries 130440
TOF Mult
BBC-S-West ADC Sum

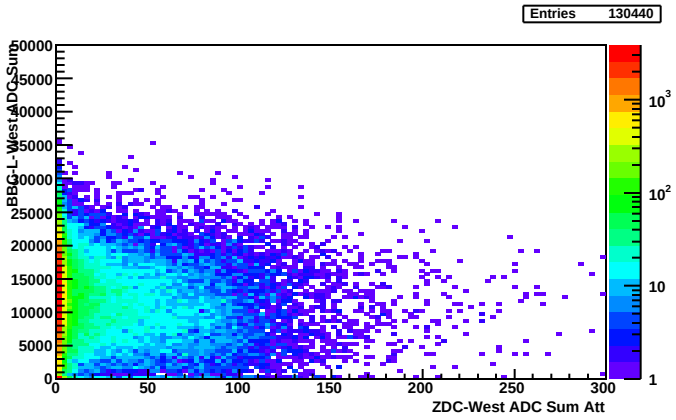

Entries 130440
TOF Mult
ZDC-East ADC Sum Att

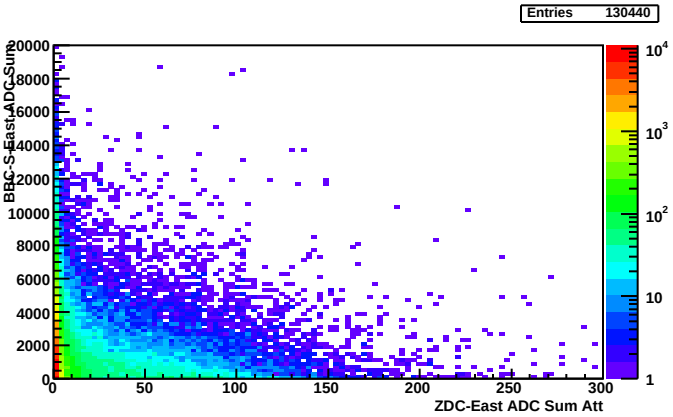

Entries 130440
TOF Mult
ZDC-West ADC Sum Att

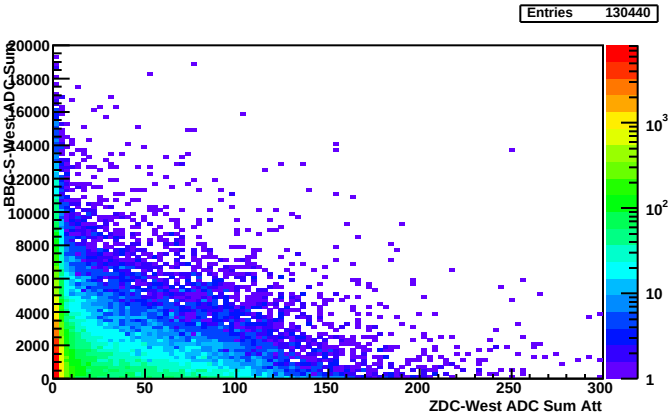

Entries 130440
BBC-L-East ADC Sum
BBC-S-East ADC Sum

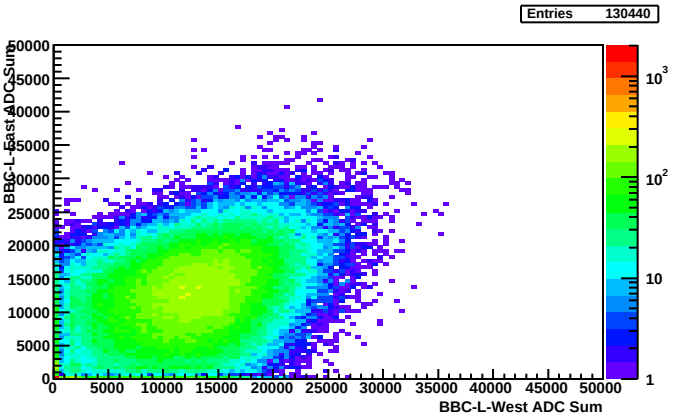

Entries 130440
BBC-L-West ADC Sum
BBC-S-West ADC Sum

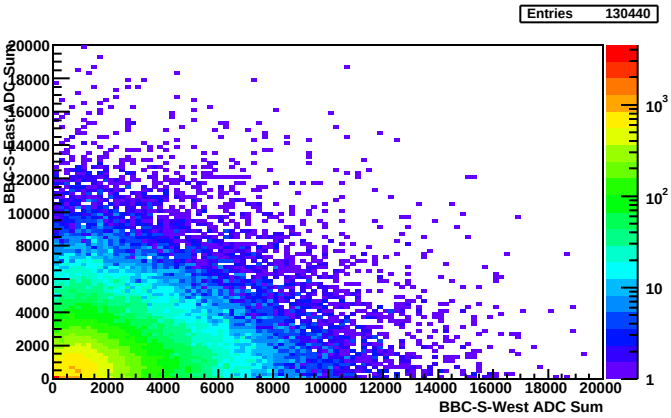

Entries 130440
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

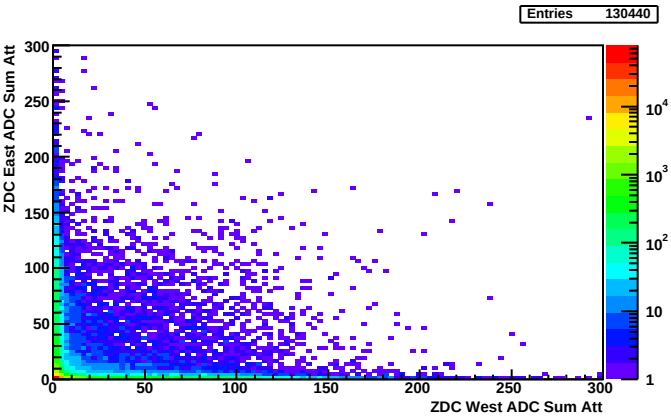

Entries 130440
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

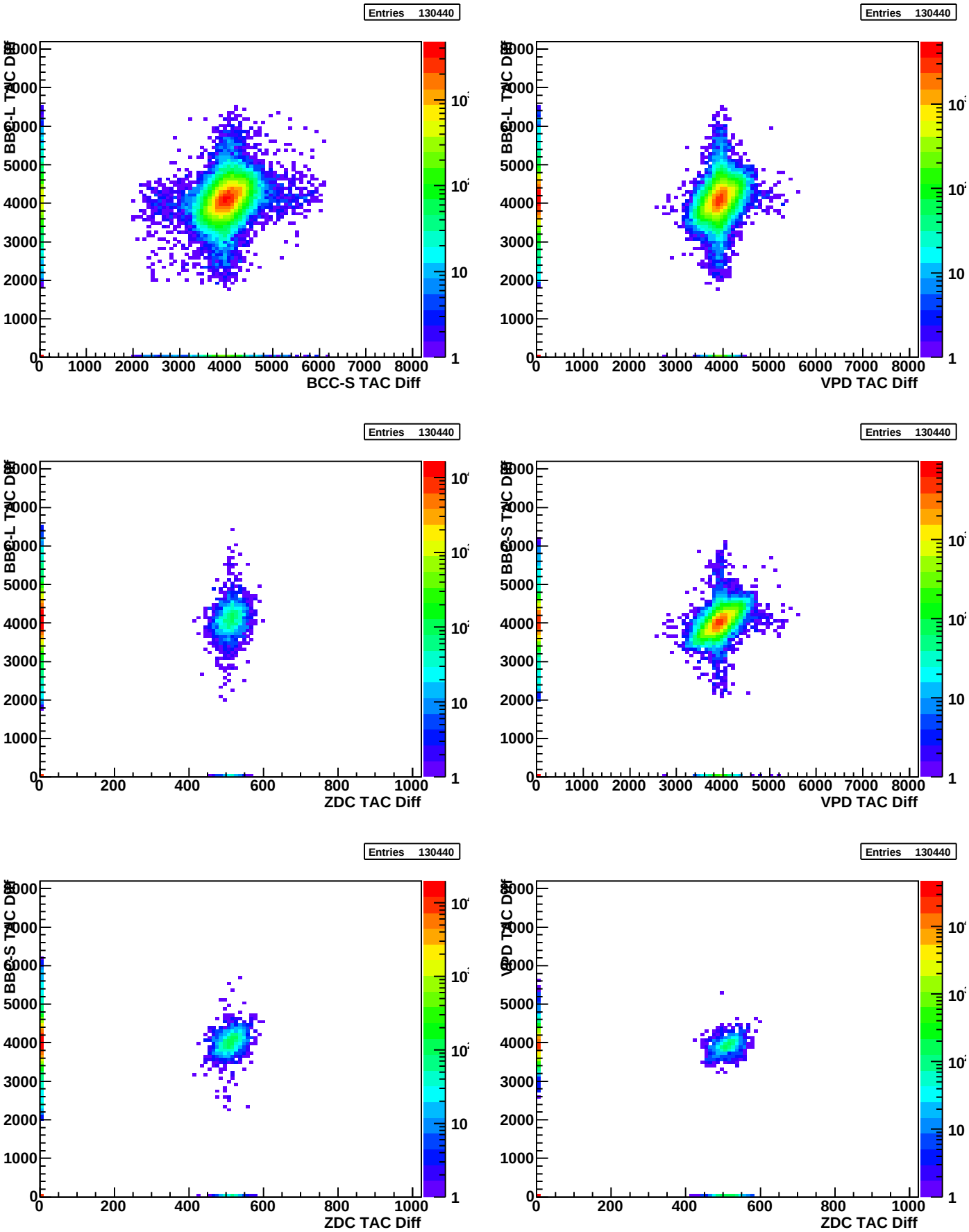

Entries 130440
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

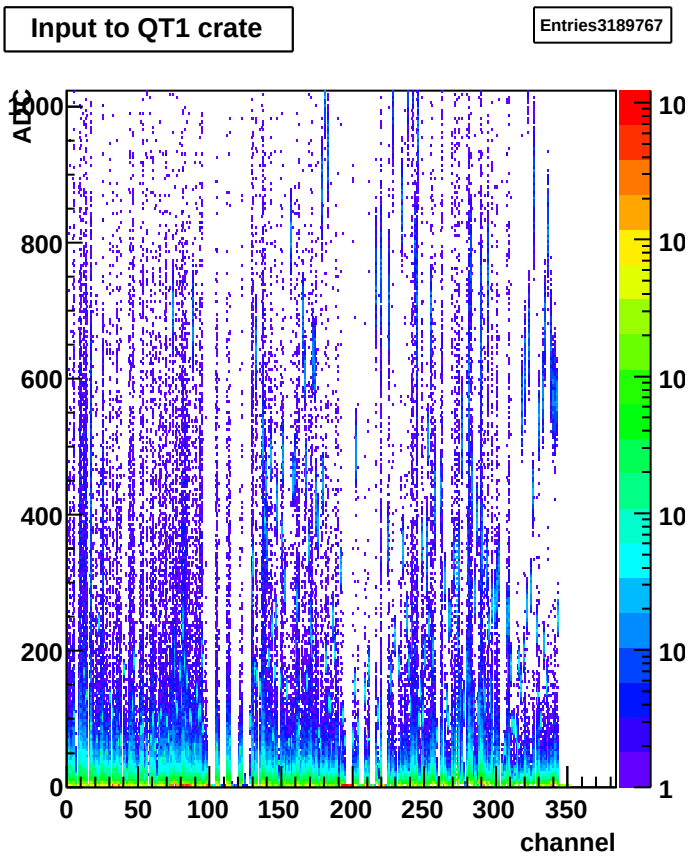

Entries 130440
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

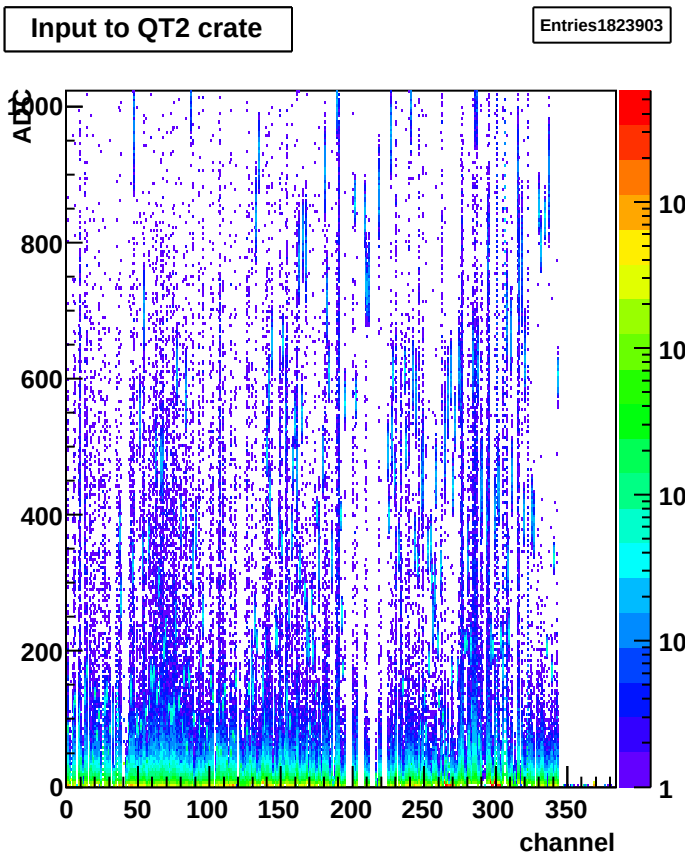

Entries 130440
BBC-L-East ADC Sum
BBC-L-West ADC Sum

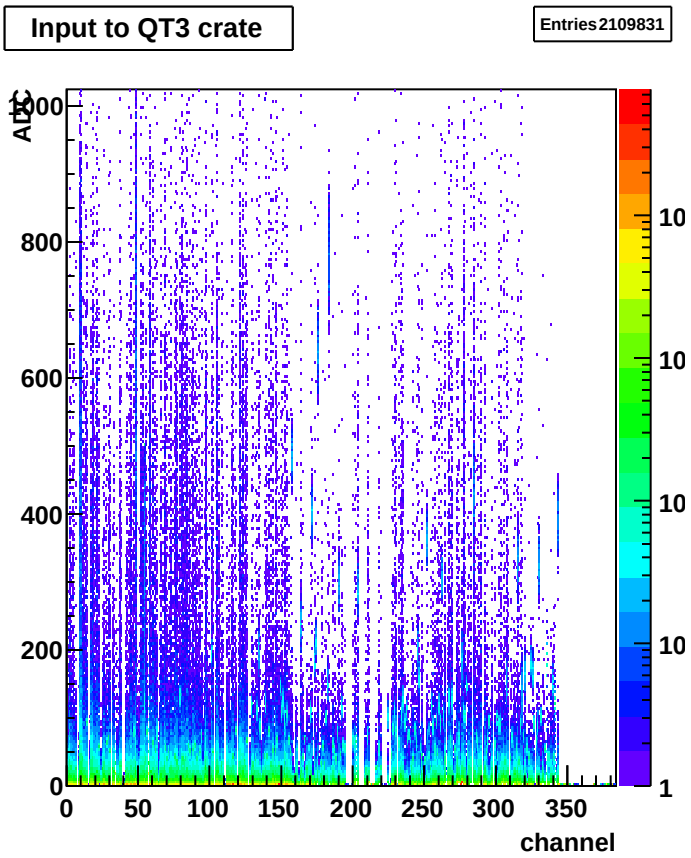

Entries 130440
BBC-S-East ADC Sum
BBC-S-West ADC Sum

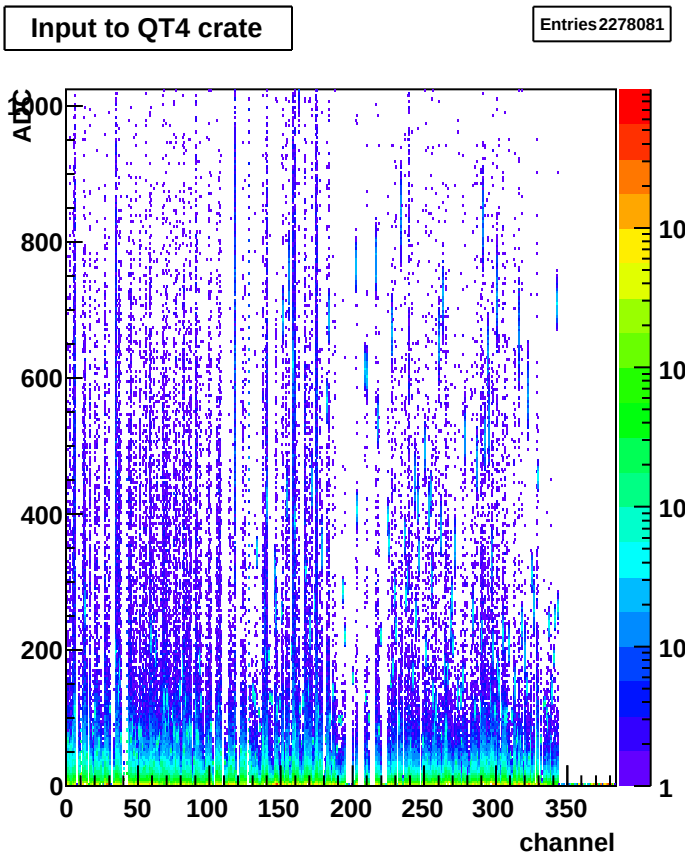

Entries 130440
ZDC East ADC Sum Att
ZDC West ADC Sum Att

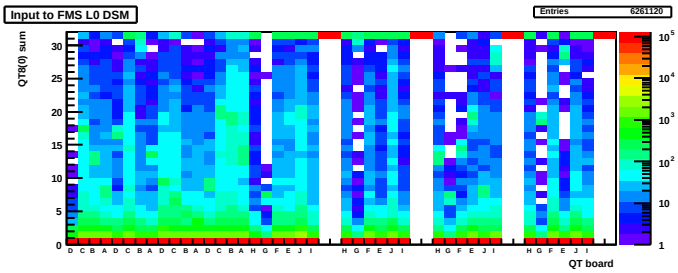

Entries 130440
BBC-L TAC Diff
BCC-S TAC Diff
Entries 130440
BBC-L TAC Diff
VPD TAC Diff
Entries 130440
BBC-L TAC Diff
ZDC TAC Diff
Entries 130440
BBC-S TAC Diff
VPD TAC Diff
Entries 130440
BBC-S TAC Diff
ZDC TAC Diff
Entries 130440
VPD TAC Diff
ZDC TAC Diff

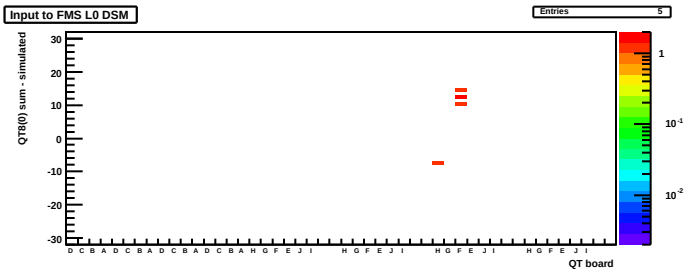

Input to QT1 crate
Entries3189767
ADC
channel

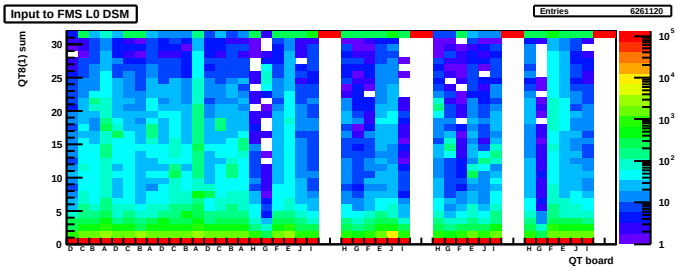

Input to QT2 crate
Entries1823903
ADC
channel

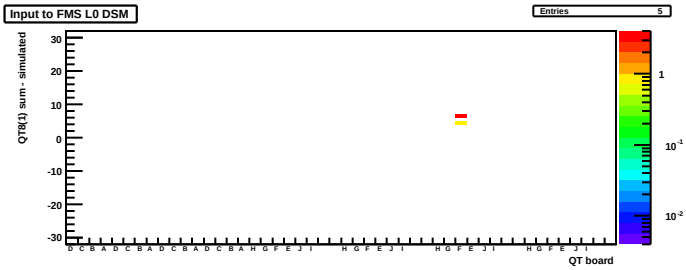

Input to QT3 crate
Entries2109831
ADC
channel

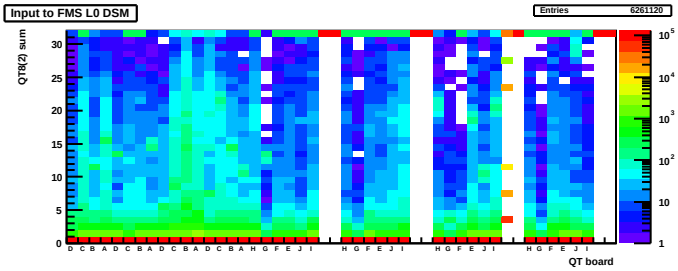

Input to QT4 crate
Entries2278081
ADC
channel

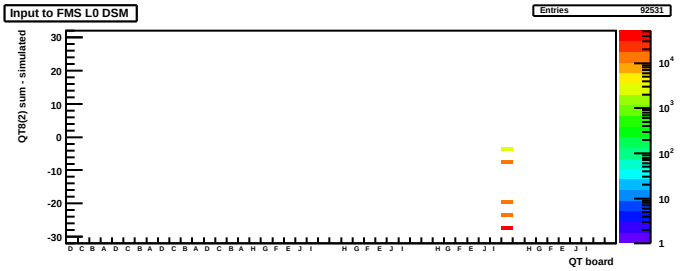

Input to FMS L0 DSM
Entries 6261120
QT8(0) sum
QT board

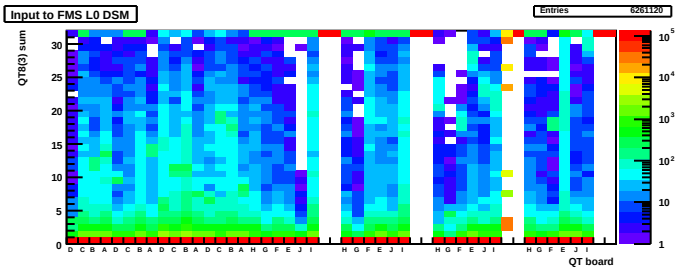

Input to FMS L0 DSM
Entries 5
QT8(0) sum - simulated
QT board

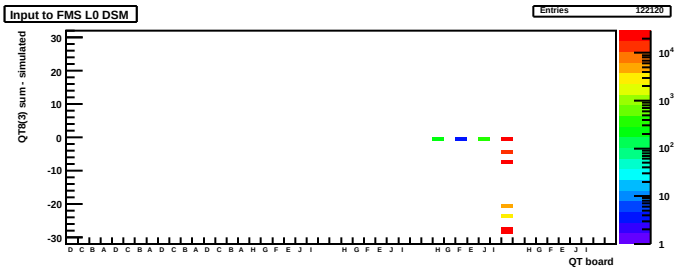

Input to FMS L0 DSM
Entries 6261120
QT8(1) sum
QT board

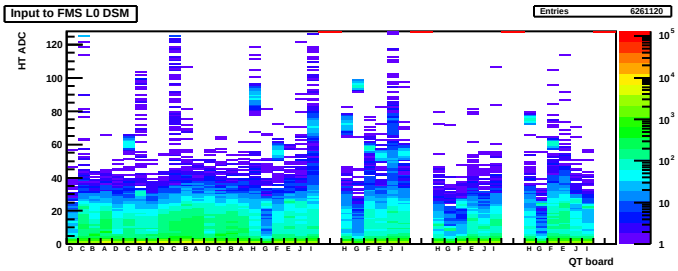

Input to FMS L0 DSM
Entries 5
QT8(1) sum - simulated
QT board

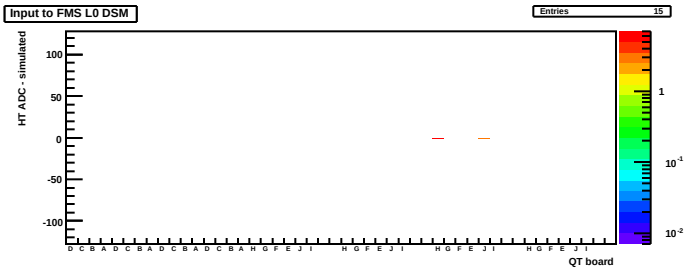

Input to FMS L0 DSM
Entries 6261120
QT8(2) sum
QT board

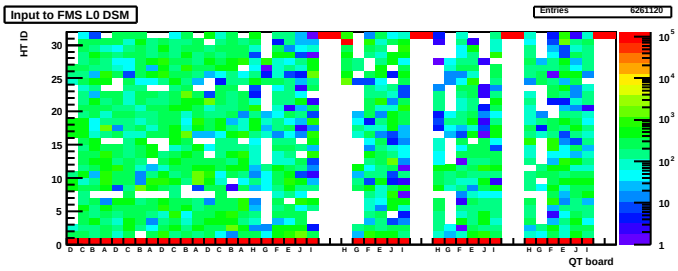

Input to FMS L0 DSM
Entries 92531
QT8(2) sum - simulated
QT board

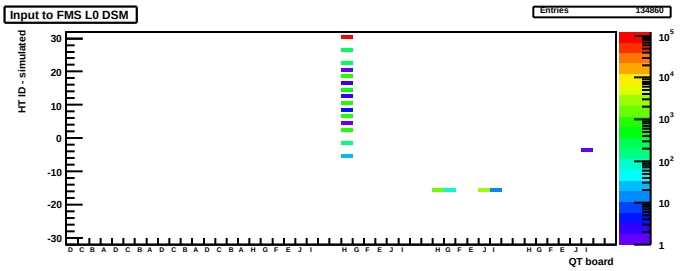

Input to FMS L0 DSM
Entries 6261120
QT8(3) sum
QT board

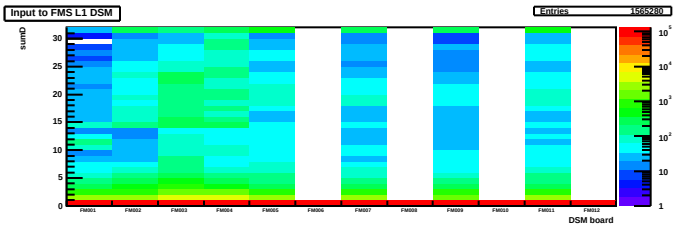

Input to FMS L0 DSM
Entries 122120
QT8(3) sum - simulated
QT board

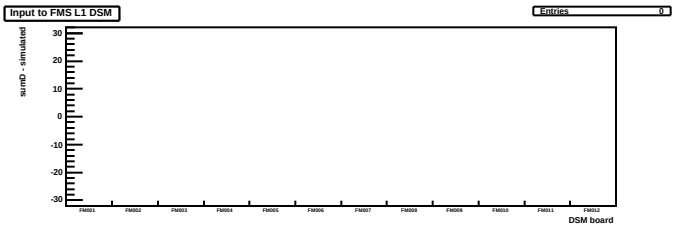

Input to FMS L0 DSM
Entries 6261120
HT ADC
QT board

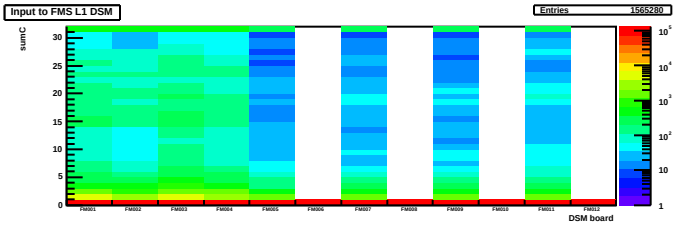

Input to FMS L0 DSM
Entries 15
HT ADC - simulated
QT board

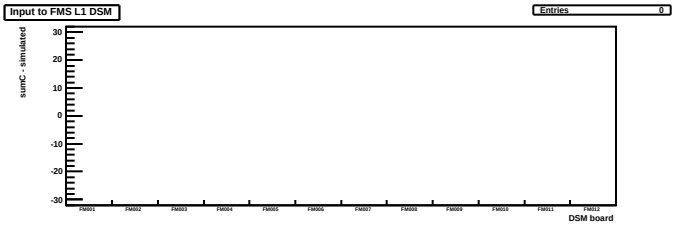

Input to FMS L0 DSM
Entries 6261120
HT ID
QT board

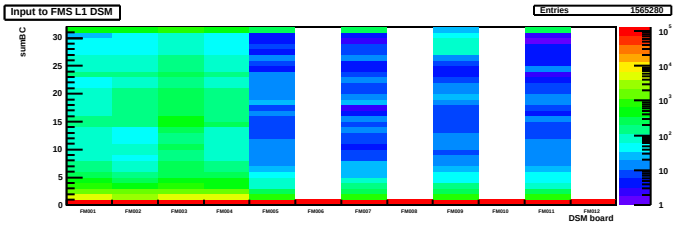

Input to FMS L0 DSM
Entries 134860
HT ID - simulated
QT board

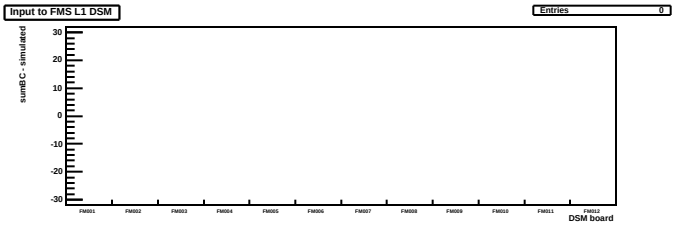

Input to FMS L1 DSM
Entries 1565280
sumD
DSM board

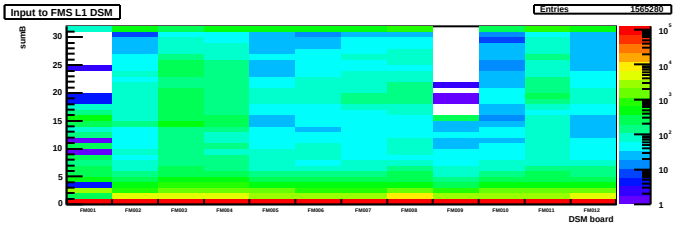

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

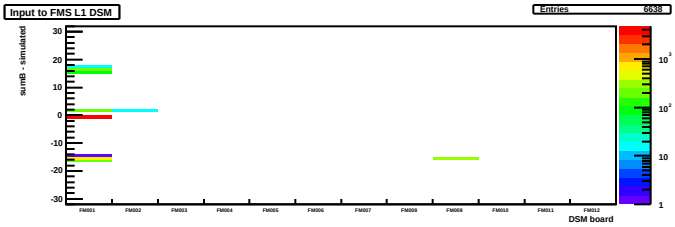

Input to FMS L1 DSM
Entries 1565280
sumC
DSM board

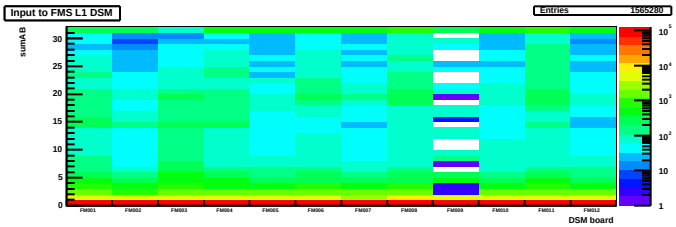

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

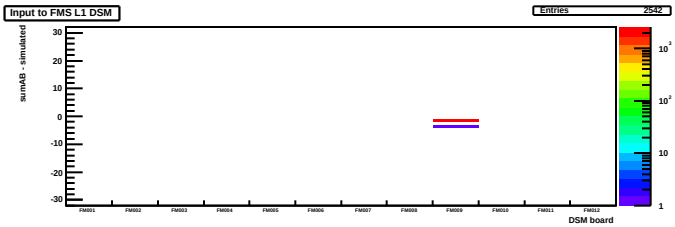

Input to FMS L1 DSM
Entries 1565280
sumBC
DSM board

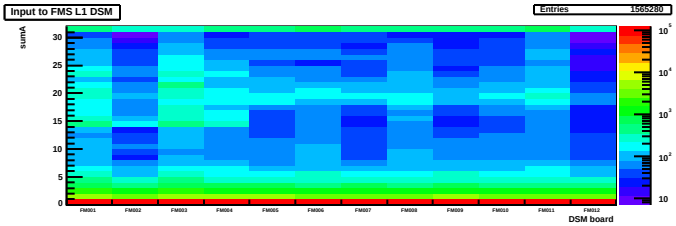

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

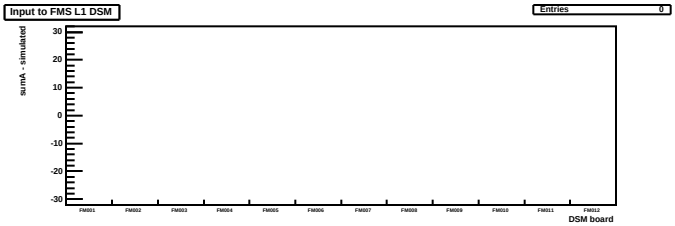

Input to FMS L1 DSM
Entries 1565280
sumB
DSM board

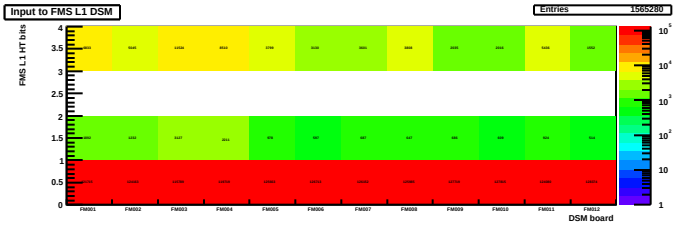

Input to FMS L1 DSM
Entries 6638
sumB - simulated
DSM board

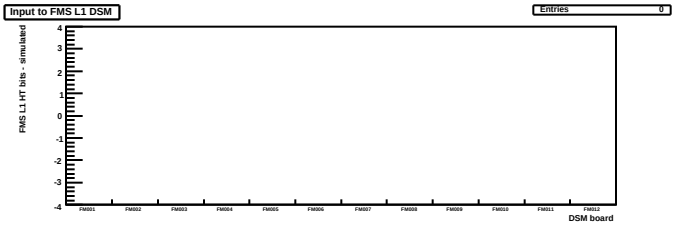

Input to FMS L1 DSM
Entries 1565280
sumAB
DSM board

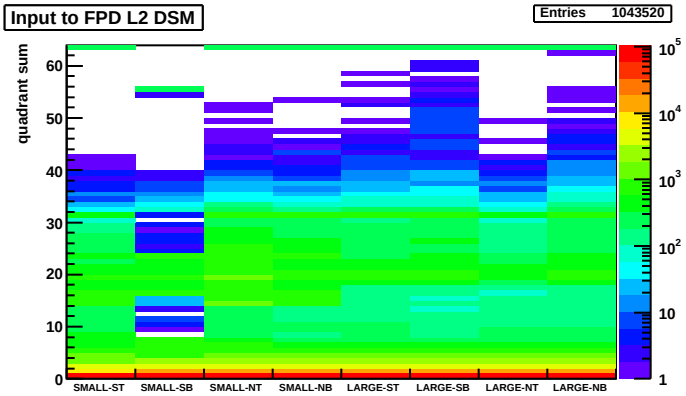

Input to FMS L1 DSM
Entries 2542
sumAB - simulated
DSM board

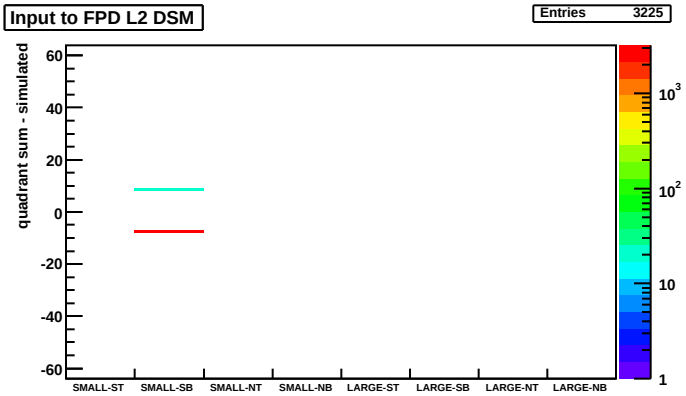

Input to FMS L1 DSM
Entries 1565280
sumA
DSM board

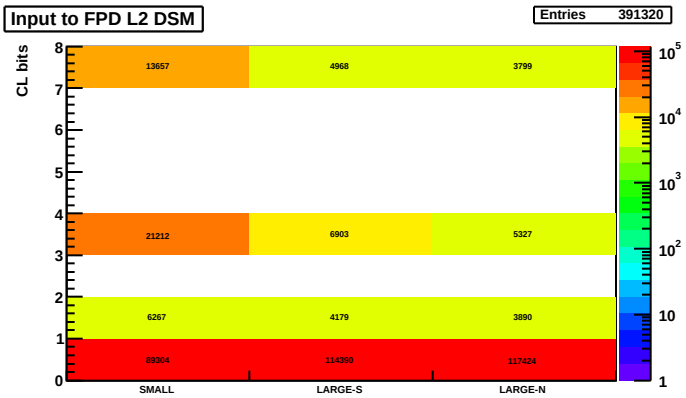

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

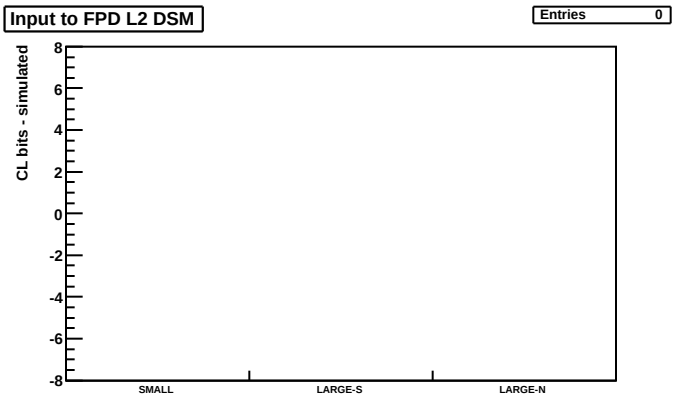

Input to FMS L1 DSM
Entries 1565280
FMS L1 HT bits
DSM board

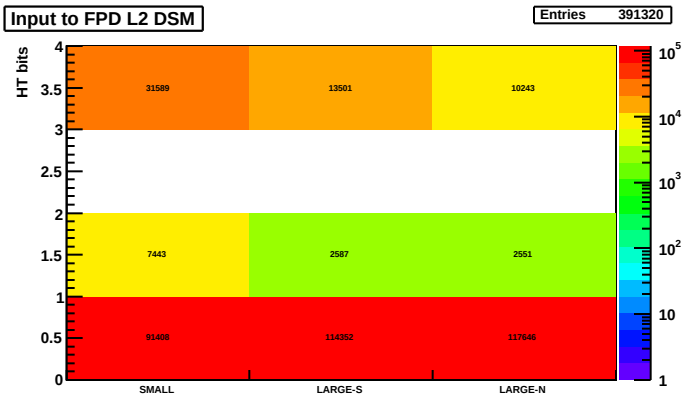

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

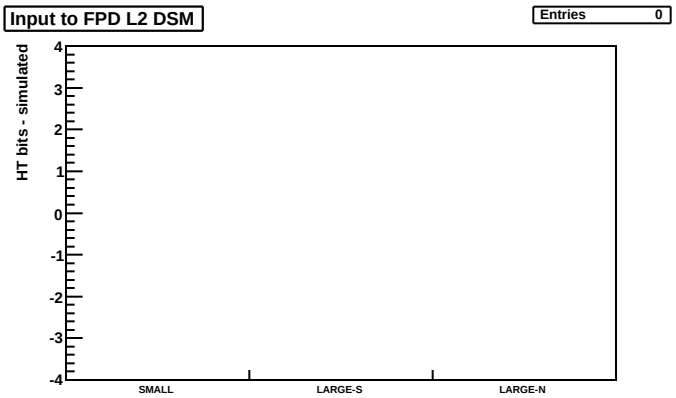

Input to FPD L2 DSM
Entries 1043520
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

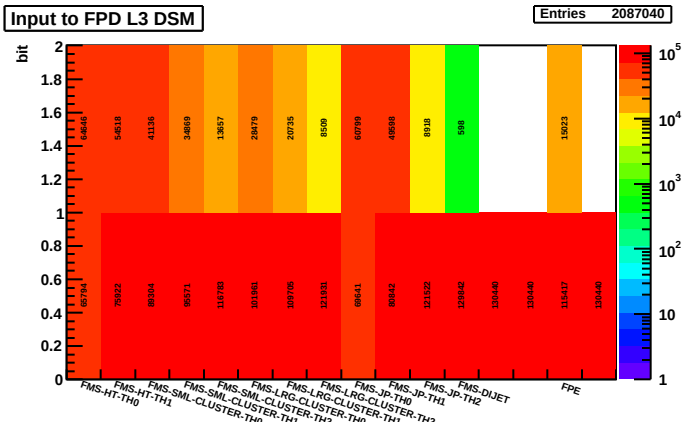

Input to FPD L2 DSM
Entries 3225
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

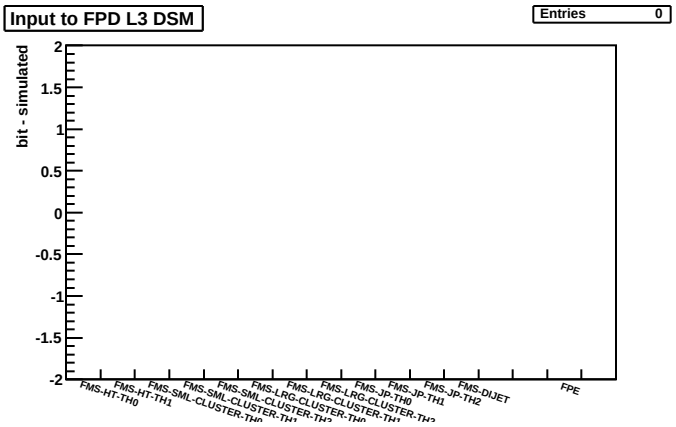

Input to FPD L2 DSM
Entries 391320
CL bits
13657
4968
3799
21212
6903
5327
6267
4179
3890
89304
114390
117424
SMALL
LARGE-S
LARGE-N

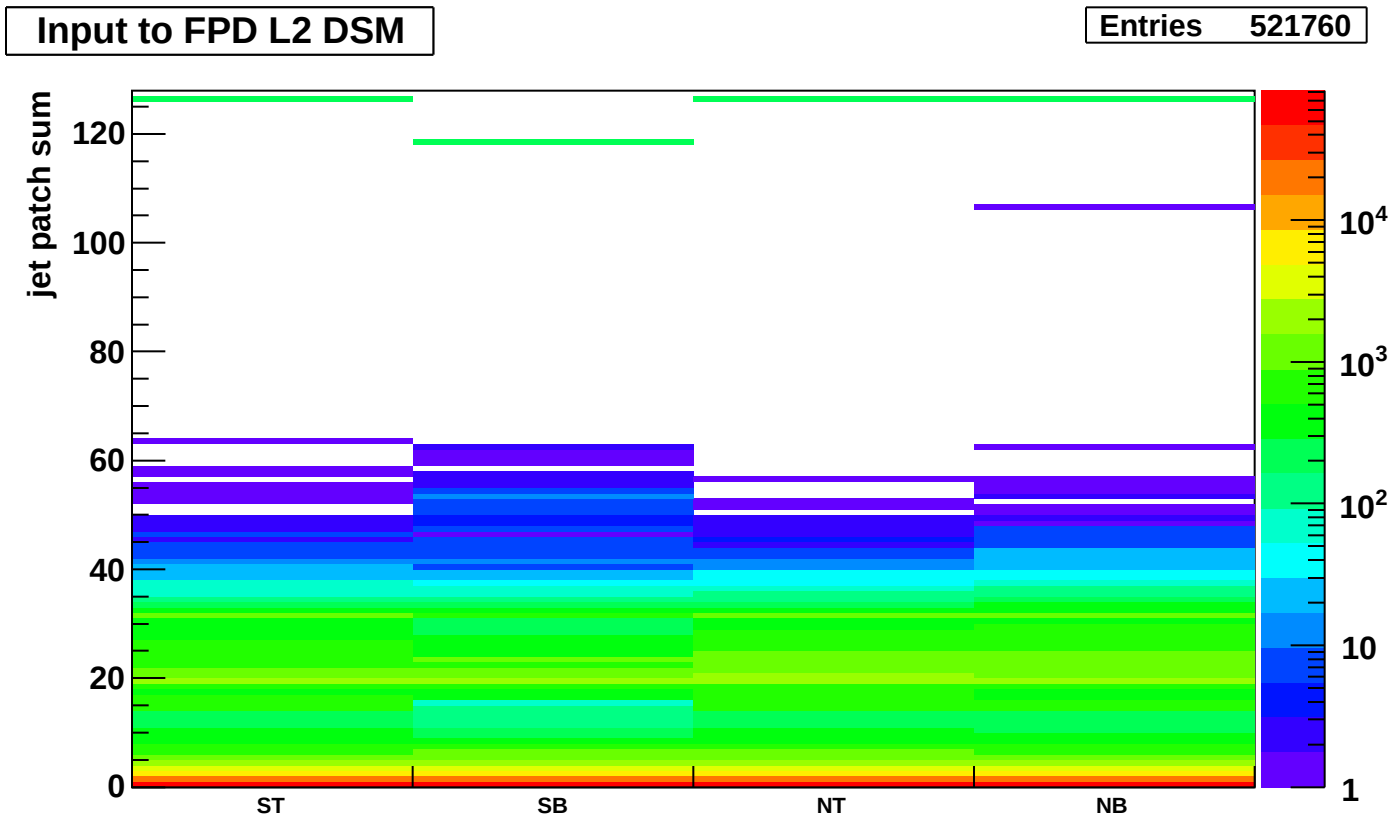

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

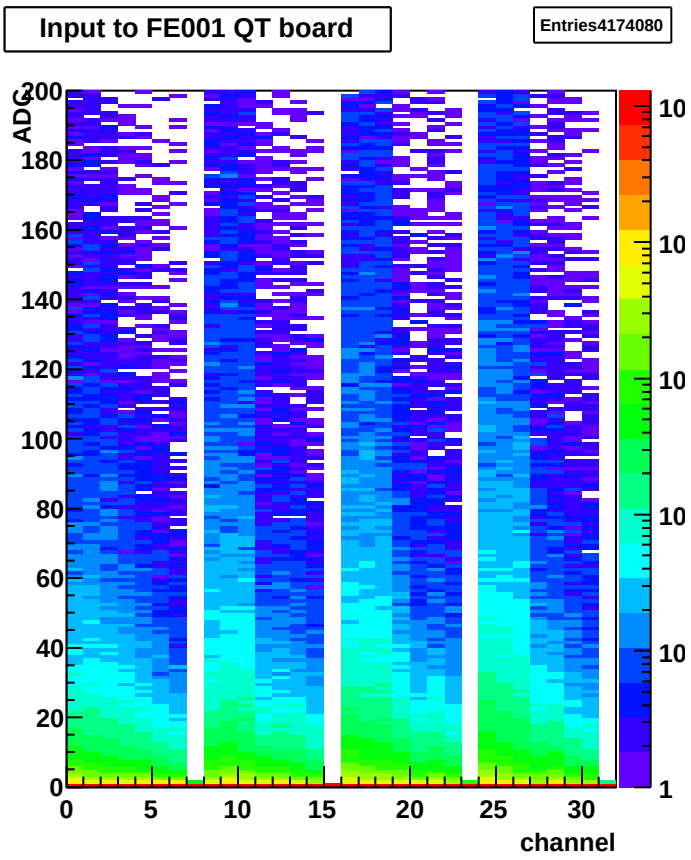

Input to FPD L2 DSM
Entries 391320
HT bits
31589
13501
10243
7443
2587
2551
91408
114352
117646
SMALL
LARGE-S
LARGE-N

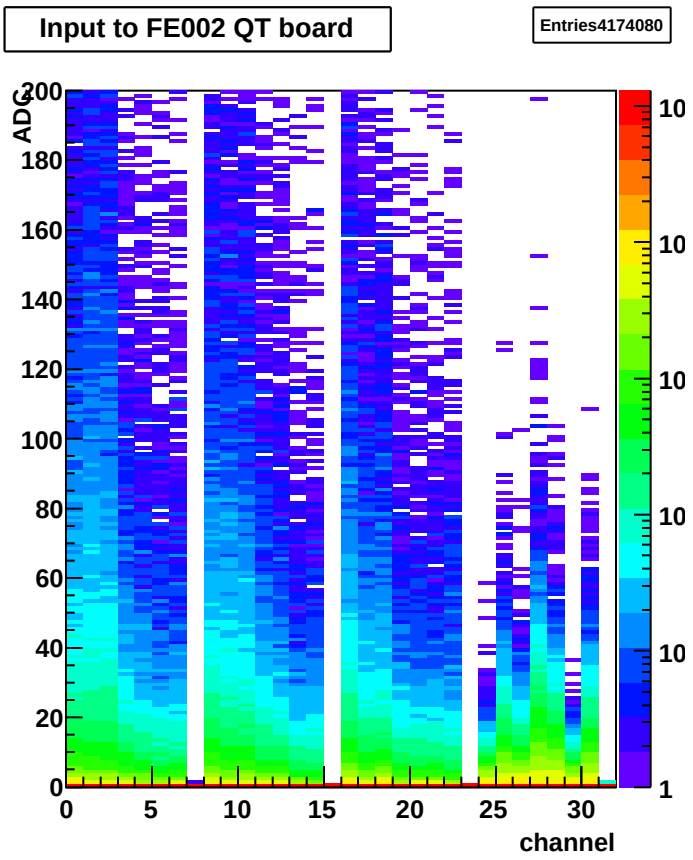

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

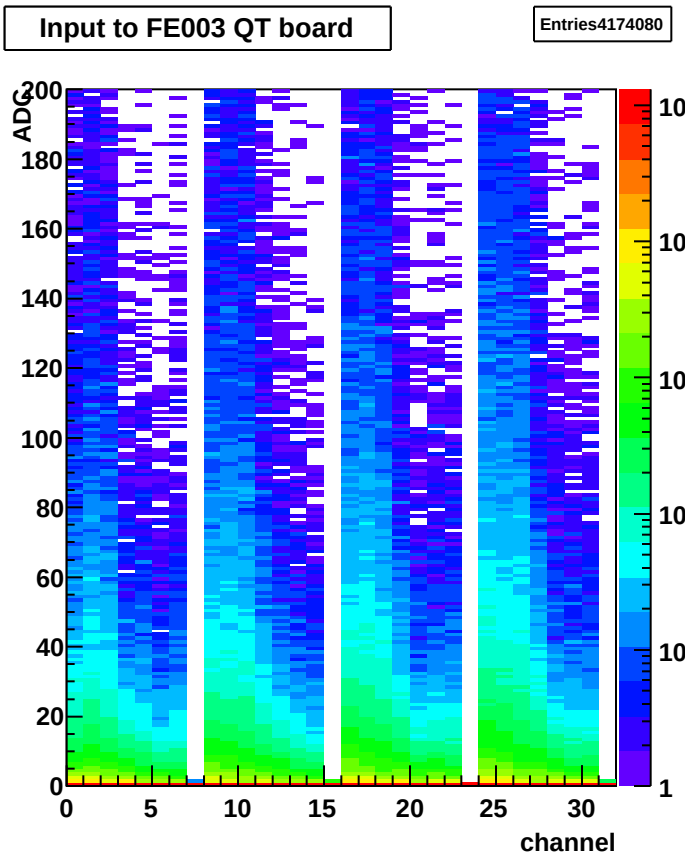

Input to FPD L3 DSM
Entries 2087040
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

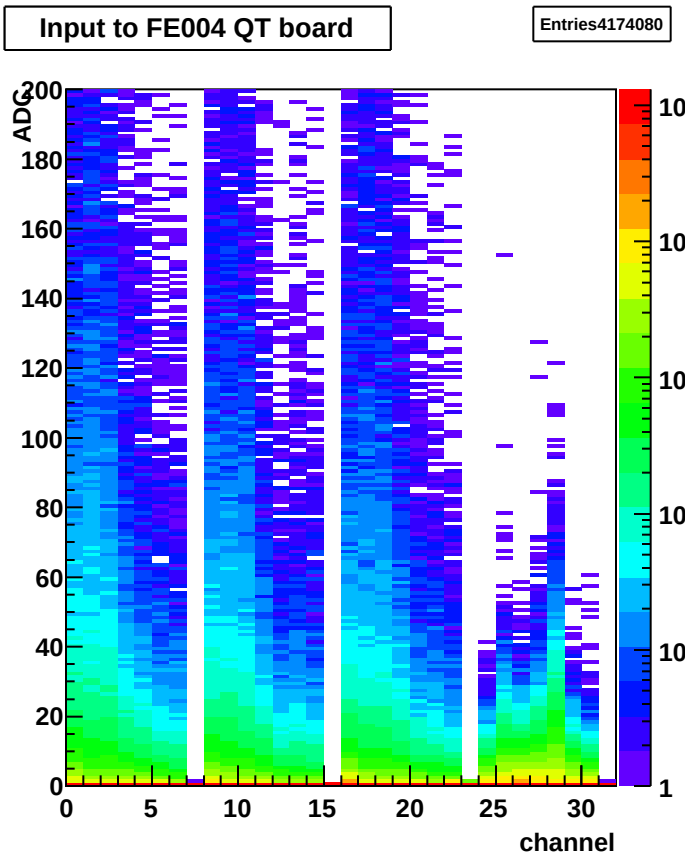

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

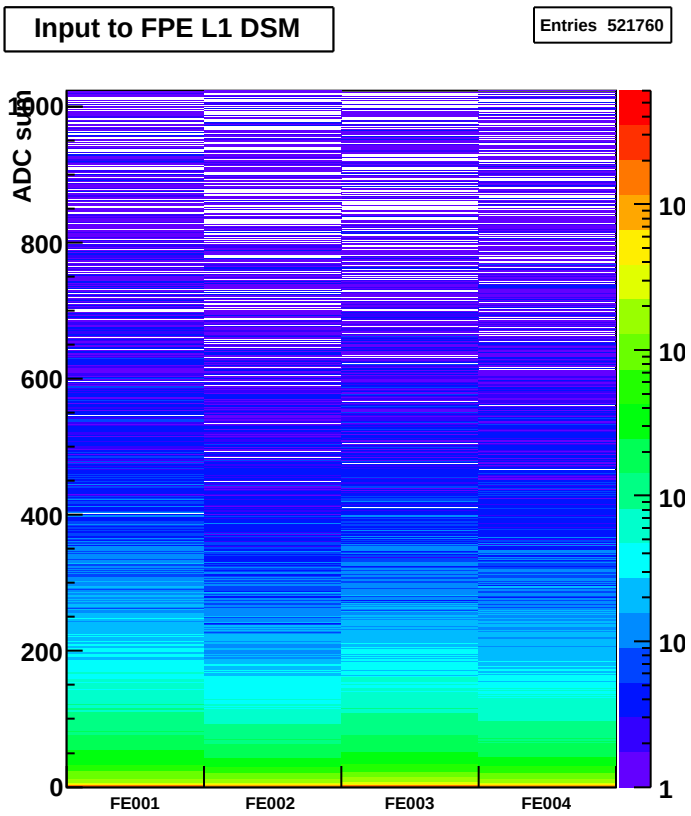

Input to FPD L2 DSM
Entries 521760
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

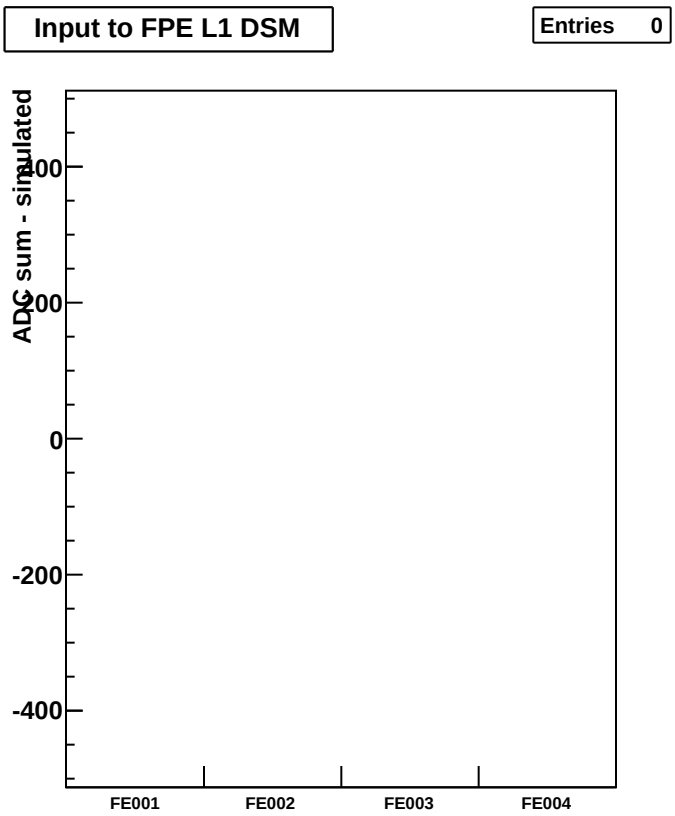

Input to FE001 QT board
Entries4174080
ADC
channel

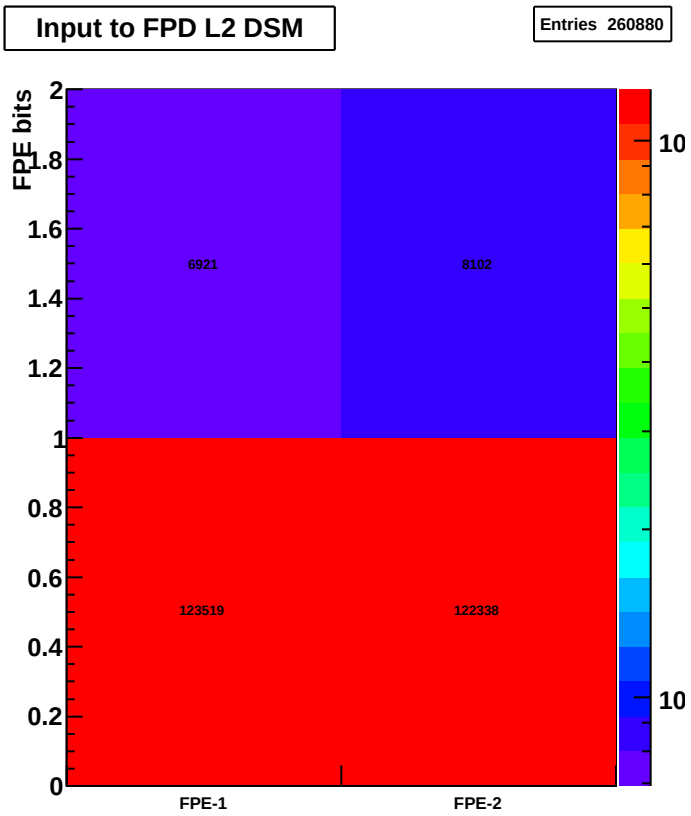

Input to FE002 QT board
Entries4174080
ADC
channel

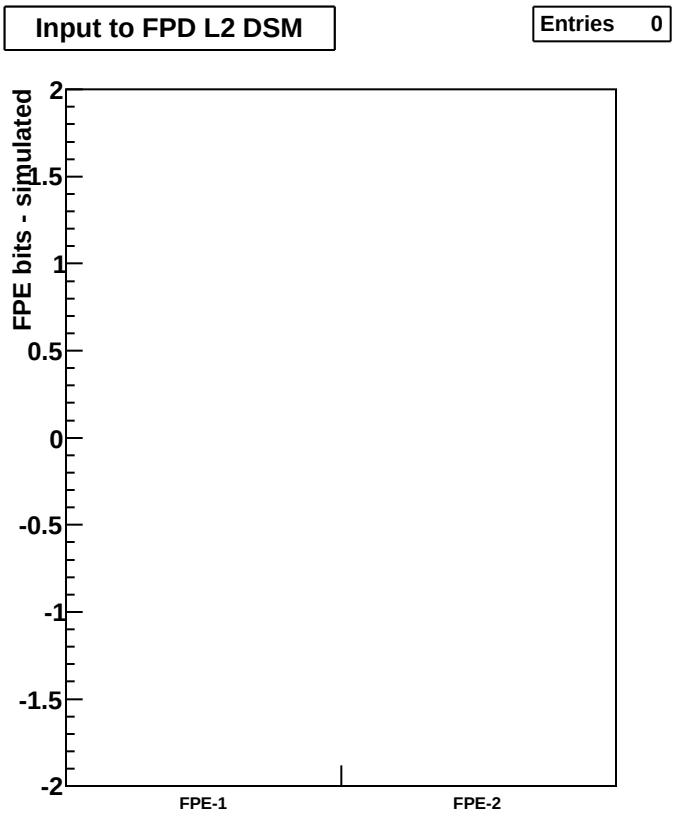

Input to FE003 QT board
Entries4174080
ADC
channel


Input to FE004 QT board
Entries4174080
ADC
channel


Input to FPE L1 DSM
Entries 521760
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 260880
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
6921
8102
123519
122338
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2