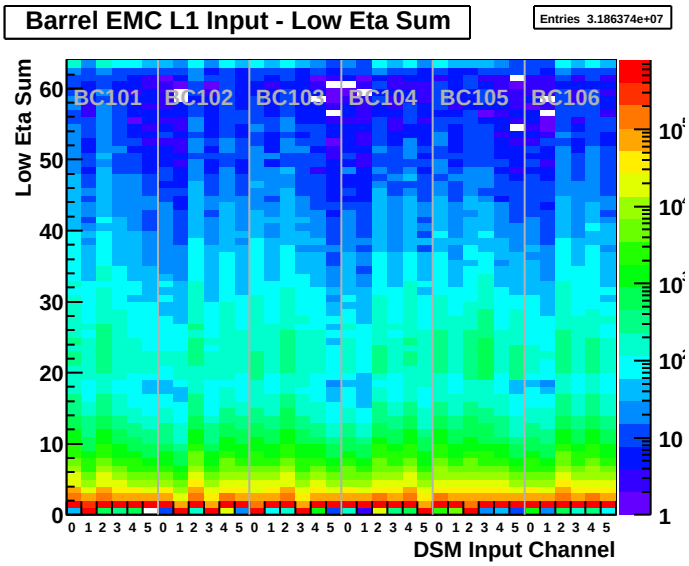
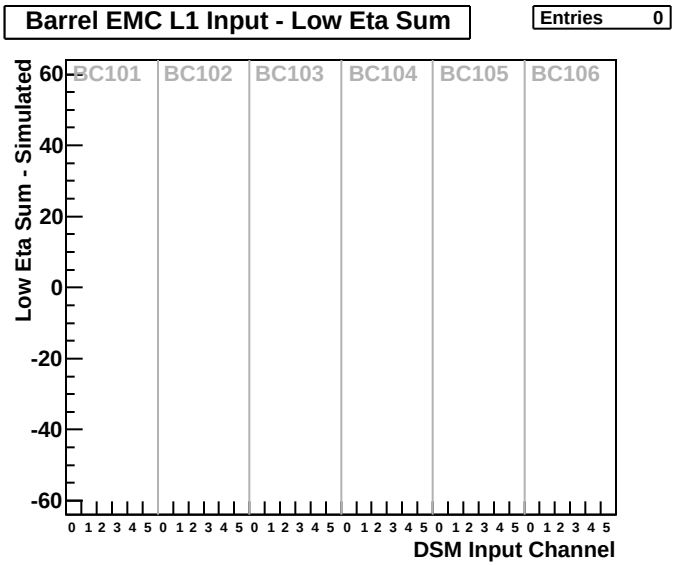
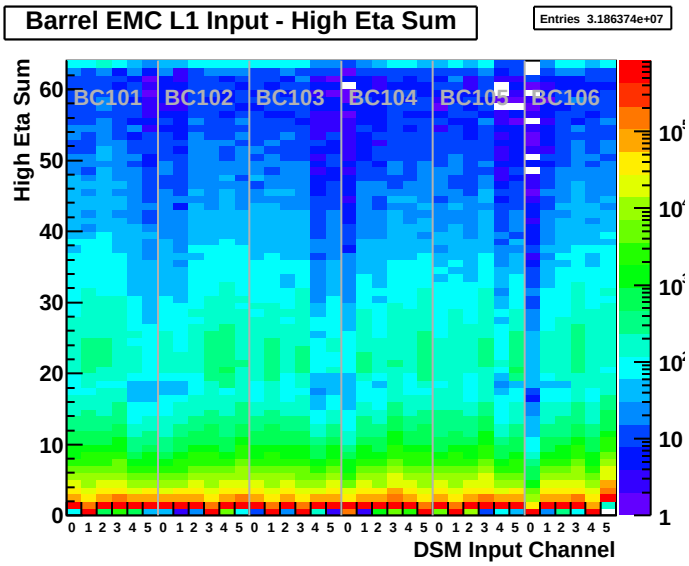
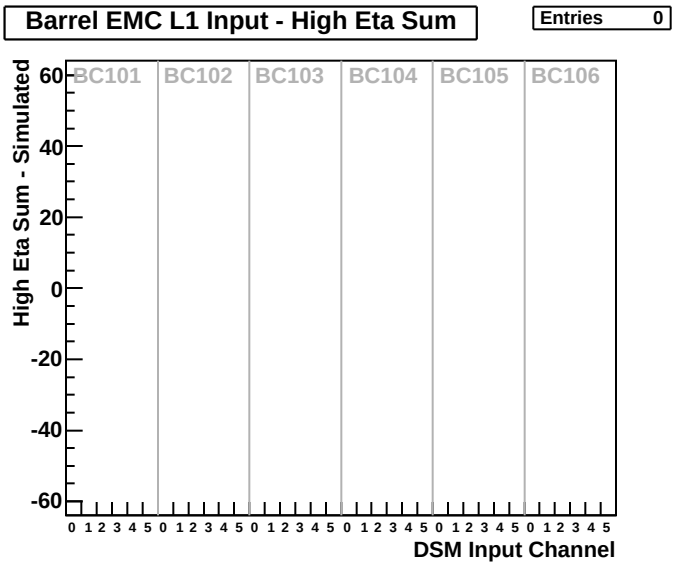
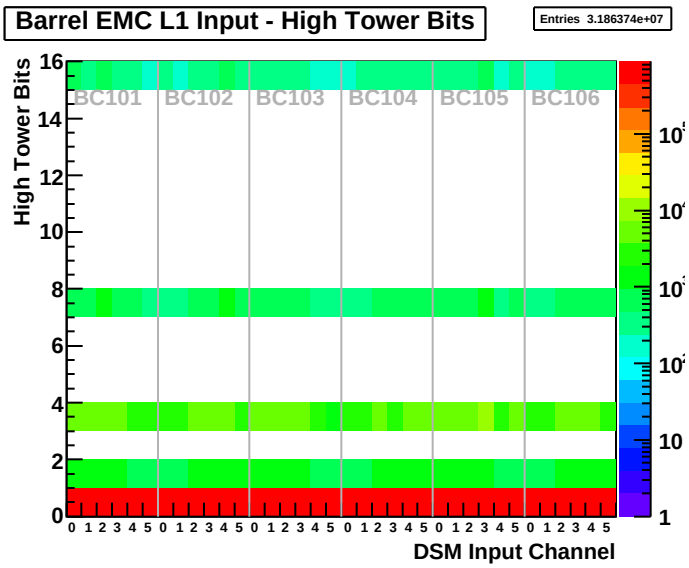
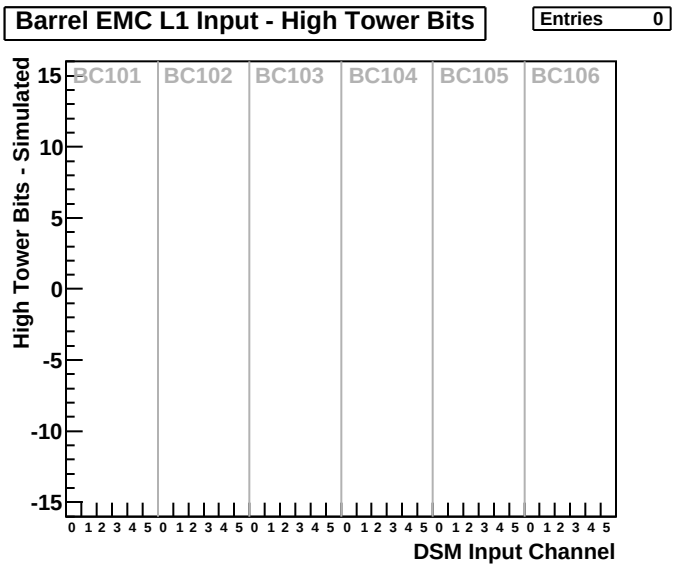

Barrel EMC L1 Input - Low Eta Sum
Entries 3.186374e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

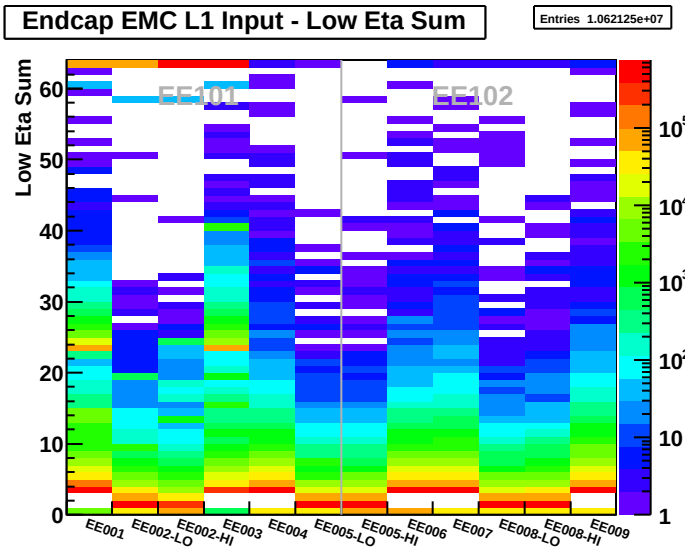

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

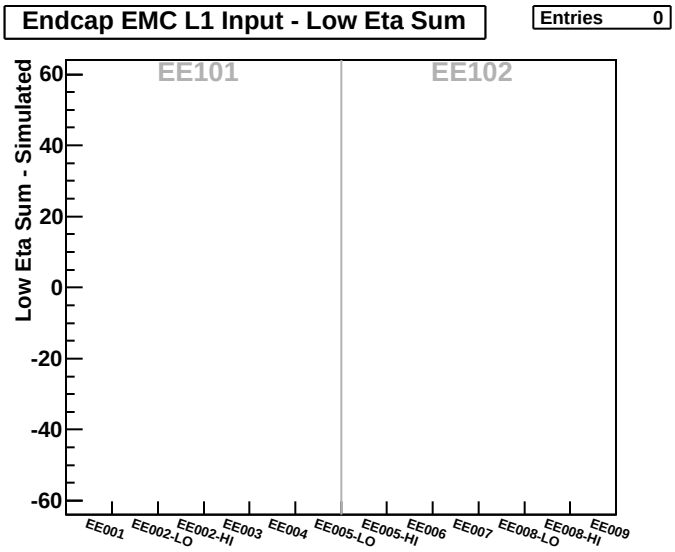

Barrel EMC L1 Input - High Eta Sum
Entries 3.186374e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

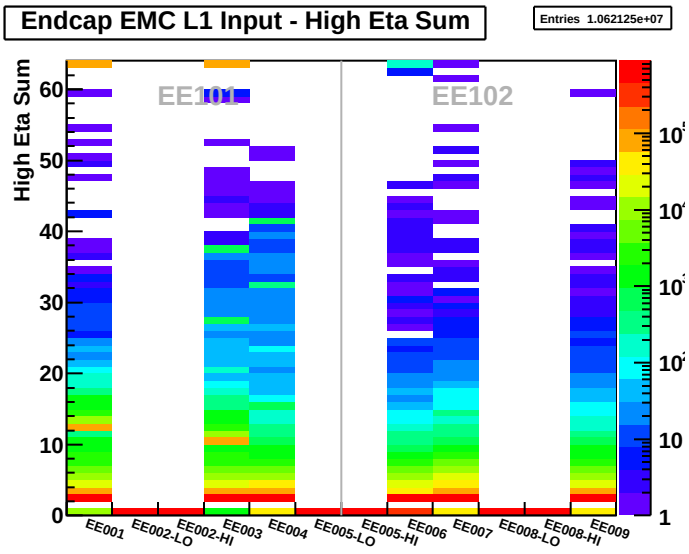

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

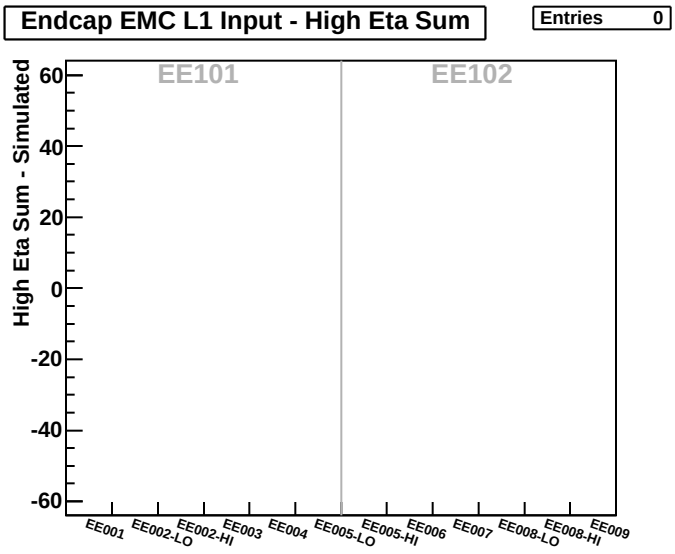

Barrel EMC L1 Input - High Tower Bits
Entries 3.186374e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

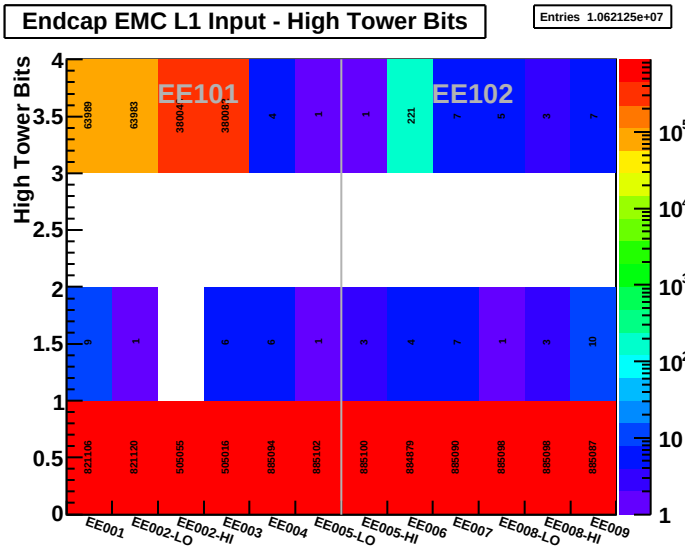

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

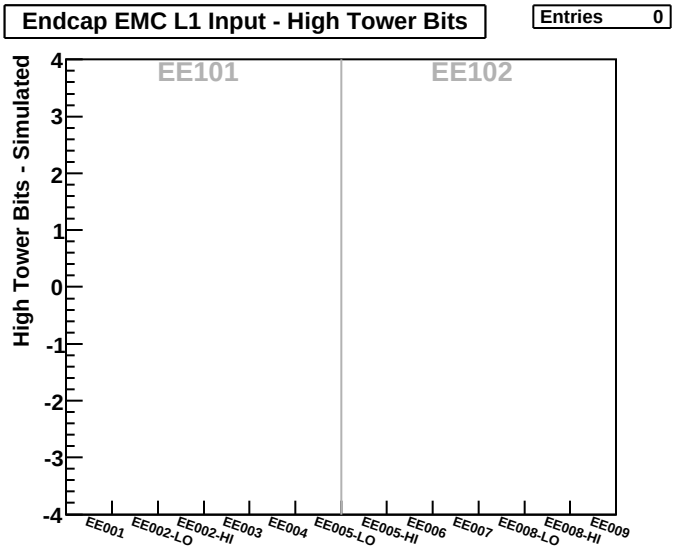

Endcap EMC L1 Input - Low Eta Sum
Entries 1.062125e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

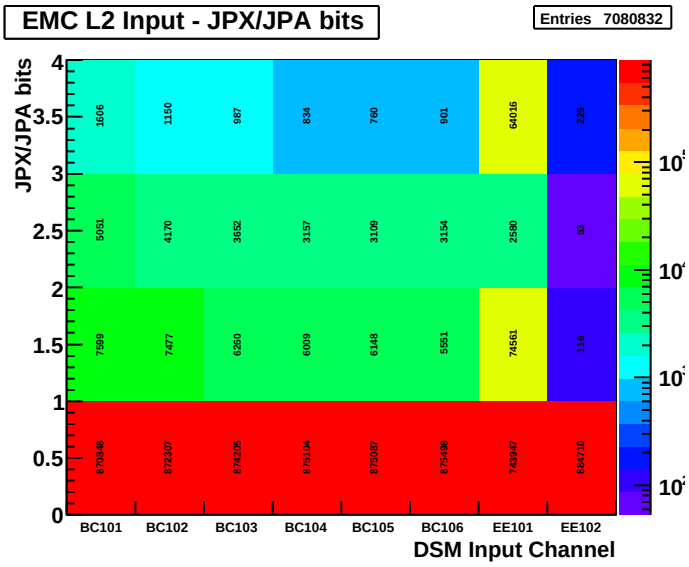

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

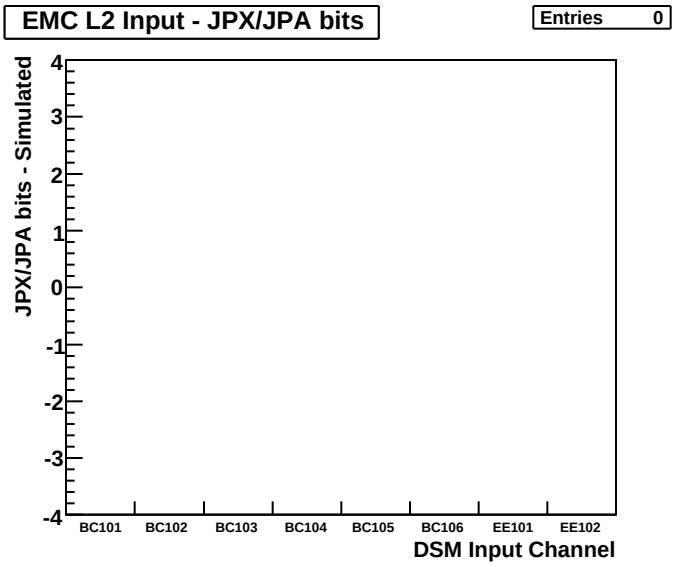

Endcap EMC L1 Input - High Eta Sum
Entries 1.062125e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

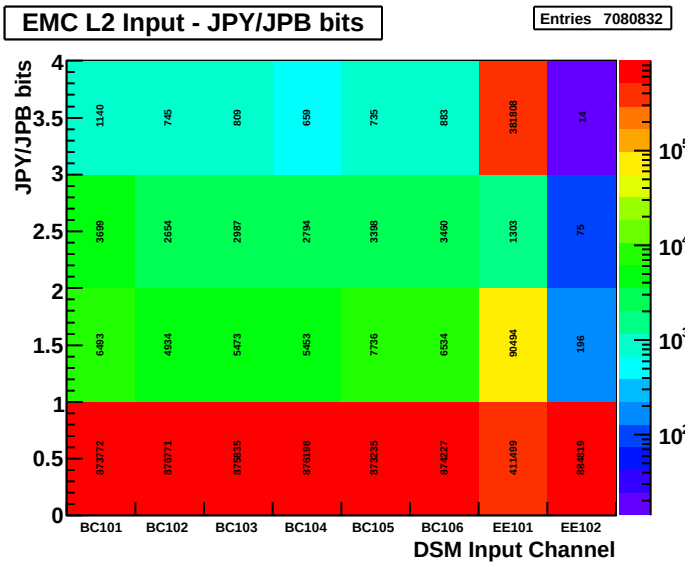

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

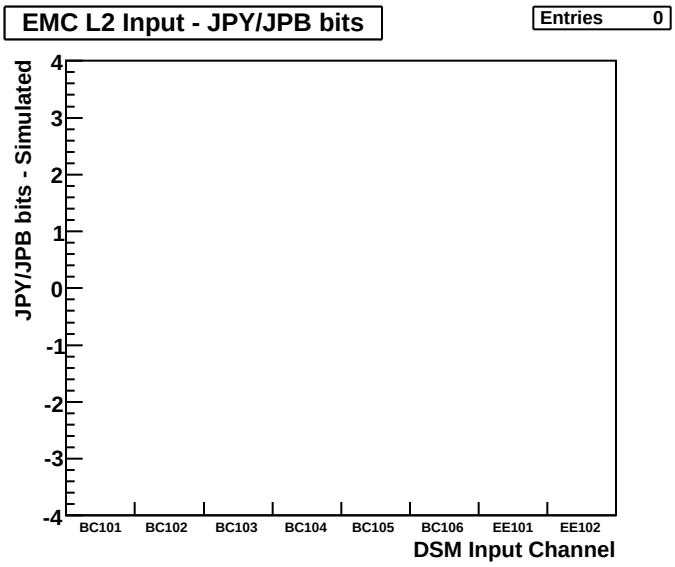

Endcap EMC L1 Input - High Tower Bits
Entries 1.062125e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

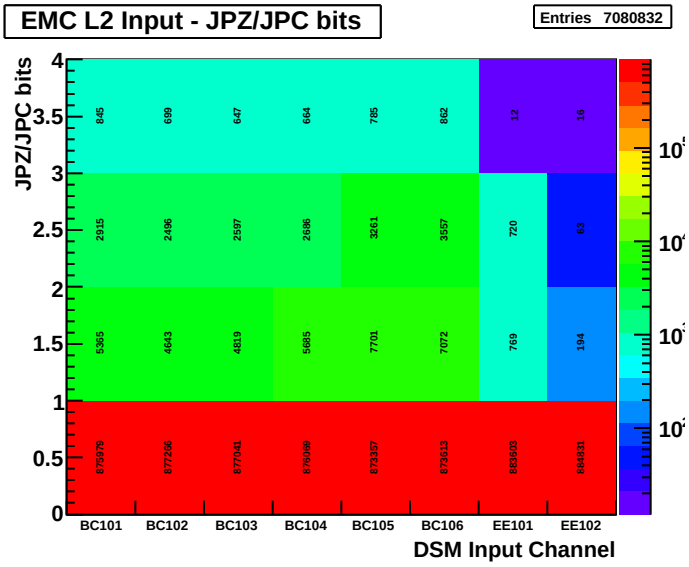

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

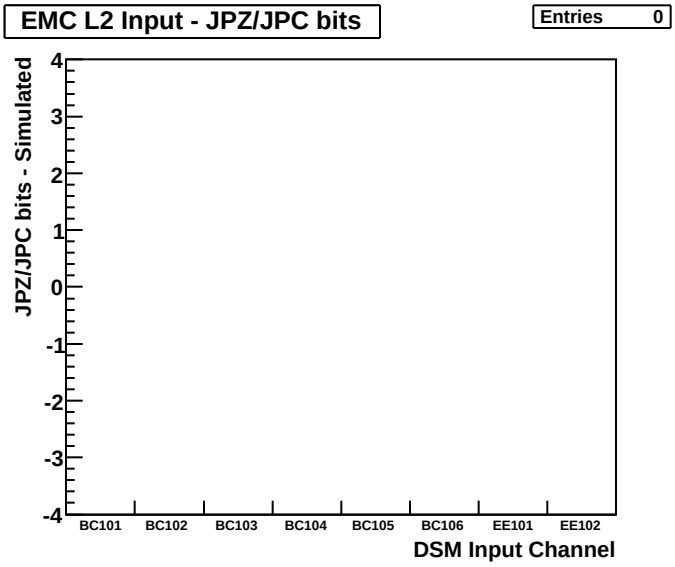

EMC L2 Input - JPX/JPA bits
Entries 7080832
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1606 1150 987 834 760 901 64016 225
5051 4170 3652 3157 3109 3154 2580 53
7599 7477 6260 6009 6148 5551 74561 116
870848 872307 874205 875104 875087 875498 743947 884710

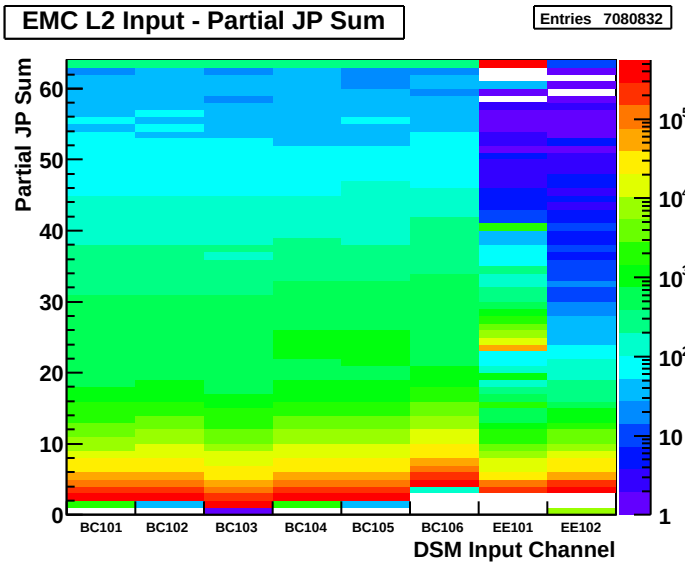

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

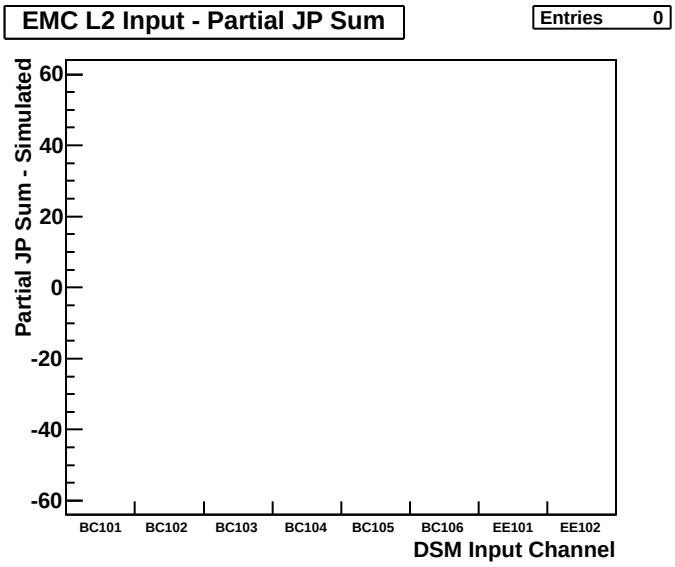

EMC L2 Input - JPY/JPB bits
Entries 7080832
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1140 745 809 659 735 883 381808 14
3699 2654 2987 2794 3398 3460 1303 75
6493 4934 5473 5453 7736 6534 90494 196
873772 876771 875835 876198 873235 874227 411499 884619

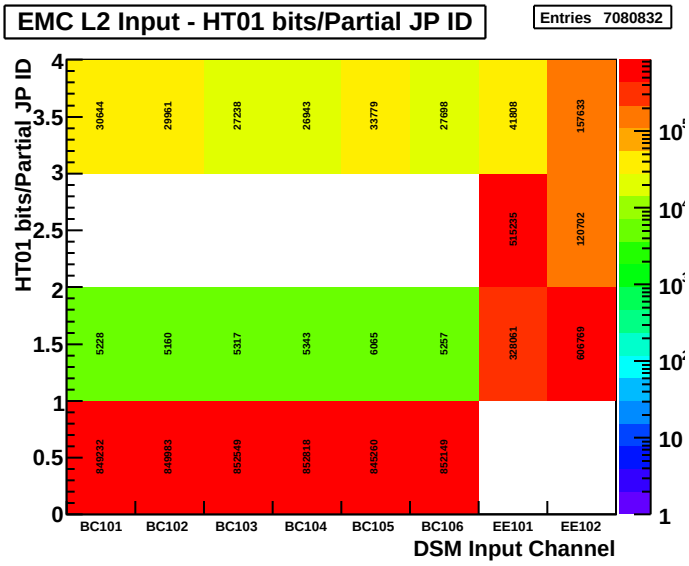

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

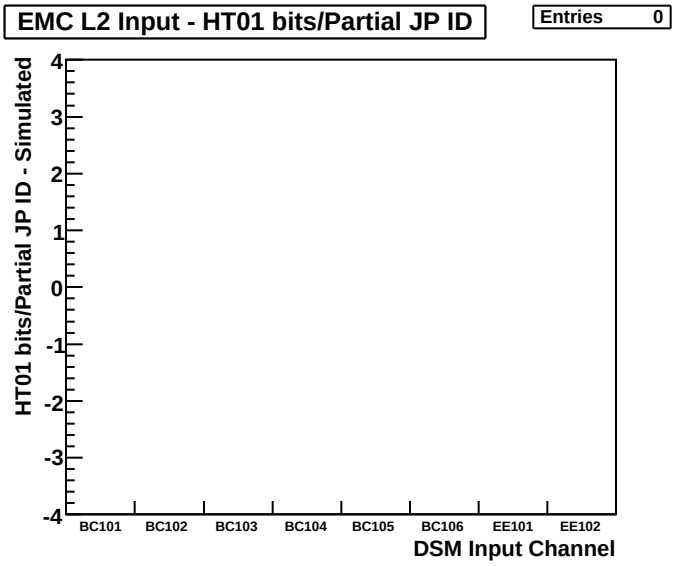

EMC L2 Input - JPZ/JPC bits
Entries 7080832
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
845 699 647 664 785 862 12 16
2915 2496 2597 2686 3261 3557 720 63
5365 4643 4819 5685 7701 7072 769 194
875979 877266 877041 876069 873357 873513 883603 884831

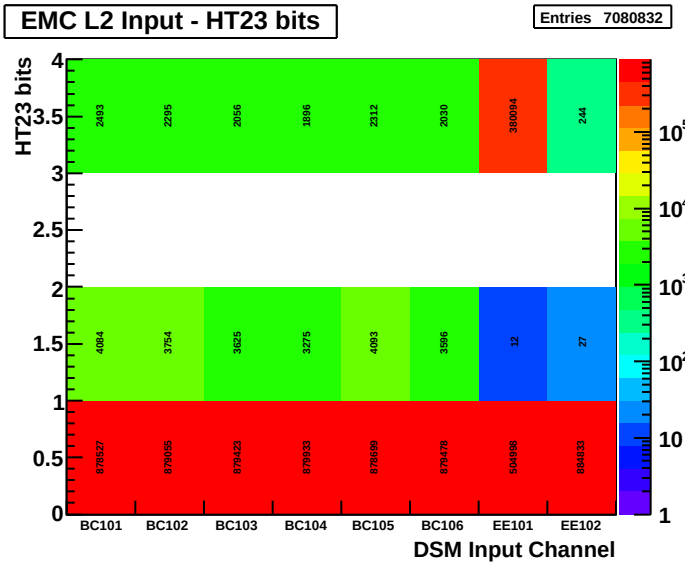

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

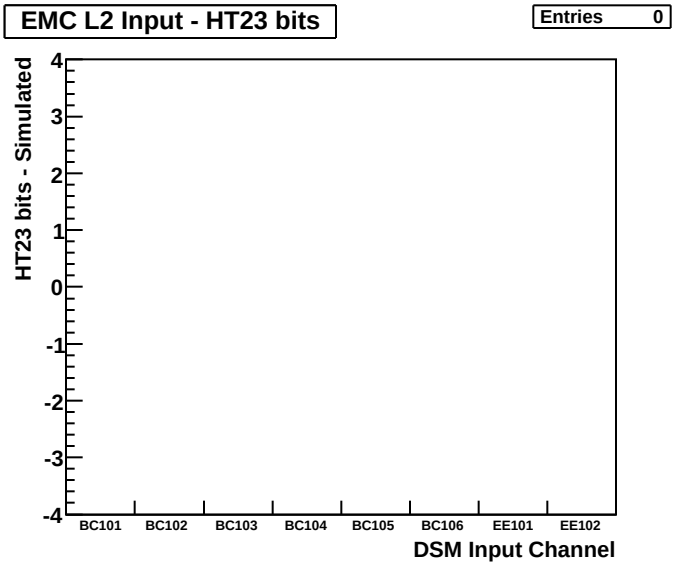

EMC L2 Input - Partial JP Sum
Entries 7080832
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

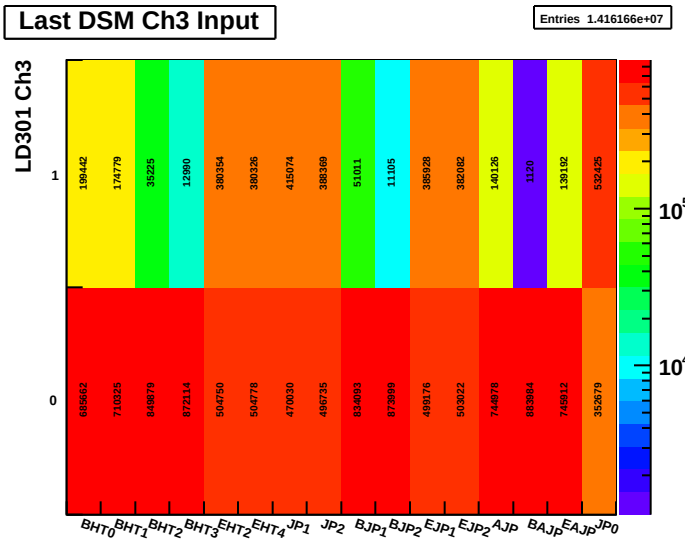

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

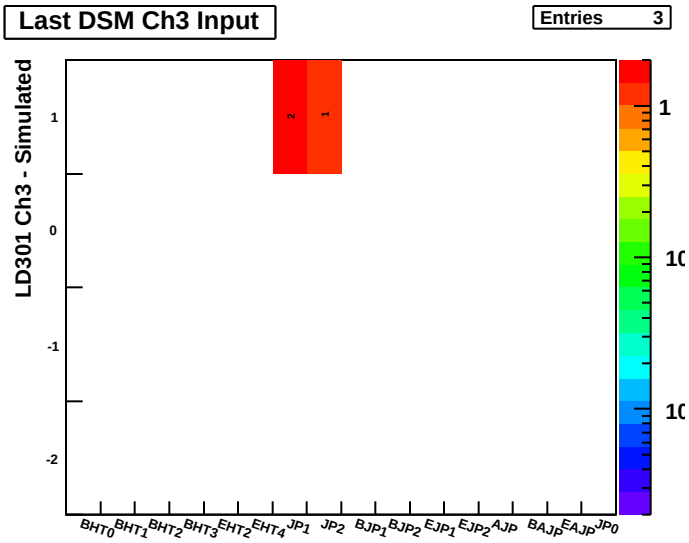

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7080832
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

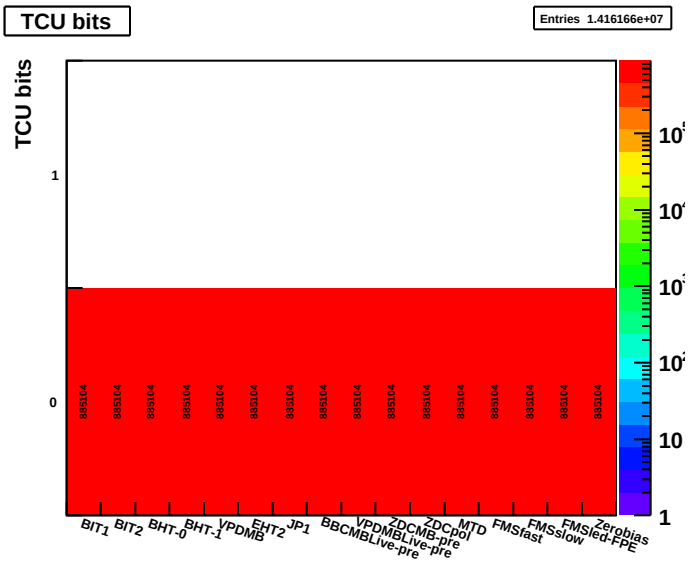

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

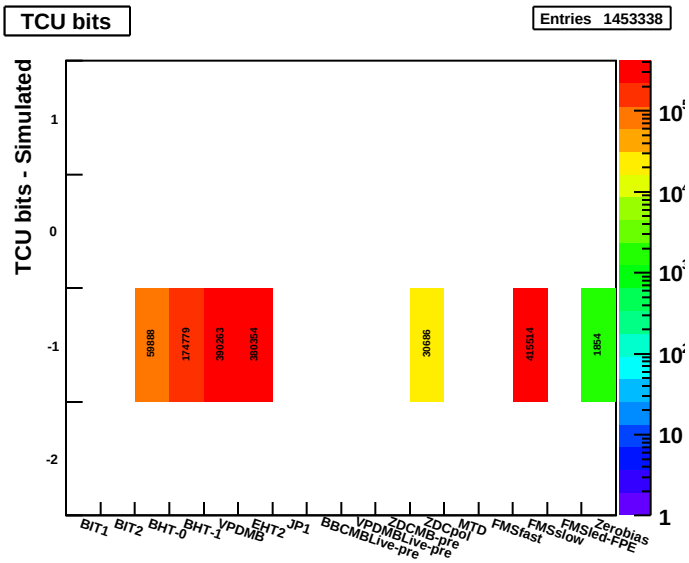

EMC L2 Input - HT23 bits
Entries 7080832
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

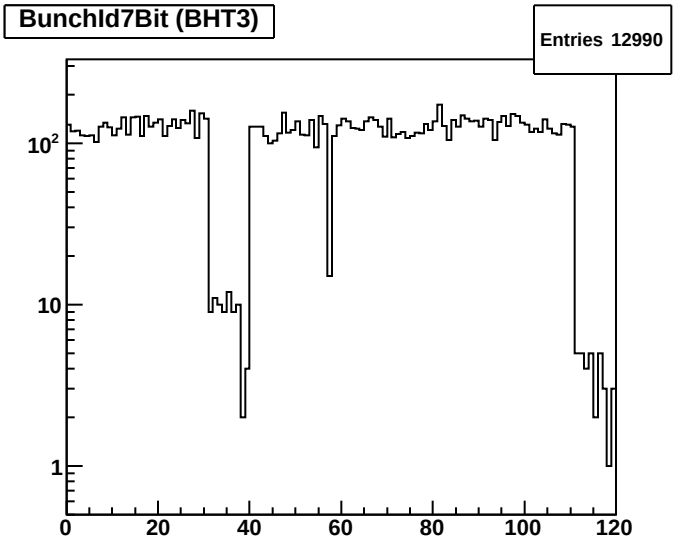

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

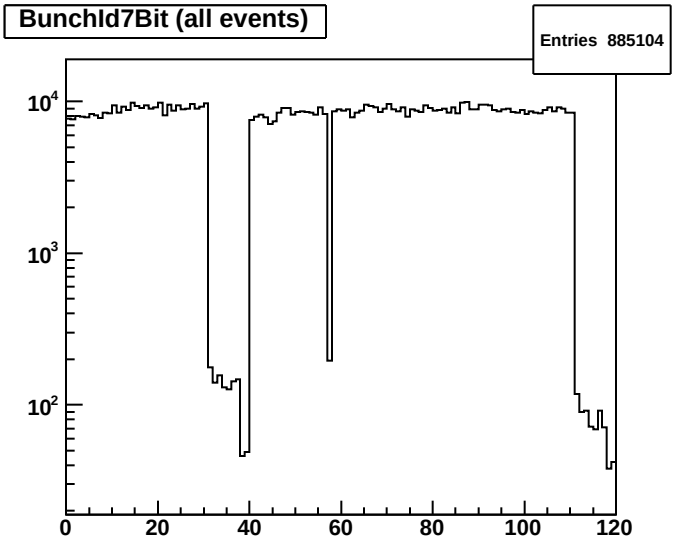

Last DSM Ch3 Input
Entries 1.416166e+07
LD301 Ch3
189442 174779 35225 12990 380354 380326 415074 380369 51011 11105 385928 382082 140126 1120 139192 532425
685662 710325 848879 872114 504750 504778 470030 496735 834093 873089 499176 503022 744978 883984 745912 352679
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

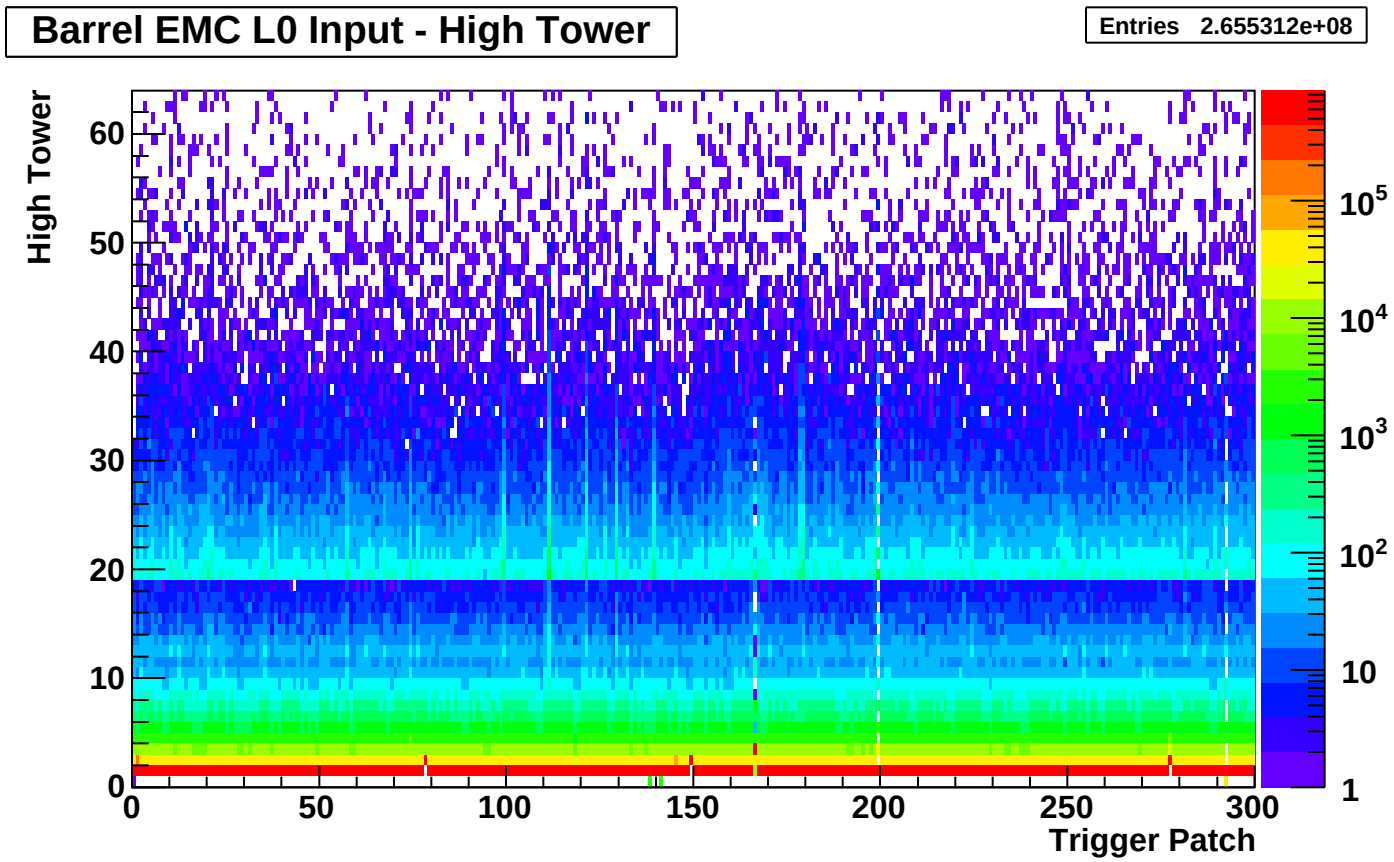

Last DSM Ch3 Input
Entries 3
LD301 Ch3 - Simulated
2 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

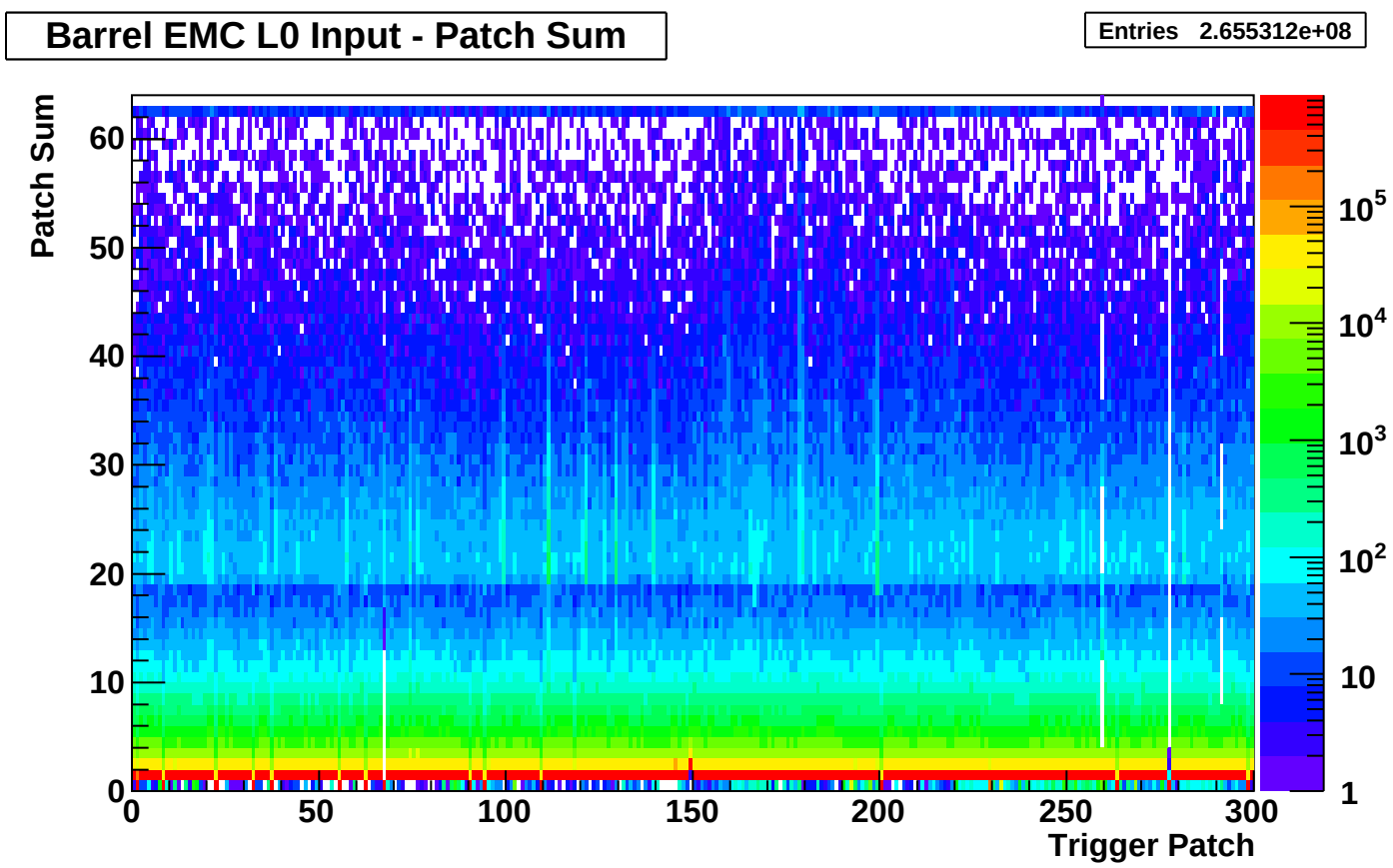

TCU bits
Entries 1.416166e+07
TCU bits
885104
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

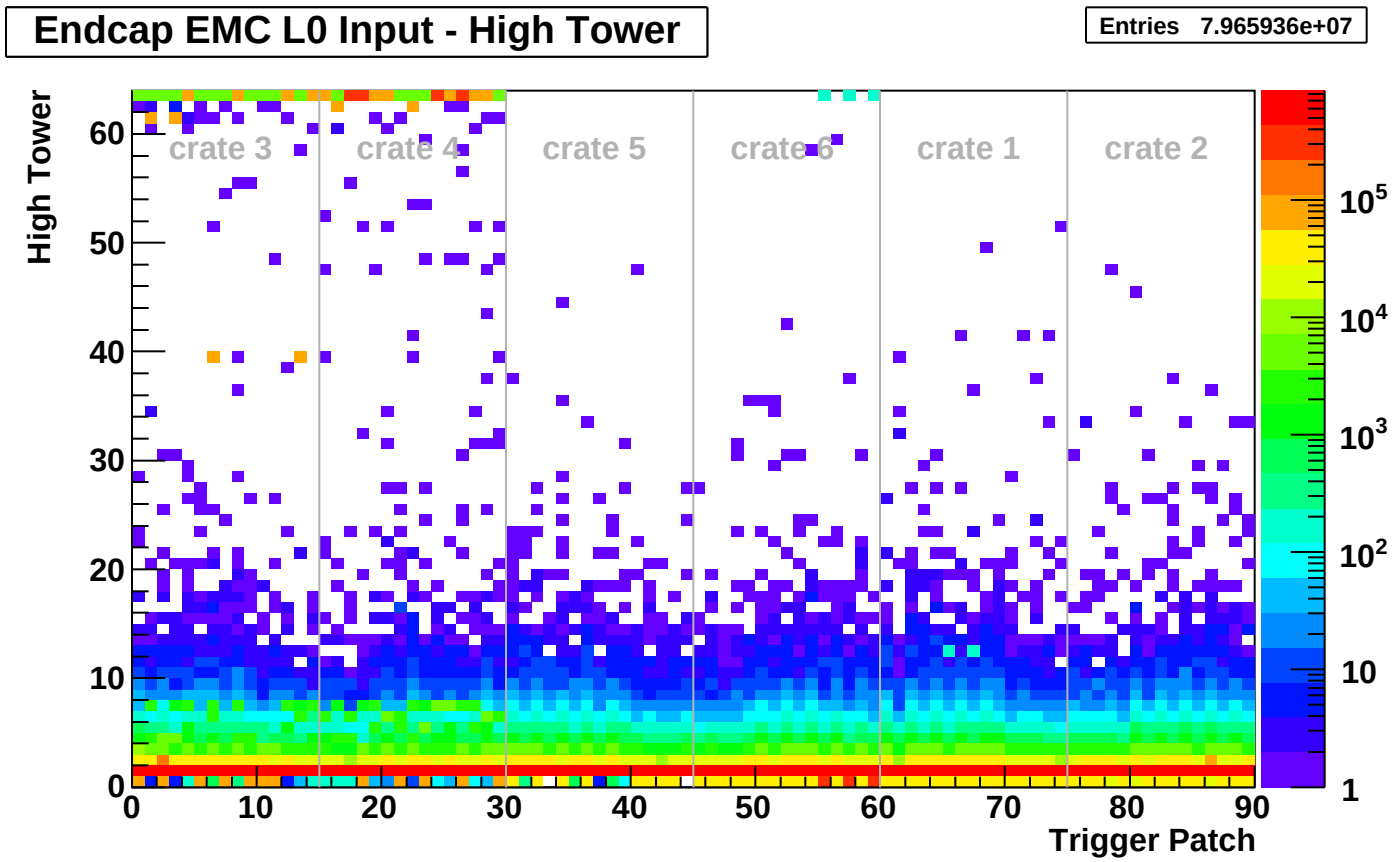

TCU bits
Entries 1453338
TCU bits - Simulated
59888 174779 390263 380354 30686 415514 1954
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

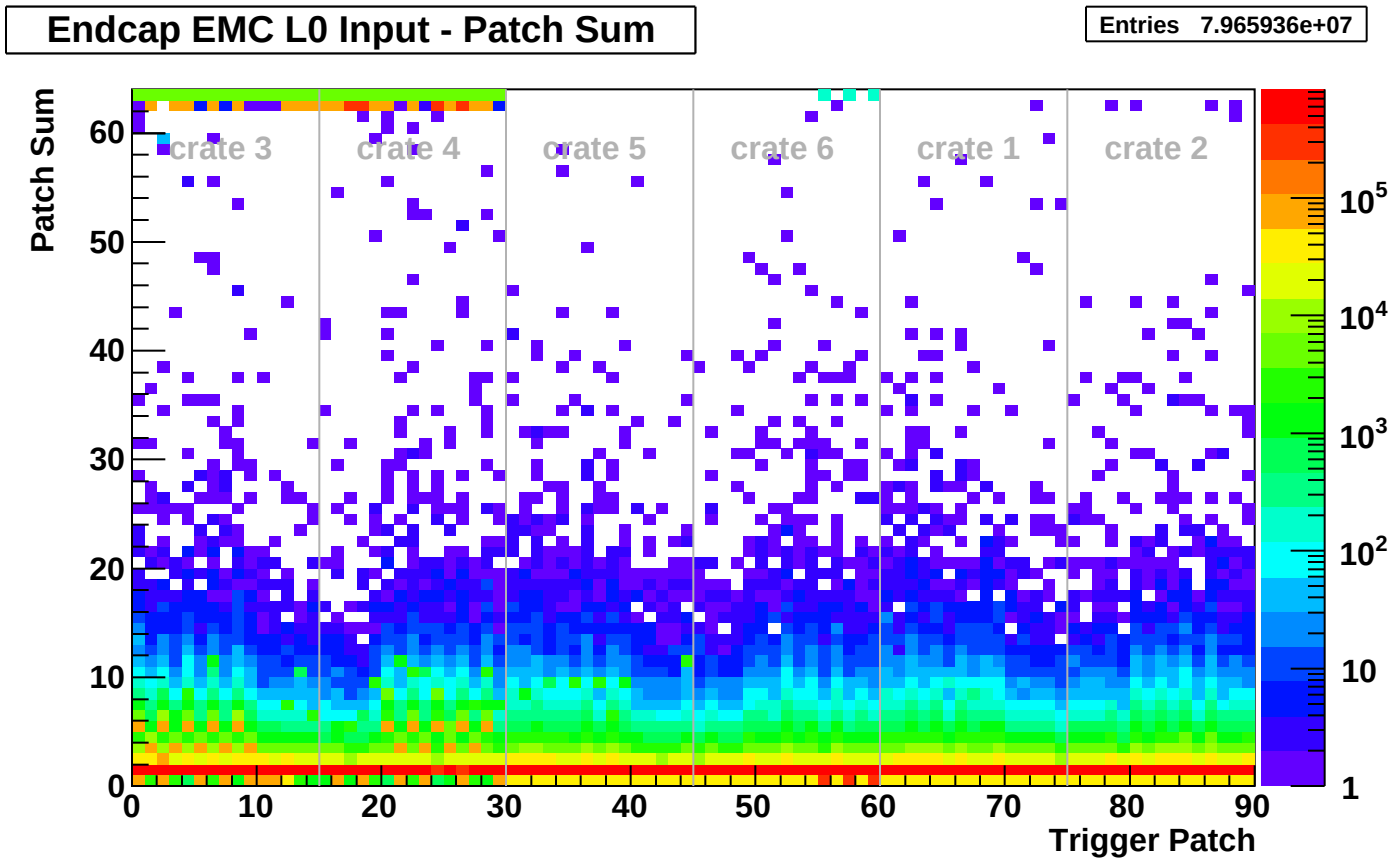

BunchId7Bit (BHT3)
Entries 12990

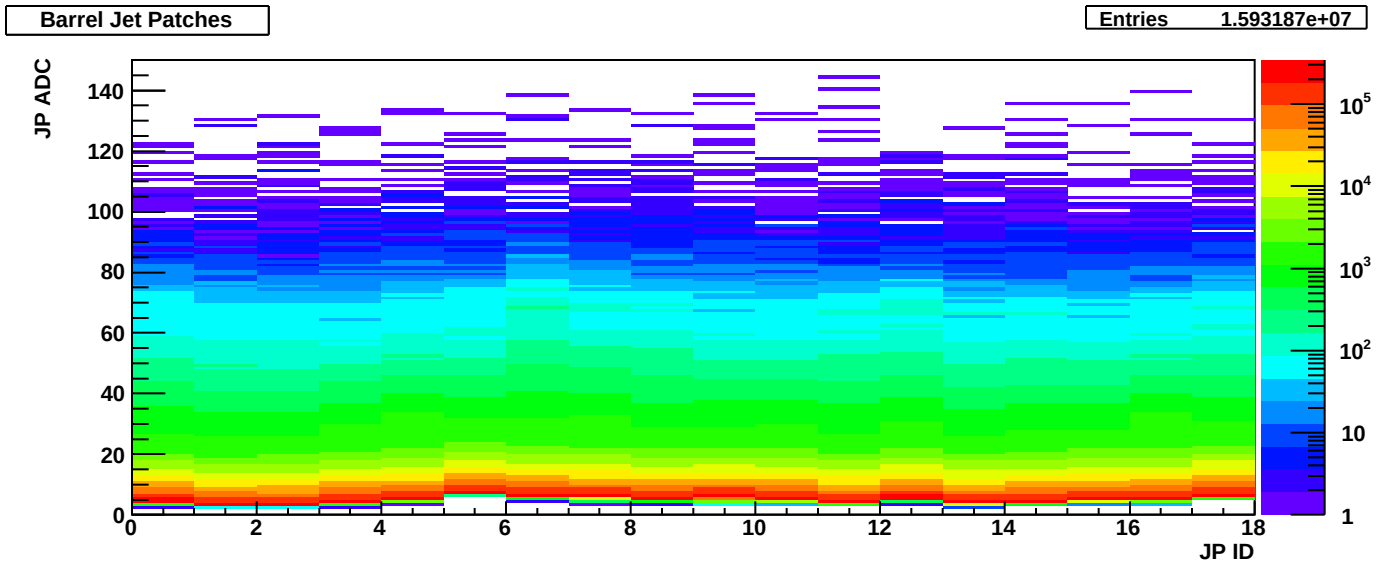

BunchId7Bit (all events)
Entries 885104

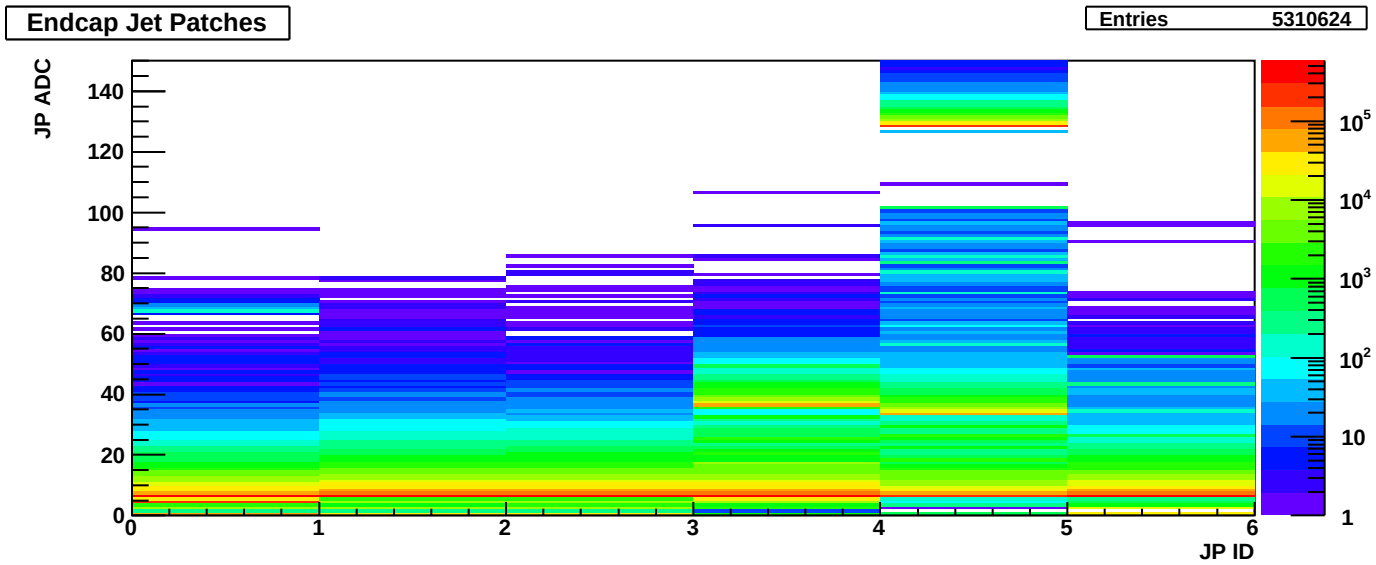

Barrel EMC L0 Input - High Tower
Entries 2.655312e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

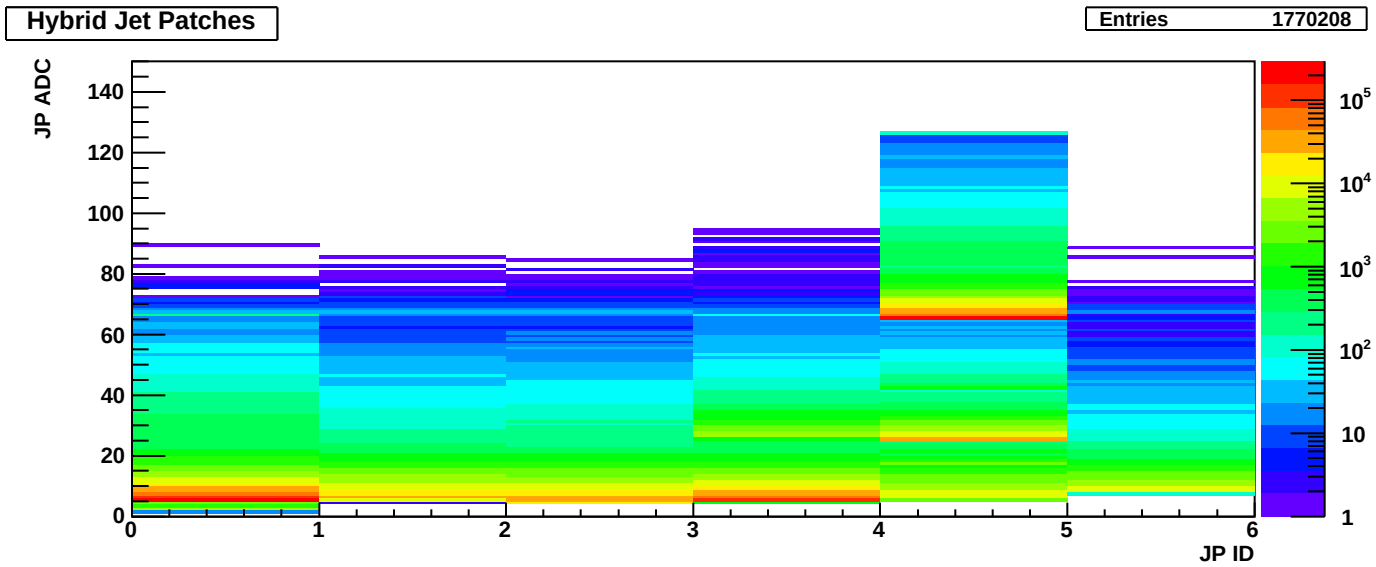

Barrel EMC L0 Input - Patch Sum
Entries 2.655312e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 7.965936e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Endcap EMC L0 Input - Patch Sum
Entries 7.965936e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

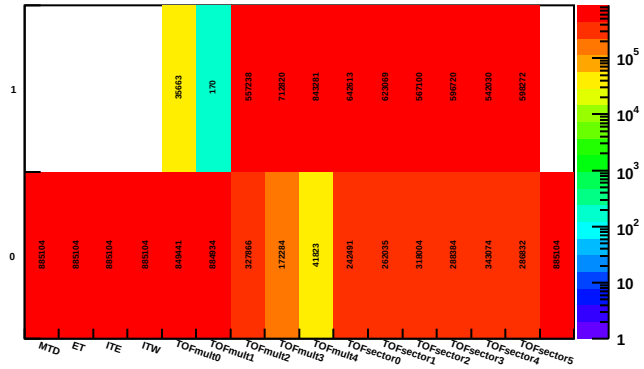

Barrel Jet Patches
Entries 1.593187e+07
JP ADC
JP ID


Endcap Jet Patches
Entries 5310624
JP ADC
JP ID


Hybrid Jet Patches
Entries 1770208
JP ADC
JP ID

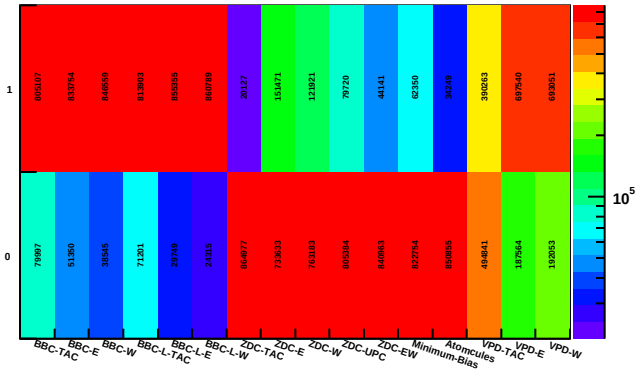
TF201 0-15 (ch0)

Entries 1.416166e+07



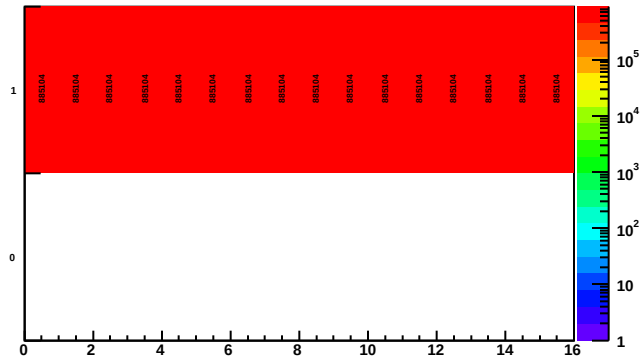
VT201 0-15 (ch1)

Entries 1.416166e+07



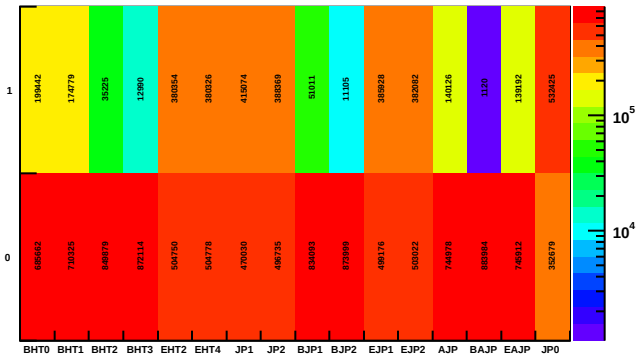
Unused (ch2)

Entries 1.416166e+07



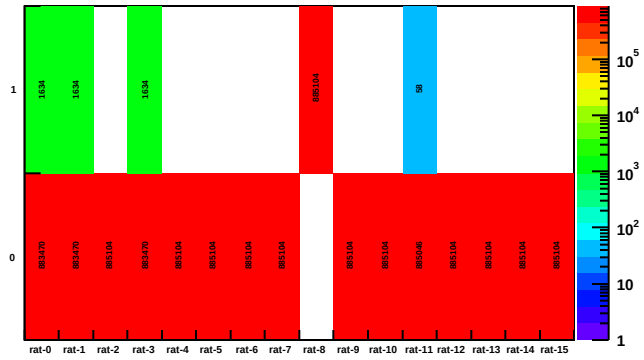
EM201 0-15 (ch3)

Entries 1.416166e+07



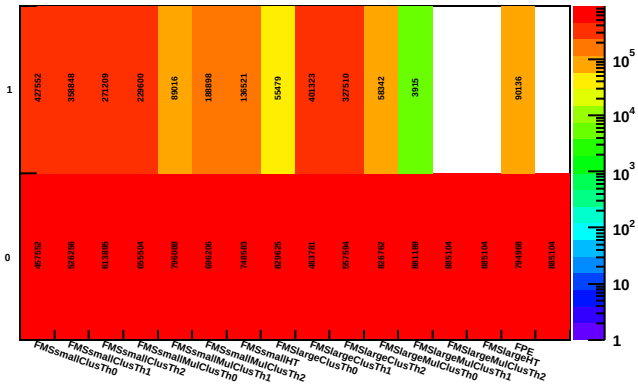
RAT board (ch4)

Entries 1.416166e+07



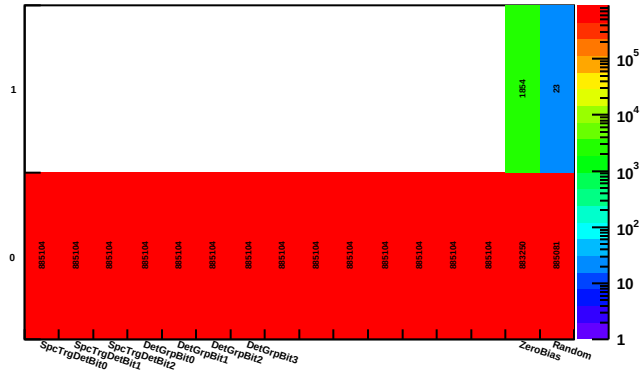
FP201 0-15 (ch5)

Entries 1.416166e+07



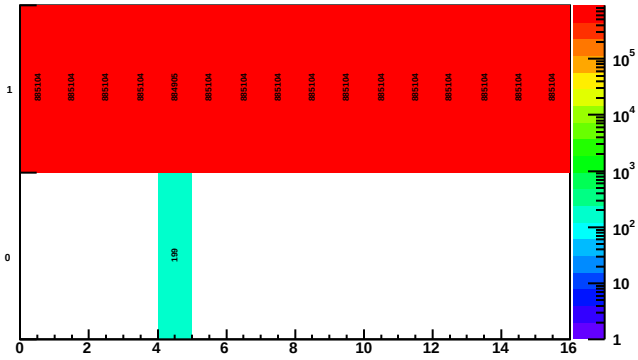
ST201 0-15 (ch6)

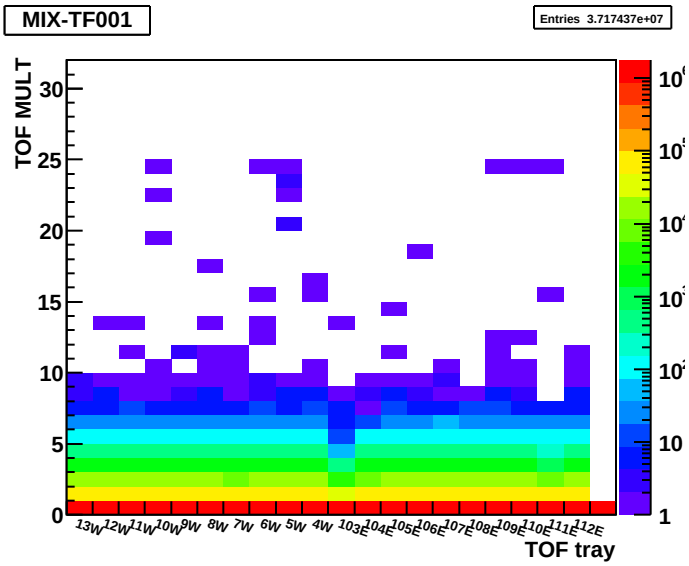
Entries 1.416166e+07

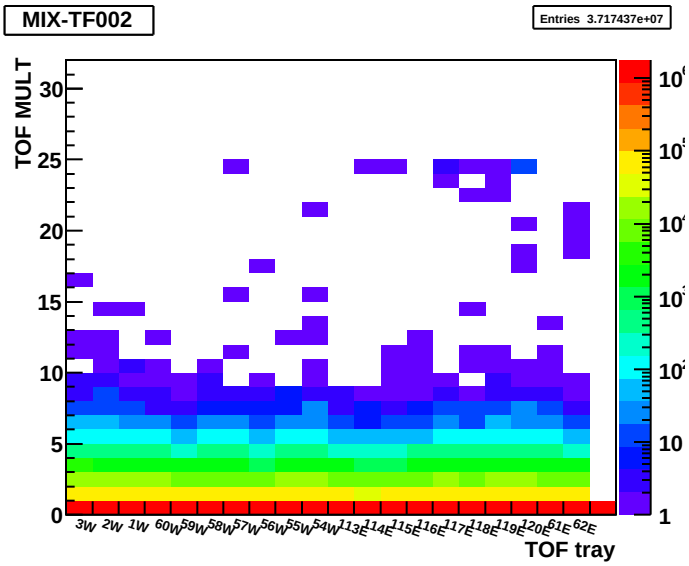


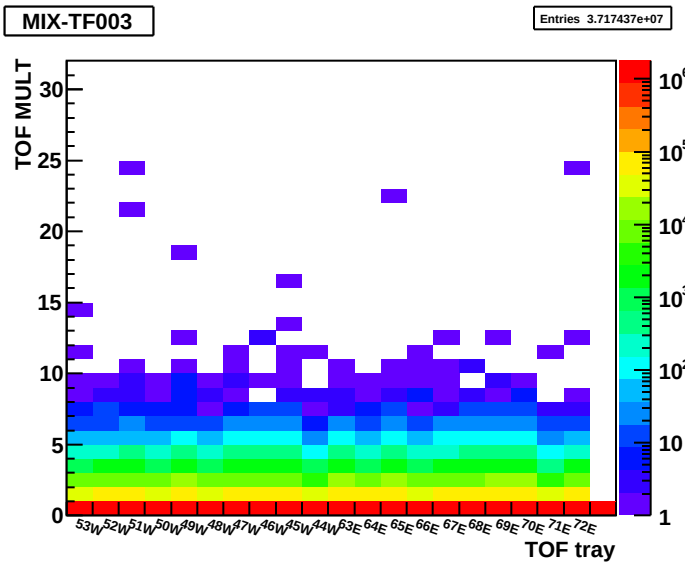
Unused (ch7)

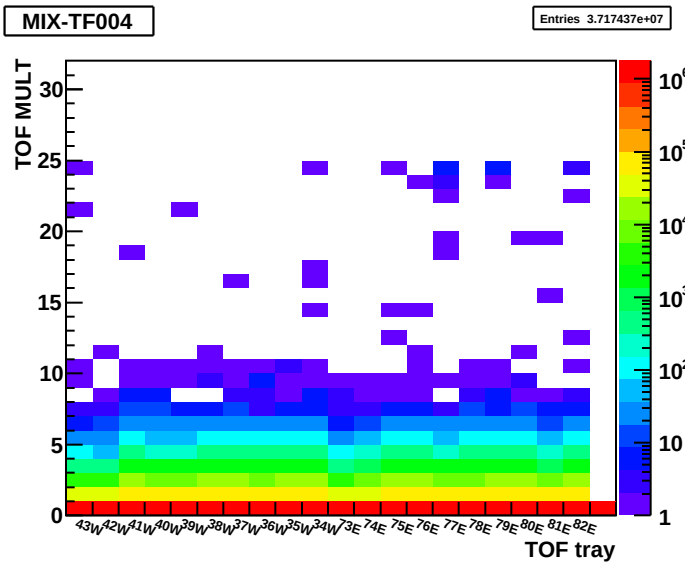
Entries 1.416166e+07

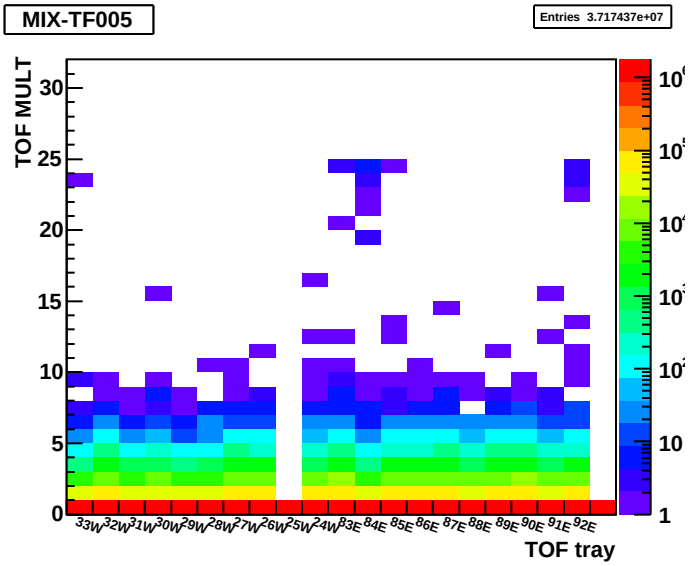


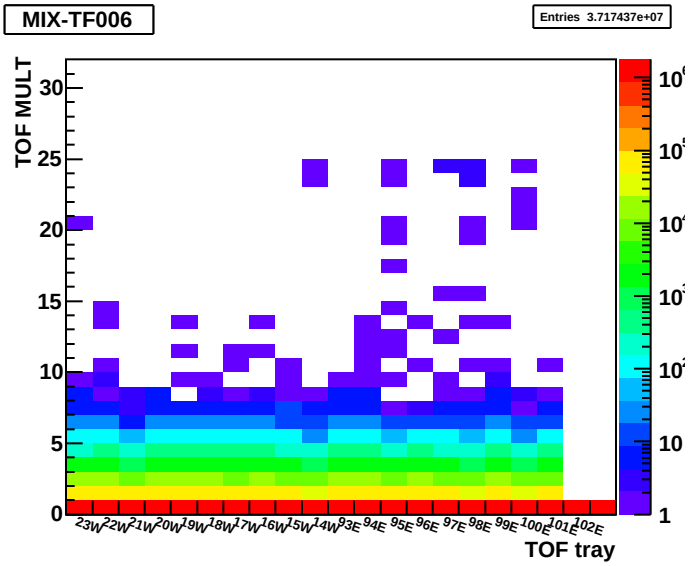

MIX-TF001
Entries 3.717437e+07
TOF MULT
TOF tray

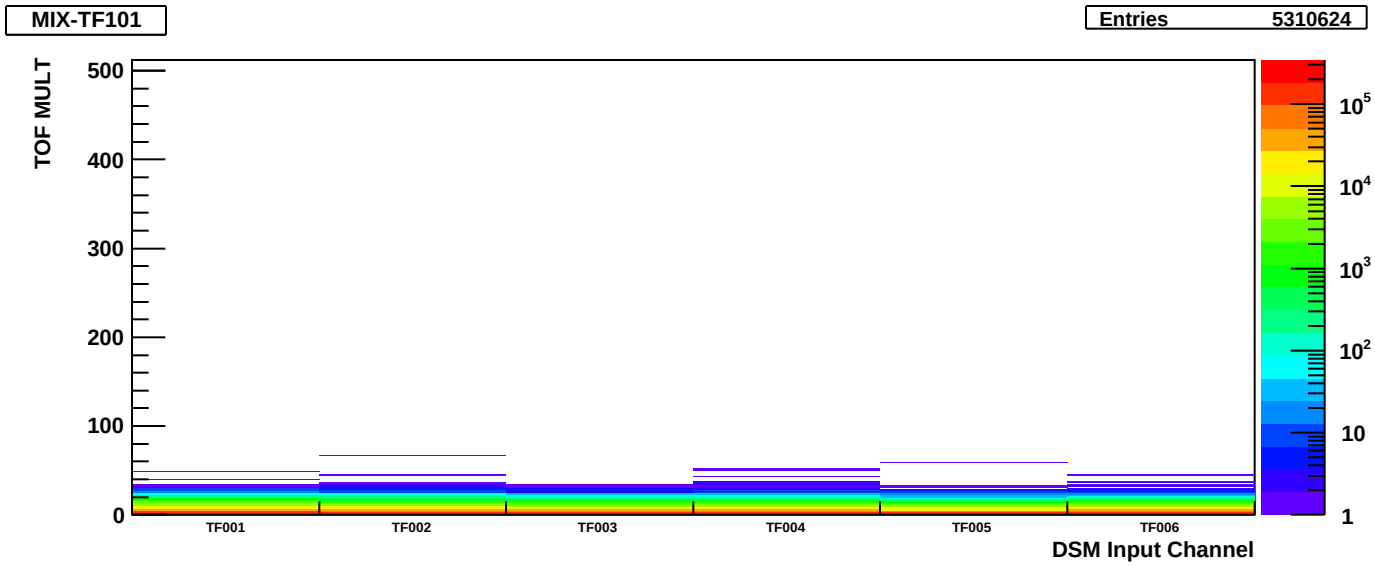

MIX-TF002
Entries 3.717437e+07
TOF MULT
TOF tray

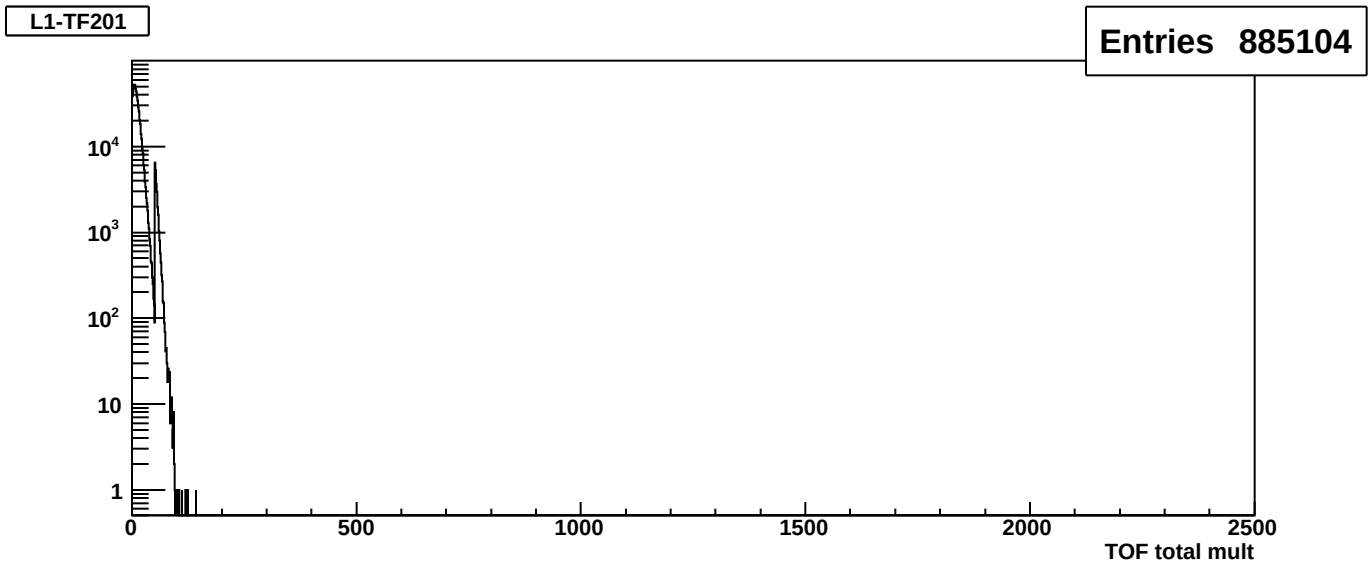

MIX-TF003
Entries 3.717437e+07
TOF MULT
TOF tray

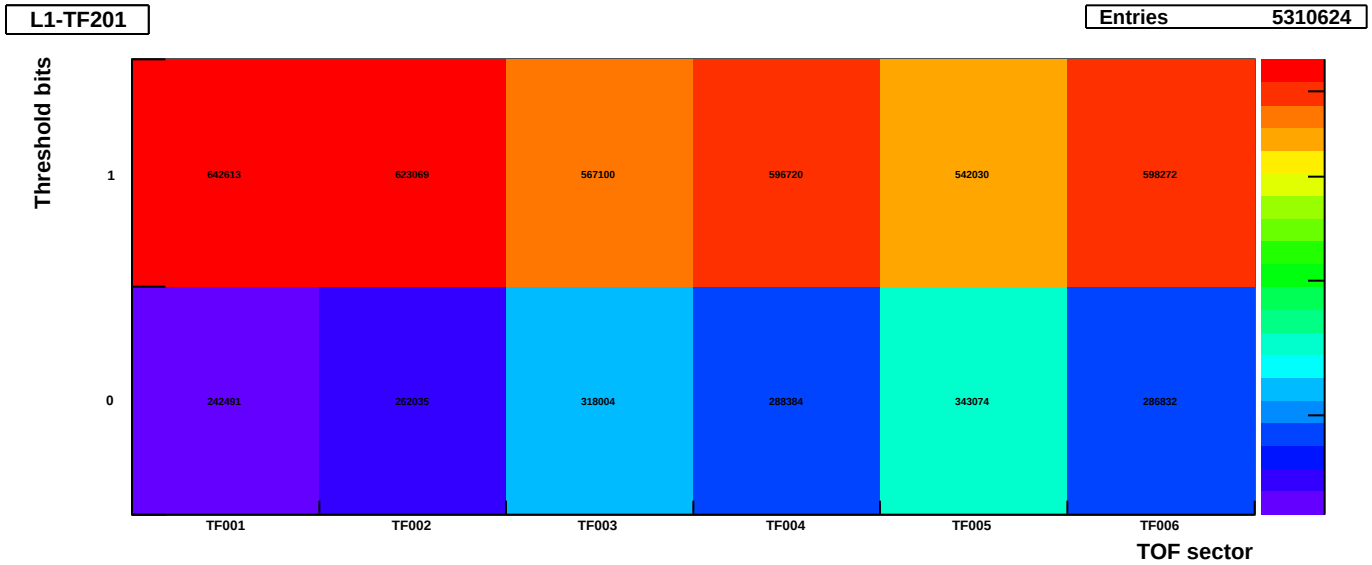

MIX-TF004
Entries 3.717437e+07
TOF MULT
TOF tray

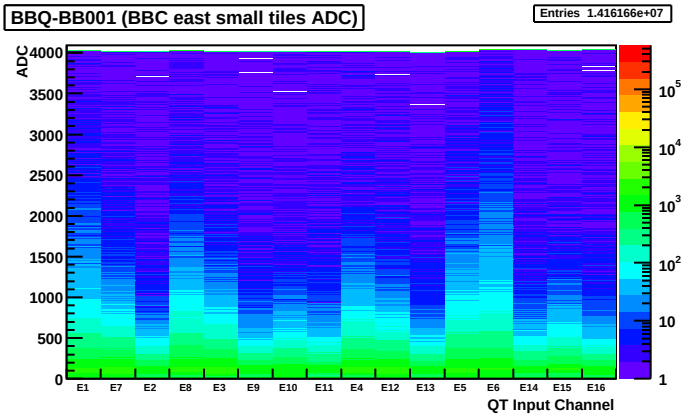

MIX-TF005
Entries 3.717437e+07
TOF MULT
TOF tray

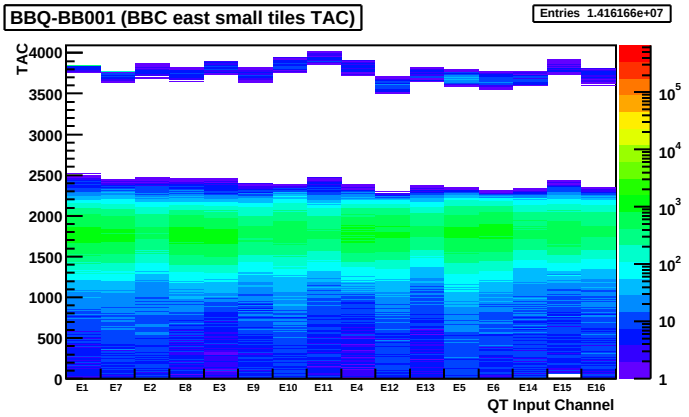

MIX-TF006
Entries 3.717437e+07
TOF MULT
TOF tray

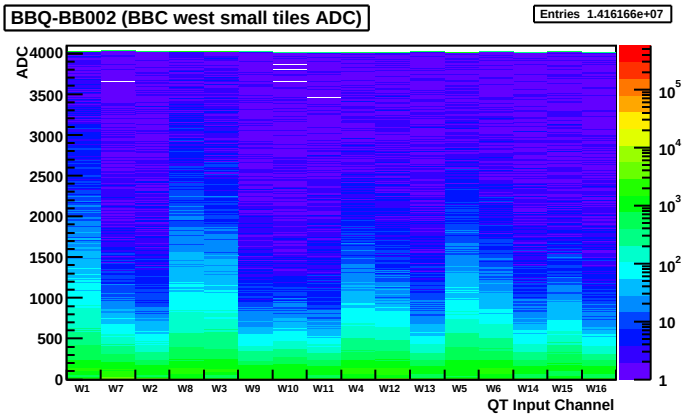

MIX-TF101
Entries 5310624
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

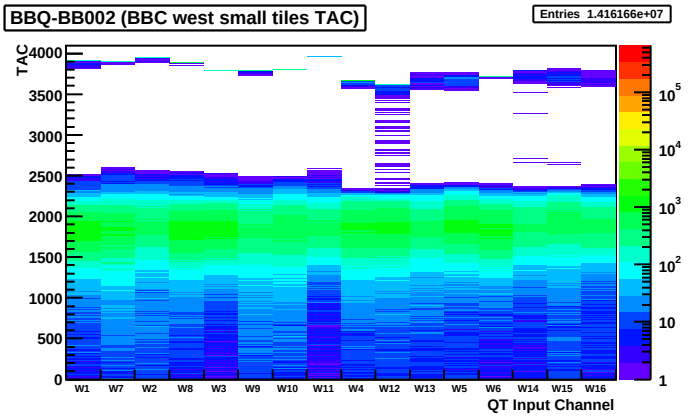

L1-TF201
Entries 885104
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

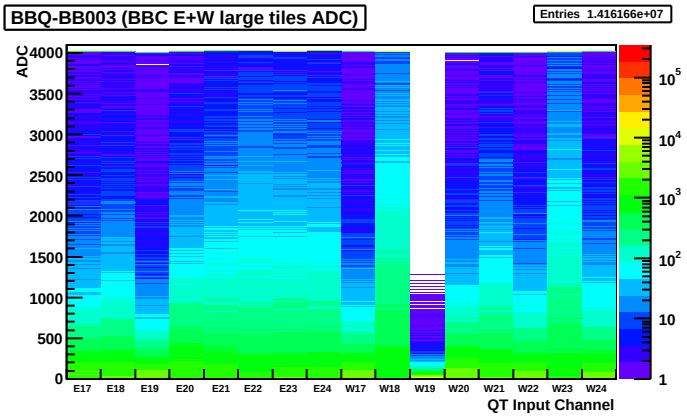

L1-TF201
Entries 5310624
Threshold bits
1
0
642613
623069
567100
596720
542030
598272
242491
262035
318004
288384
343074
286832
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

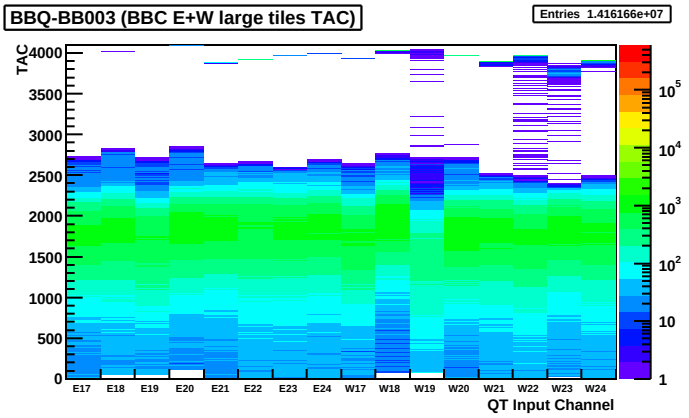

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.416166e+07
ADC
QT Input Channel

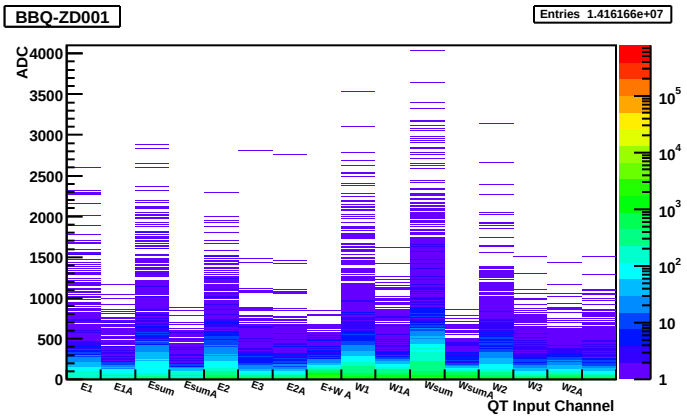

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.416166e+07
TAC
QT Input Channel

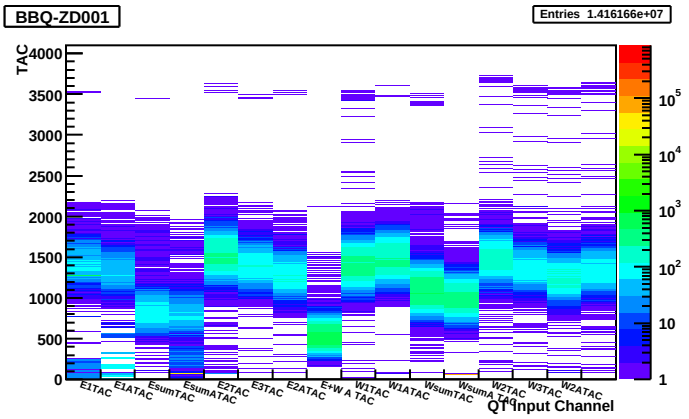

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.416166e+07
ADC
QT Input Channel

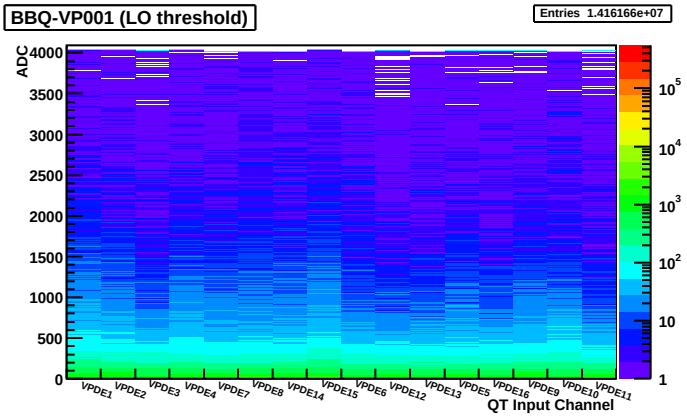

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.416166e+07
TAC
QT Input Channel

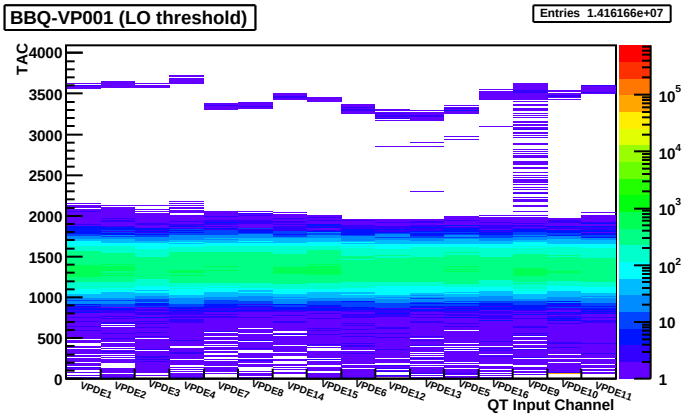

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.416166e+07
ADC
QT Input Channel

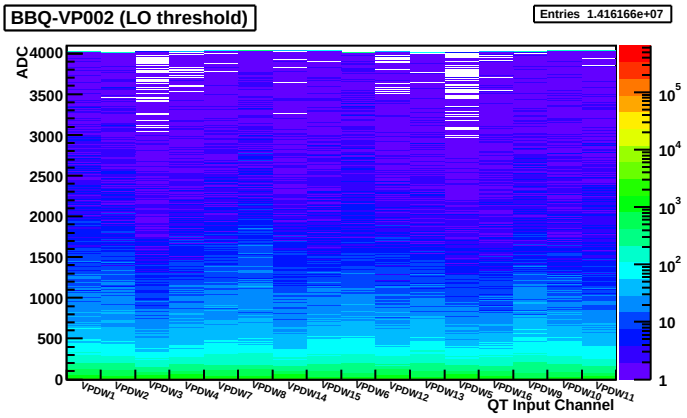

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.416166e+07
TAC
QT Input Channel

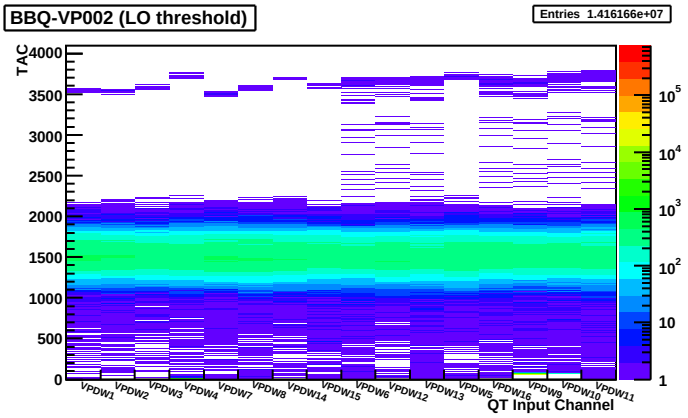

BBQ-ZD001
Entries 1.416166e+07
ADC
QT Input Channel

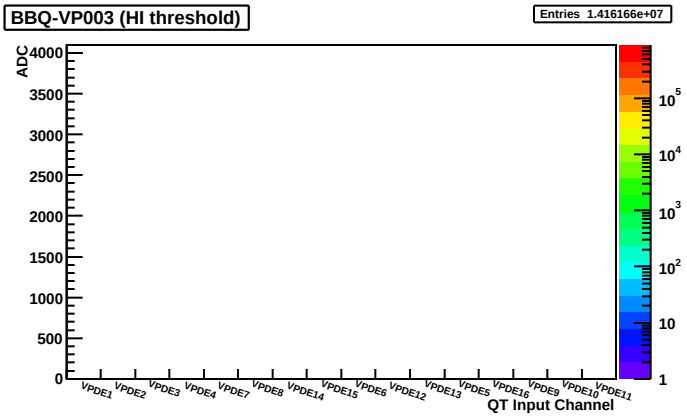

BBQ-ZD001
Entries 1.416166e+07
TAC
QT Input Channel

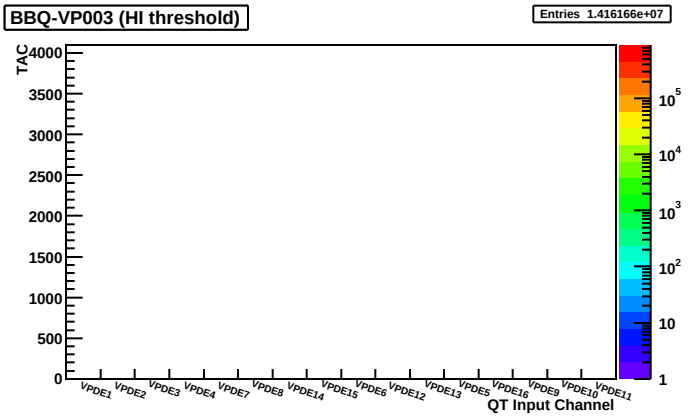

BBQ-VP001 (LO threshold)
Entries 1.416166e+07
ADC
QT Input Channel

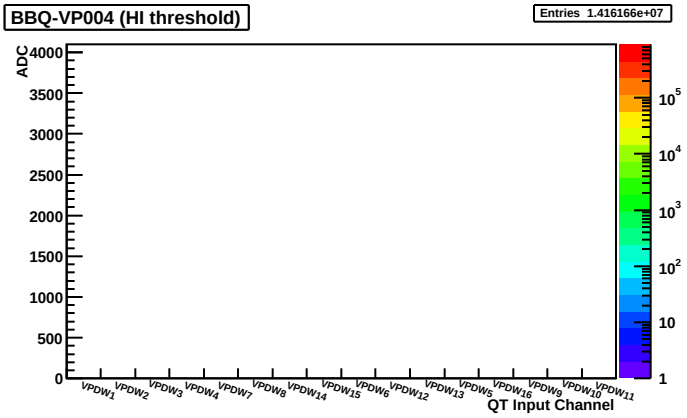

BBQ-VP001 (LO threshold)
Entries 1.416166e+07
TAC
QT Input Channel

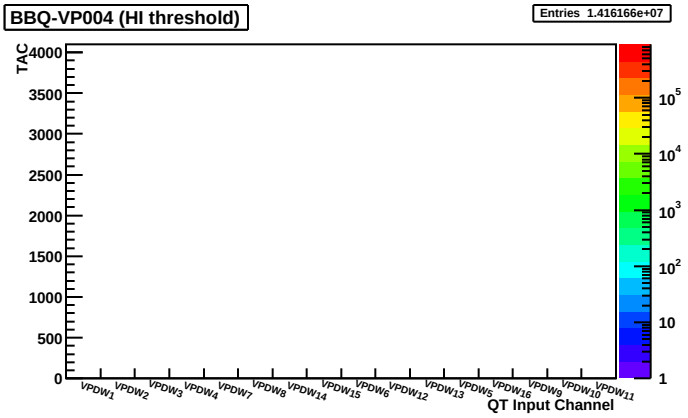

BBQ-VP002 (LO threshold)
Entries 1.416166e+07
ADC
QT Input Channel

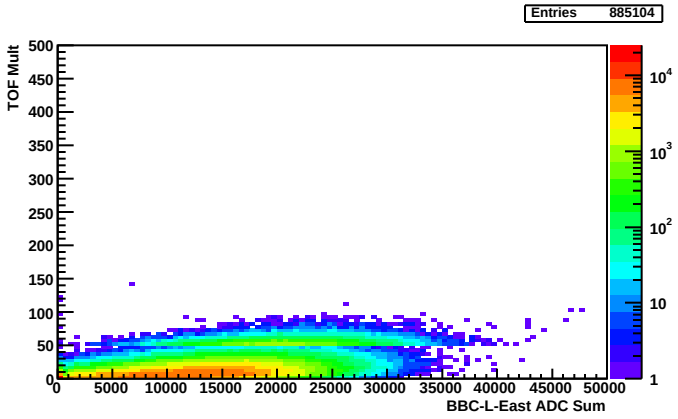

BBQ-VP002 (LO threshold)
Entries 1.416166e+07
TAC
QT Input Channel

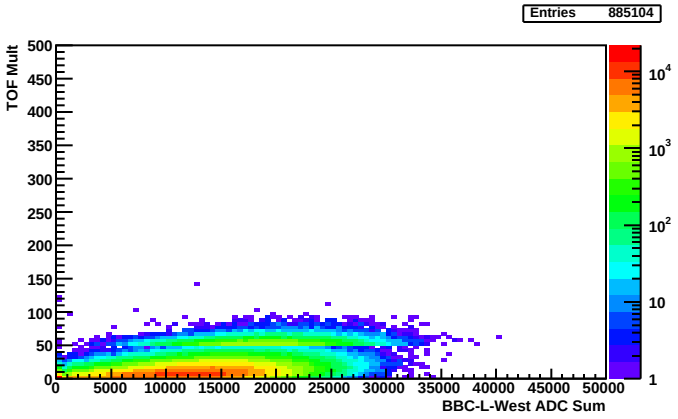

BBQ-VP003 (HI threshold)
Entries 1.416166e+07
ADC
QT Input Channel

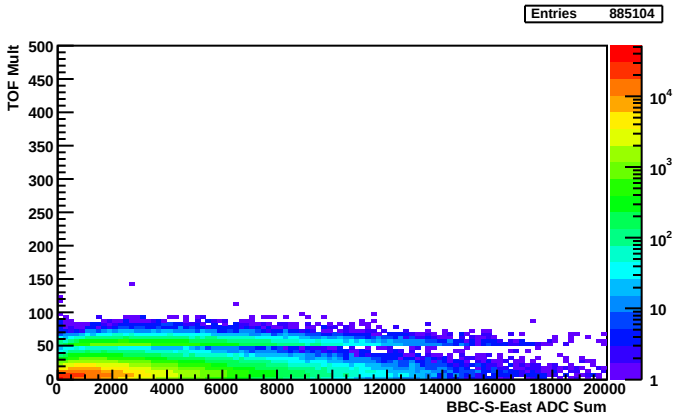

BBQ-VP003 (HI threshold)
Entries 1.416166e+07
TAC
QT Input Channel

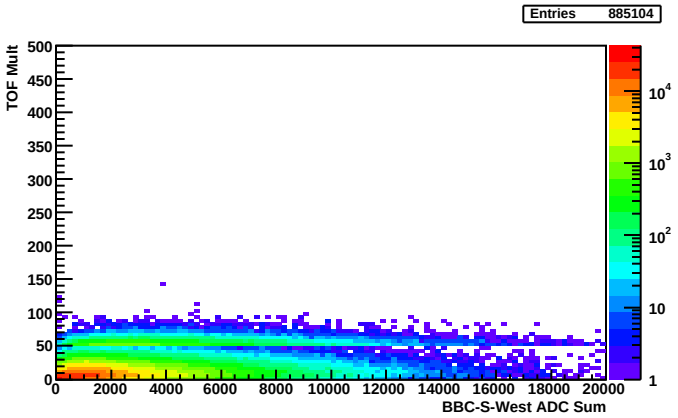

BBQ-VP004 (HI threshold)
Entries 1.416166e+07
ADC
QT Input Channel

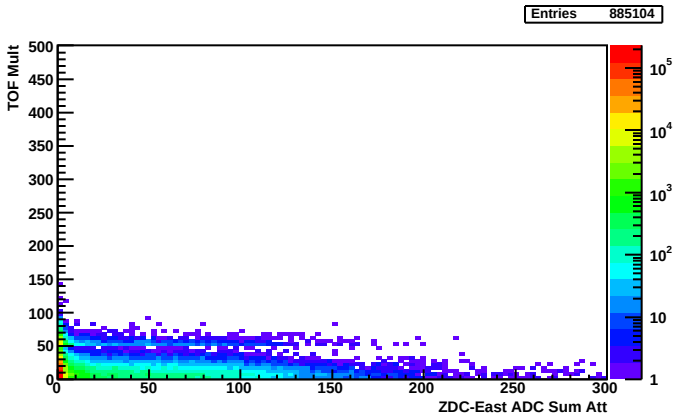

BBQ-VP004 (HI threshold)
Entries 1.416166e+07
TAC
QT Input Channel

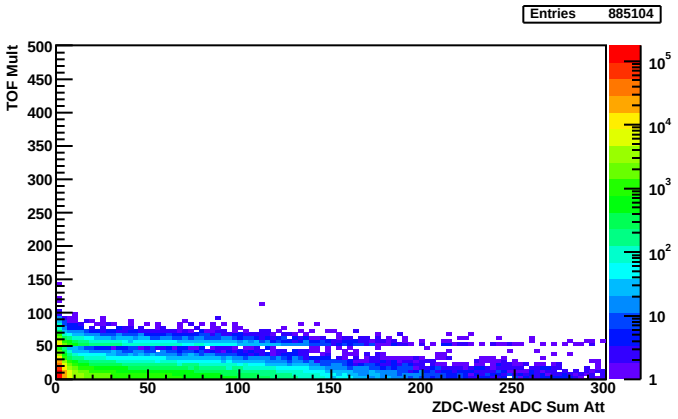

Entries 885104
TOF Mult
BBC-L-East ADC Sum

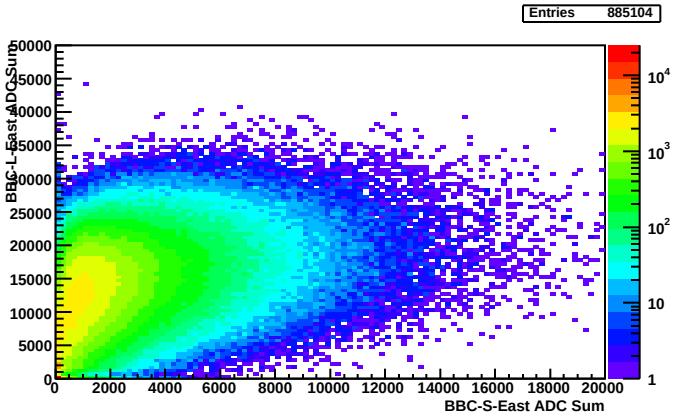

Entries 885104
TOF Mult
BBC-L-West ADC Sum

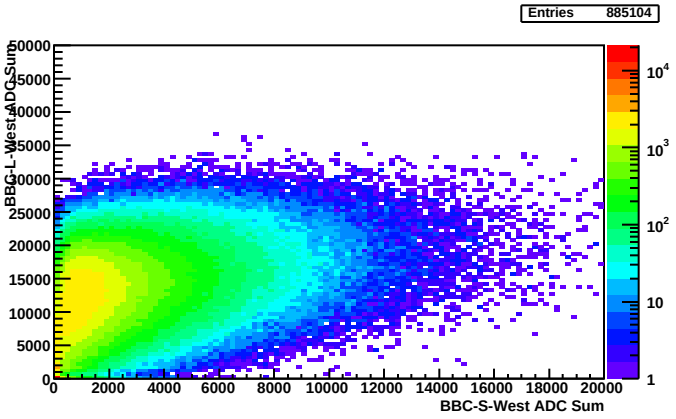

Entries 885104
TOF Mult
BBC-S-East ADC Sum

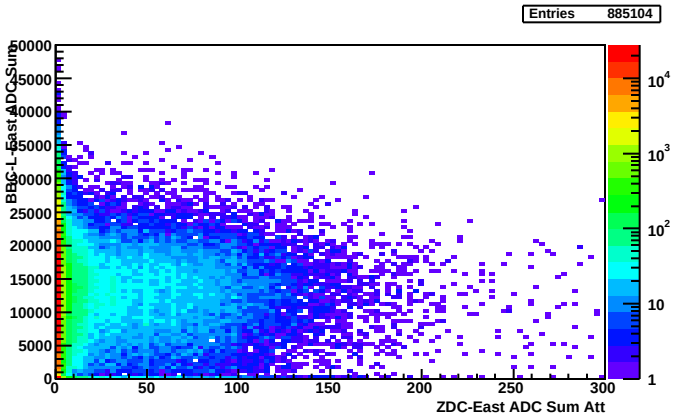

Entries 885104
TOF Mult
BBC-S-West ADC Sum

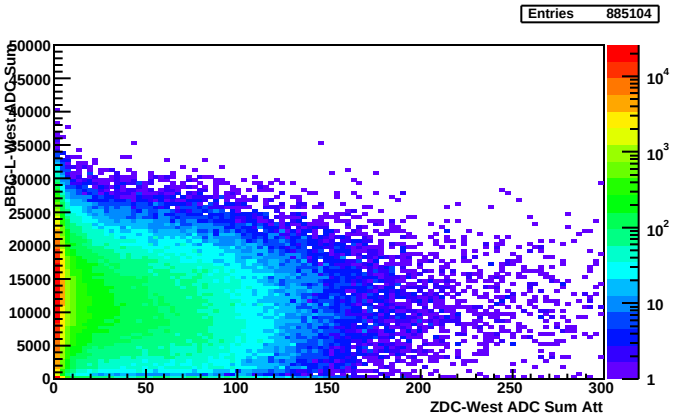

Entries 885104
TOF Mult
ZDC-East ADC Sum Att

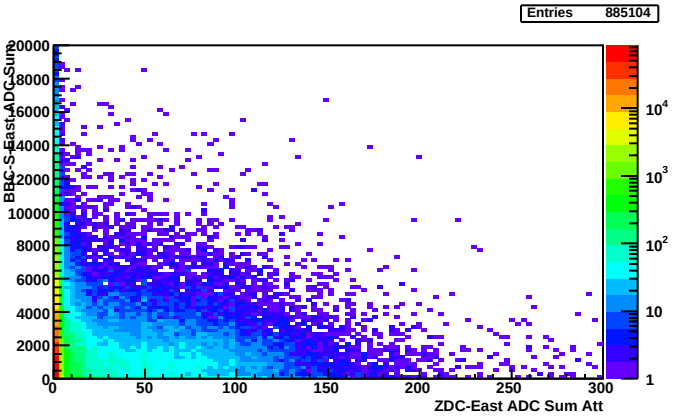

Entries 885104
TOF Mult
ZDC-West ADC Sum Att

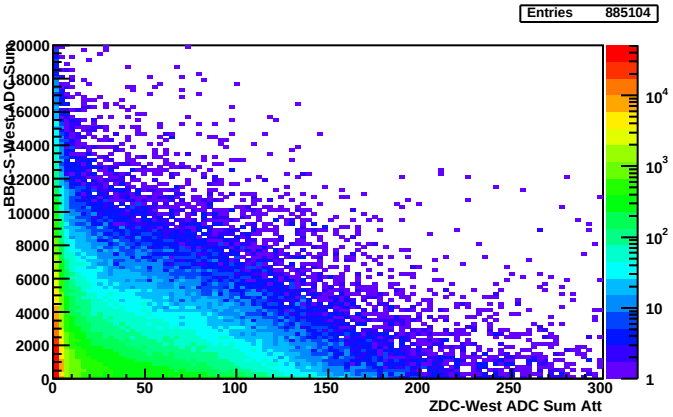

Entries 885104
BBC-L-East ADC Sum
BBC-S-East ADC Sum

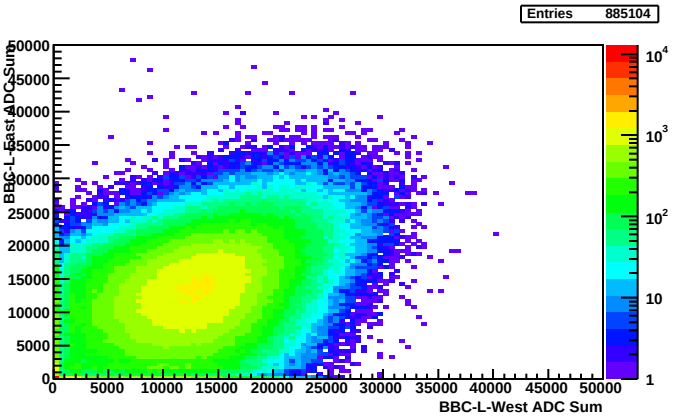

Entries 885104
BBC-L-West ADC Sum
BBC-S-West ADC Sum

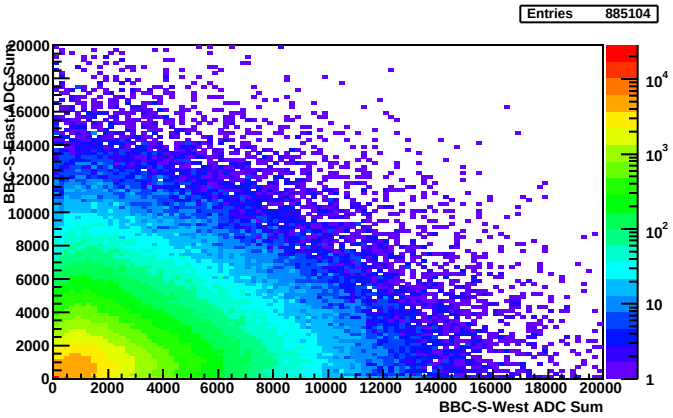

Entries 885104
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

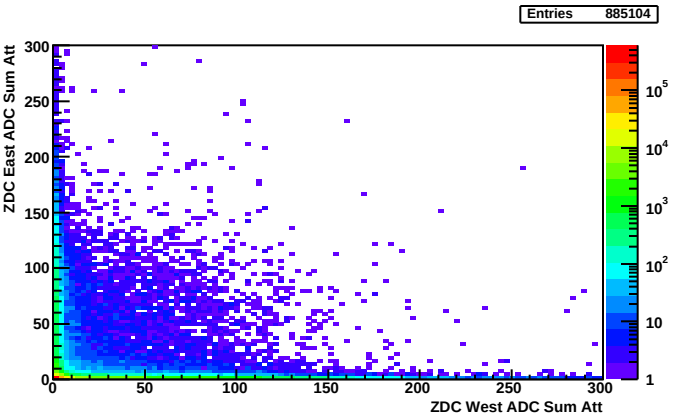

Entries 885104
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

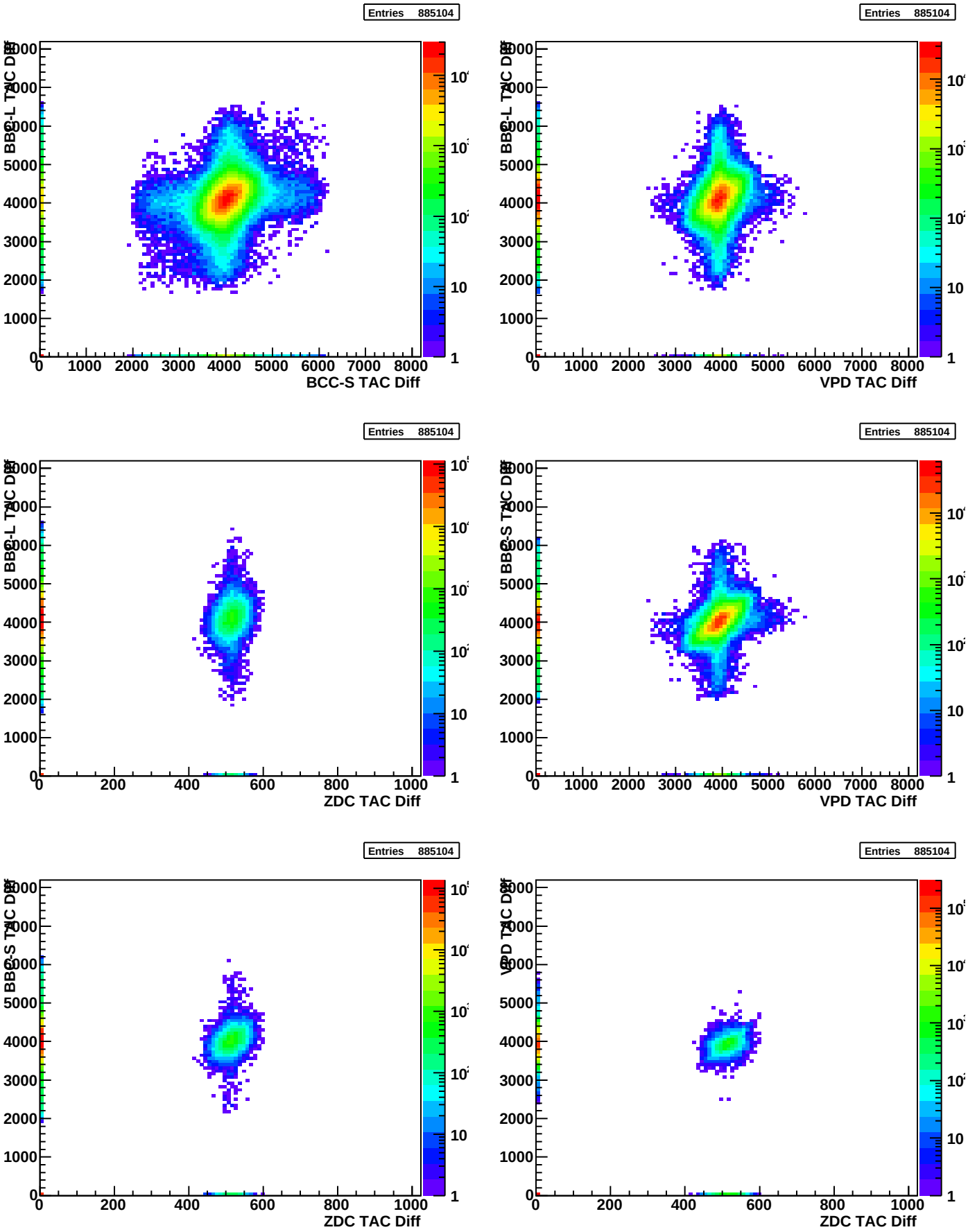

Entries 885104
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

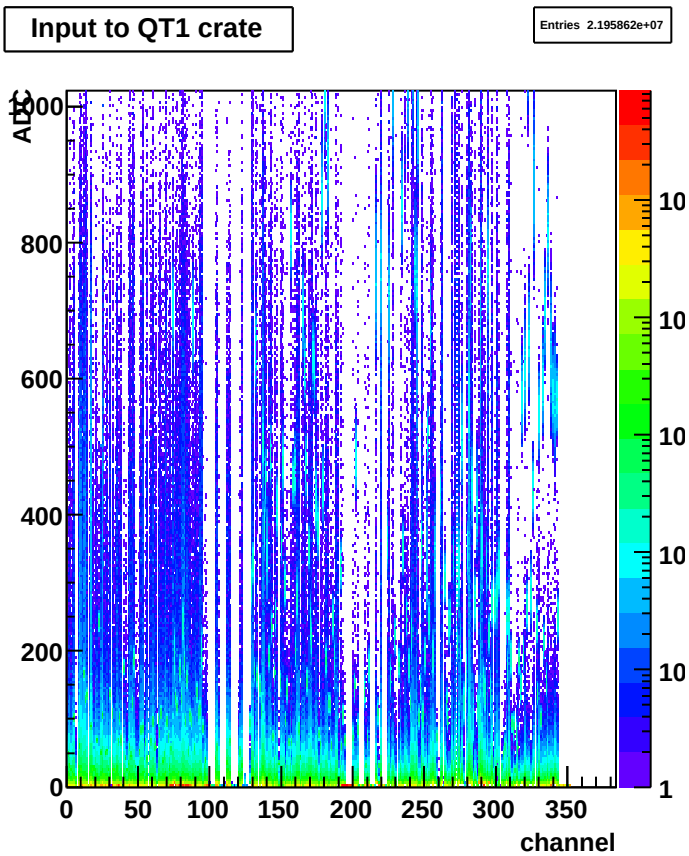

Entries 885104
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

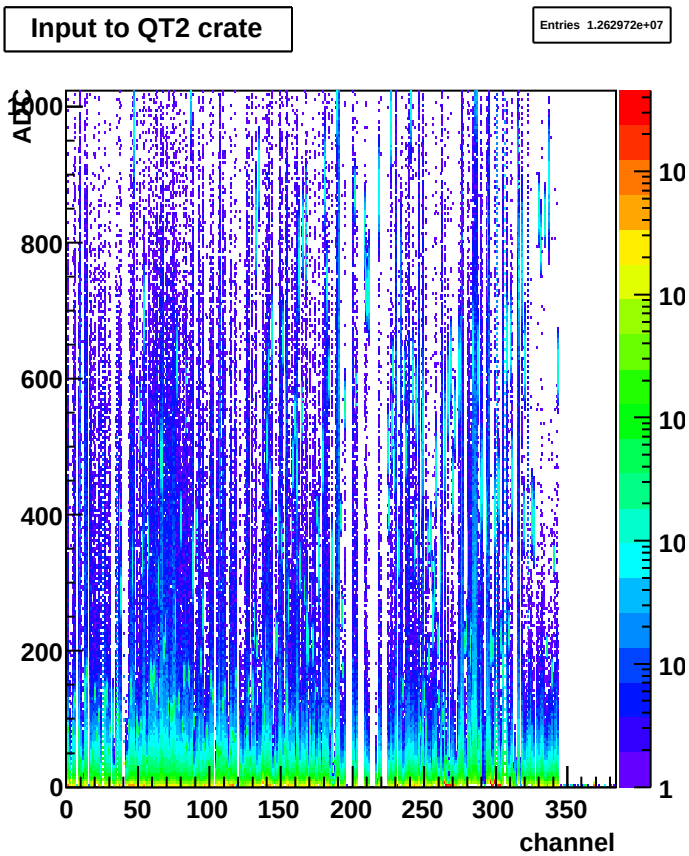

Entries 885104
BBC-L-East ADC Sum
BBC-L-West ADC Sum

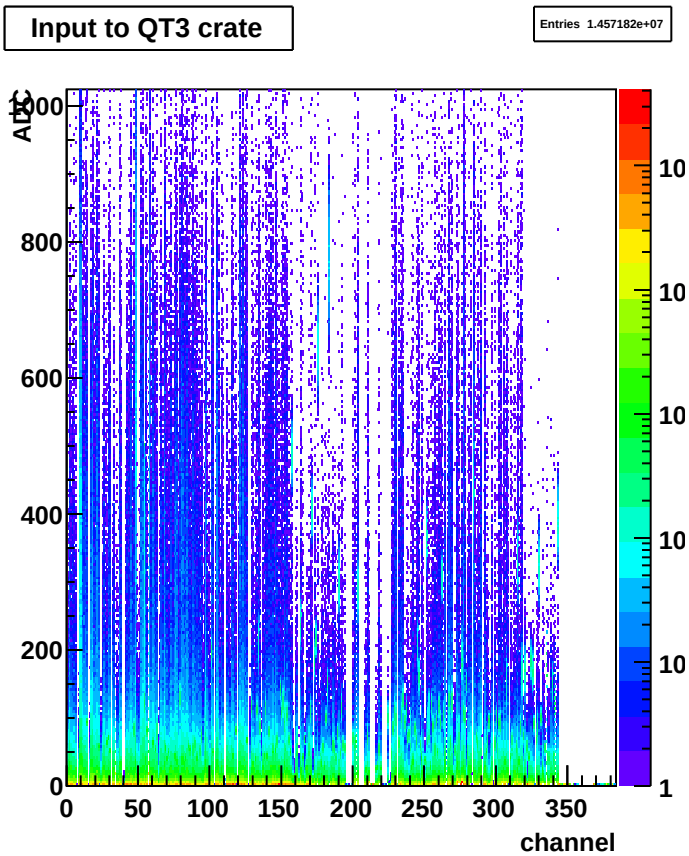

Entries 885104
BBC-S-East ADC Sum
BBC-S-West ADC Sum

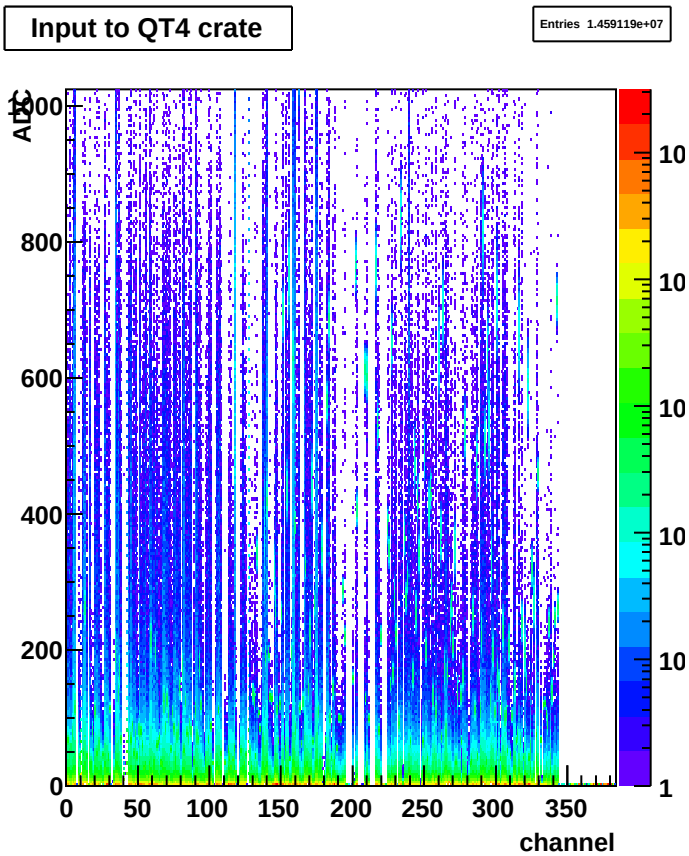

Entries 885104
ZDC East ADC Sum Att
ZDC West ADC Sum Att

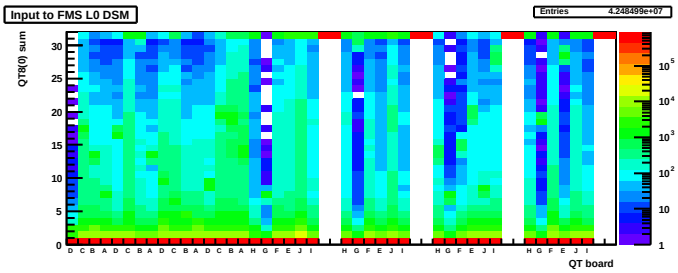

Entries 885104
BBC-L TAC Diff
BCC-S TAC Diff
Entries 885104
BBC-L TAC Diff
VPD TAC Diff
Entries 885104
BBC-L TAC Diff
ZDC TAC Diff
Entries 885104
BBC-S TAC Diff
VPD TAC Diff
Entries 885104
BBC-S TAC Diff
ZDC TAC Diff
Entries 885104
VPD TAC Diff
ZDC TAC Diff

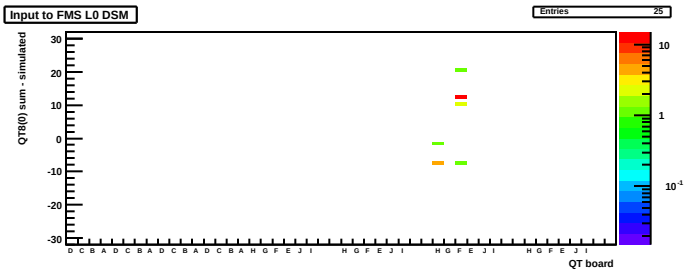

Input to QT1 crate
Entries 2.195862e+07
ADC
channel

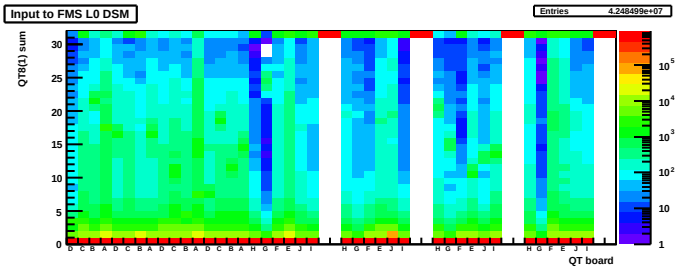

Input to QT2 crate
Entries 1.262972e+07
ADC
channel

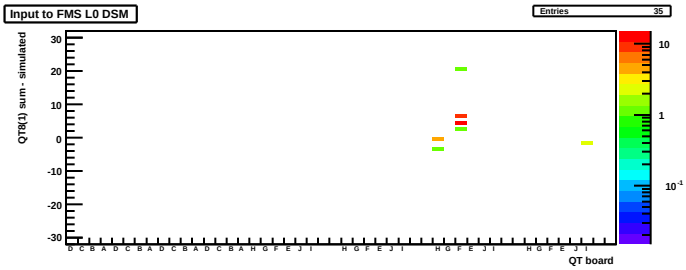

Input to QT3 crate
Entries 1.457182e+07
ADC
channel

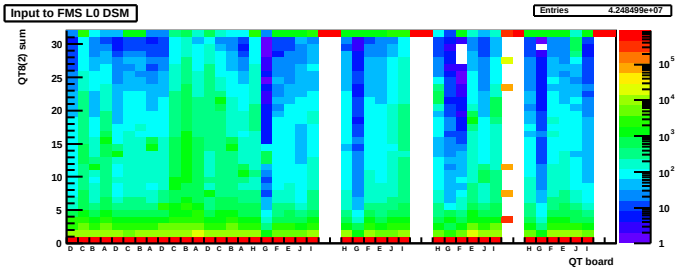

Input to QT4 crate
Entries 1.459119e+07
ADC
channel

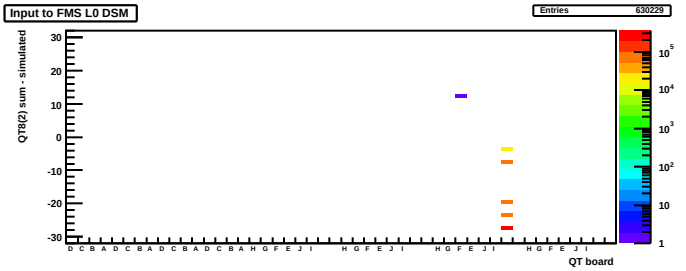

Input to FMS L0 DSM
Entries 4.248499e+07
QT8(0) sum
QT board

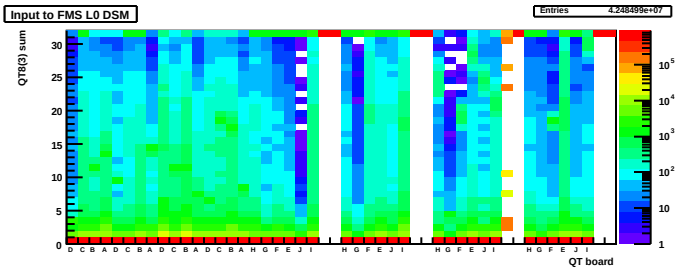

Input to FMS L0 DSM
Entries 25
QT8(0) sum - simulated
QT board

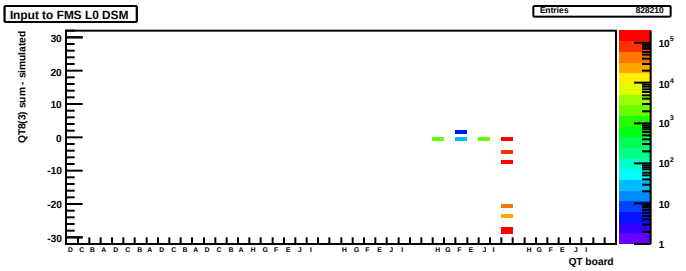

Input to FMS L0 DSM
Entries 4.248499e+07
QT8(1) sum
QT board

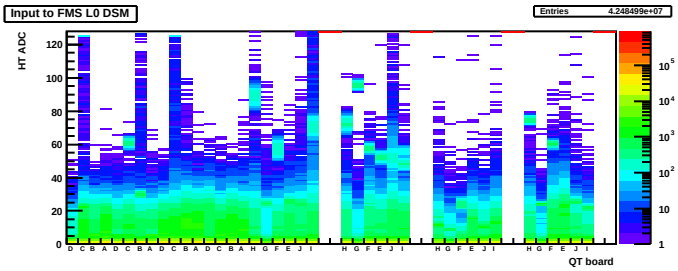

Input to FMS L0 DSM
Entries 35
QT8(1) sum - simulated
QT board

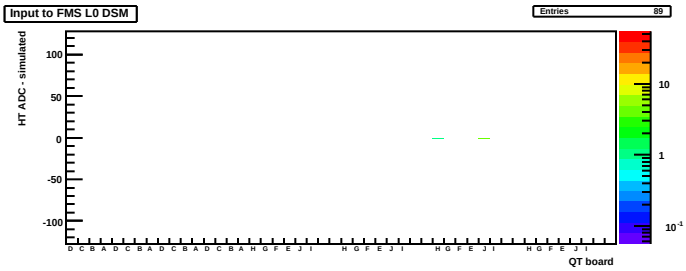

Input to FMS L0 DSM
Entries 4.248499e+07
QT8(2) sum
QT board

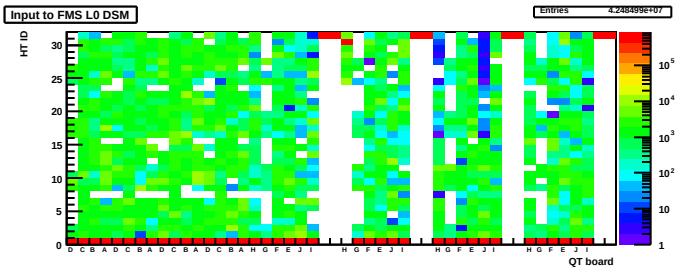

Input to FMS L0 DSM
Entries 630229
QT8(2) sum - simulated
QT board

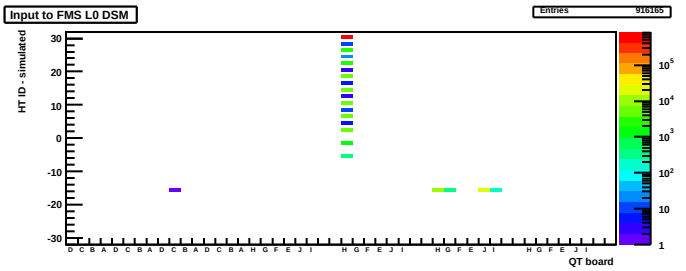

Input to FMS L0 DSM
Entries 4.248499e+07
QT8(3) sum
QT board

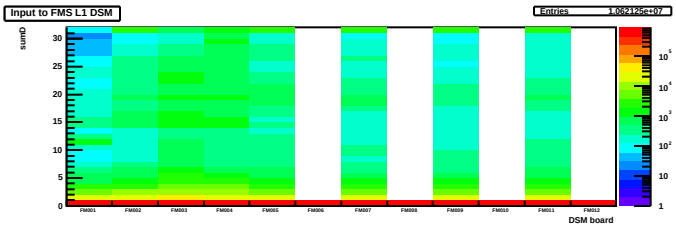

Input to FMS L0 DSM
Entries 828210
QT8(3) sum - simulated
QT board

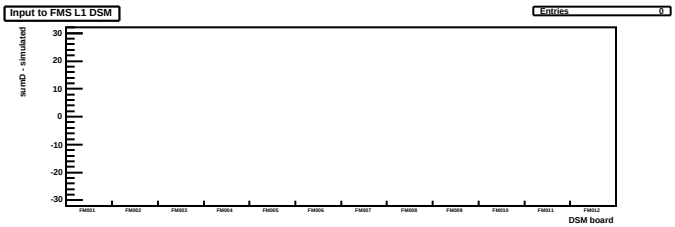

Input to FMS L0 DSM
Entries 4.248499e+07
HT ADC
QT board

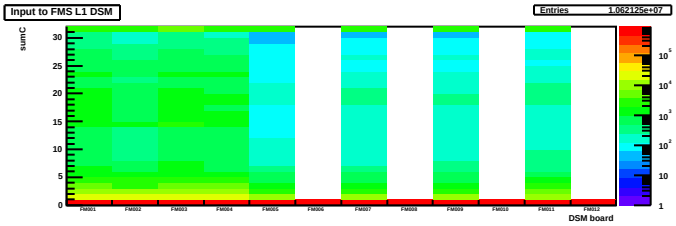

Input to FMS L0 DSM
Entries 89
HT ADC - simulated
QT board

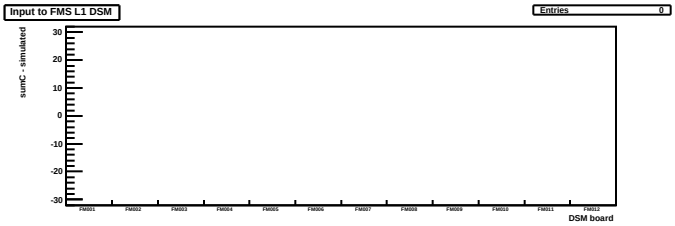

Input to FMS L0 DSM
Entries 4.248499e+07
HT ID
QT board

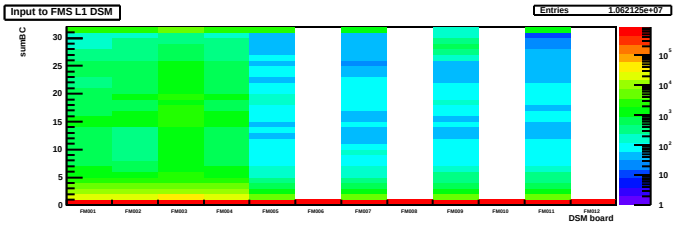

Input to FMS L0 DSM
Entries 916165
HT ID - simulated
QT board

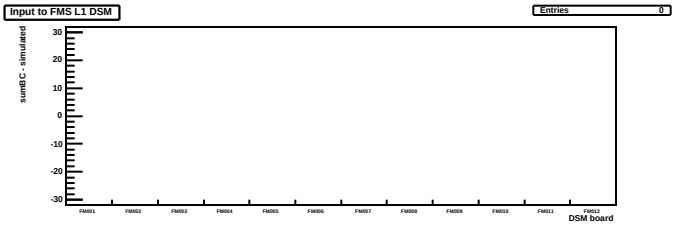

Input to FMS L1 DSM
Entries 1.062125e+07
sumD
DSM board

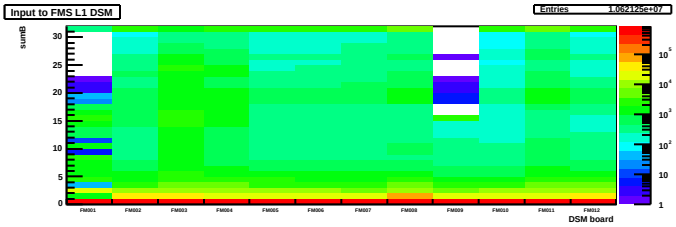

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

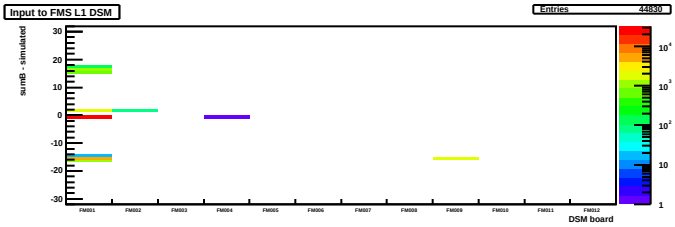

Input to FMS L1 DSM
Entries 1.062125e+07
sumC
DSM board

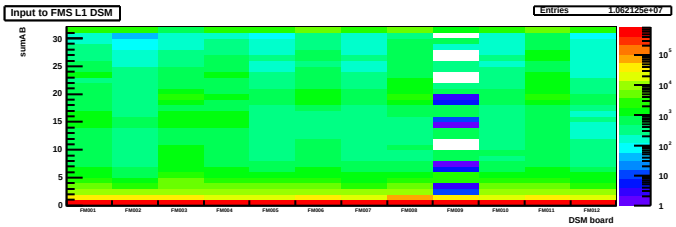

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

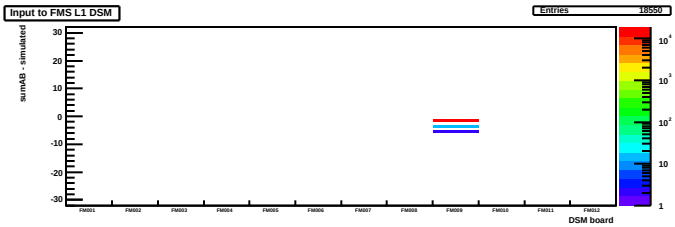

Input to FMS L1 DSM
Entries 1.062125e+07
sumBC
DSM board

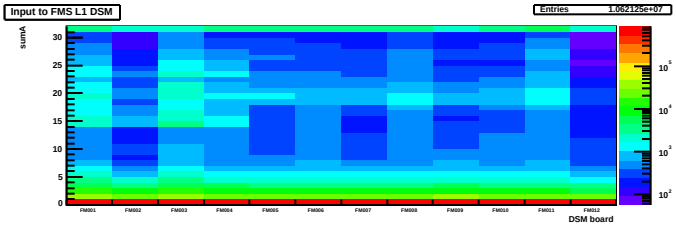

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

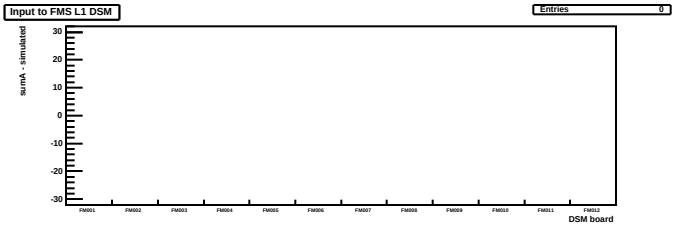

Input to FMS L1 DSM
Entries 1.062125e+07
sumB
DSM board

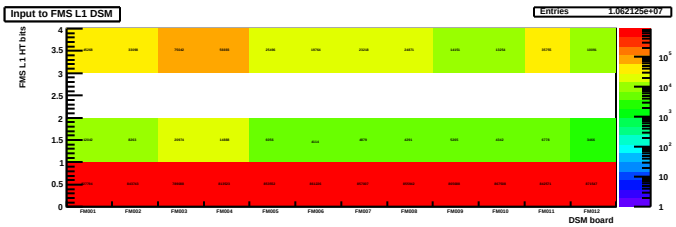

Input to FMS L1 DSM
Entries 44830
sumB - simulated
DSM board

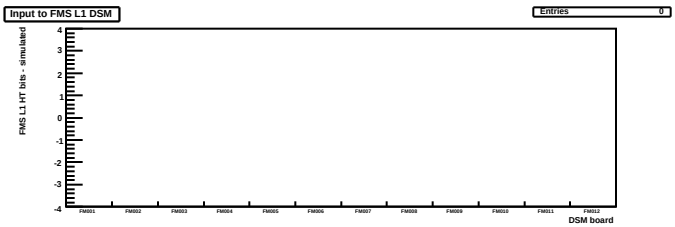

Input to FMS L1 DSM
Entries 1.062125e+07
sumAB
DSM board

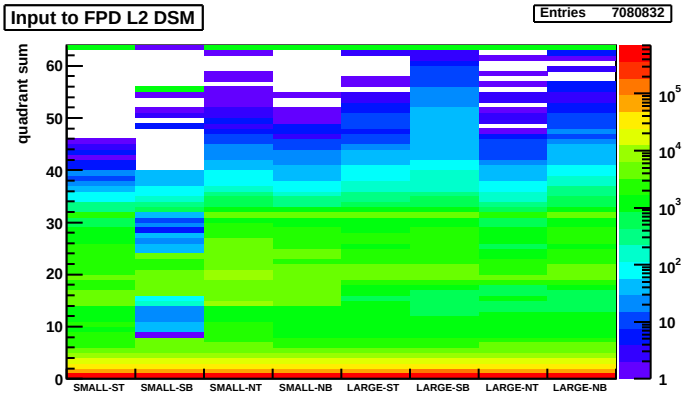

Input to FMS L1 DSM
Entries 18550
sumAB - simulated
DSM board

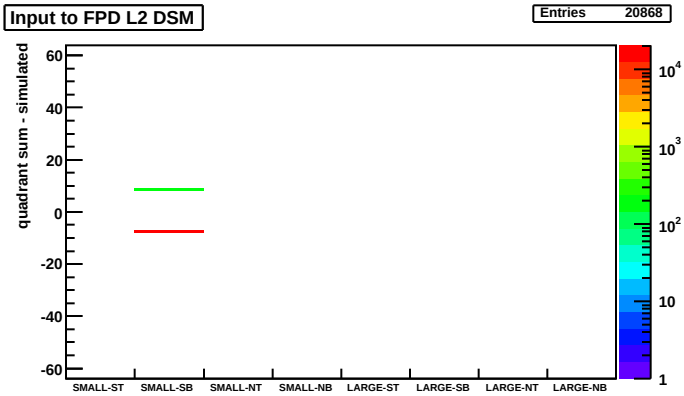

Input to FMS L1 DSM
Entries 1.062125e+07
sumA
DSM board

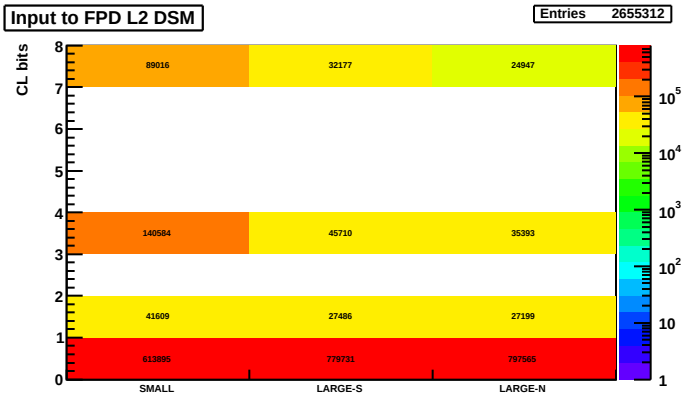

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

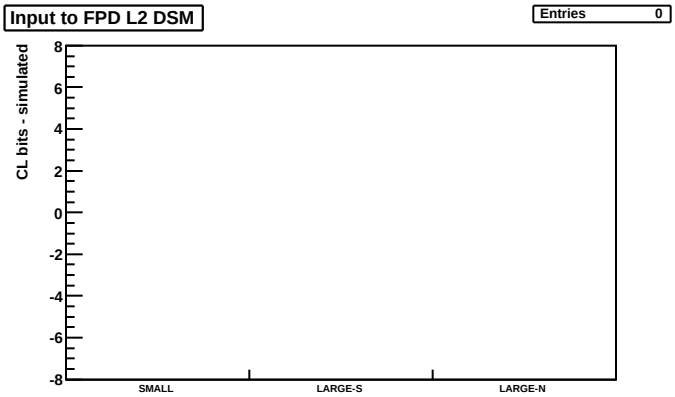

Input to FMS L1 DSM
Entries 1.062125e+07
FMS L1 HT bits
DSM board

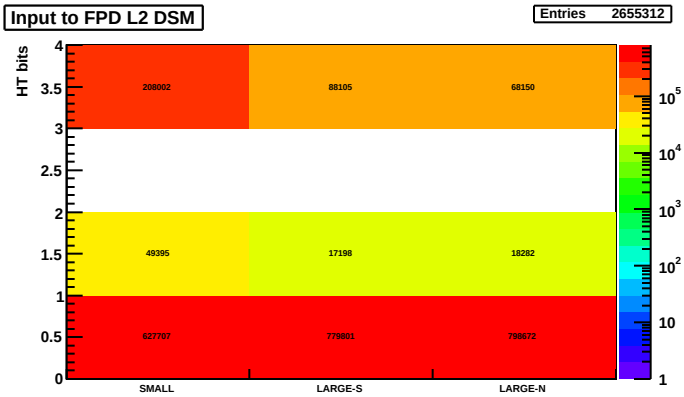

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

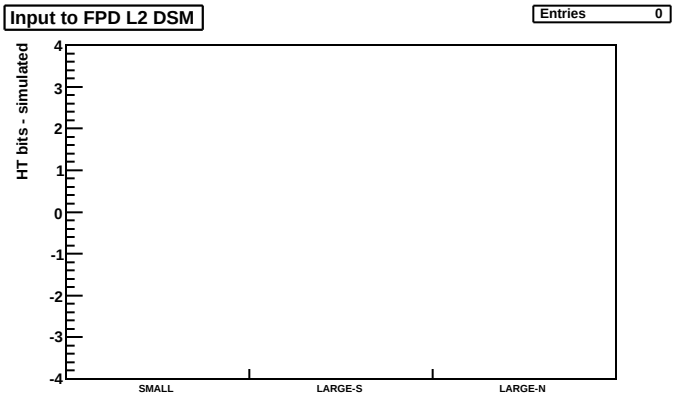

Input to FPD L2 DSM
Entries 7080832
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

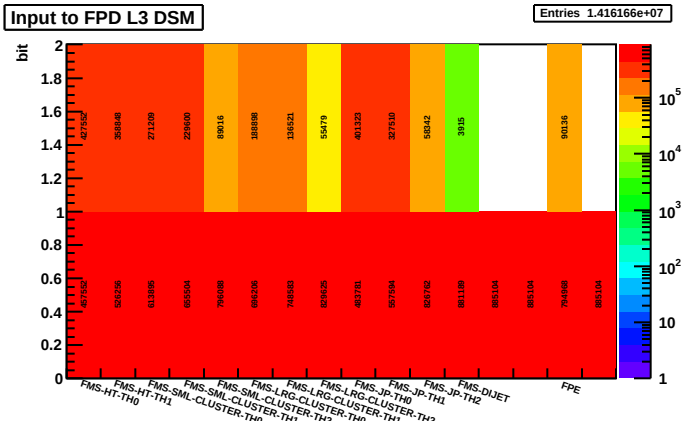

Input to FPD L2 DSM
Entries 20868
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

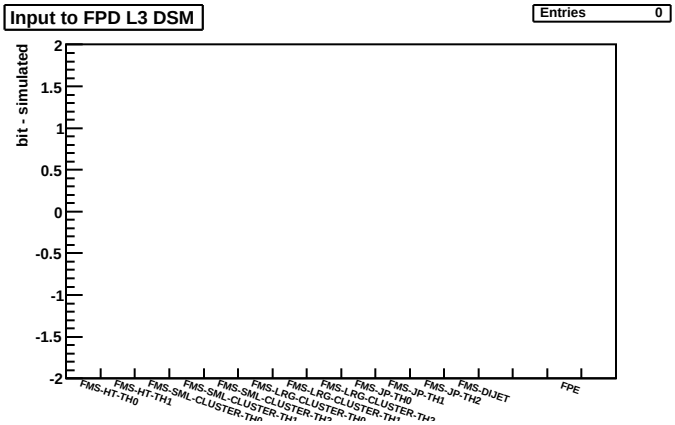

Input to FPD L2 DSM
Entries 2655312
CL bits
89016
32177
24947
140584
45710
35393
41609
27486
27199
613895
779731
797565
SMALL
LARGE-S
LARGE-N

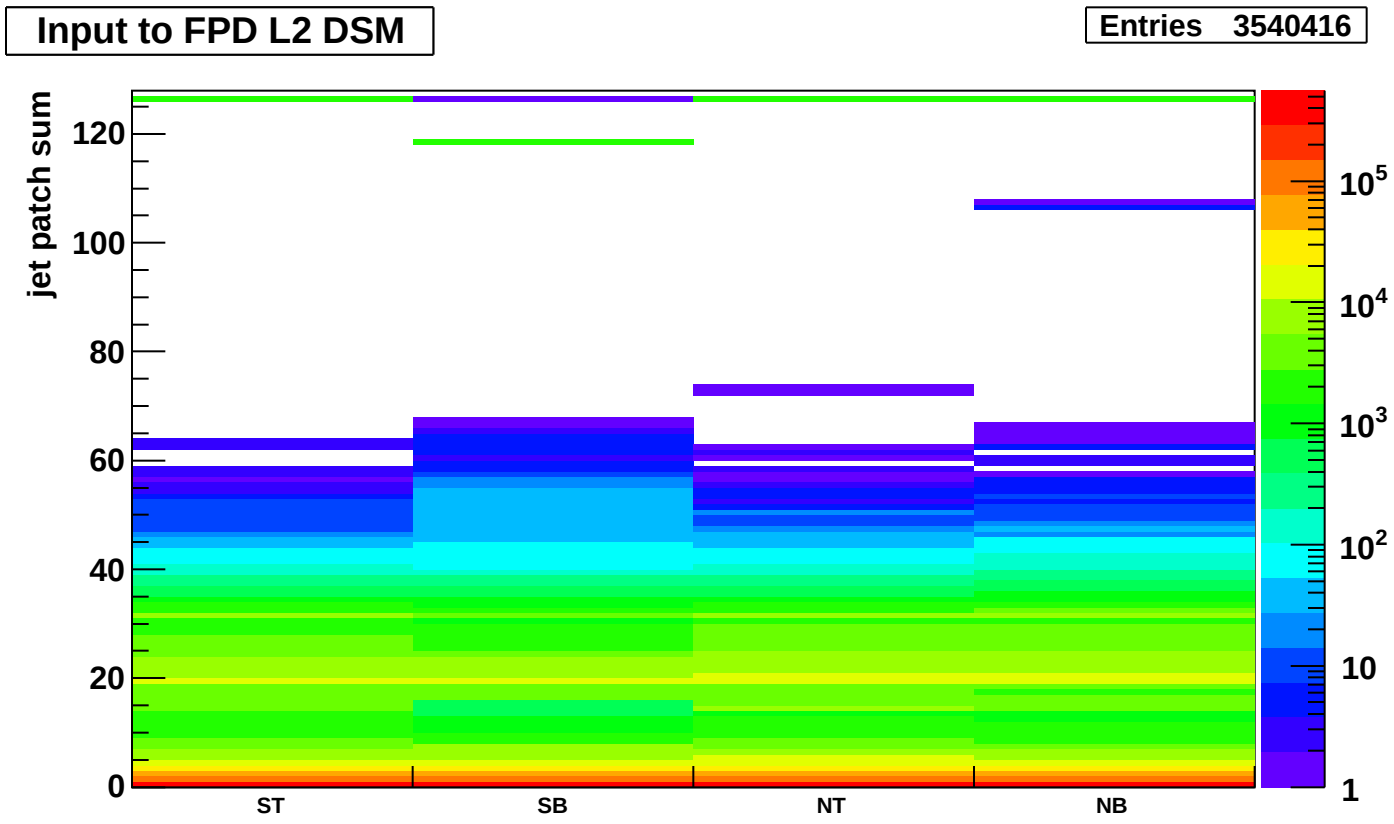

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

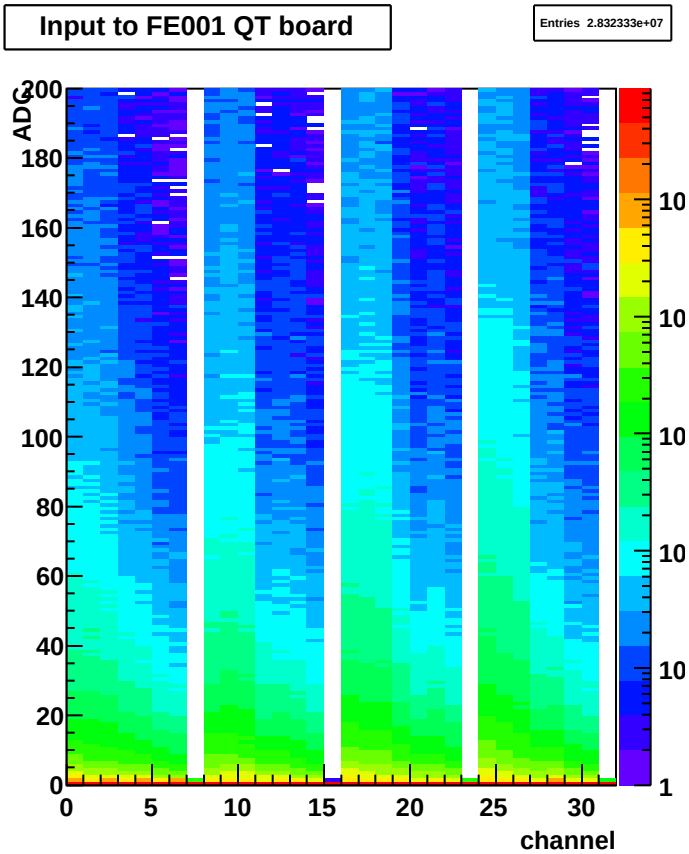

Input to FPD L2 DSM
Entries 2655312
HT bits
208002
88105
68150
49395
17198
18282
627707
779801
798672
SMALL
LARGE-S
LARGE-N

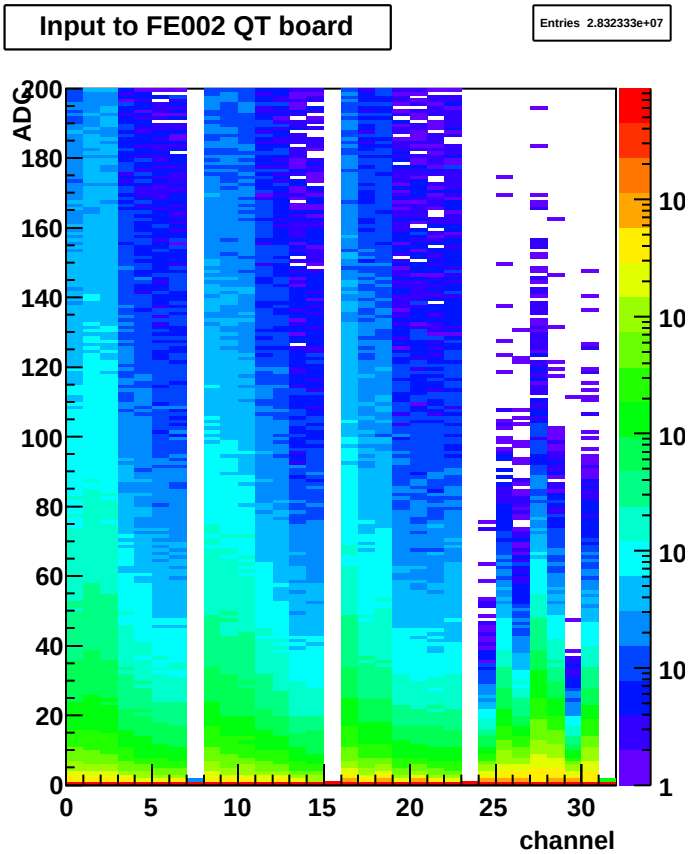

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

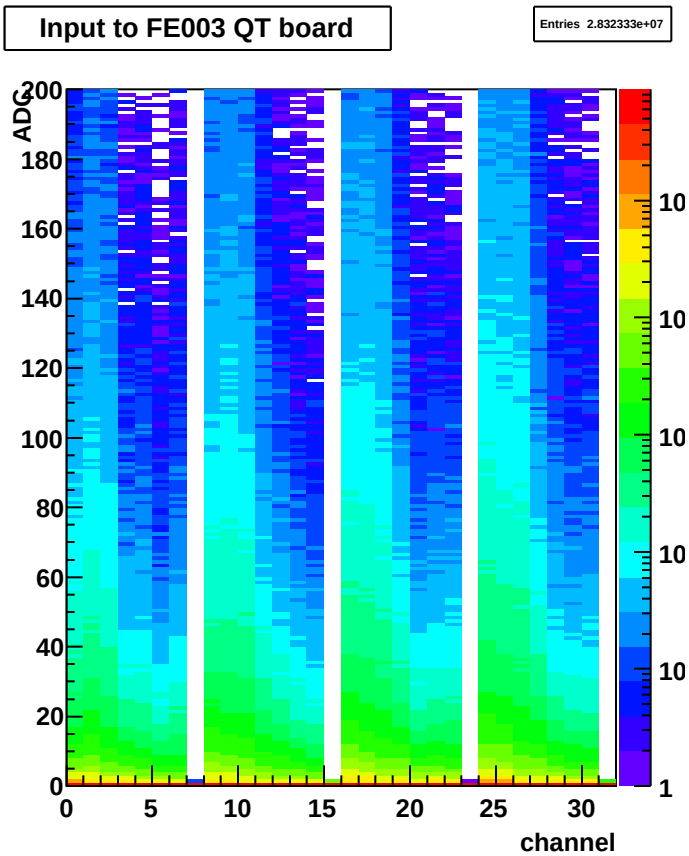

Input to FPD L3 DSM
Entries 1.416166e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

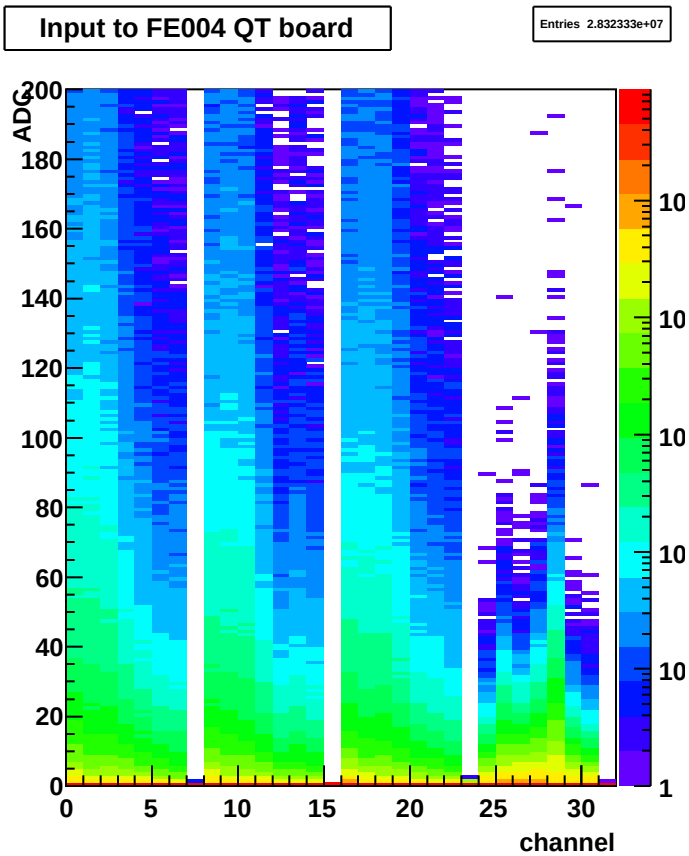

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

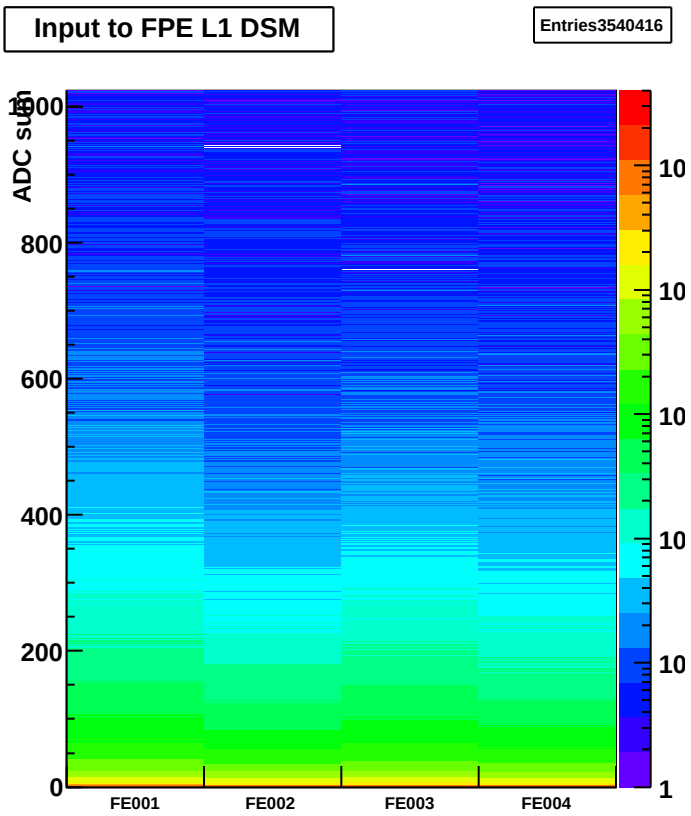

Input to FPD L2 DSM
Entries 3540416
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

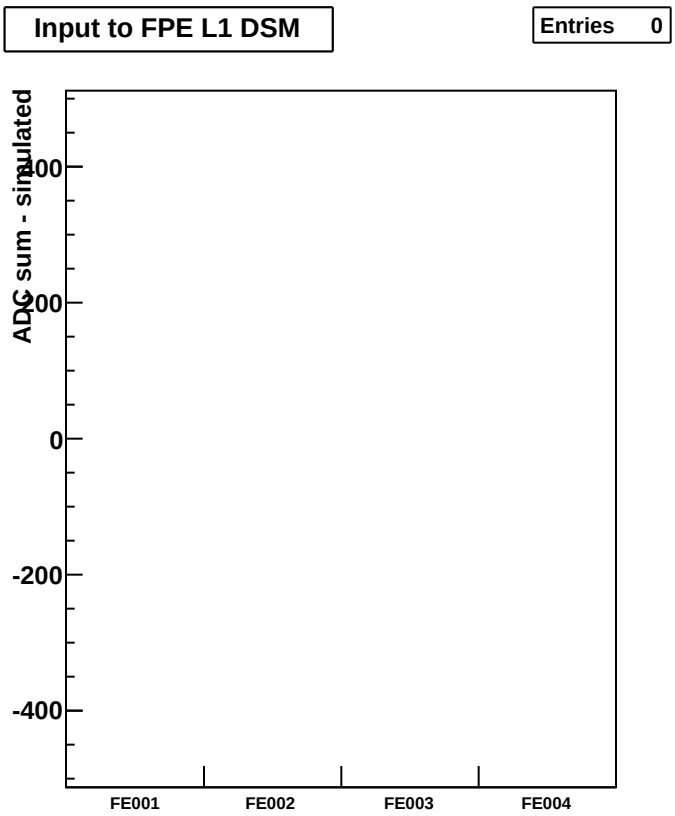

Input to FE001 QT board
Entries 2.832333e+07
ADC
channel

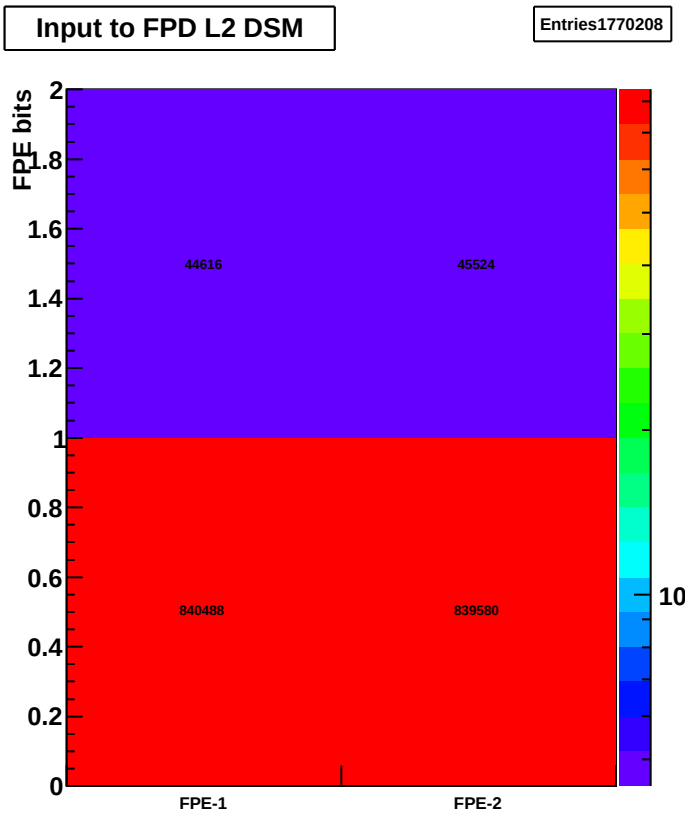

Input to FE002 QT board
Entries 2.832333e+07
ADC
channel

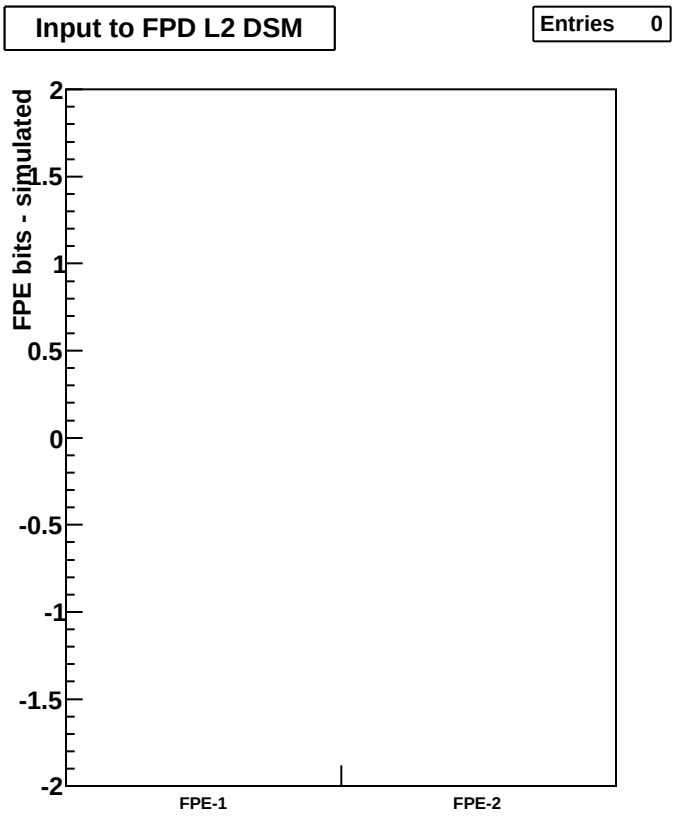

Input to FE003 QT board
Entries 2.832333e+07
ADC
channel


Input to FE004 QT board
Entries 2.832333e+07
ADC
channel


Input to FPE L1 DSM
Entries3540416
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1770208
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
44616
45524
840488
839580
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2