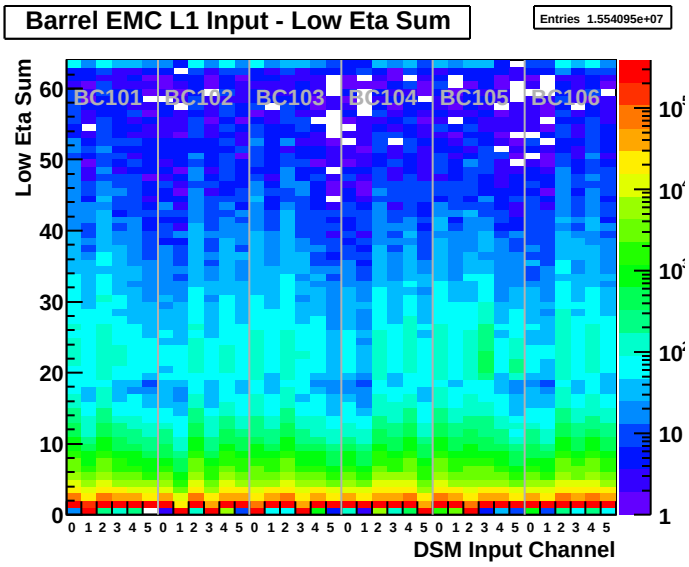
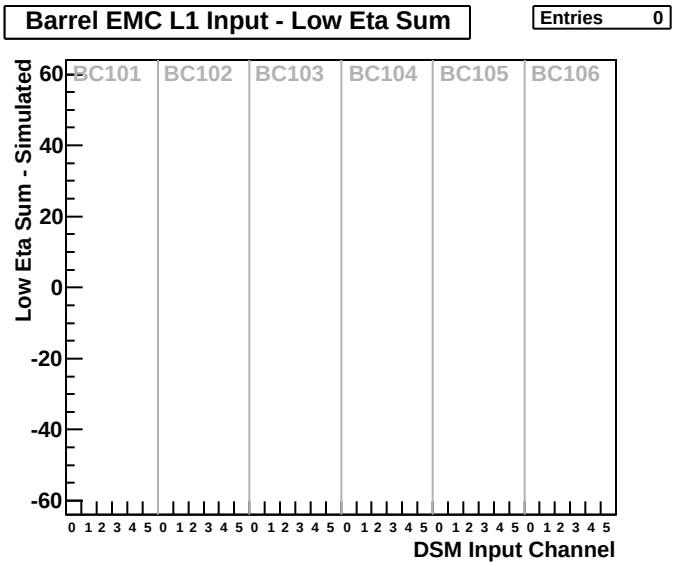
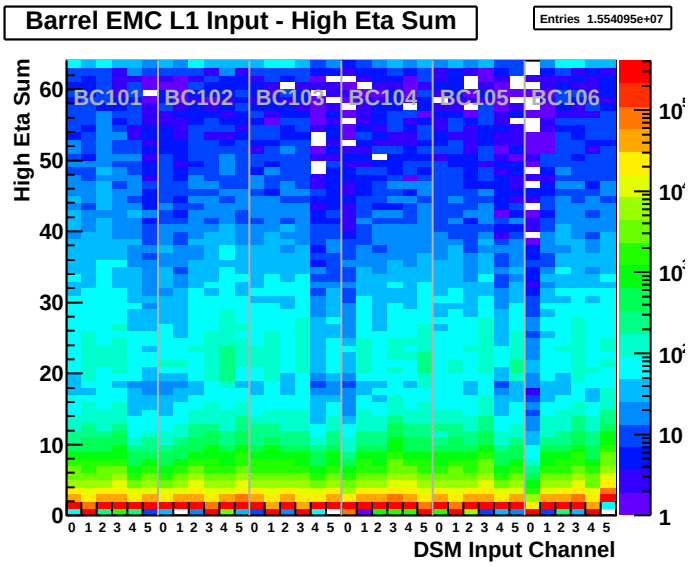
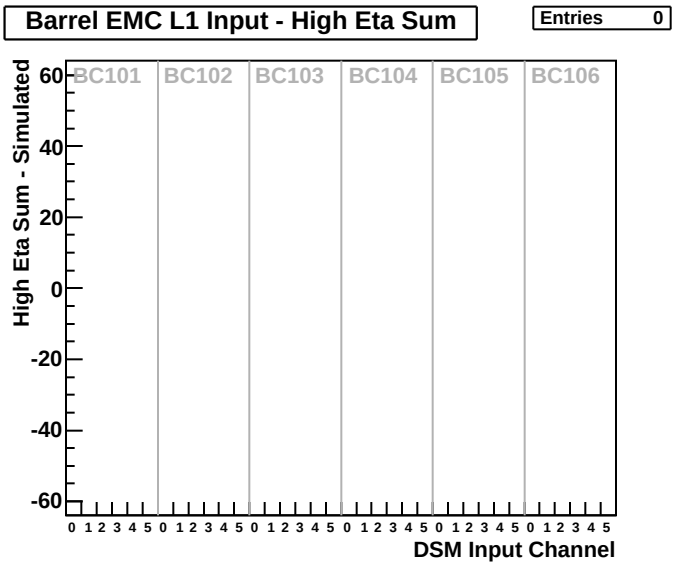
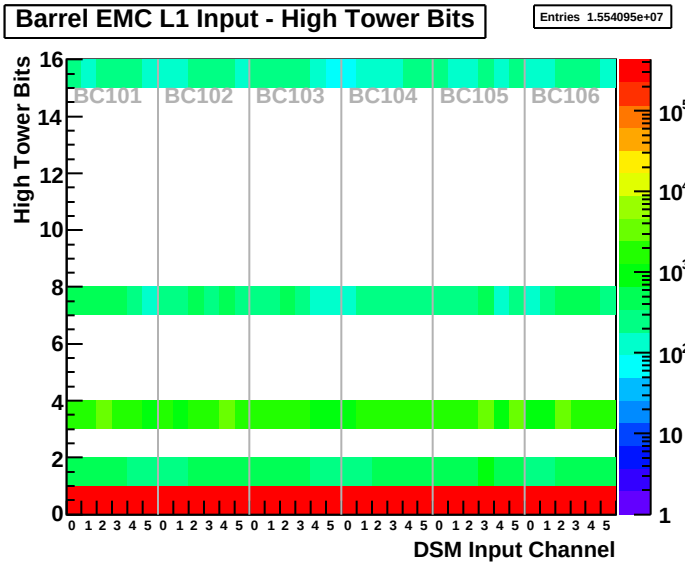
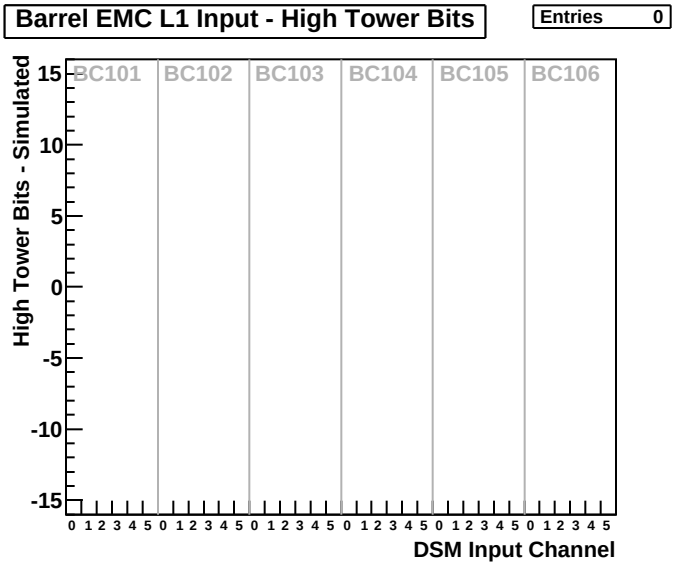

Barrel EMC L1 Input - Low Eta Sum
Entries 1.554095e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

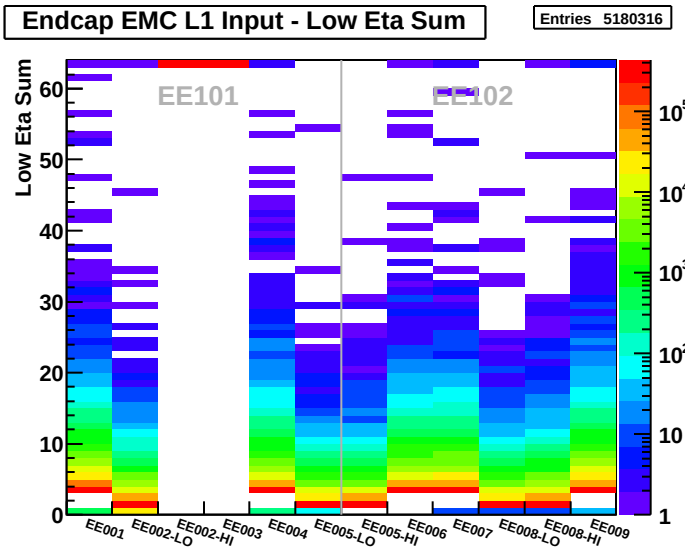

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

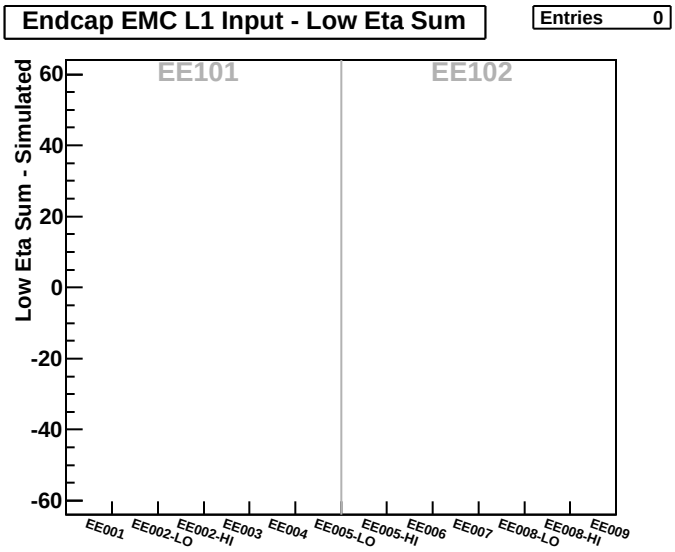

Barrel EMC L1 Input - High Eta Sum
Entries 1.554095e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

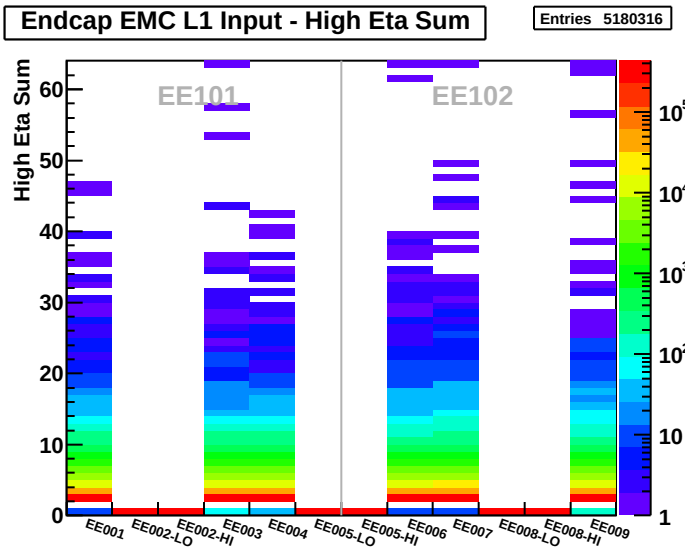

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

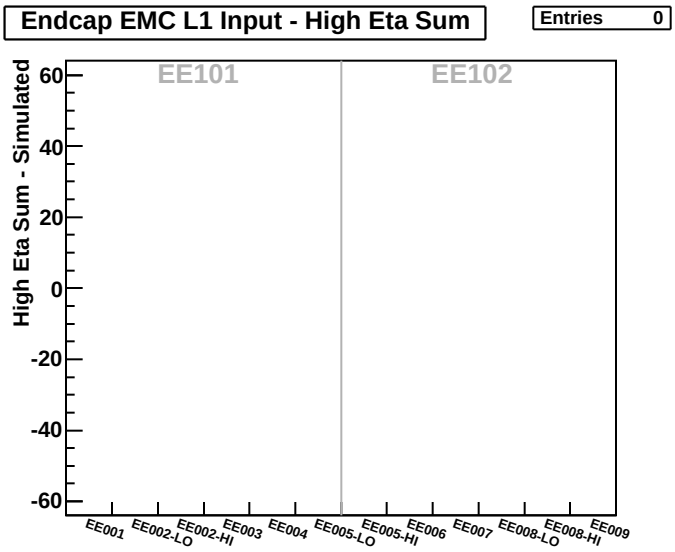

Barrel EMC L1 Input - High Tower Bits
Entries 1.554095e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

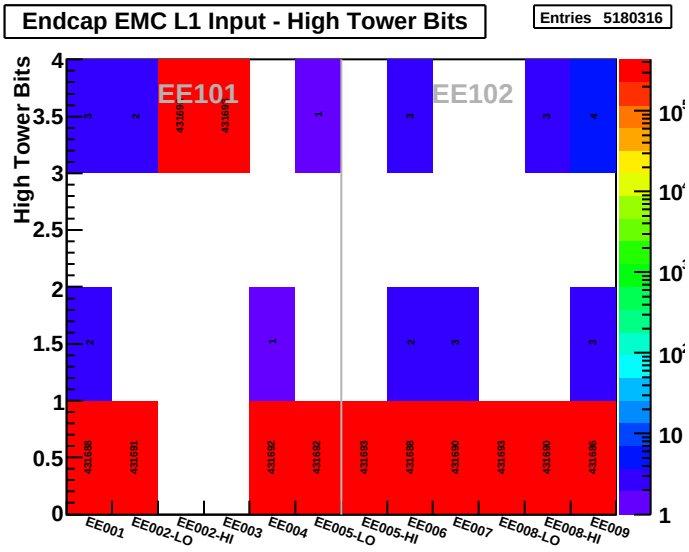

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

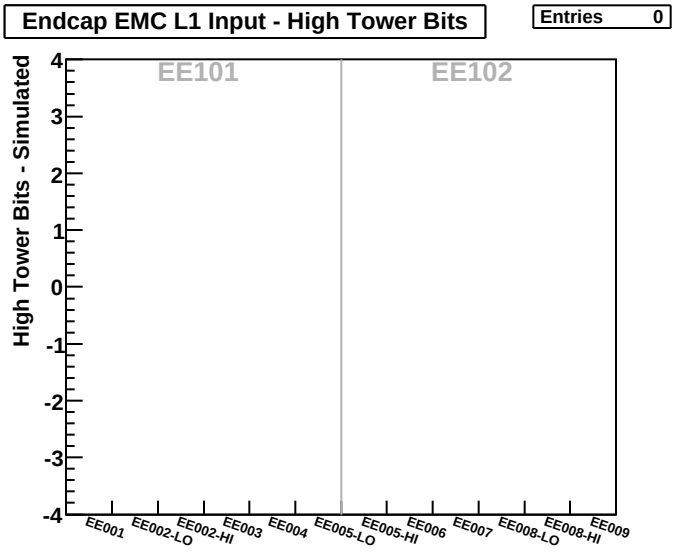

Endcap EMC L1 Input - Low Eta Sum
Entries 5180316
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

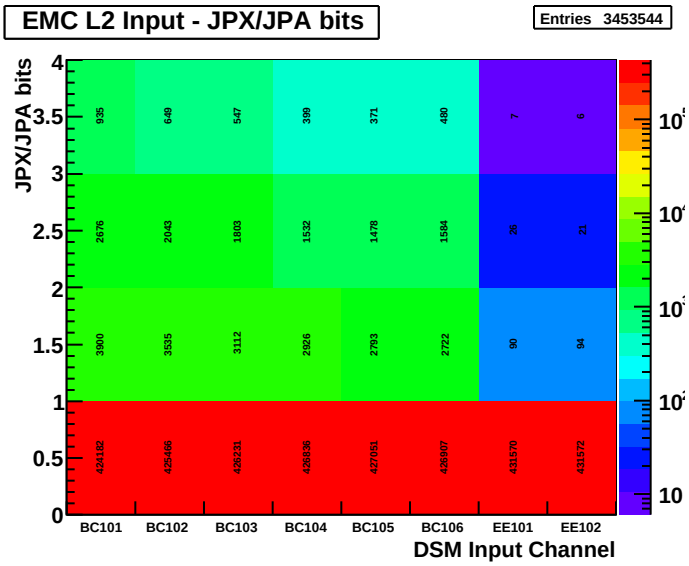

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

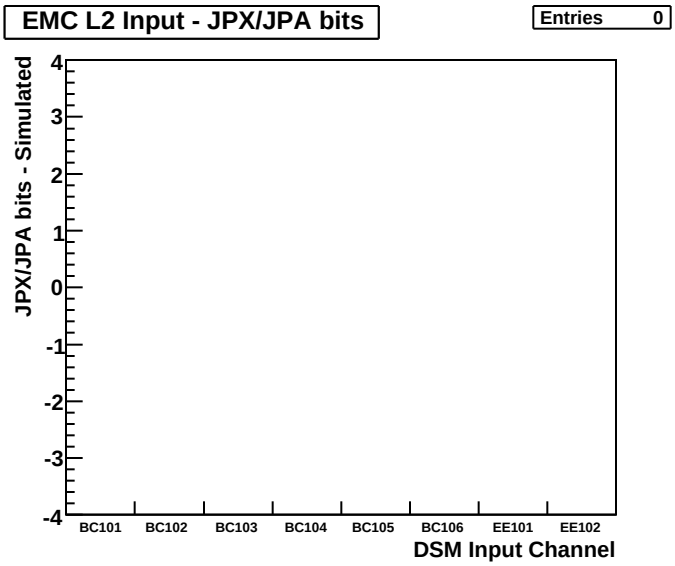

Endcap EMC L1 Input - High Eta Sum
Entries 5180316
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

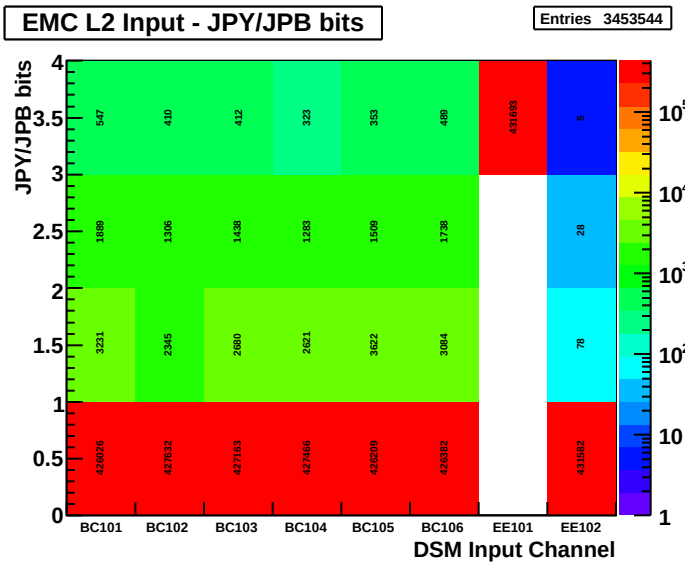

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

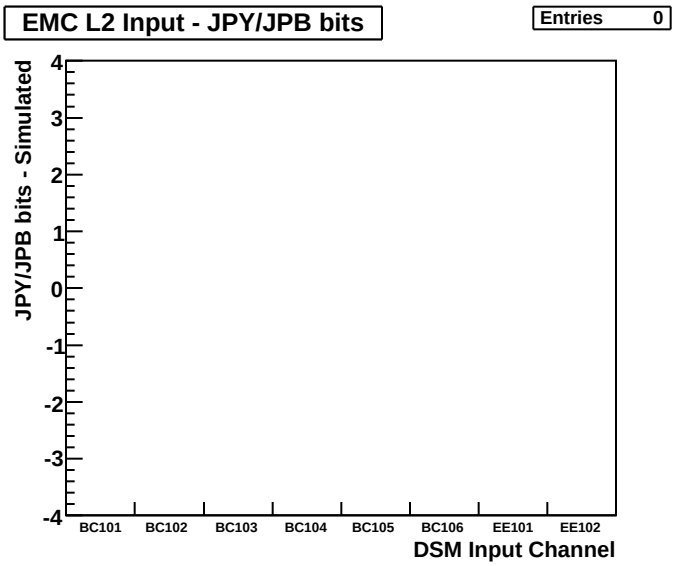

Endcap EMC L1 Input - High Tower Bits
Entries 5180316
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

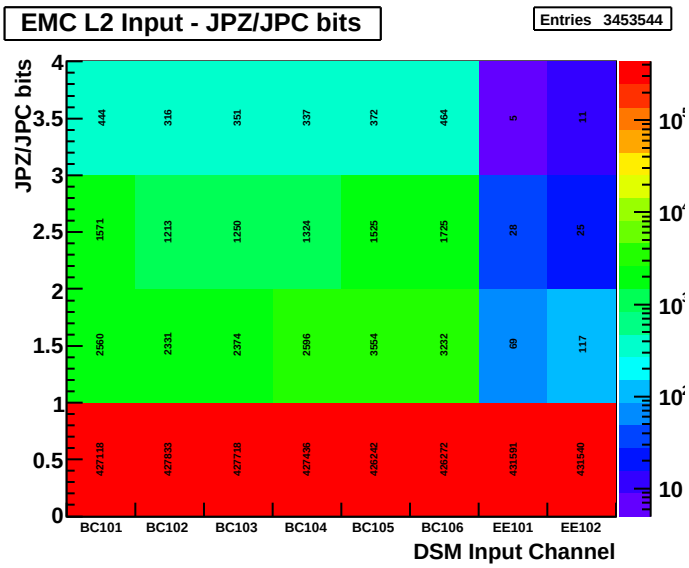

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

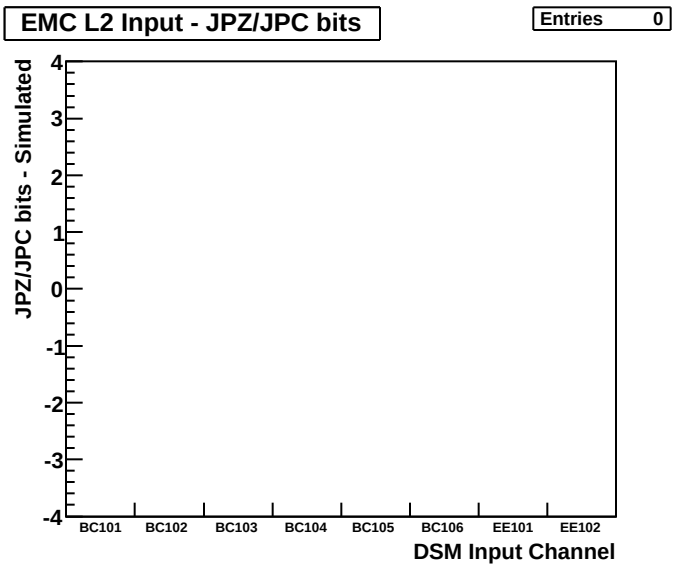

EMC L2 Input - JPX/JPA bits
Entries 3453544
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
935 649 547 399 371 480 7 6
2676 2043 1803 1532 1478 1584 28 21
3900 3535 3112 2926 2793 2722 90 94
424182 425466 426231 426836 427051 426907 431570 431572

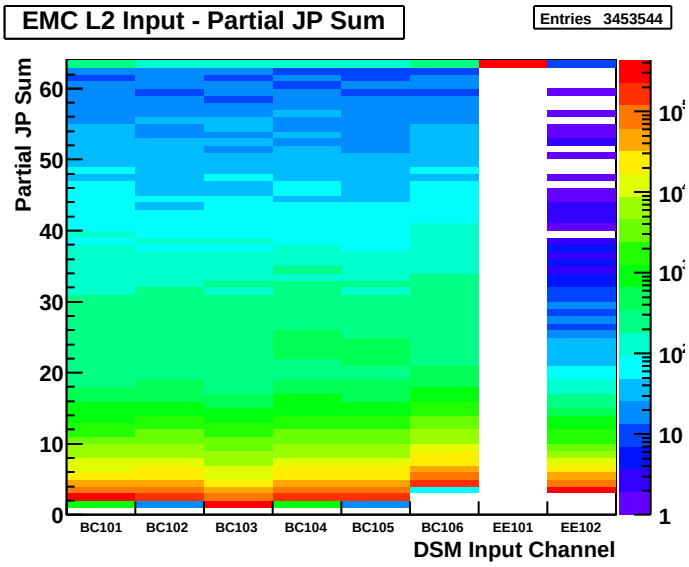

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

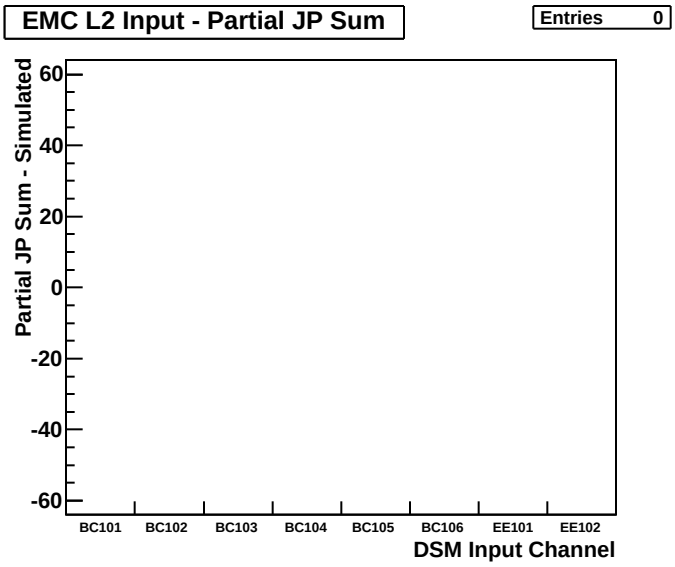

EMC L2 Input - JPY/JPB bits
Entries 3453544
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
547 410 412 323 353 489 431693 5
1889 1306 1438 1283 1509 1738 28
3231 2345 2680 2621 3622 3084 78
426026 427632 427163 427466 426209 426382 431582

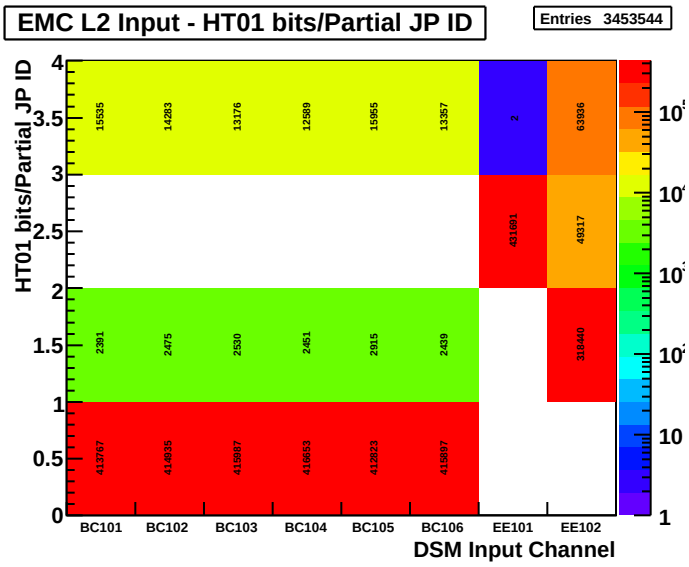

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

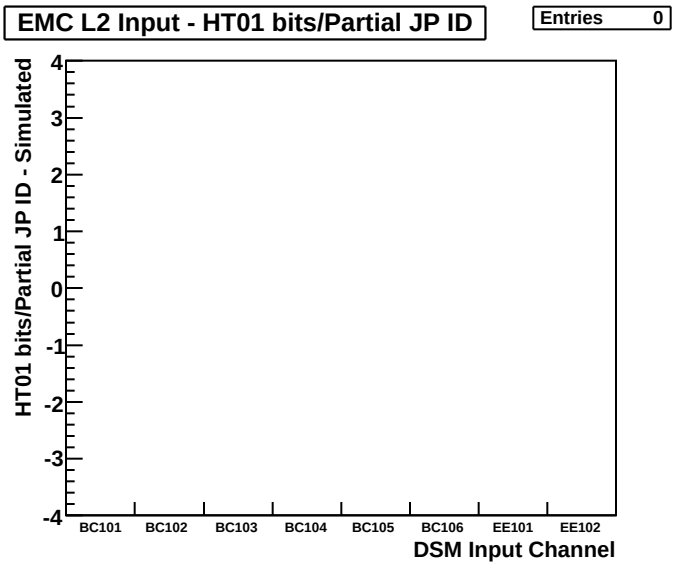

EMC L2 Input - JPZ/JPC bits
Entries 3453544
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
444 316 351 337 372 464 5 11
1571 1213 1250 1324 1525 1725 28 25
2560 2331 2374 2596 3554 3232 69 117
427118 427833 427719 427436 426242 426272 431591 431540

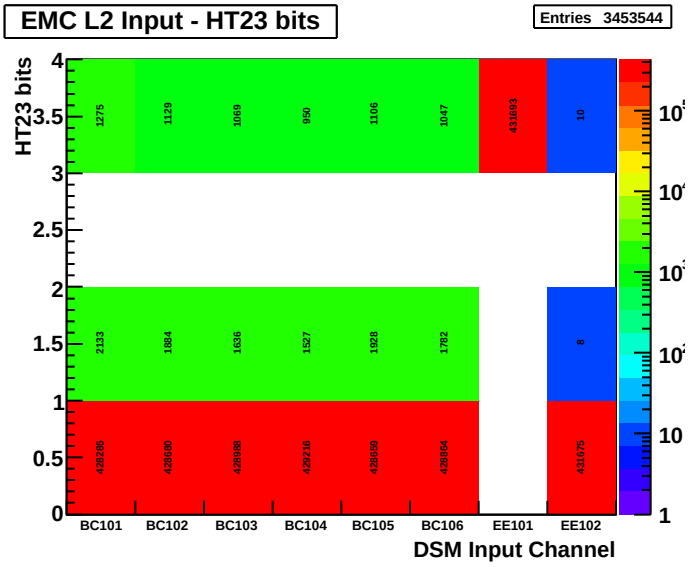

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

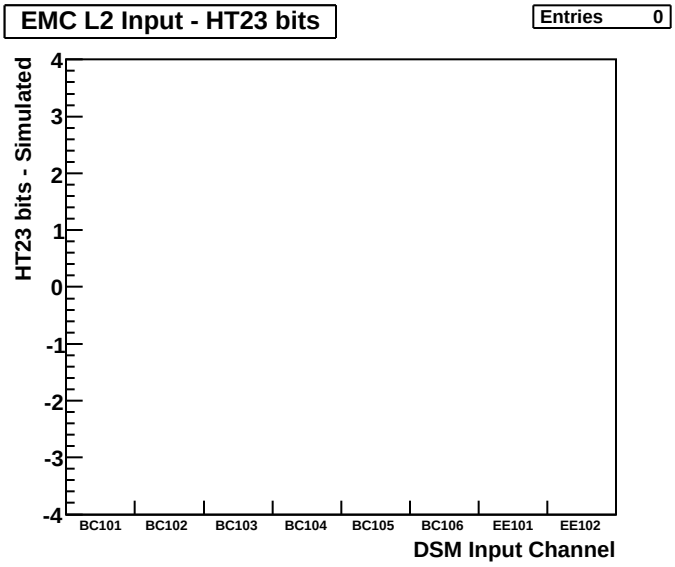

EMC L2 Input - Partial JP Sum
Entries 3453544
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

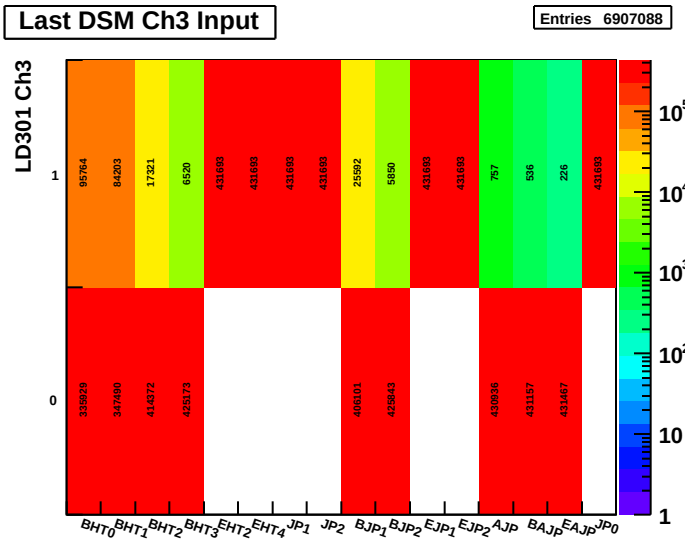

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

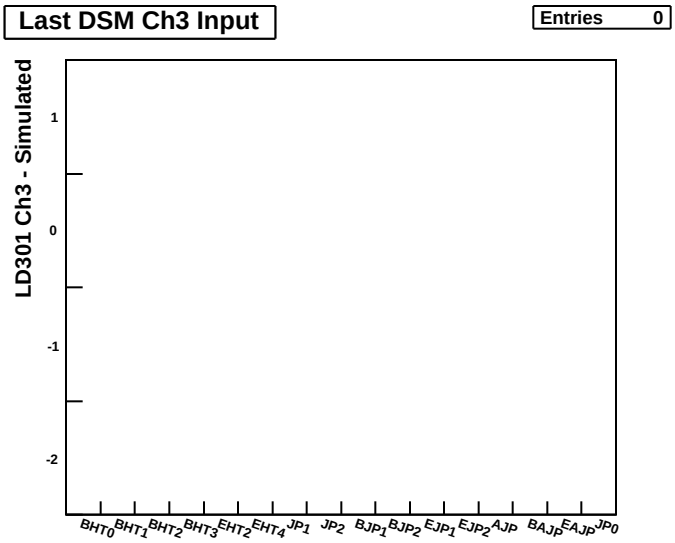

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3453544
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

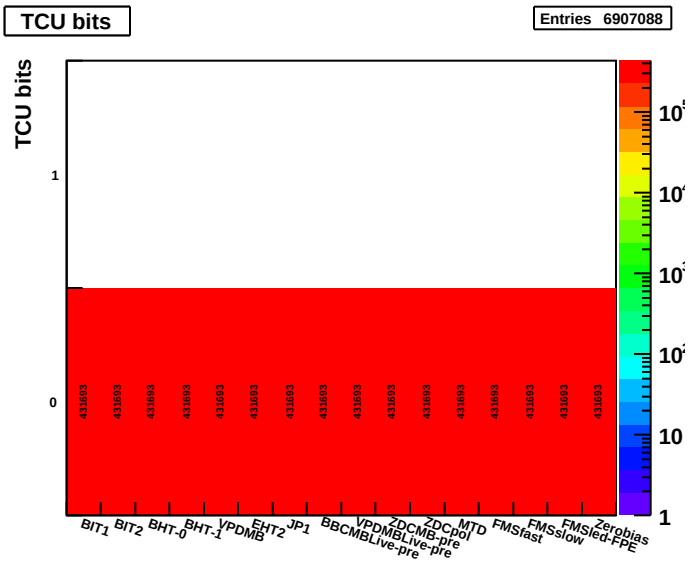

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

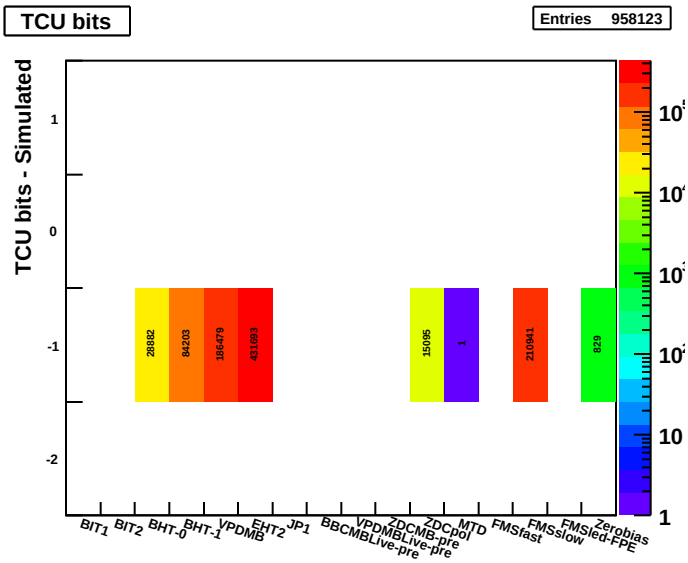

EMC L2 Input - HT23 bits
Entries 3453544
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

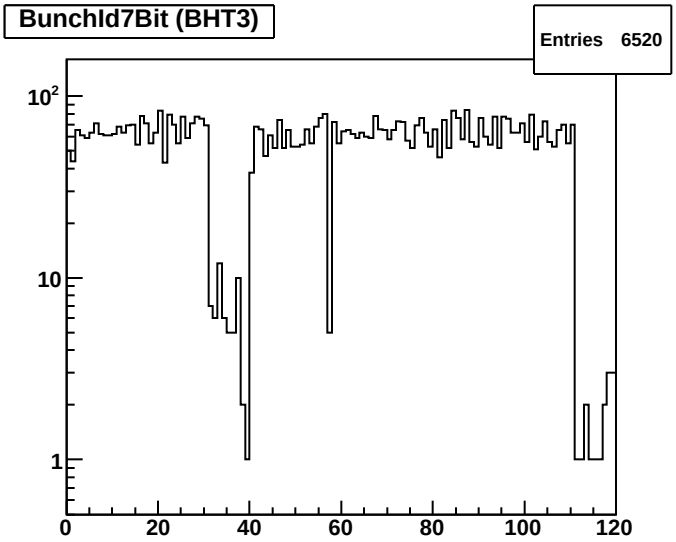

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

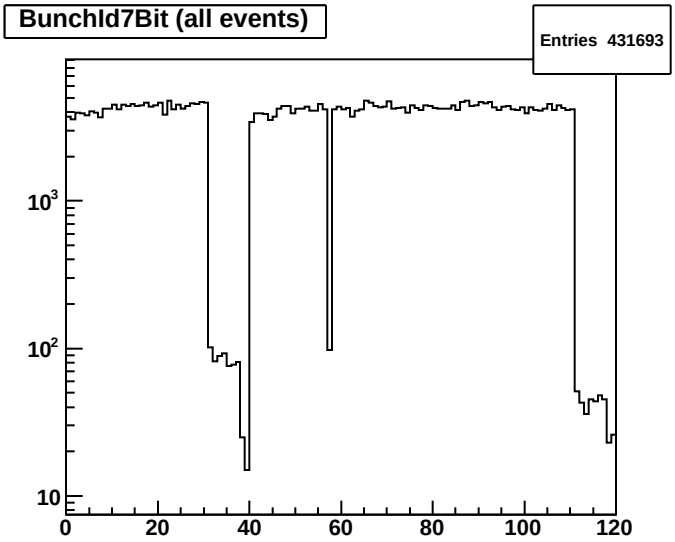

Last DSM Ch3 Input
Entries 6907088
LD301 Ch3
95764
84203
17321
6520
431693
431693
431693
431693
25592
5850
431693
431693
757
536
226
431693
339829
347490
414372
425173
406101
425943
430936
431157
431467
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

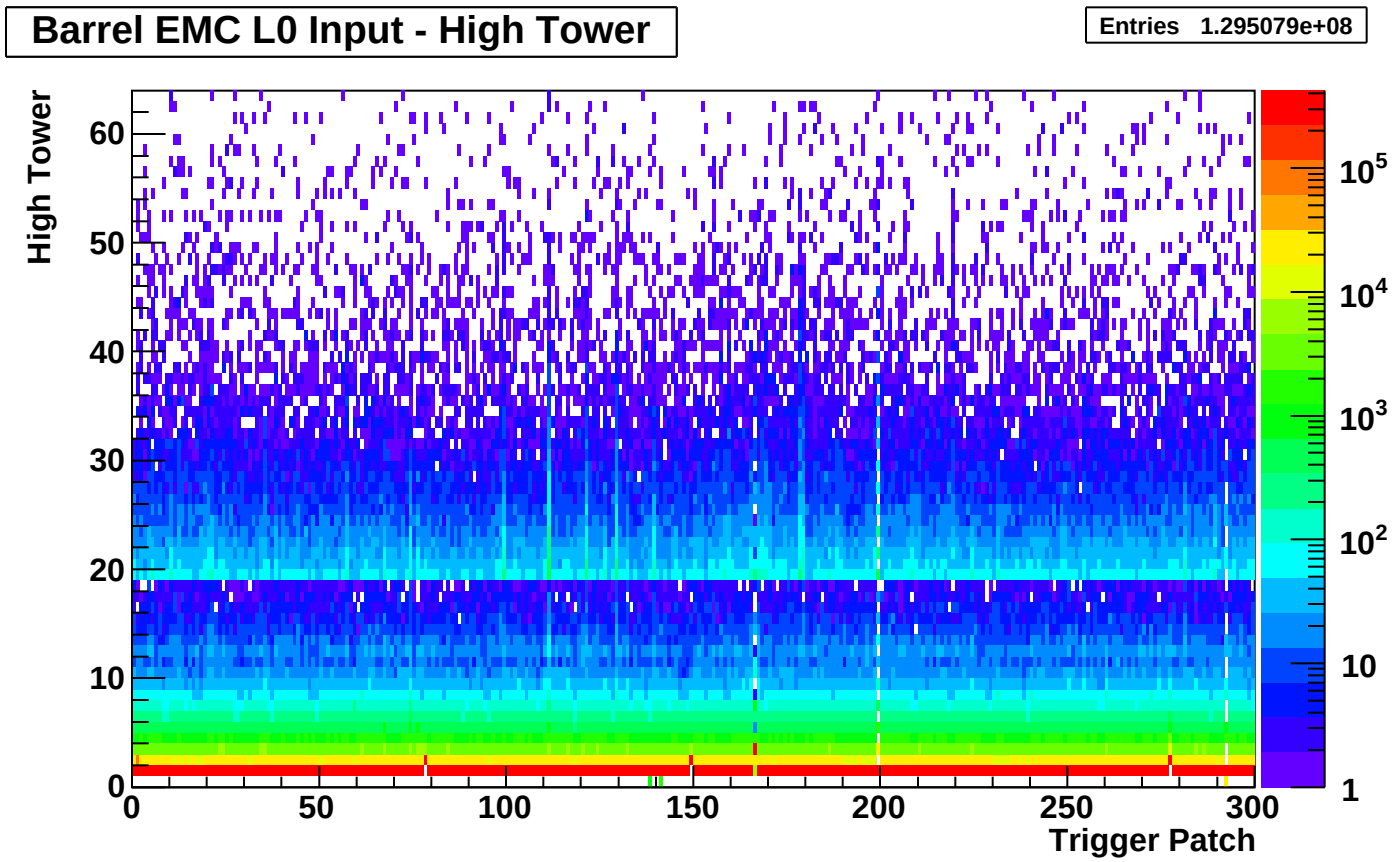

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

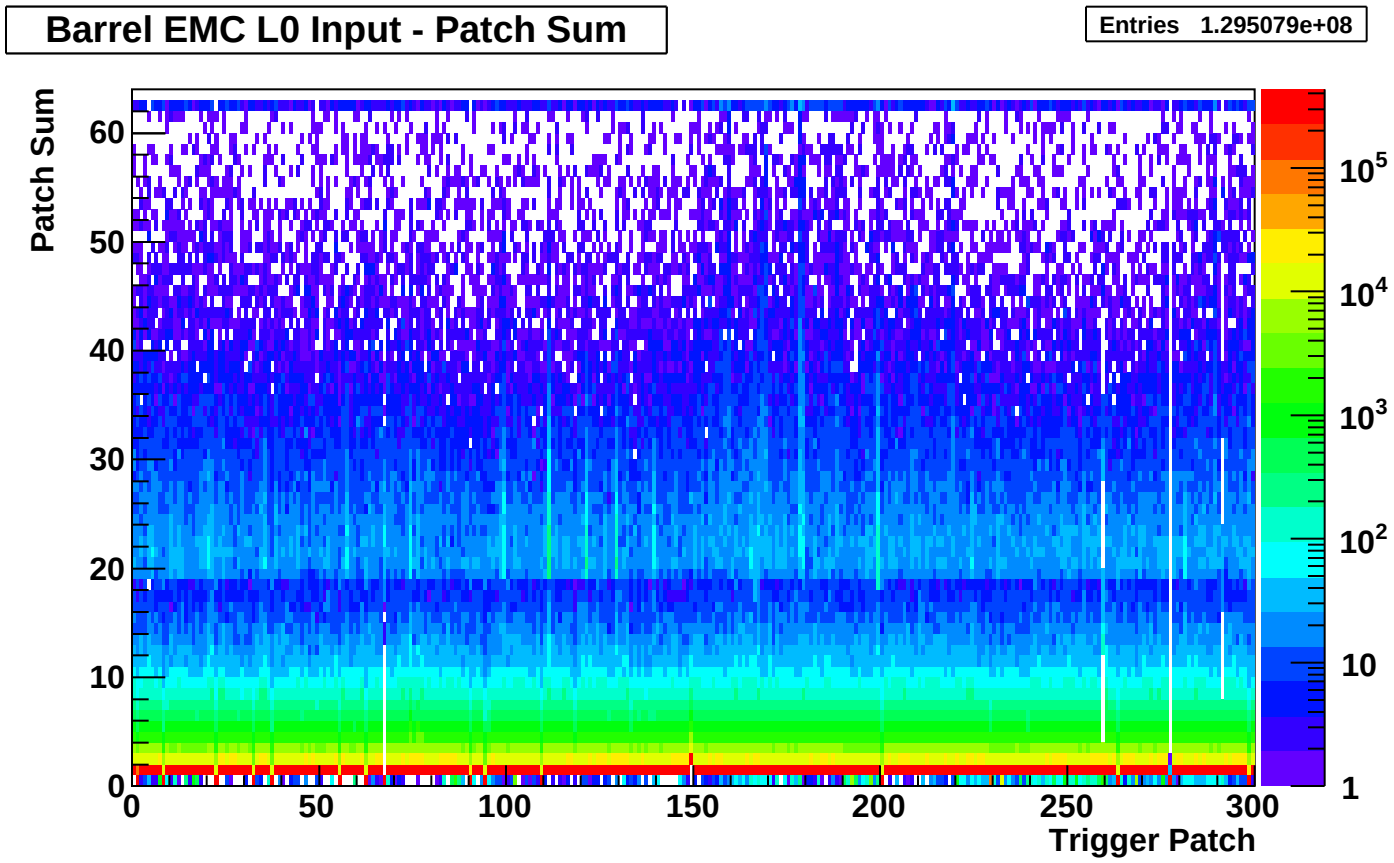

TCU bits
Entries 6907088
TCU bits
431693
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

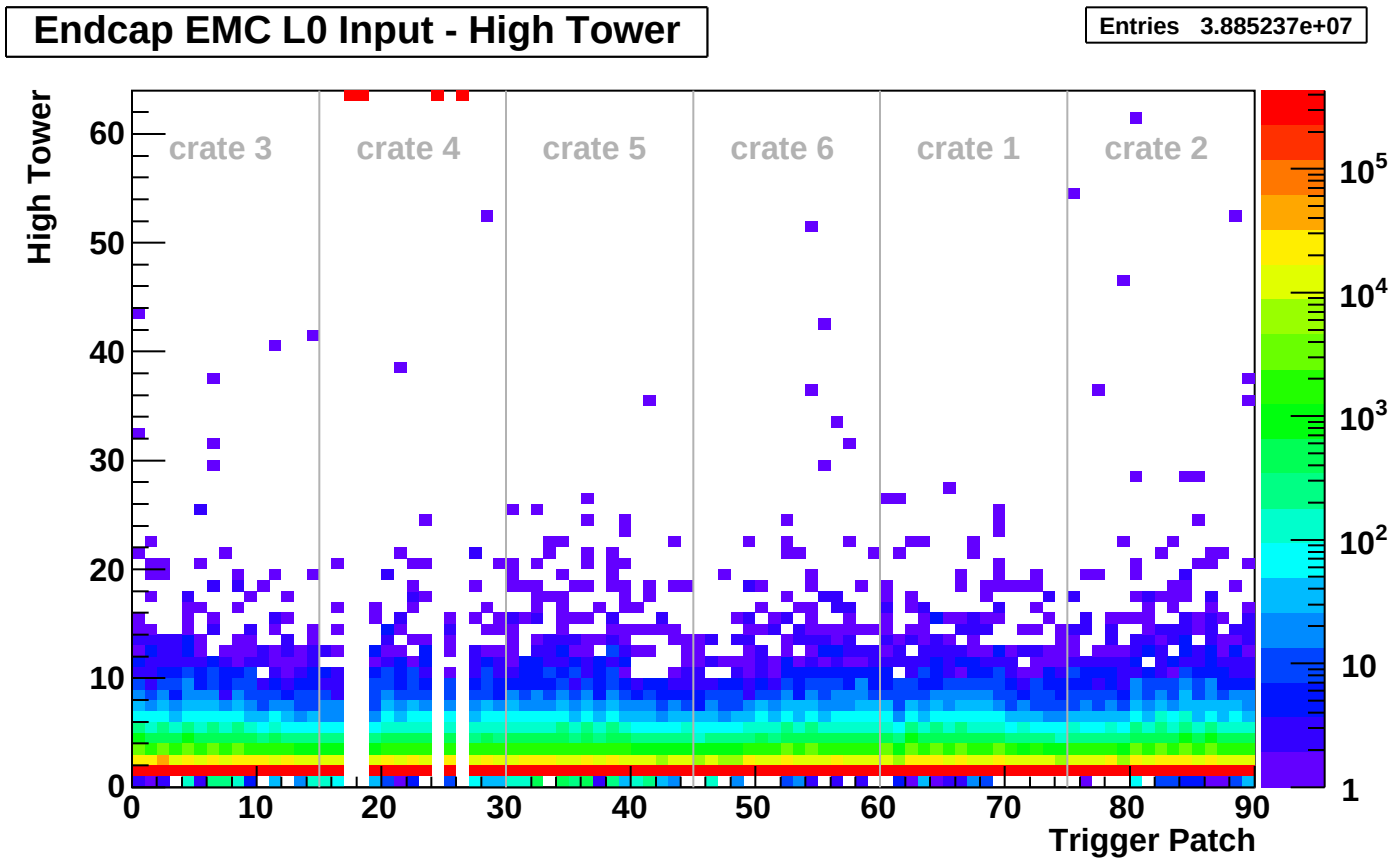

TCU bits
Entries 958123
TCU bits - Simulated
28882
84203
186479
431693
15095
1
210941
829
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

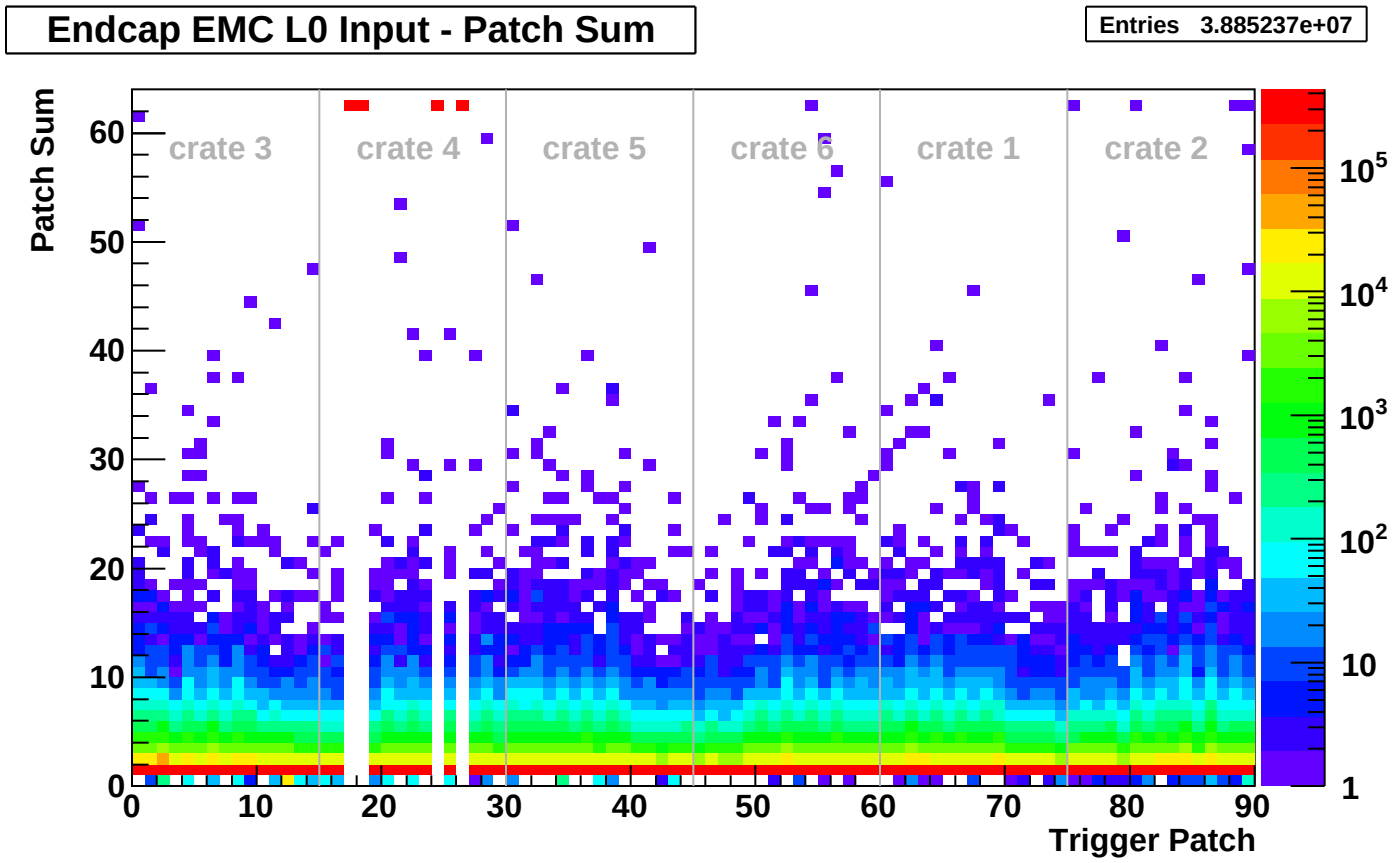

BunchId7Bit (BHT3)
Entries 6520

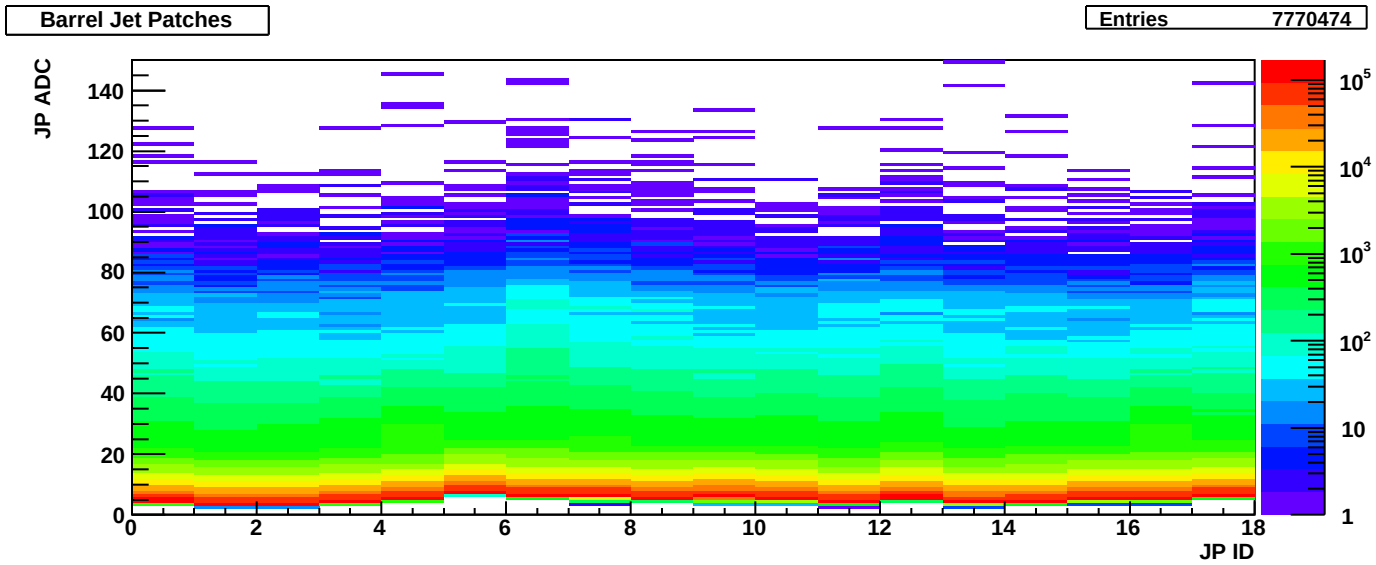

BunchId7Bit (all events)
Entries 431693

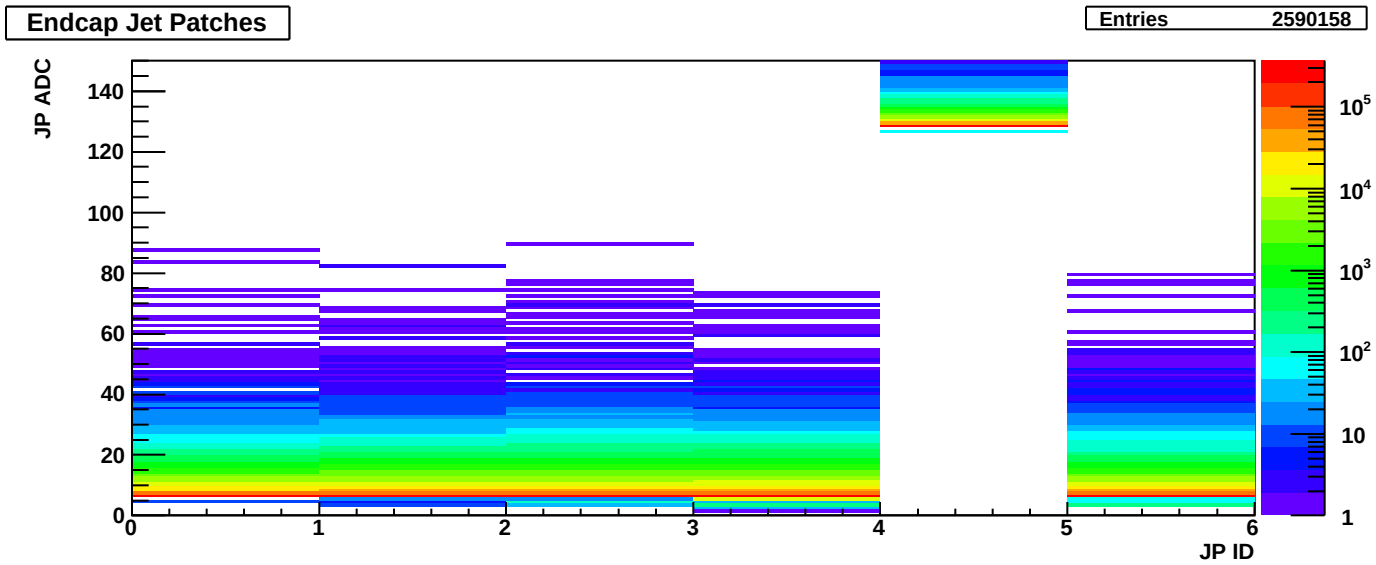

Barrel EMC L0 Input - High Tower
Entries 1.295079e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

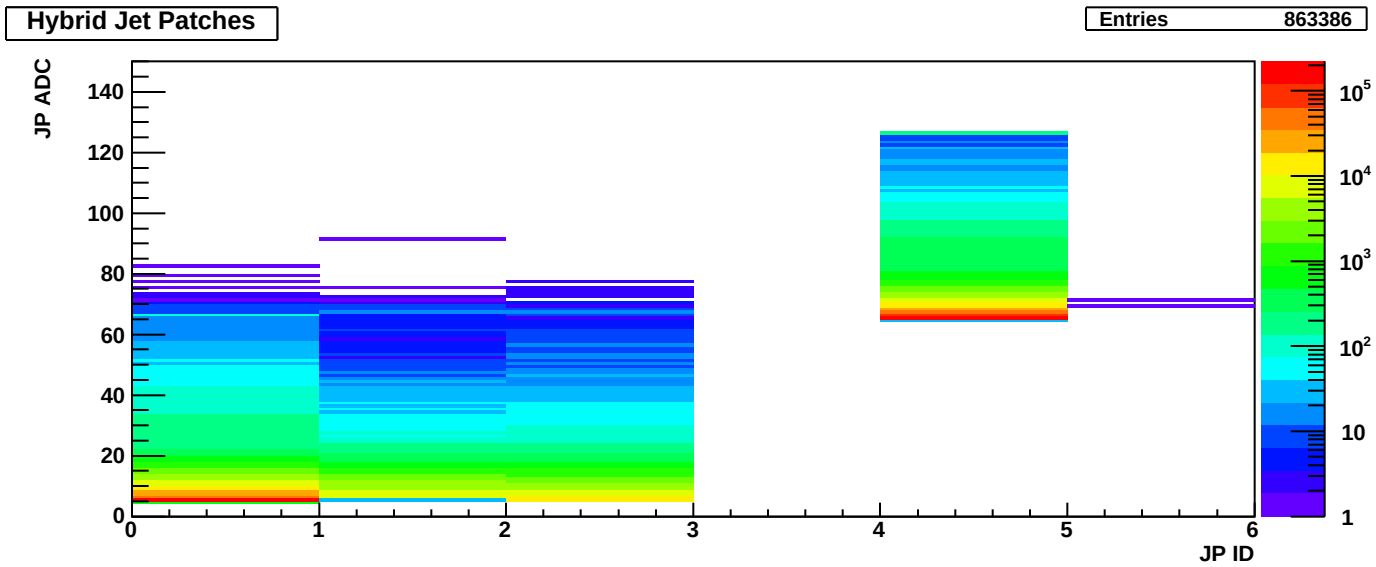

Barrel EMC L0 Input - Patch Sum
Entries 1.295079e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

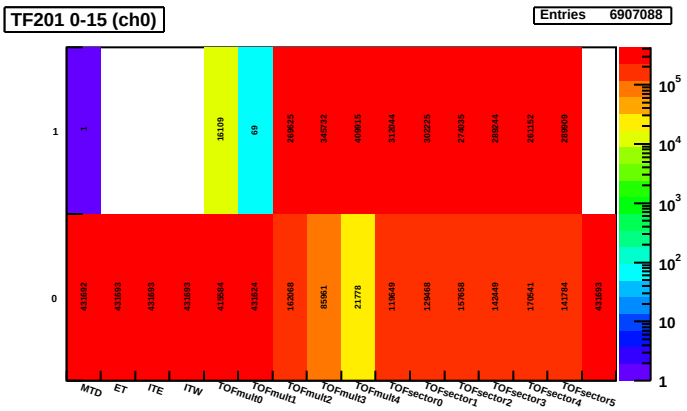

Endcap EMC L0 Input - High Tower
Entries 3.885237e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

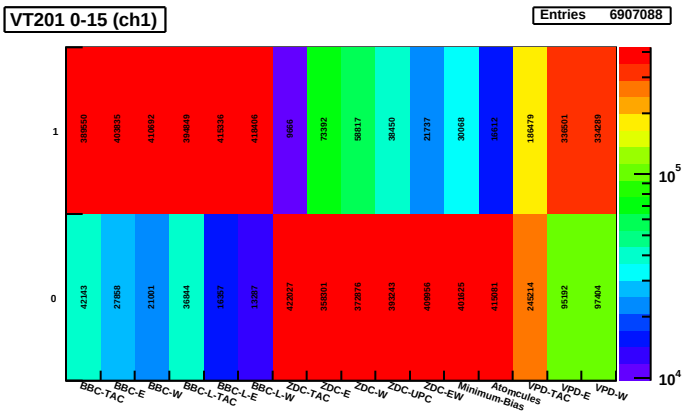

Endcap EMC L0 Input - Patch Sum
Entries 3.885237e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

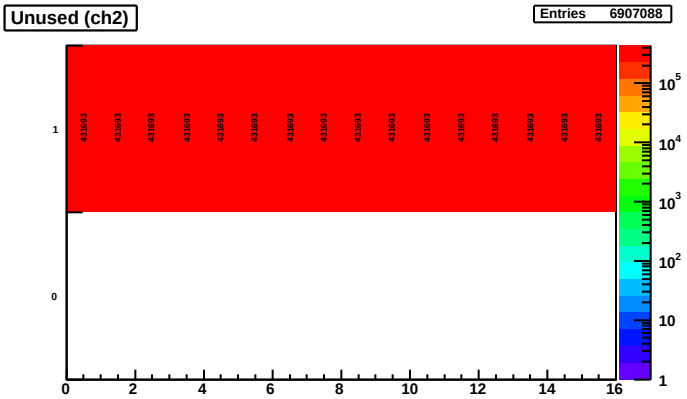

Barrel Jet Patches
Entries 7770474
JP ADC
JP ID

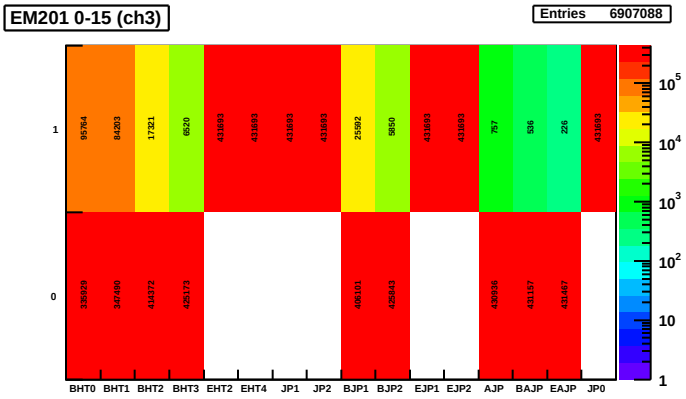

Endcap Jet Patches
Entries 2590158
JP ADC
JP ID

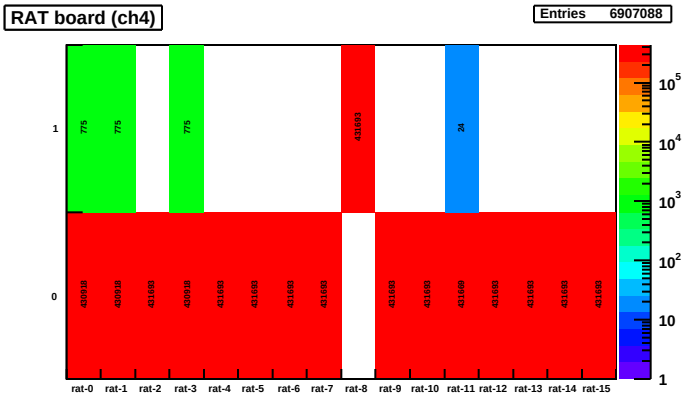

Hybrid Jet Patches
Entries 863386
JP ADC
JP ID

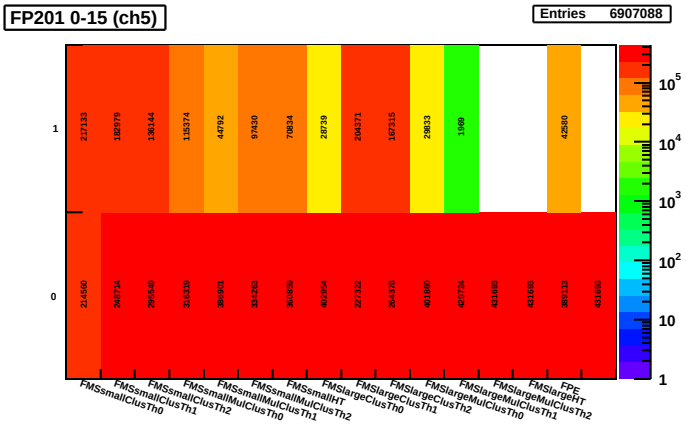

TF201 0-15 (ch0)
Entries 6907088
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

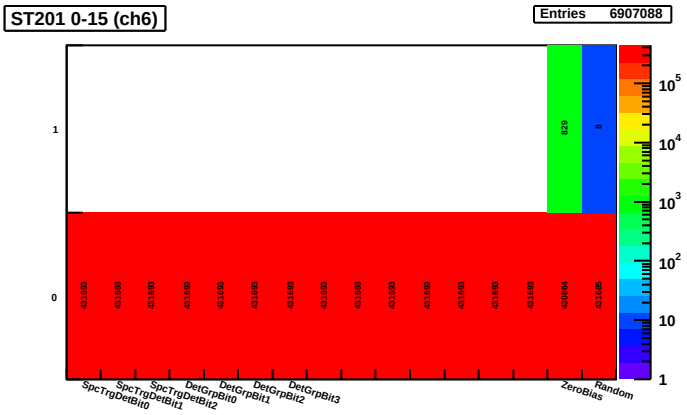

VT201 0-15 (ch1)
Entries 6907088
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

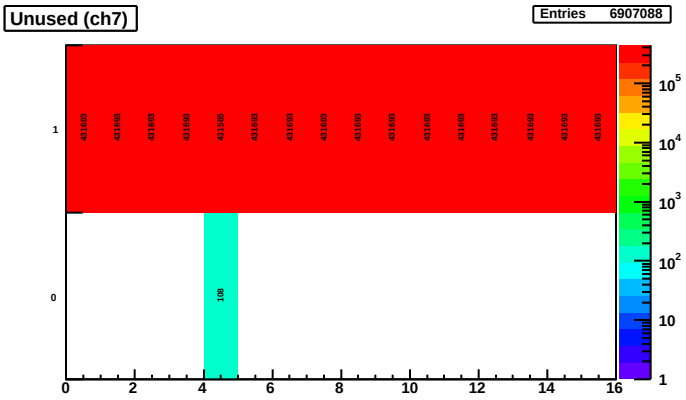

Unused (ch2)
Entries 6907088

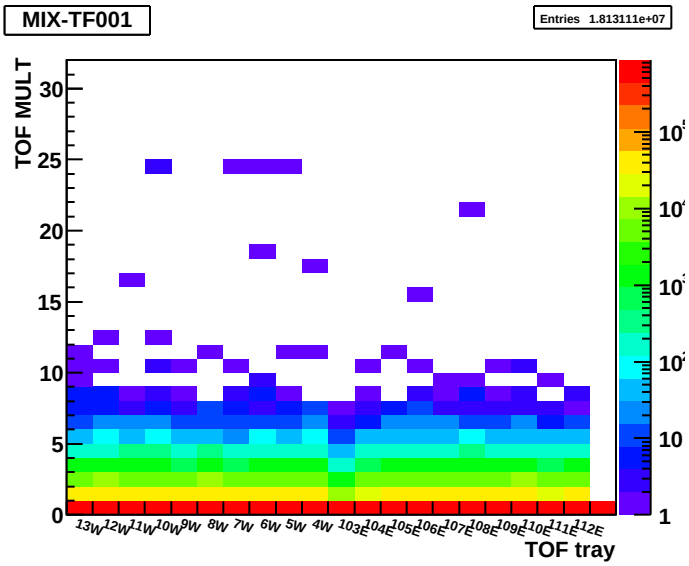

EM201 0-15 (ch3)
Entries 6907088
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

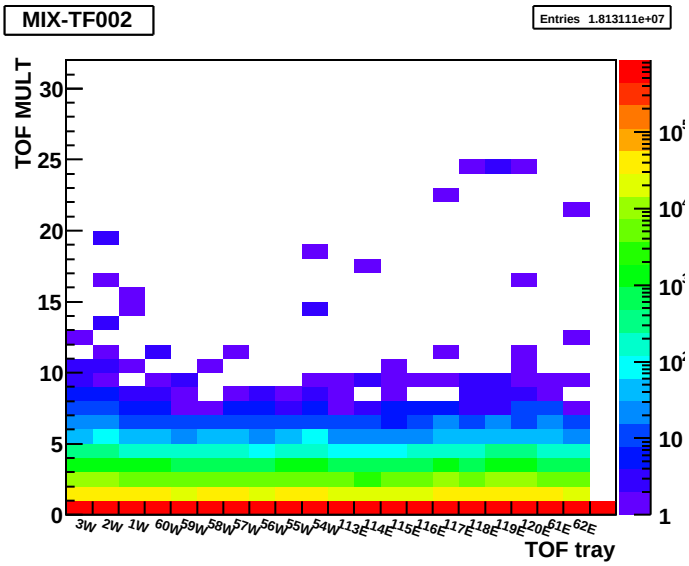

RAT board (ch4)
Entries 6907088
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

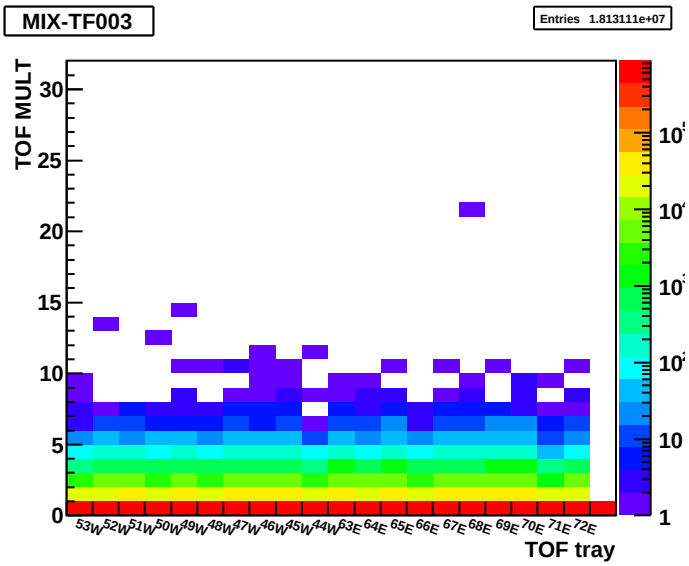

FP201 0-15 (ch5)
Entries 6907088
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

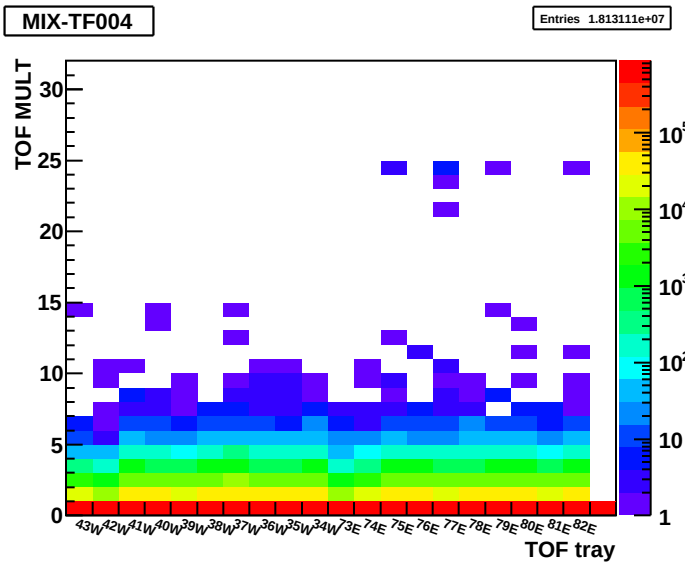

ST201 0-15 (ch6)
Entries 6907088
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

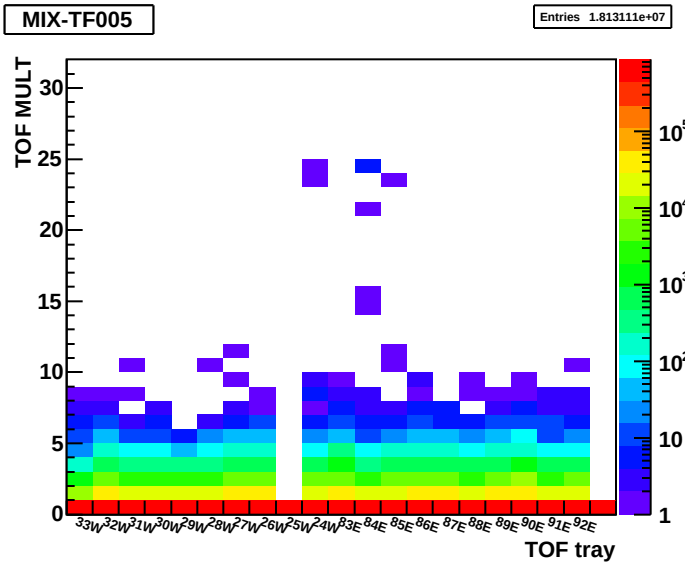

Unused (ch7)
Entries 6907088

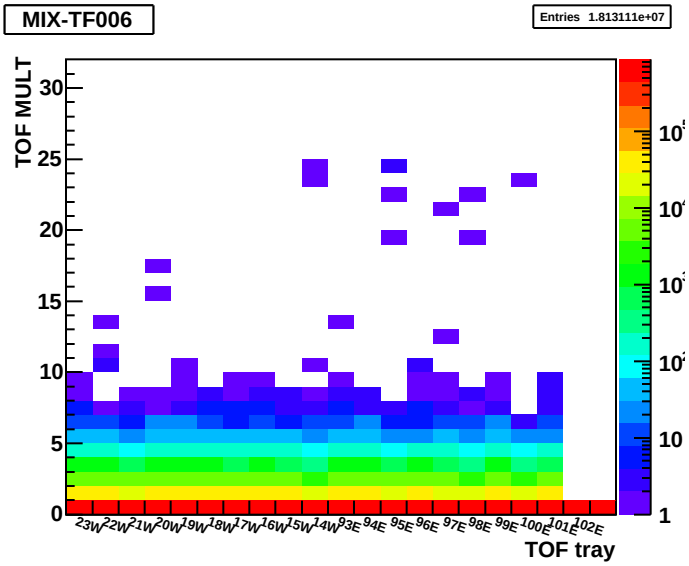

MIX-TF001
Entries 1.813111e+07
TOF MULT
TOF tray

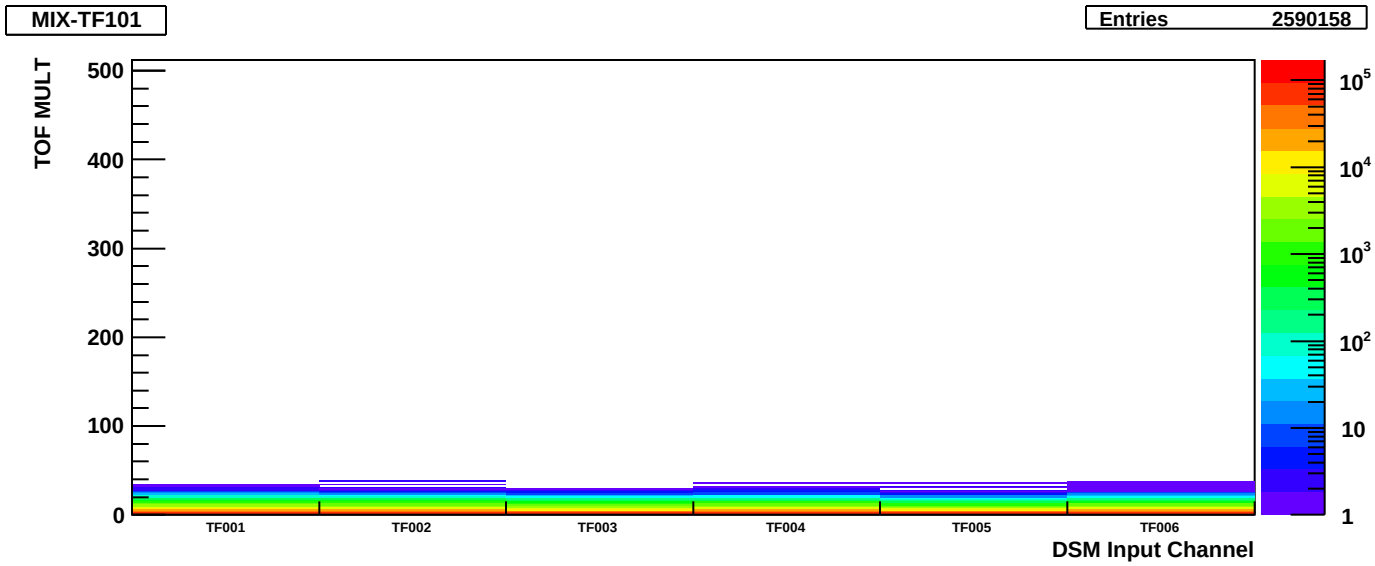

MIX-TF002
Entries 1.813111e+07
TOF MULT
TOF tray

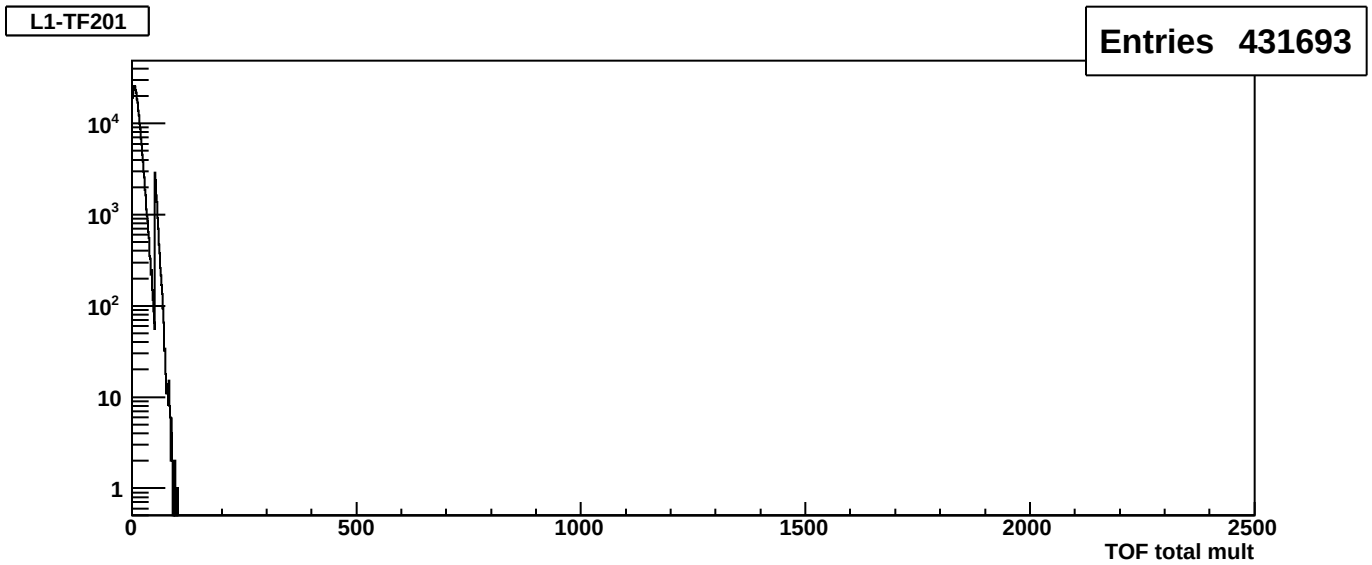

MIX-TF003
Entries 1.813111e+07
TOF MULT
TOF tray

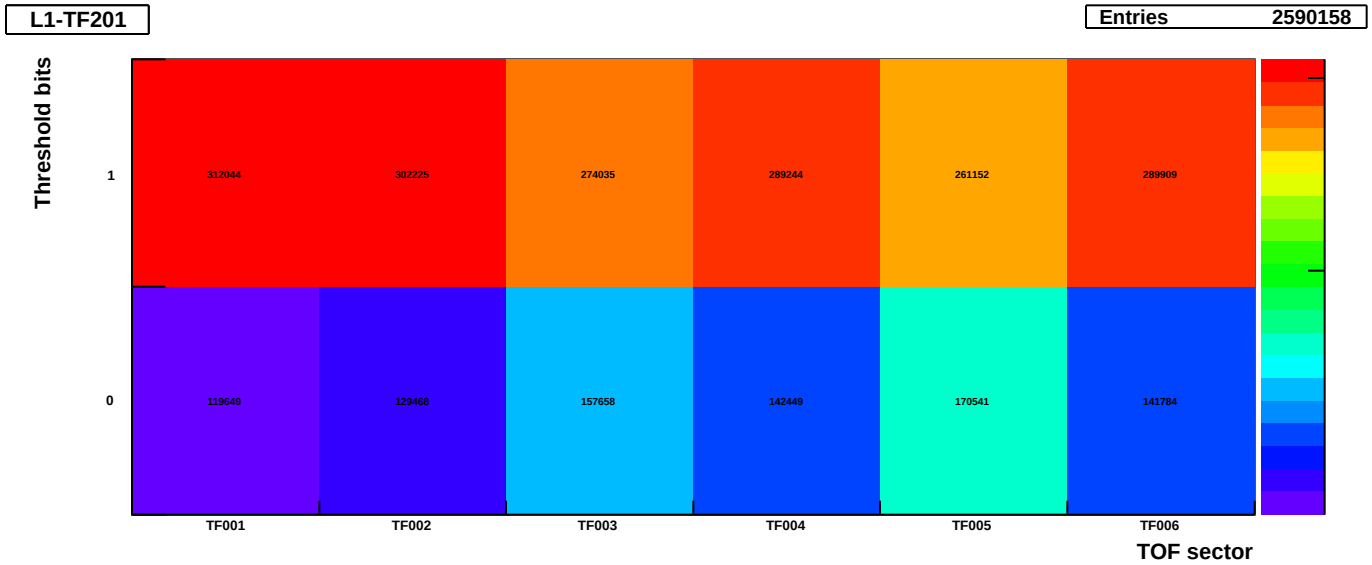

MIX-TF004
Entries 1.813111e+07
TOF MULT
TOF tray

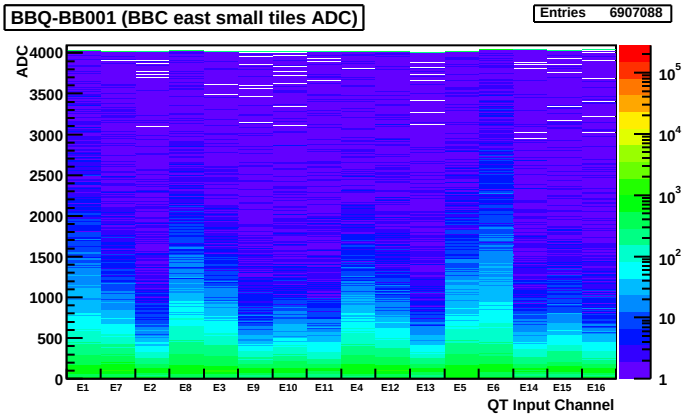

MIX-TF005
Entries 1.813111e+07
TOF MULT
TOF tray

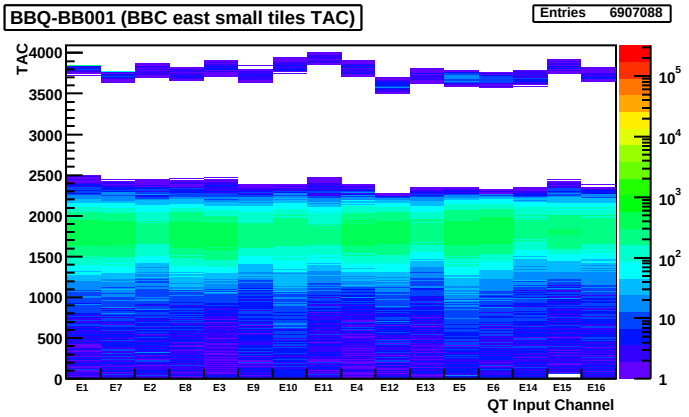

MIX-TF006
Entries 1.813111e+07
TOF MULT
TOF tray

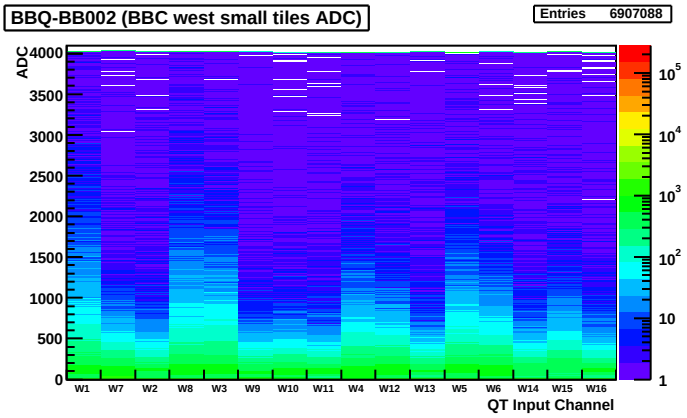

MIX-TF101
Entries 2590158
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

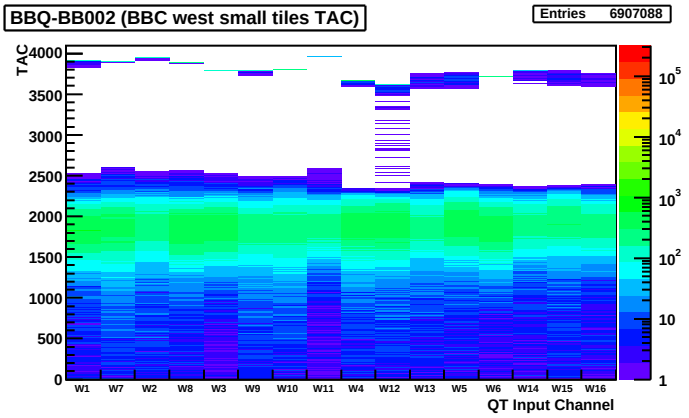

L1-TF201
Entries 431693
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

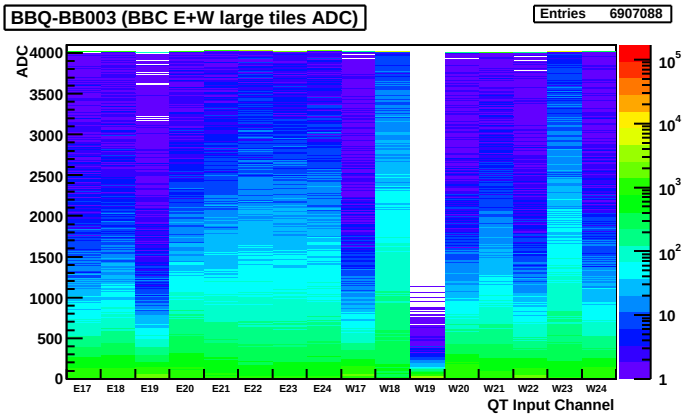

L1-TF201
Entries 2590158
Threshold bits
1
0
312044
302225
274035
289244
261152
289909
119649
129468
157658
142449
170541
141784
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

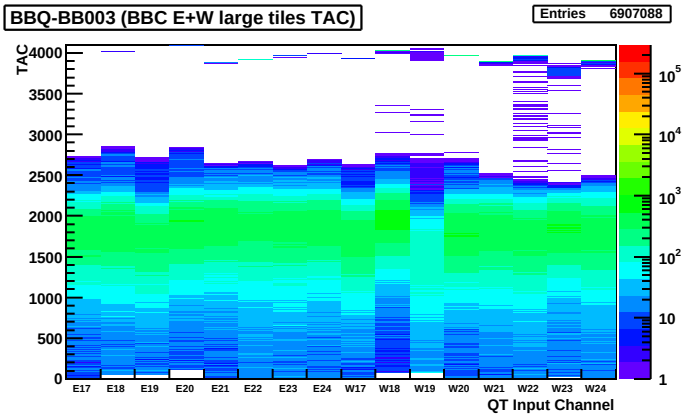

BBQ-BB001 (BBC east small tiles ADC)
Entries 6907088
ADC
QT Input Channel

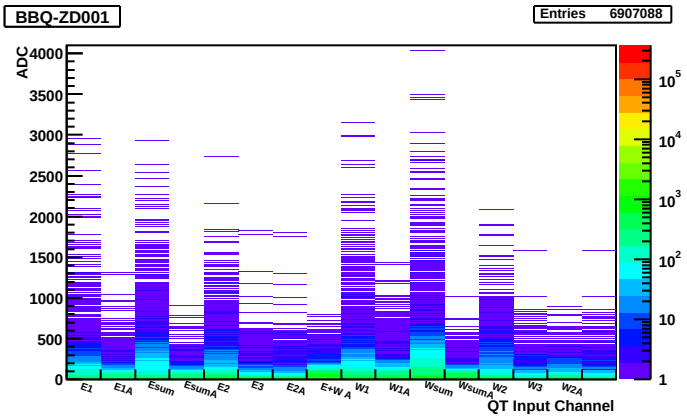

BBQ-BB001 (BBC east small tiles TAC)
Entries 6907088
TAC
QT Input Channel

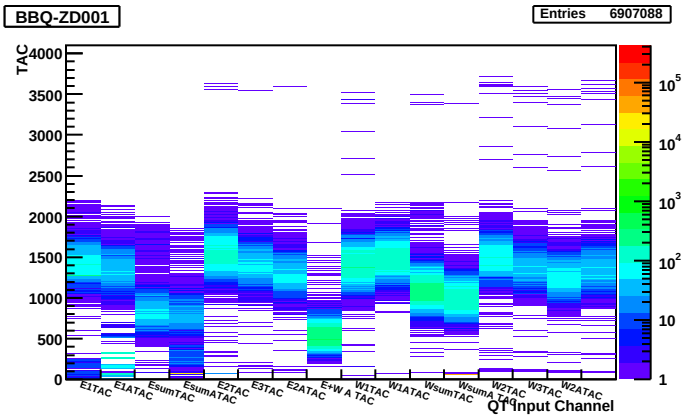

BBQ-BB002 (BBC west small tiles ADC)
Entries 6907088
ADC
QT Input Channel

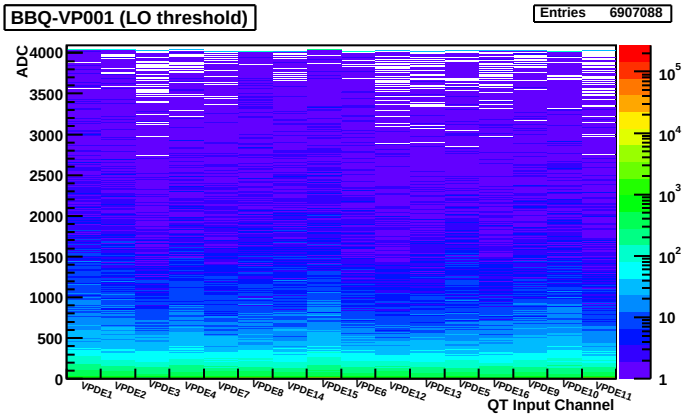

BBQ-BB002 (BBC west small tiles TAC)
Entries 6907088
TAC
QT Input Channel

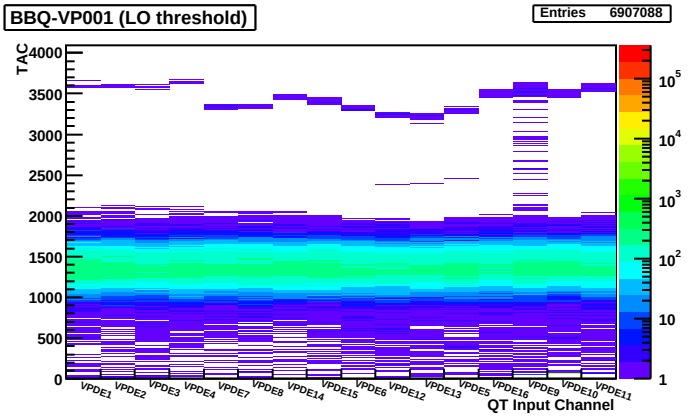

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6907088
ADC
QT Input Channel

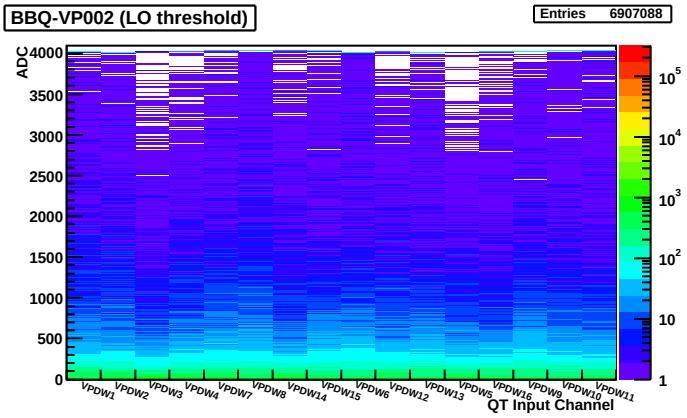

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6907088
TAC
QT Input Channel

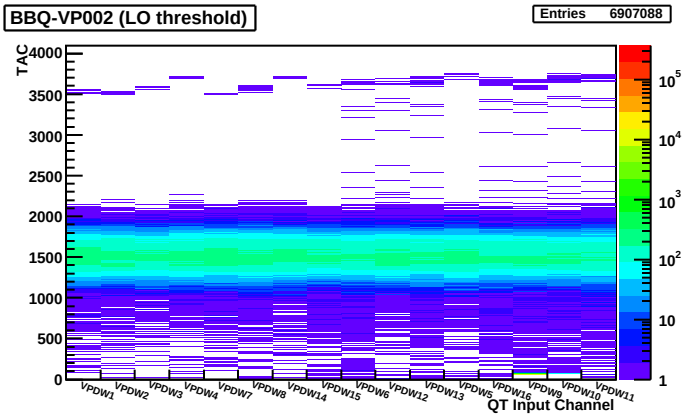

BBQ-ZD001
Entries 6907088
ADC
QT Input Channel

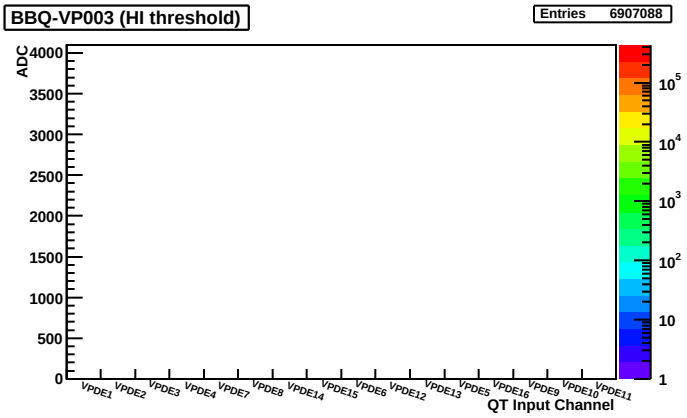

BBQ-ZD001
Entries 6907088
TAC
QT Input Channel

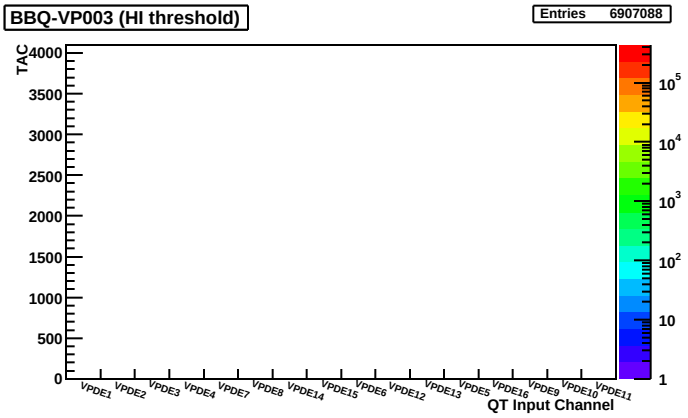

BBQ-VP001 (LO threshold)
Entries 6907088
ADC
QT Input Channel

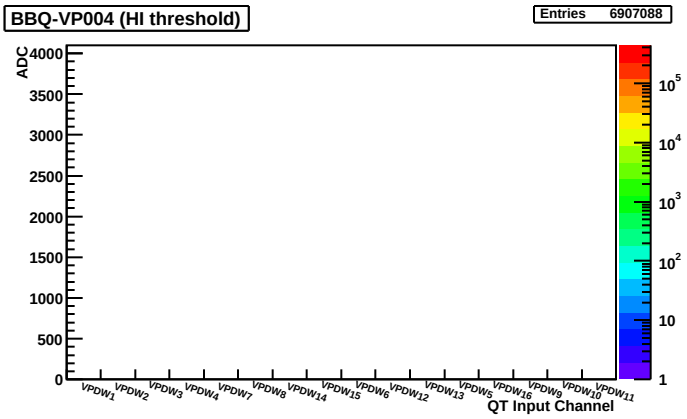

BBQ-VP001 (LO threshold)
Entries 6907088
TAC
QT Input Channel

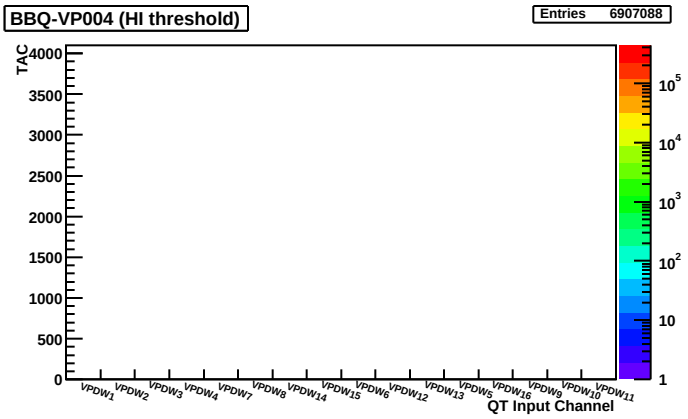

BBQ-VP002 (LO threshold)
Entries 6907088
ADC
QT Input Channel

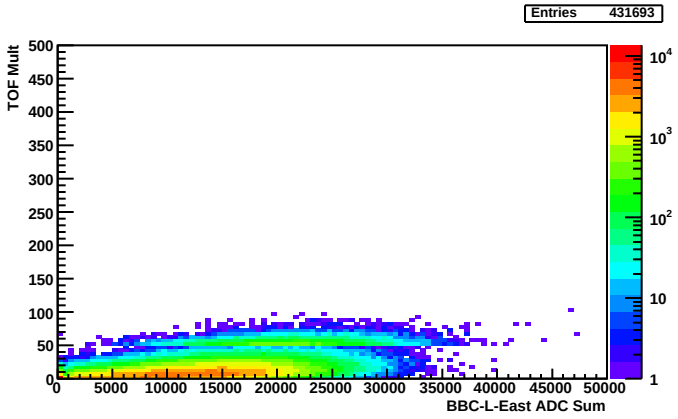

BBQ-VP002 (LO threshold)
Entries 6907088
TAC
QT Input Channel

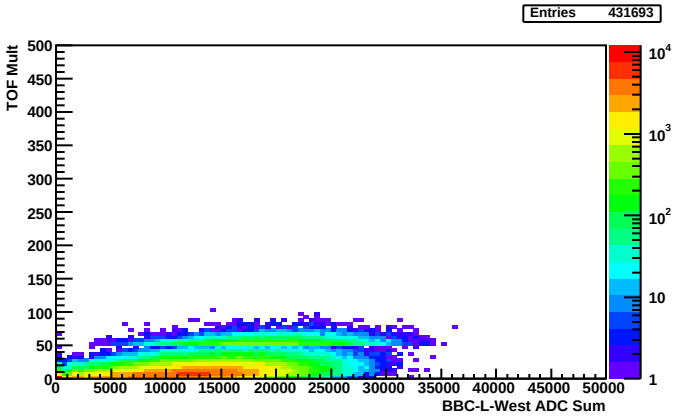

BBQ-VP003 (HI threshold)
Entries 6907088
ADC
QT Input Channel

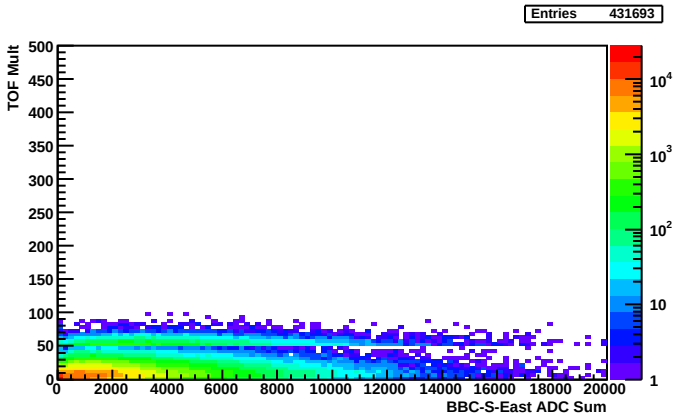

BBQ-VP003 (HI threshold)
Entries 6907088
TAC
QT Input Channel

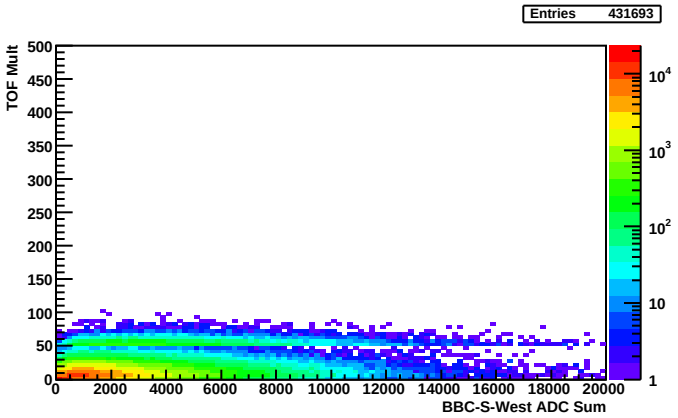

BBQ-VP004 (HI threshold)
Entries 6907088
ADC
QT Input Channel

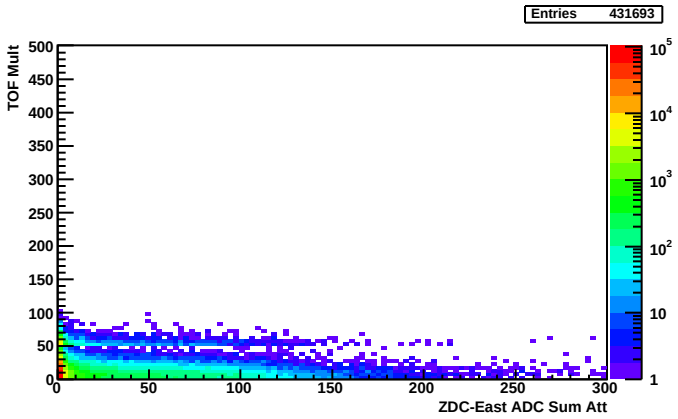

BBQ-VP004 (HI threshold)
Entries 6907088
TAC
QT Input Channel

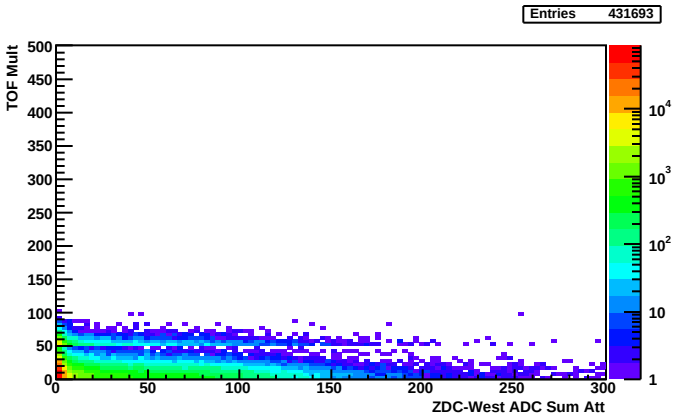

Entries 431693
TOF Mult
BBC-L-East ADC Sum

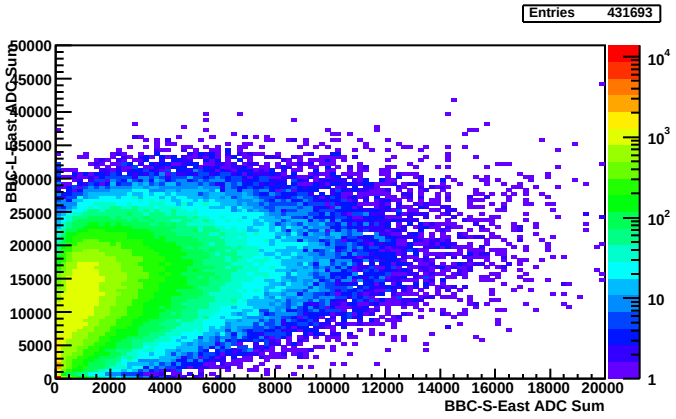

Entries 431693
TOF Mult
BBC-L-West ADC Sum

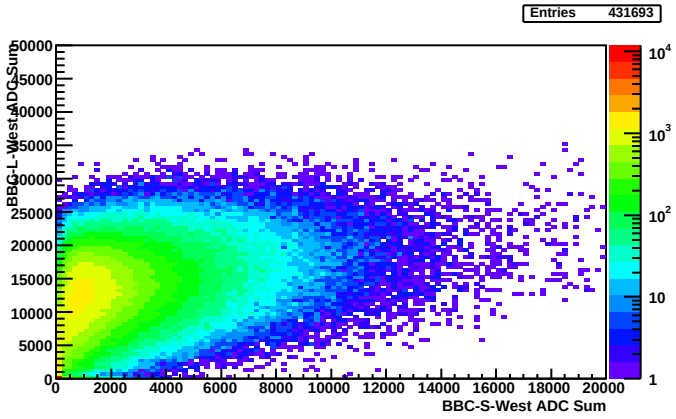

Entries 431693
TOF Mult
BBC-S-East ADC Sum

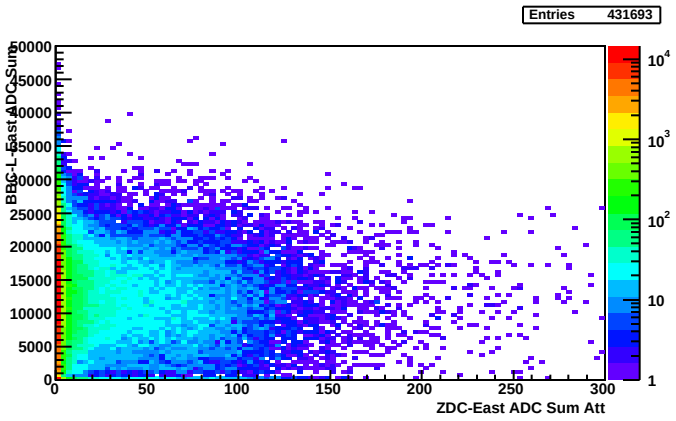

Entries 431693
TOF Mult
BBC-S-West ADC Sum

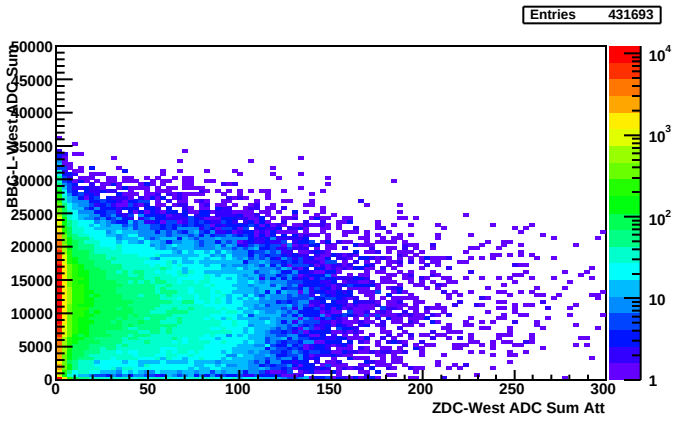

Entries 431693
TOF Mult
ZDC-East ADC Sum Att

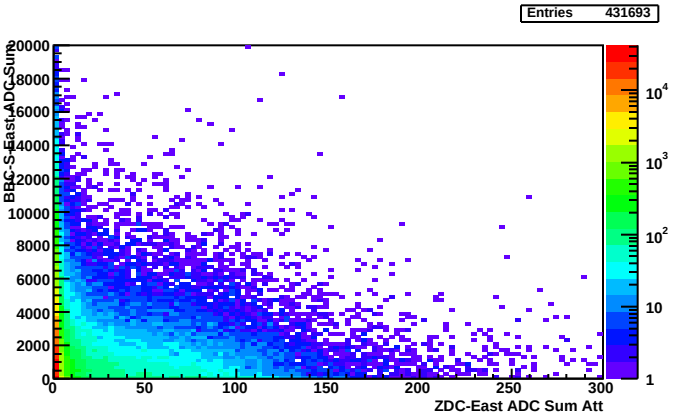

Entries 431693
TOF Mult
ZDC-West ADC Sum Att

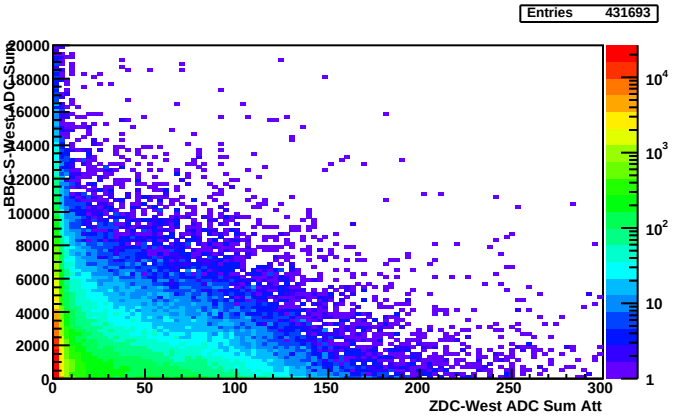

Entries 431693
BBC-L-East ADC Sum
BBC-S-East ADC Sum

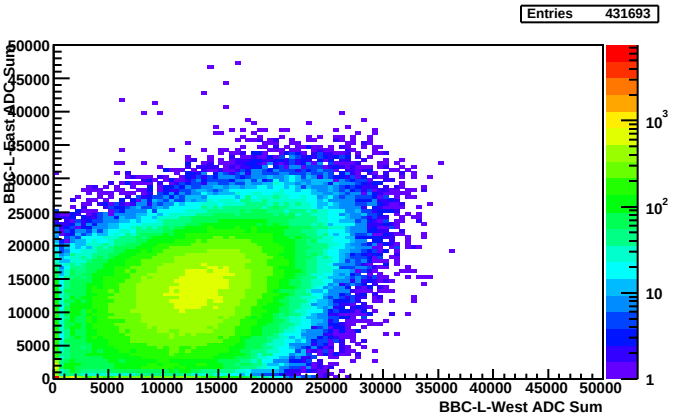

Entries 431693
BBC-L-West ADC Sum
BBC-S-West ADC Sum

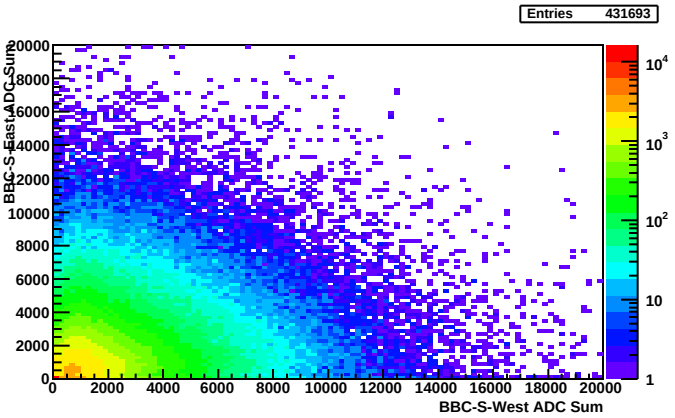

Entries 431693
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

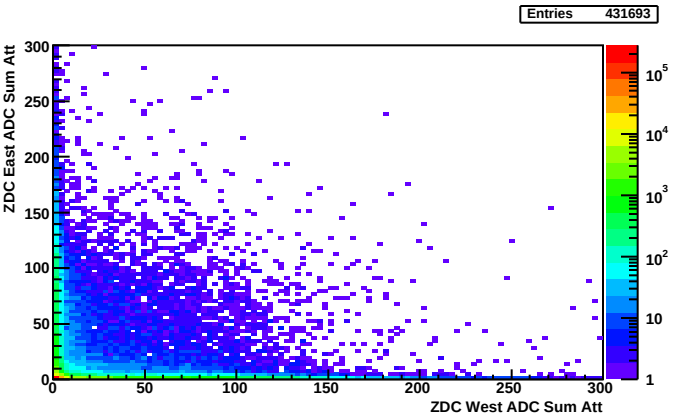

Entries 431693
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

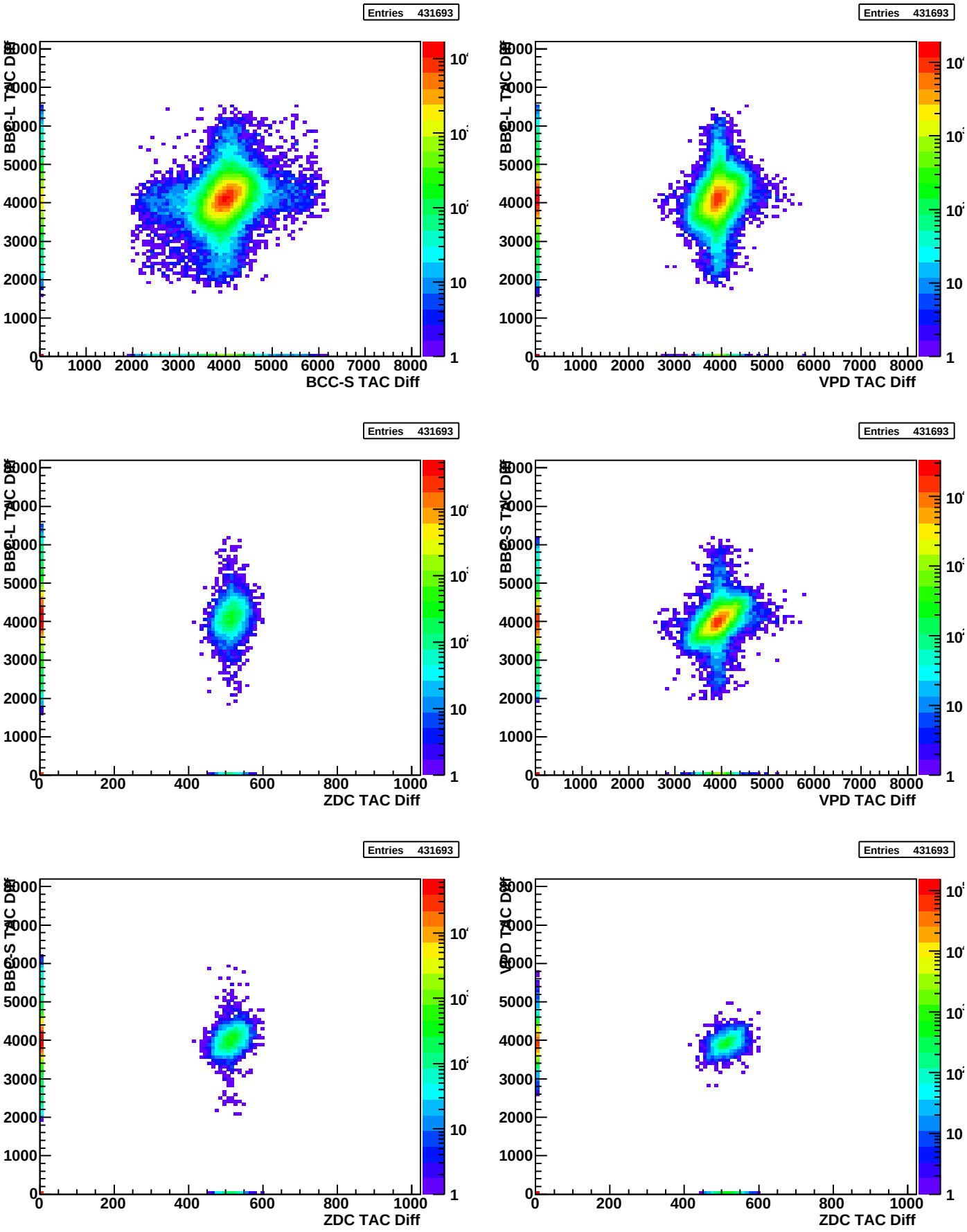

Entries 431693
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

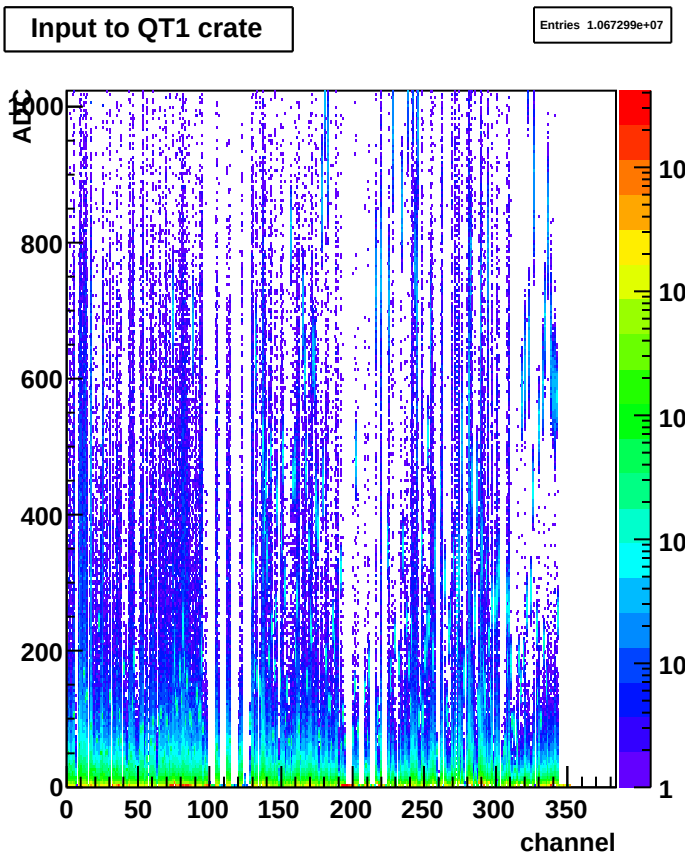

Entries 431693
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

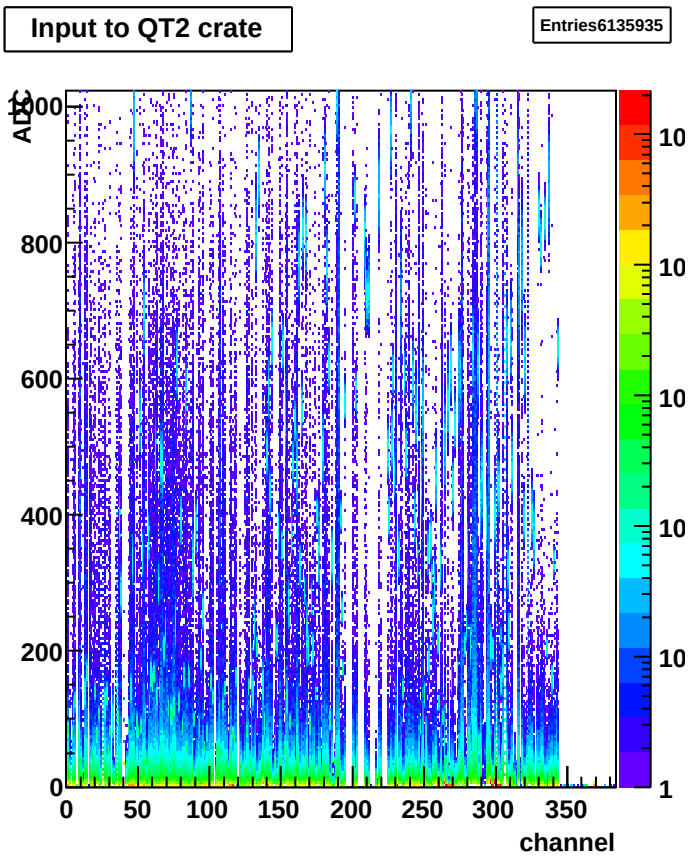

Entries 431693
BBC-L-East ADC Sum
BBC-L-West ADC Sum

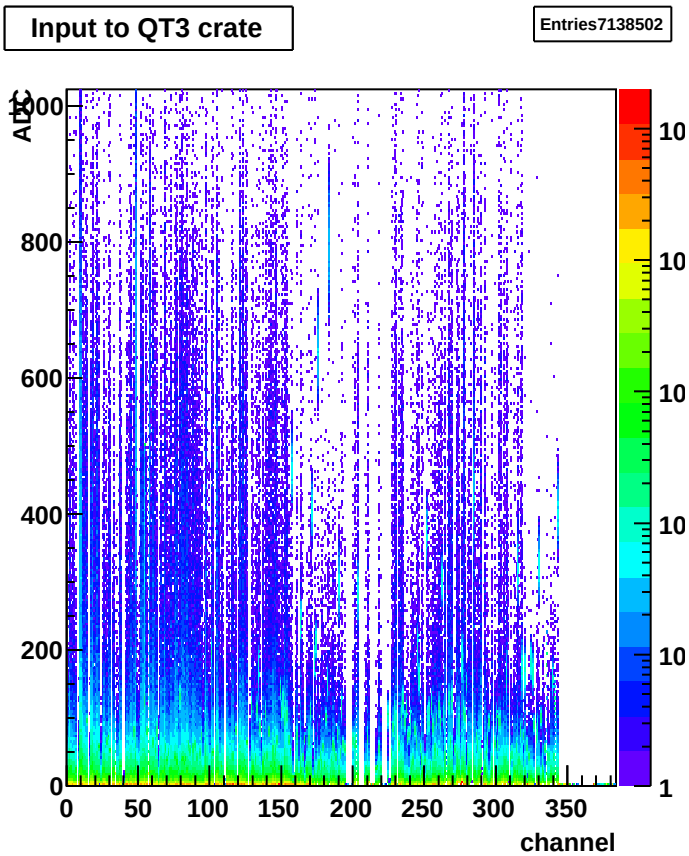

Entries 431693
BBC-S-East ADC Sum
BBC-S-West ADC Sum

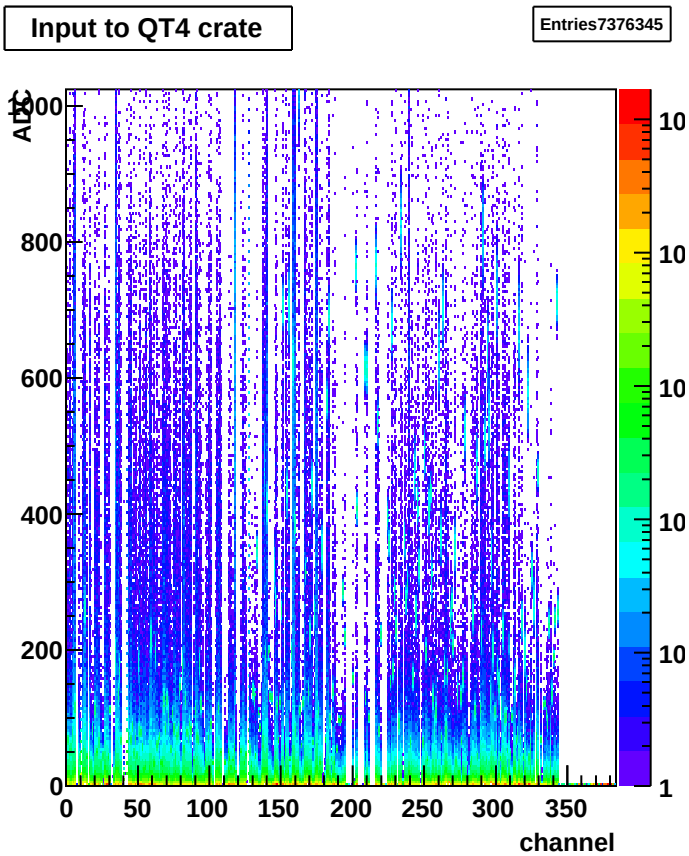

Entries 431693
ZDC East ADC Sum Att
ZDC West ADC Sum Att

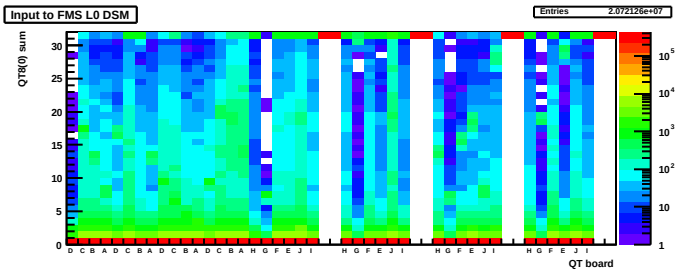

Entries 431693
BBC-L TAC Diff
BCC-S TAC Diff
Entries 431693
BBC-L TAC Diff
VPD TAC Diff
Entries 431693
BBC-L TAC Diff
ZDC TAC Diff
Entries 431693
BBC-S TAC Diff
VPD TAC Diff
Entries 431693
BBC-S TAC Diff
ZDC TAC Diff
Entries 431693
VPD TAC Diff
ZDC TAC Diff

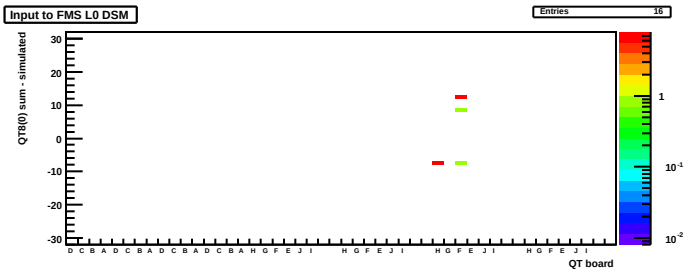

Input to QT1 crate
Entries 1.067299e+07
ADC
channel

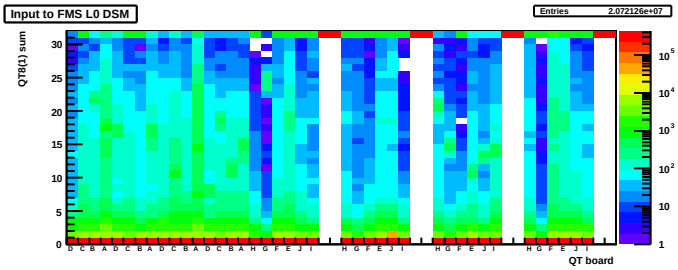

Input to QT2 crate
Entries6135935
ADC
channel

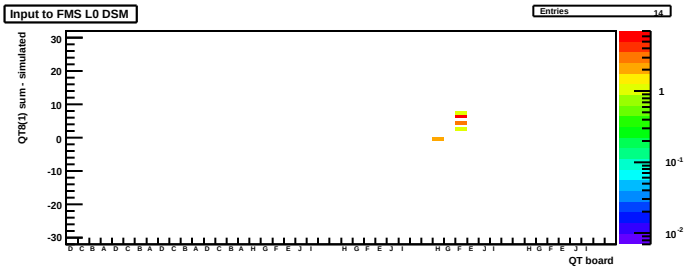

Input to QT3 crate
Entries7138502
ADC
channel

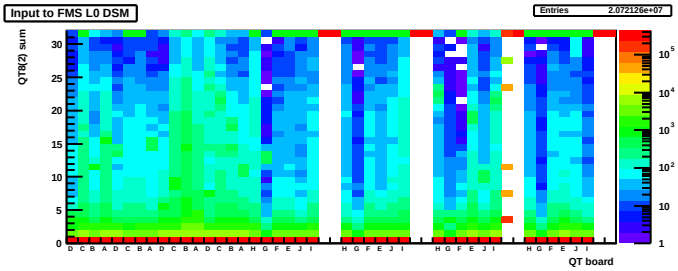

Input to QT4 crate
Entries7376345
ADC
channel

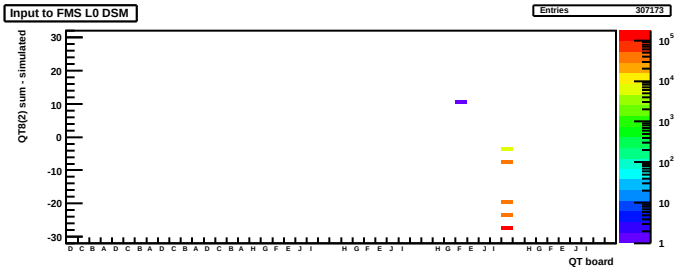

Input to FMS L0 DSM
Entries 2.072126e+07
QT8(0) sum
QT board

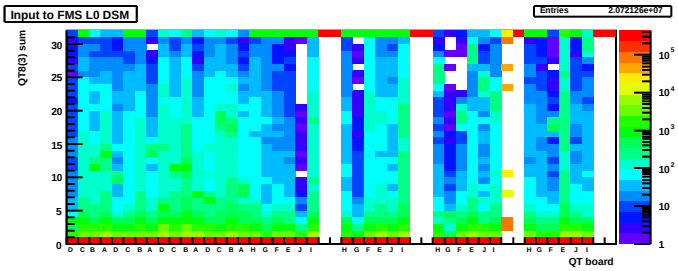

Input to FMS L0 DSM
Entries 16
QT8(0) sum - simulated
QT board

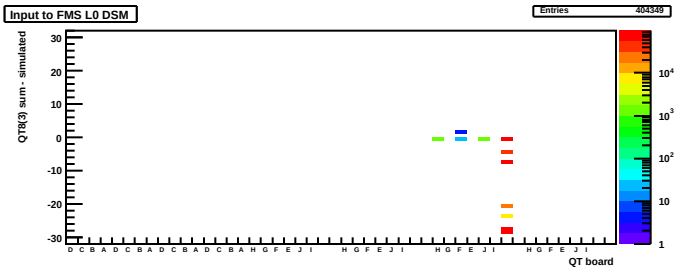

Input to FMS L0 DSM
Entries 2.072126e+07
QT8(1) sum
QT board

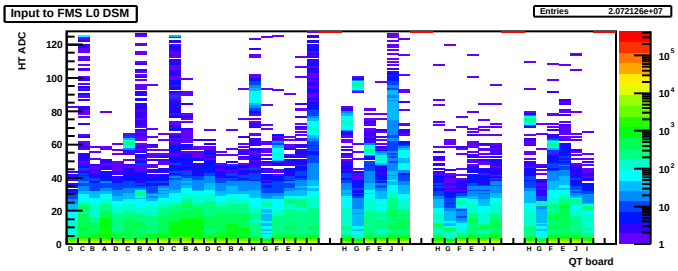

Input to FMS L0 DSM
Entries 14
QT8(1) sum - simulated
QT board

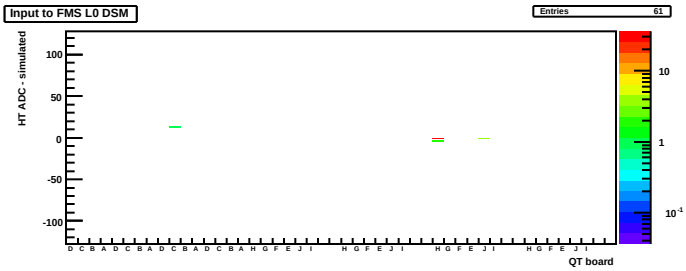

Input to FMS L0 DSM
Entries 2.072126e+07
QT8(2) sum
QT board

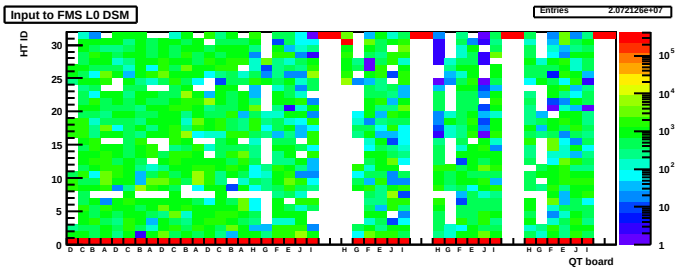

Input to FMS L0 DSM
Entries 307173
QT8(2) sum - simulated
QT board

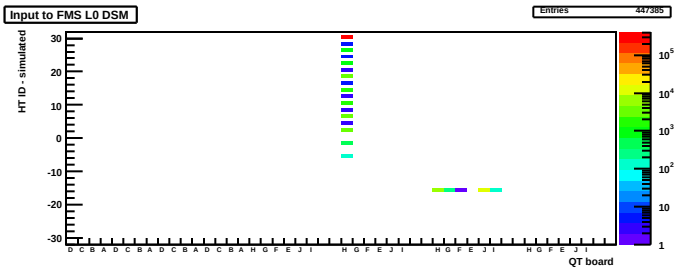

Input to FMS L0 DSM
Entries 2.072126e+07
QT8(3) sum
QT board

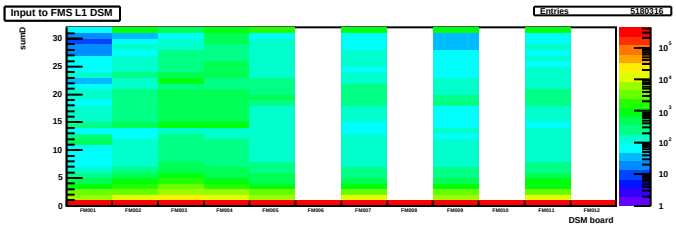

Input to FMS L0 DSM
Entries 404349
QT8(3) sum - simulated
QT board

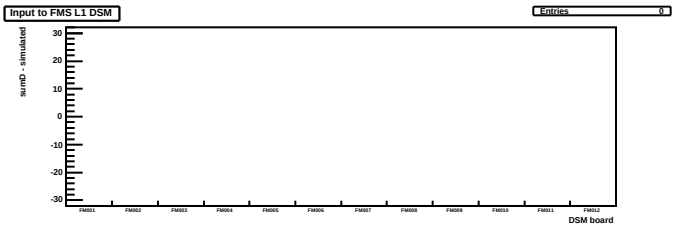

Input to FMS L0 DSM
Entries 2.072126e+07
HT ADC
QT board

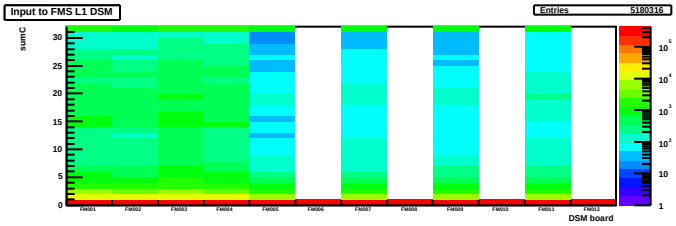

Input to FMS L0 DSM
Entries 61
HT ADC - simulated
QT board

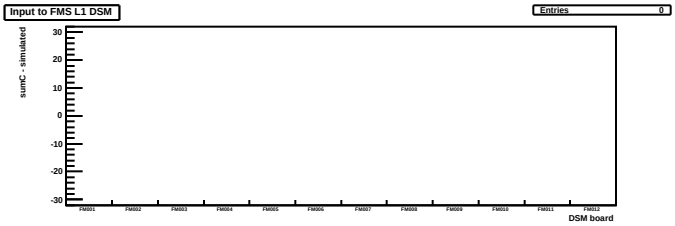

Input to FMS L0 DSM
Entries 2.072126e+07
HT ID
QT board

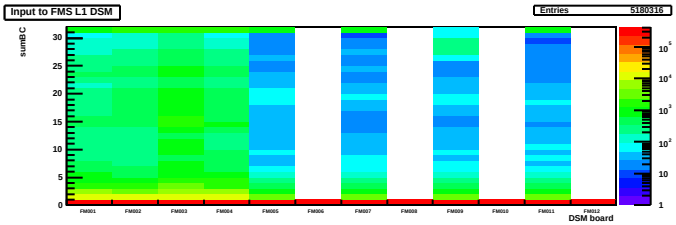

Input to FMS L0 DSM
Entries 447385
HT ID - simulated
QT board

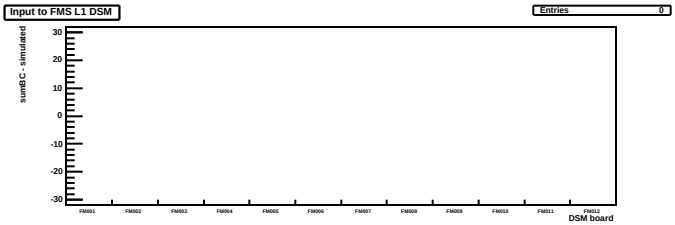

Input to FMS L1 DSM
Entries 5180316
sumD
DSM board

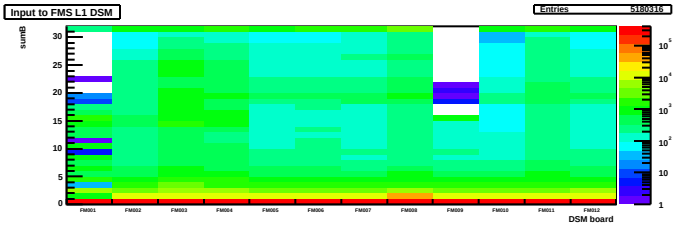

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

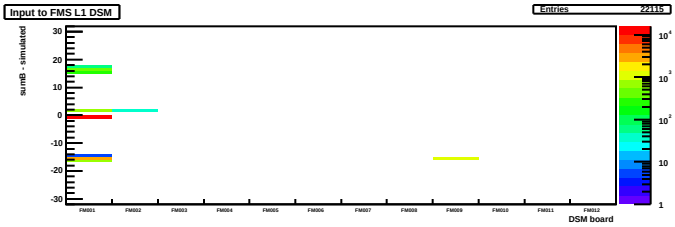

Input to FMS L1 DSM
Entries 5180316
sumC
DSM board

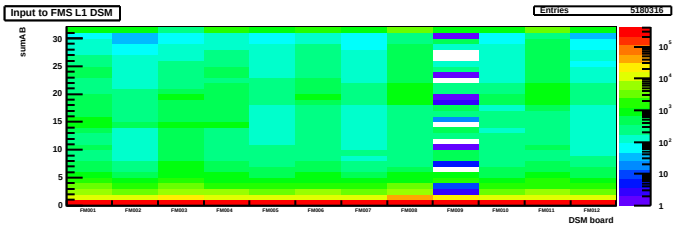

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

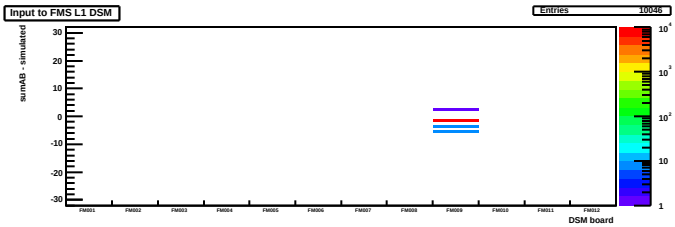

Input to FMS L1 DSM
Entries 5180316
sumBC
DSM board

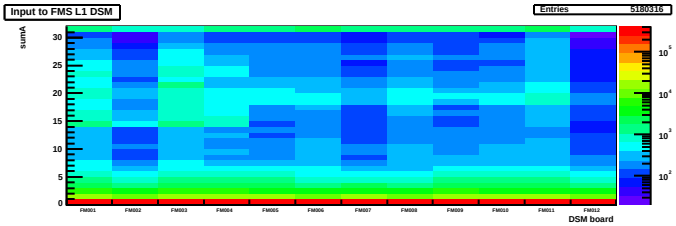

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

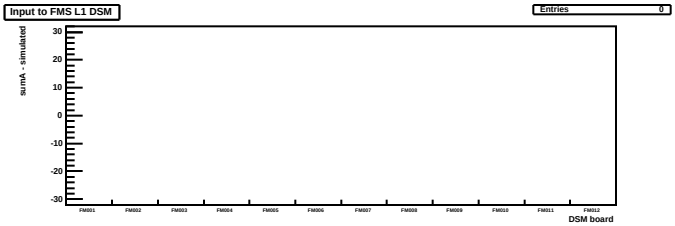

Input to FMS L1 DSM
Entries 5180316
sumB
DSM board

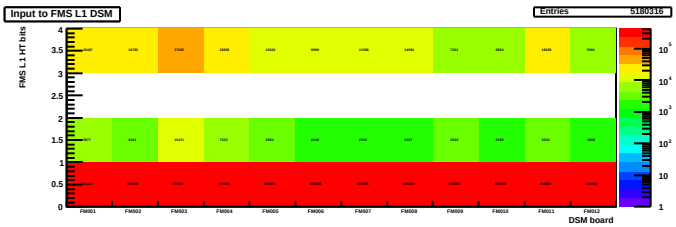

Input to FMS L1 DSM
Entries 22115
sumB - simulated
DSM board

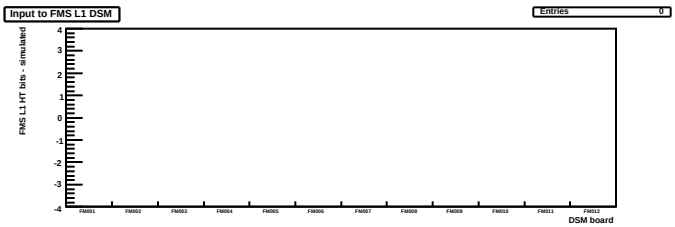

Input to FMS L1 DSM
Entries 5180316
sumAB
DSM board

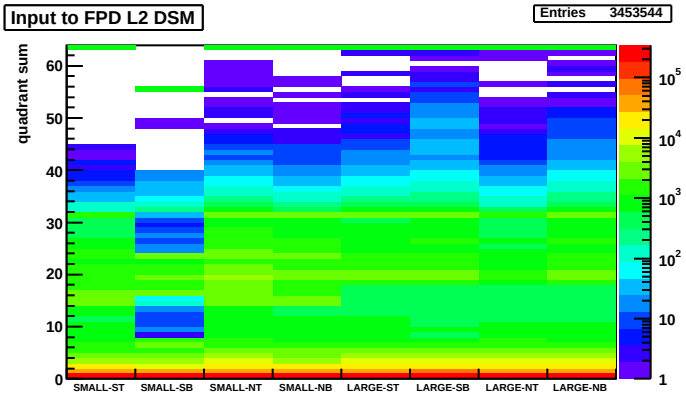

Input to FMS L1 DSM
Entries 10046
sumAB - simulated
DSM board

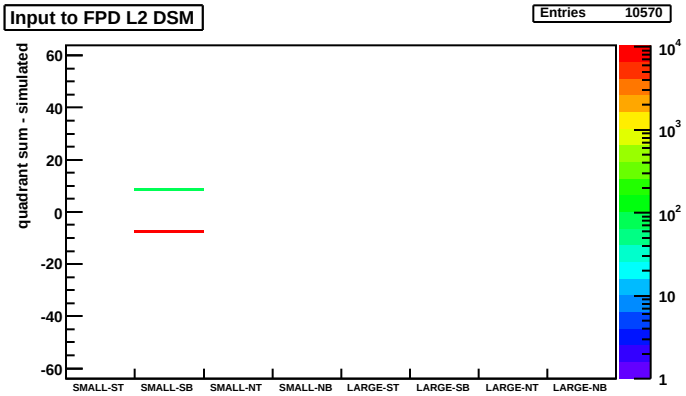

Input to FMS L1 DSM
Entries 5180316
sumA
DSM board

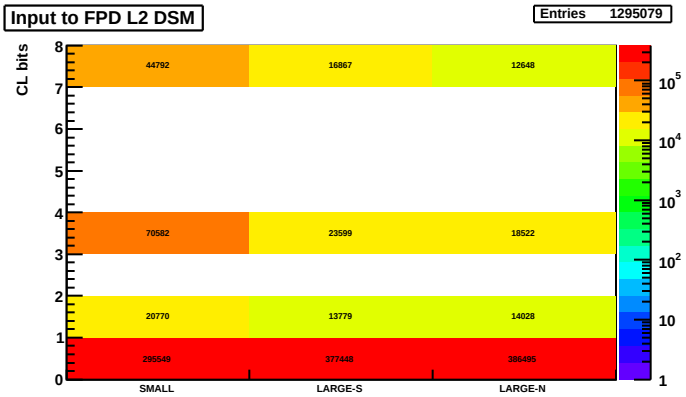

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

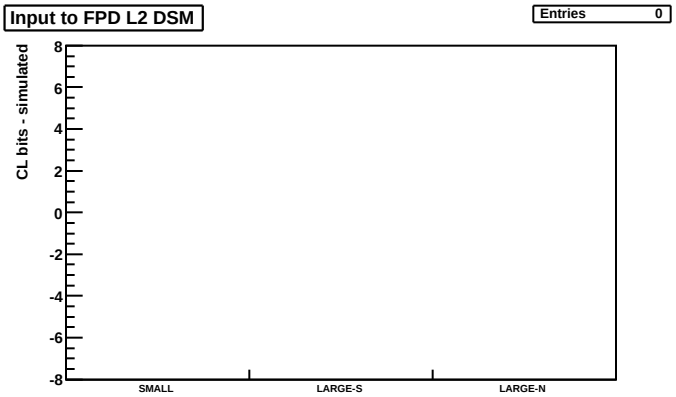

Input to FMS L1 DSM
Entries 5180316
FMS L1 HT bits
DSM board

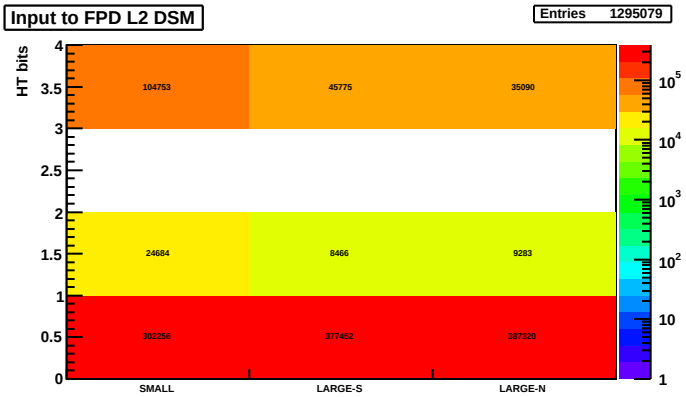

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

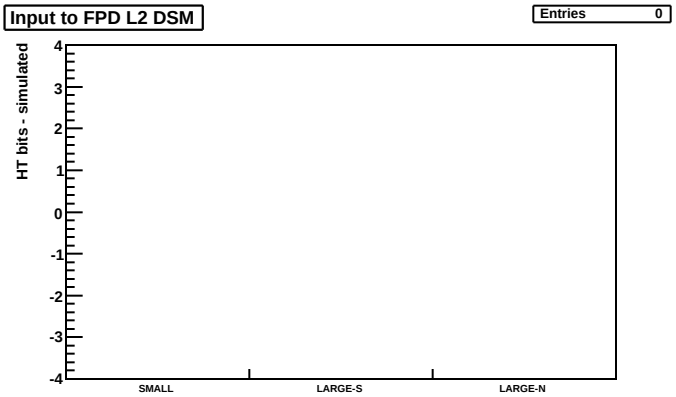

Input to FPD L2 DSM
Entries 3453544
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

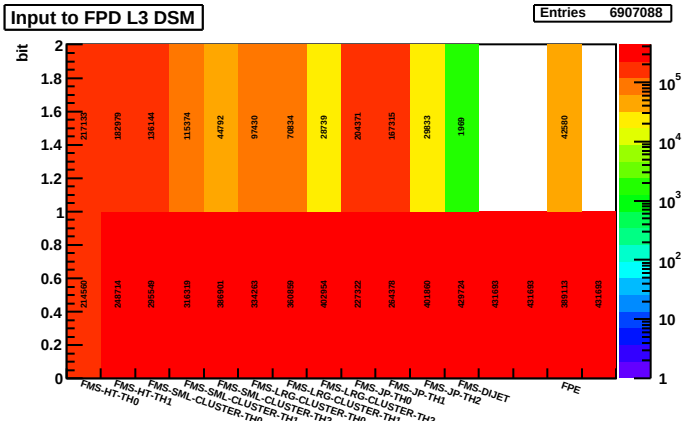

Input to FPD L2 DSM
Entries 10570
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

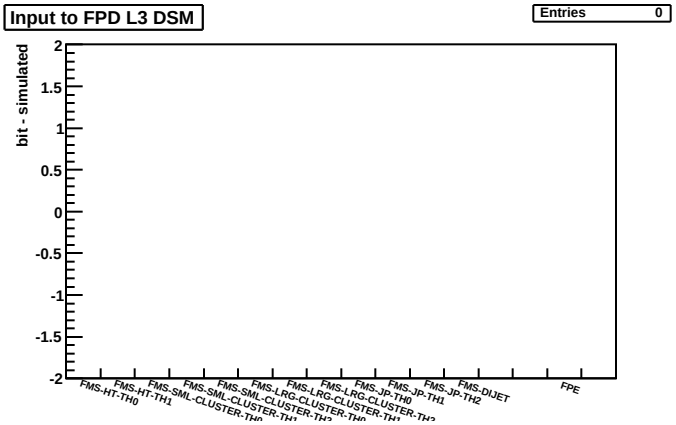

Input to FPD L2 DSM
Entries 1295079
CL bits
44792
16867
12648
70582
23599
18522
20770
13779
14028
295549
377448
386495
SMALL
LARGE-S
LARGE-N

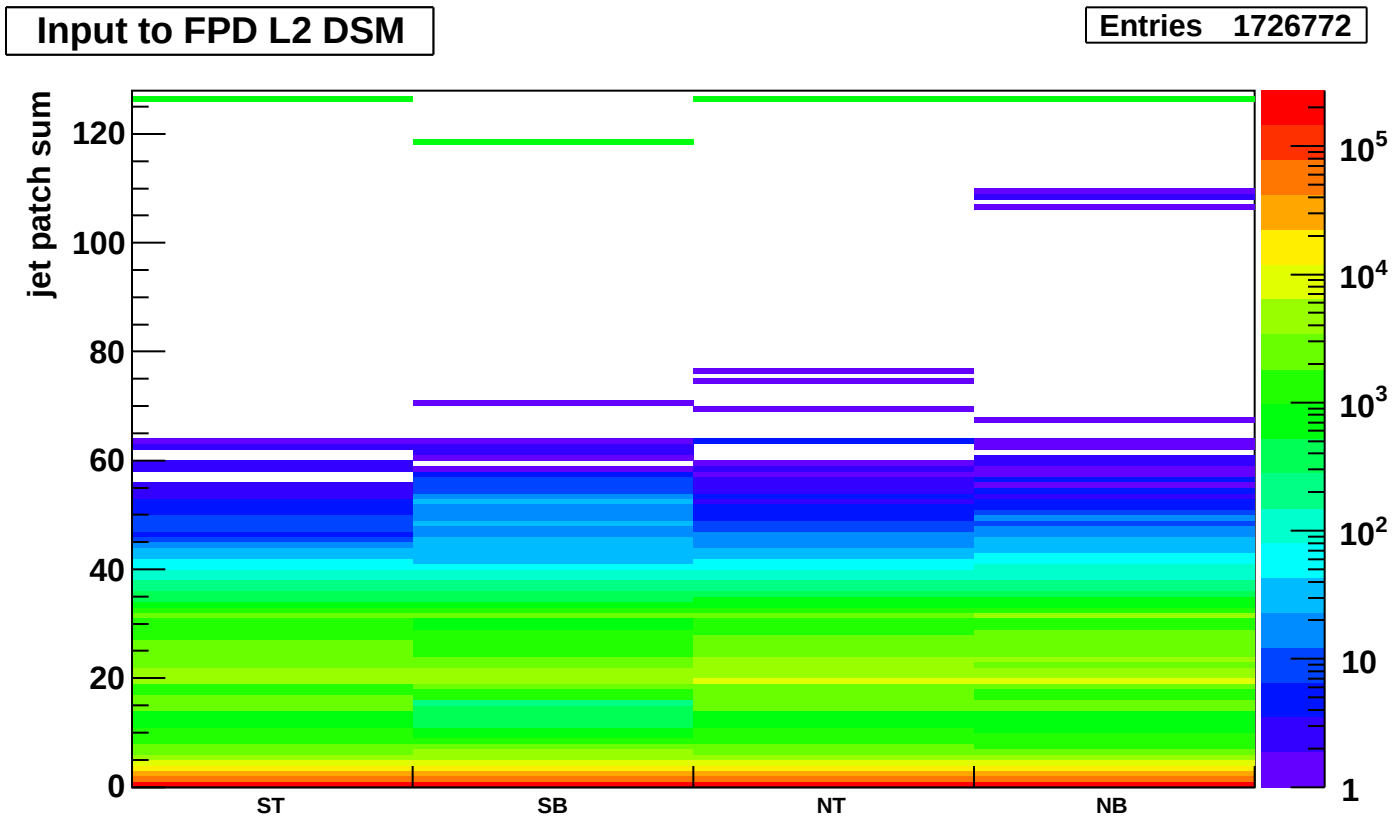

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

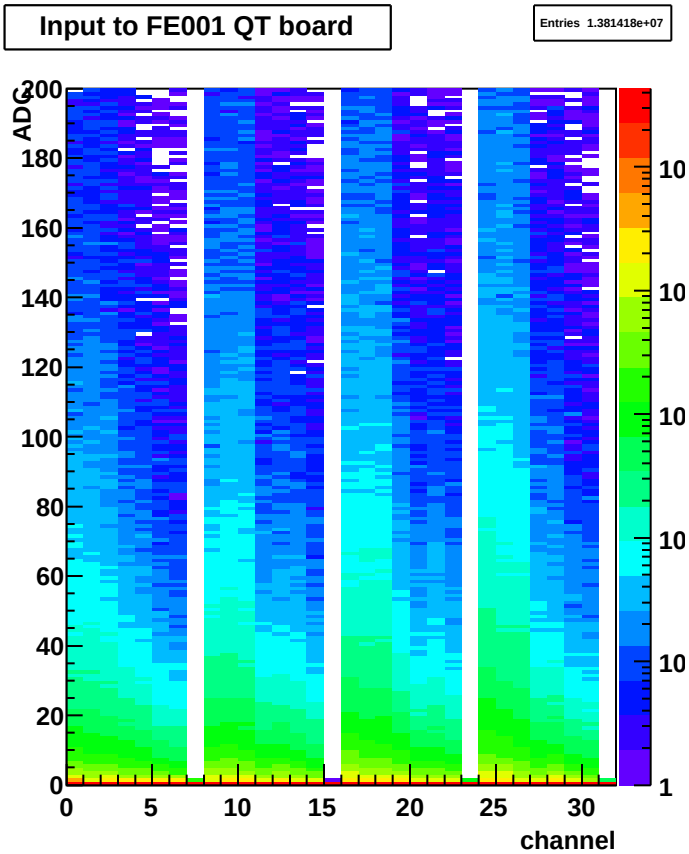

Input to FPD L2 DSM
Entries 1295079
HT bits
104753
45775
35090
24684
8466
9283
302256
377452
387320
SMALL
LARGE-S
LARGE-N

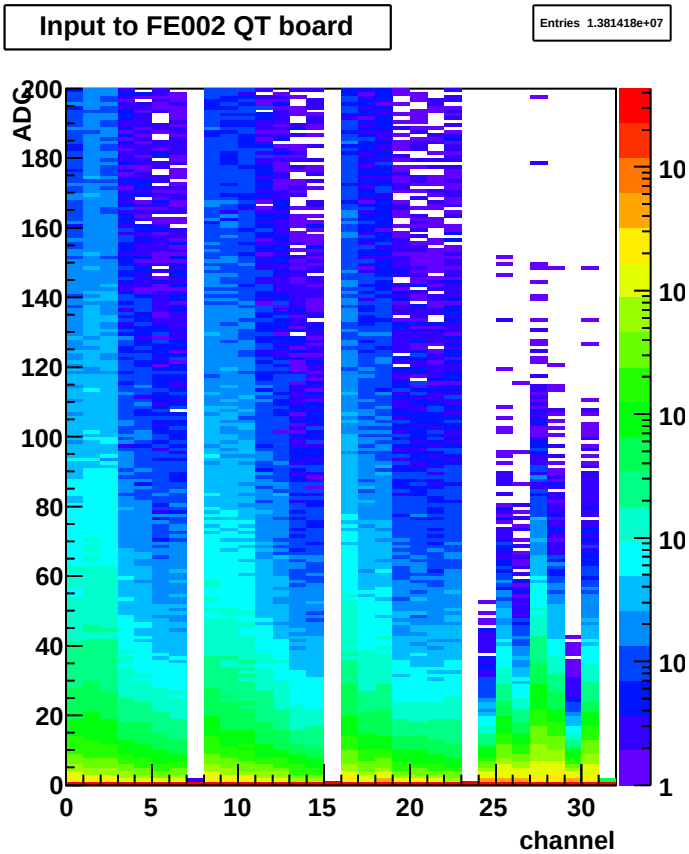

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

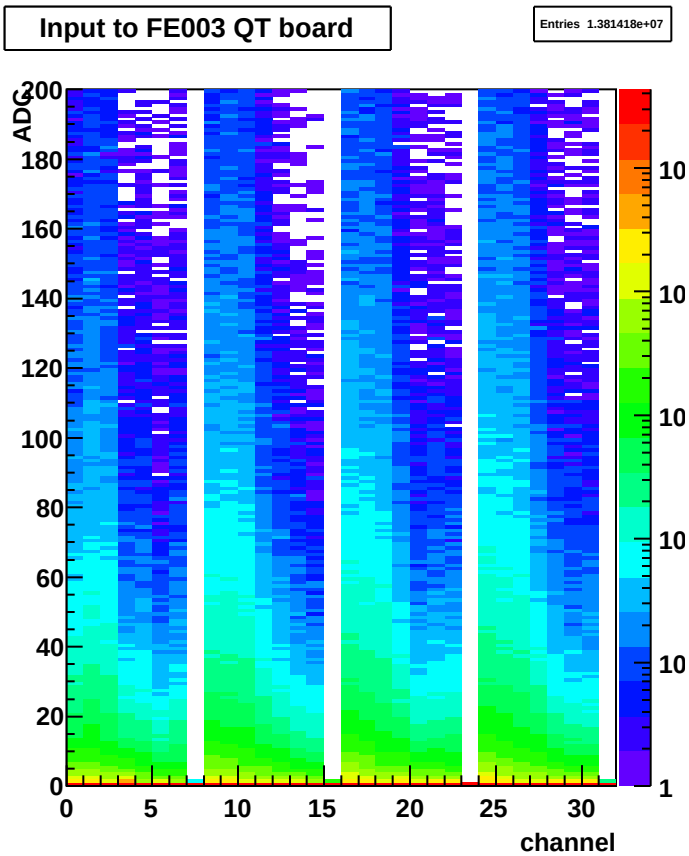

Input to FPD L3 DSM
Entries 6907088
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

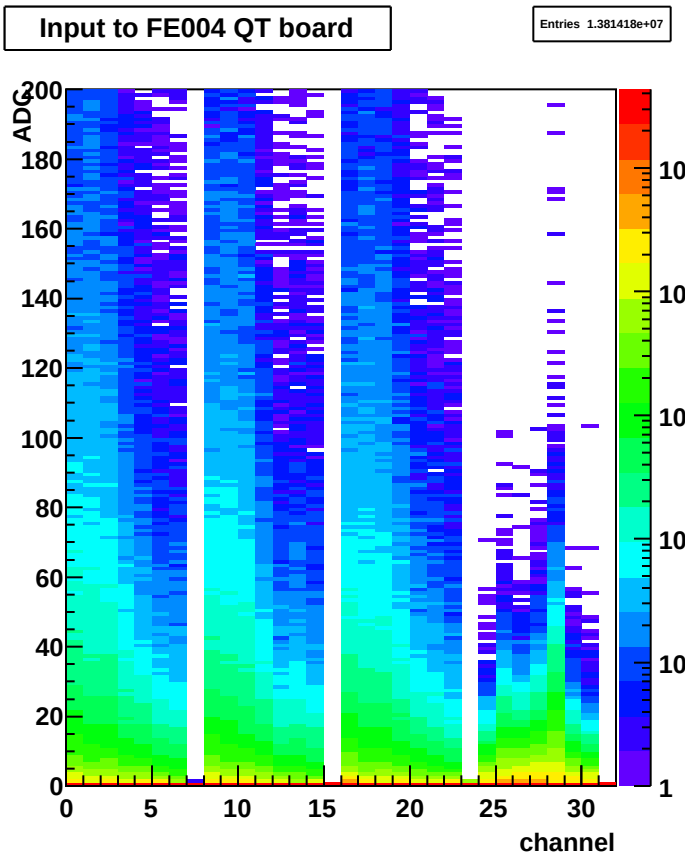

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

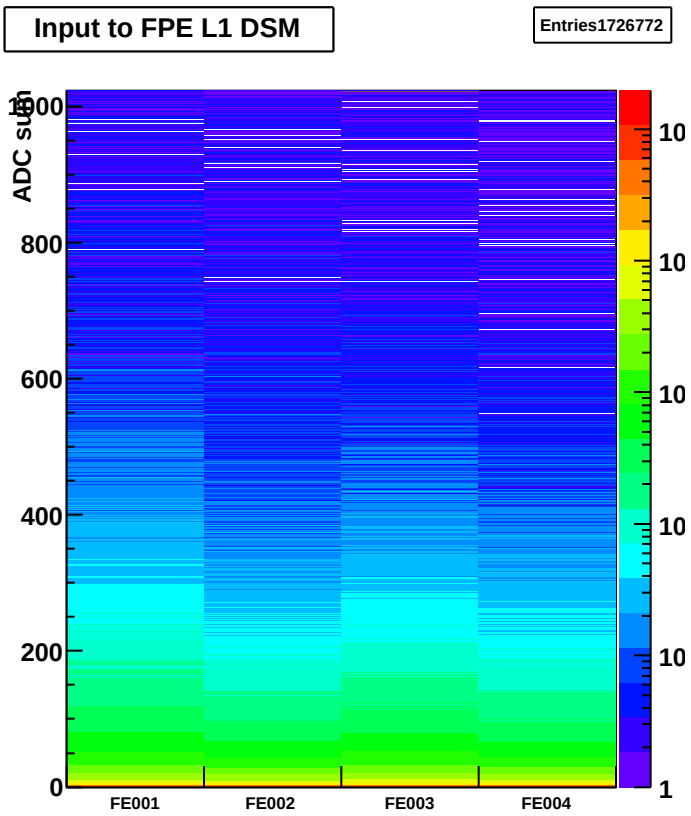

Input to FPD L2 DSM
Entries 1726772
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

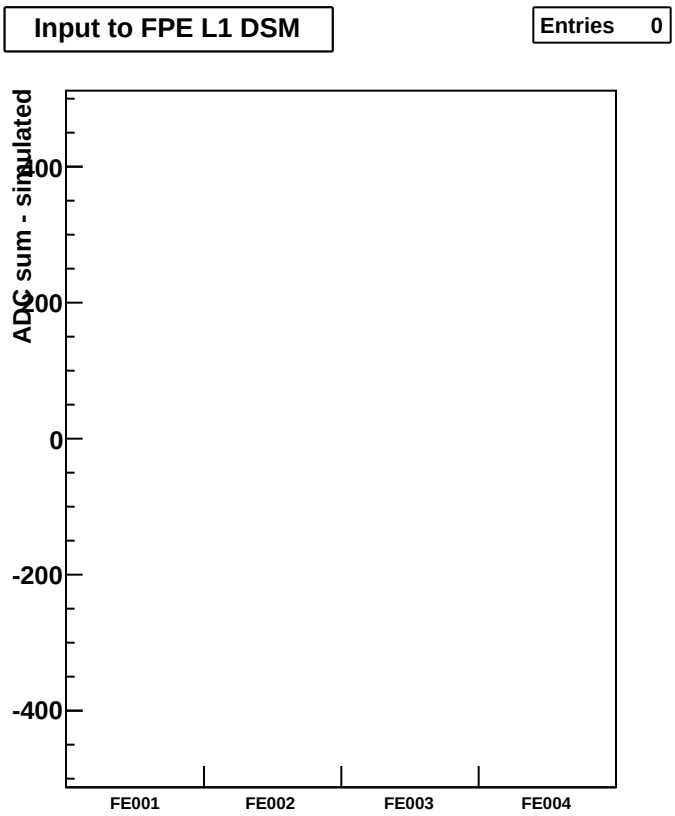

Input to FE001 QT board
Entries 1.381418e+07
ADC
channel

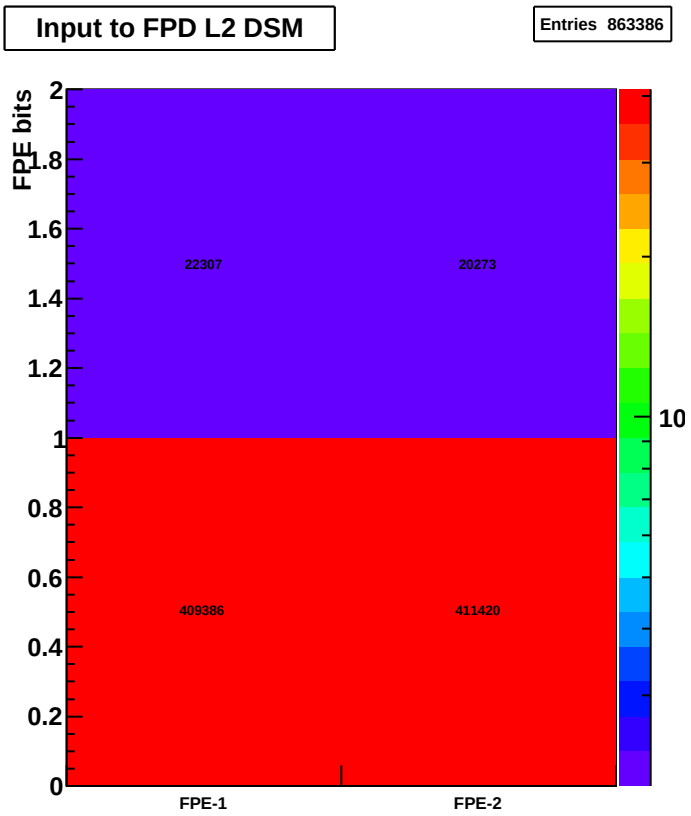

Input to FE002 QT board
Entries 1.381418e+07
ADC
channel

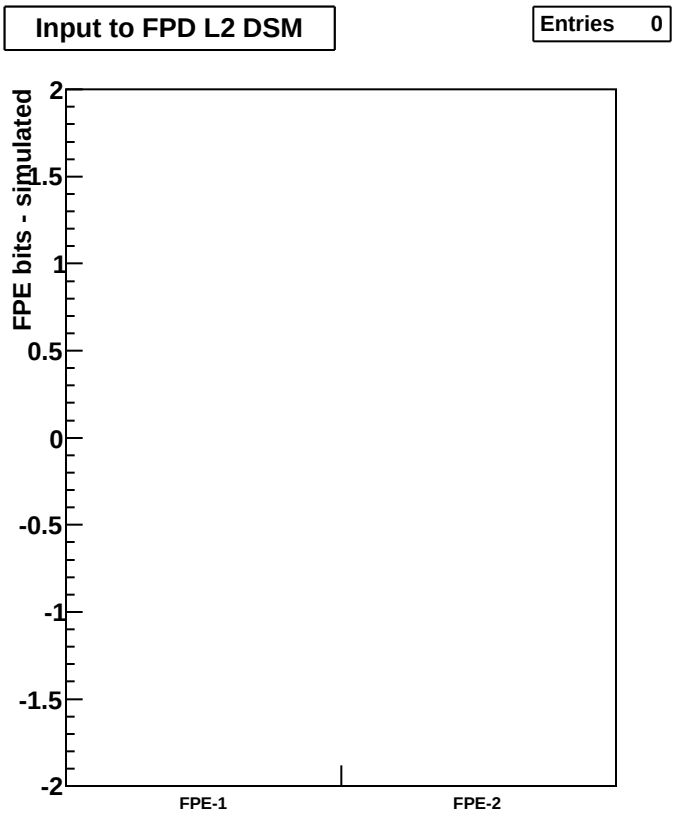

Input to FE003 QT board
Entries 1.381418e+07
ADC
channel


Input to FE004 QT board
Entries 1.381418e+07
ADC
channel


Input to FPE L1 DSM
Entries1726772
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 863386
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
22307
20273
409386
411420
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2