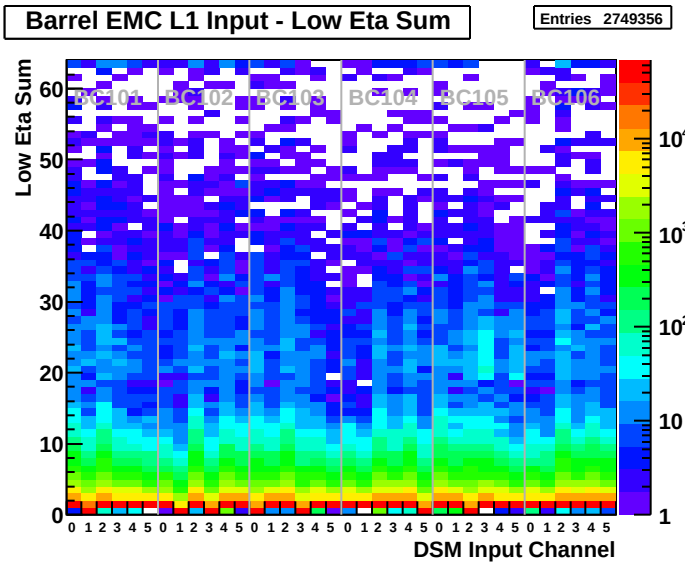
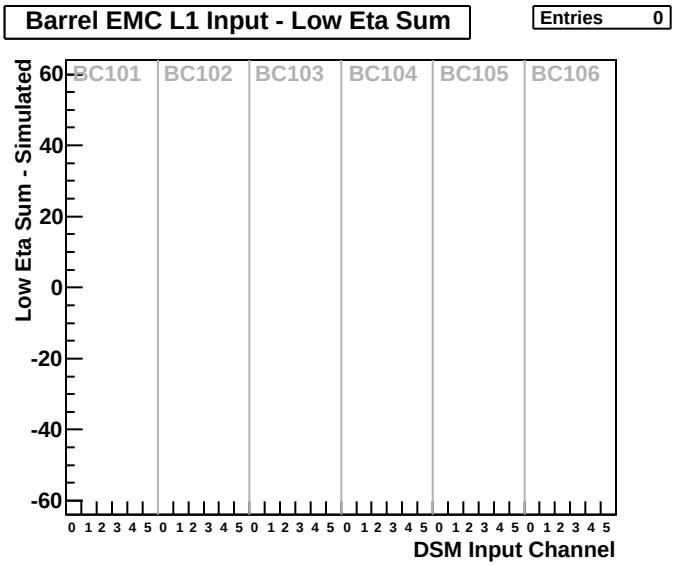
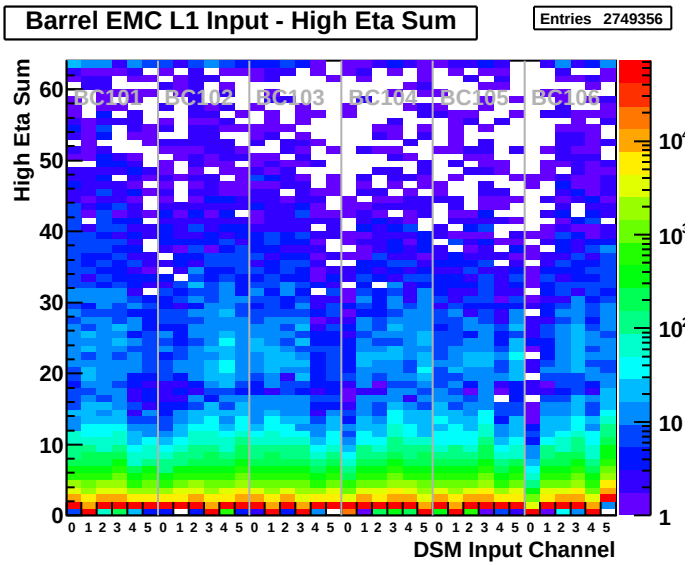
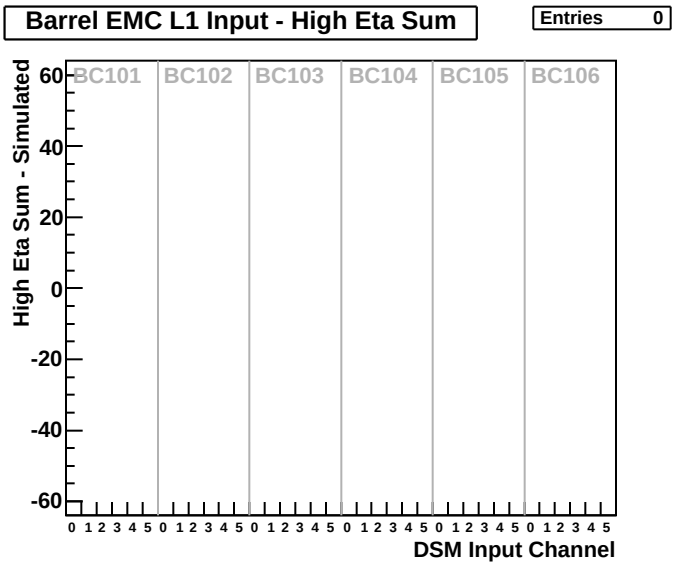
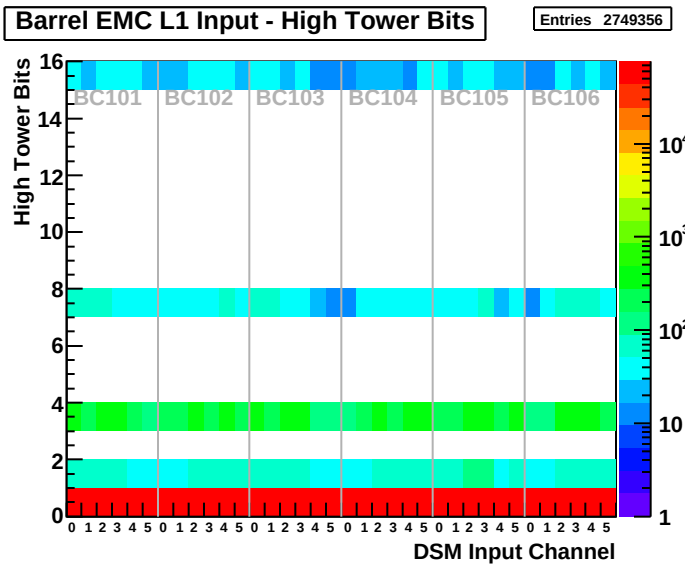
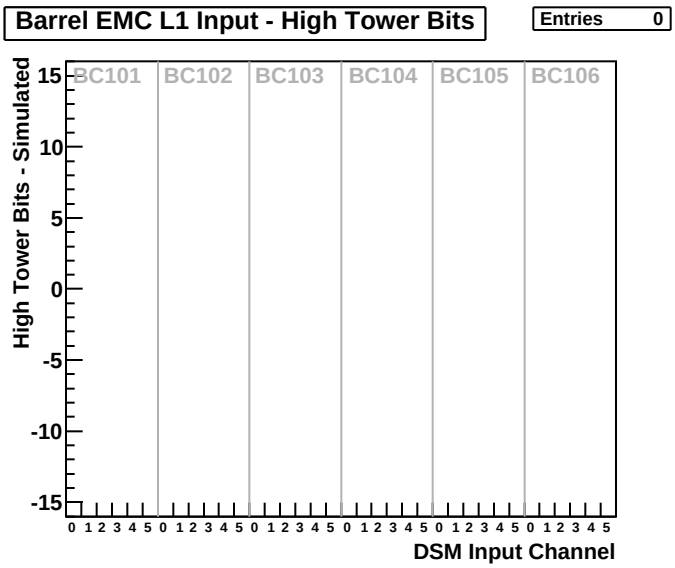

Barrel EMC L1 Input - Low Eta Sum
Entries 2749356
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

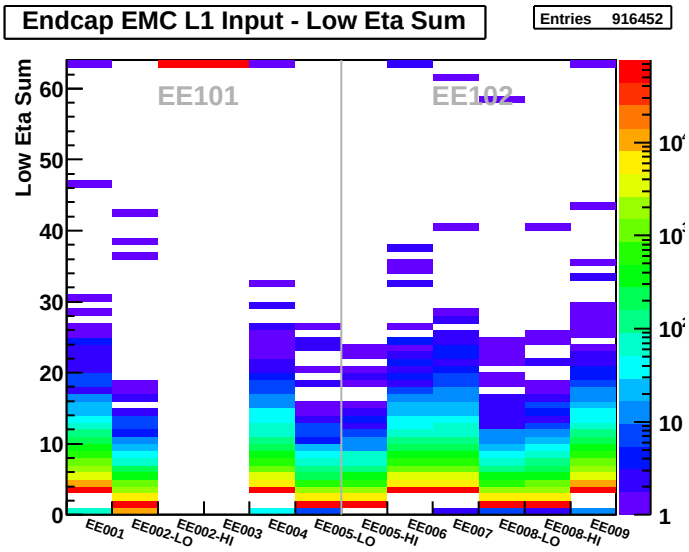

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

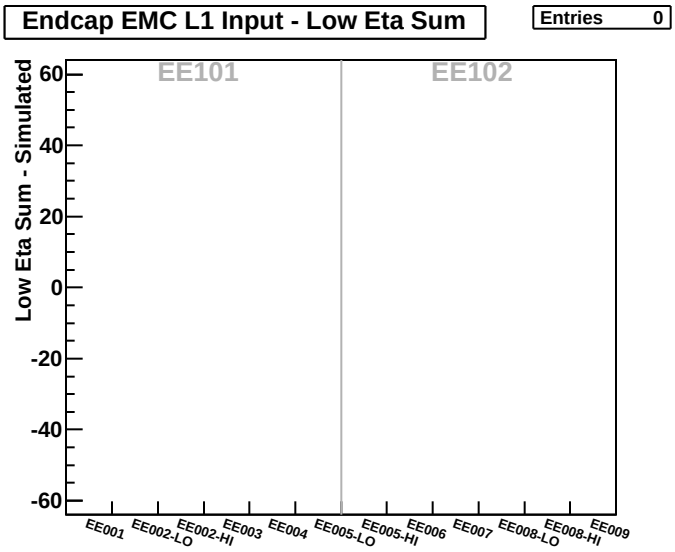

Barrel EMC L1 Input - High Eta Sum
Entries 2749356
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

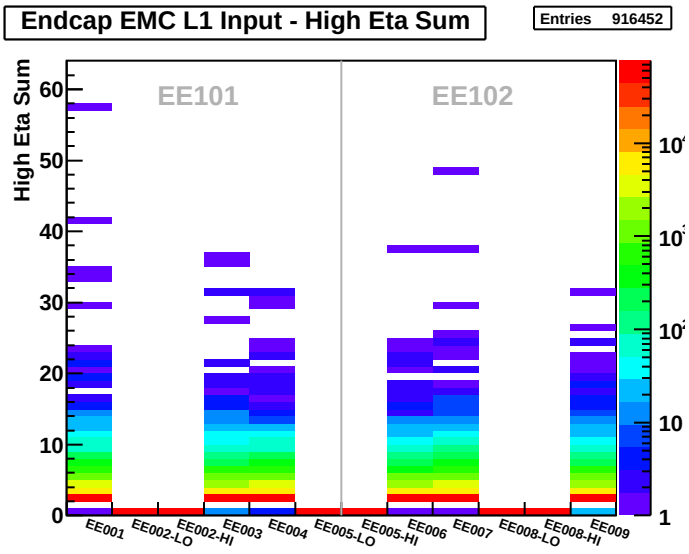

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

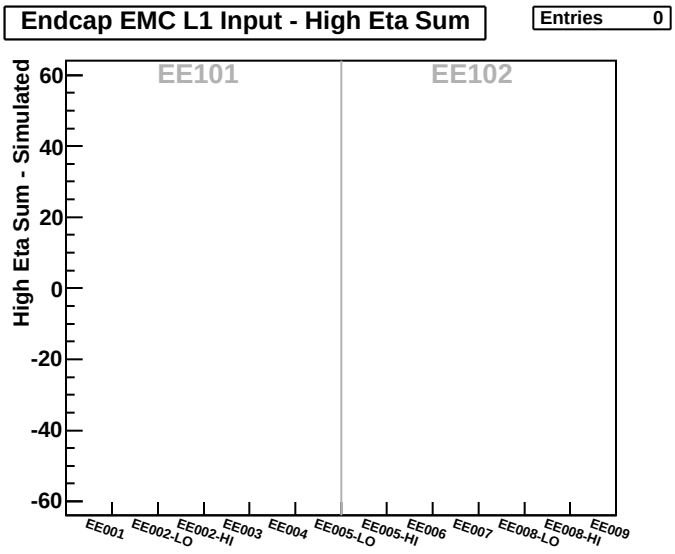

Barrel EMC L1 Input - High Tower Bits
Entries 2749356
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

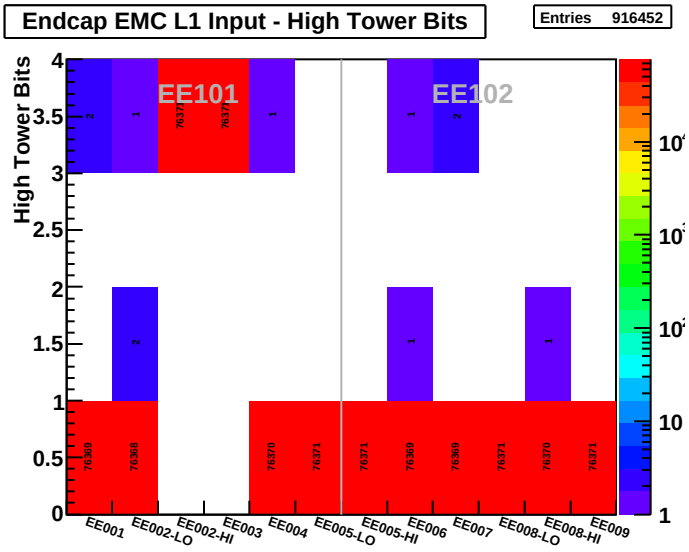

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

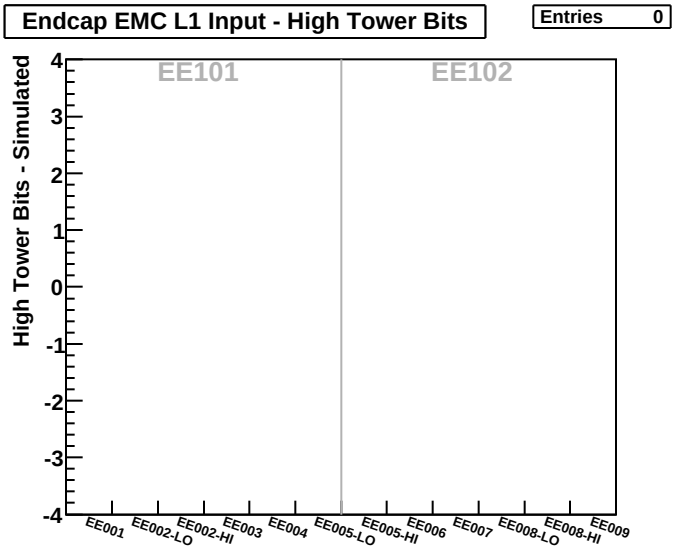

Endcap EMC L1 Input - Low Eta Sum
Entries 916452
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

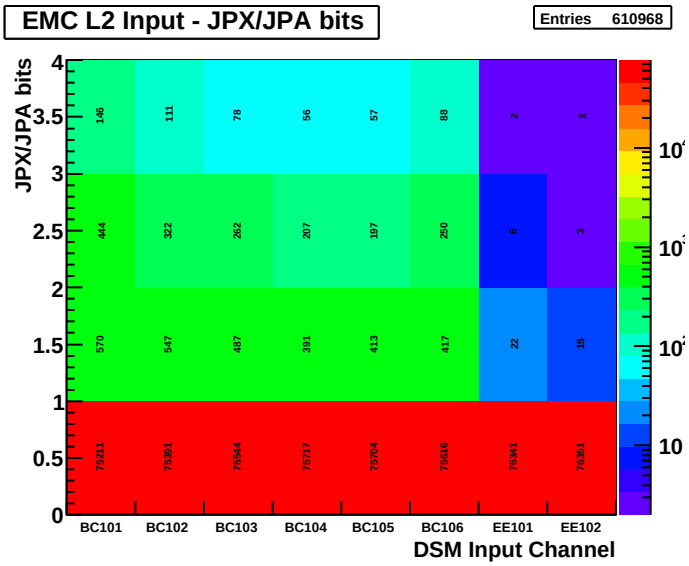

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

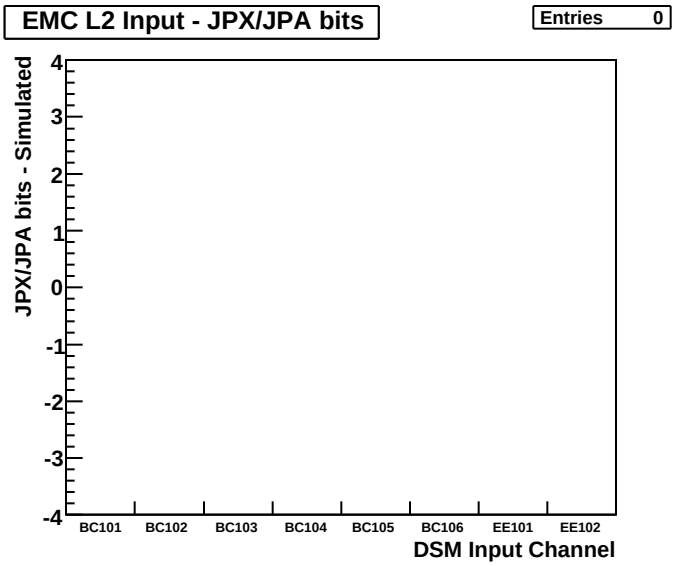

Endcap EMC L1 Input - High Eta Sum
Entries 916452
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

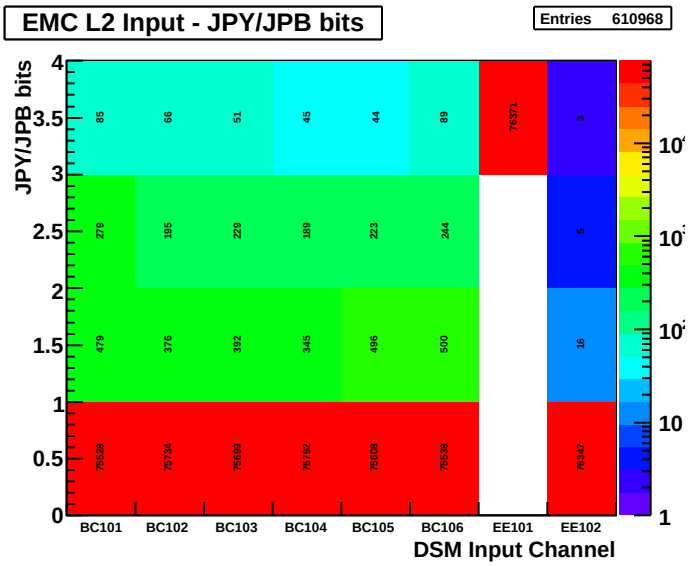

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

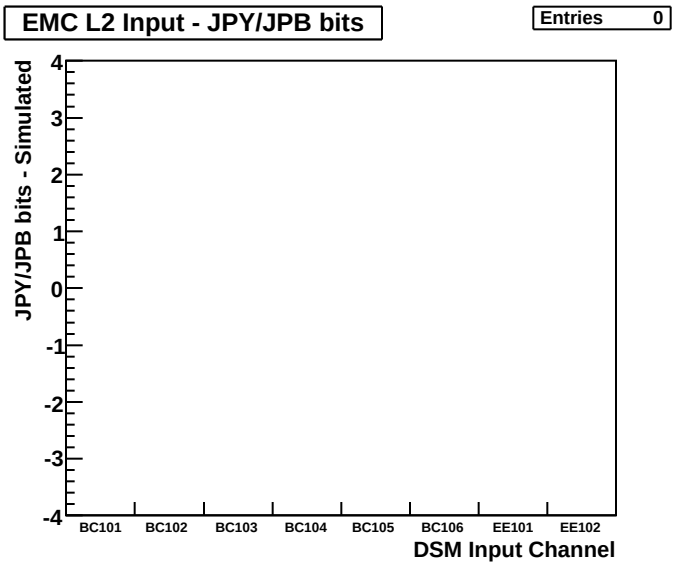

Endcap EMC L1 Input - High Tower Bits
Entries 916452
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

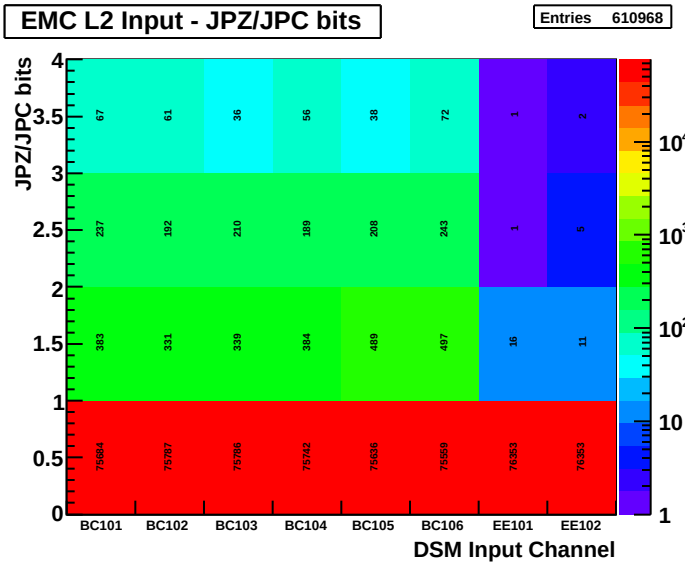

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

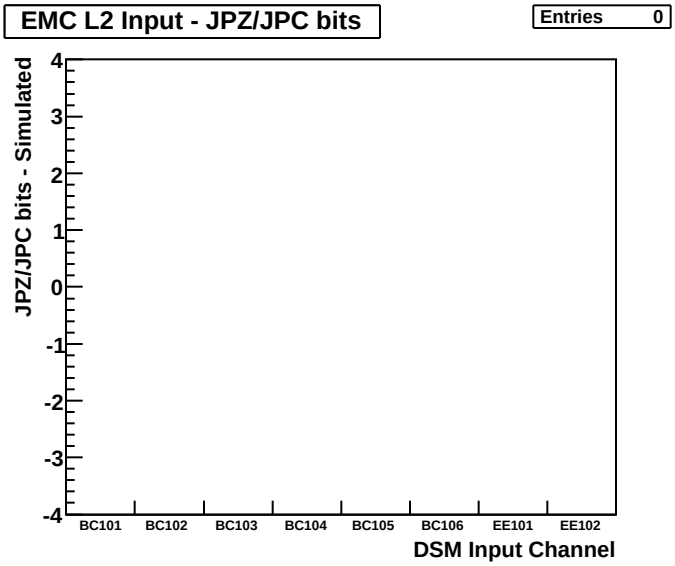

EMC L2 Input - JPX/JPA bits
Entries 610968
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
146 111 78 56 57 88 2 2
444 322 262 207 197 250 6 3
570 547 487 391 413 417 22 15
75211 75391 75544 75717 75704 75616 76341 76351

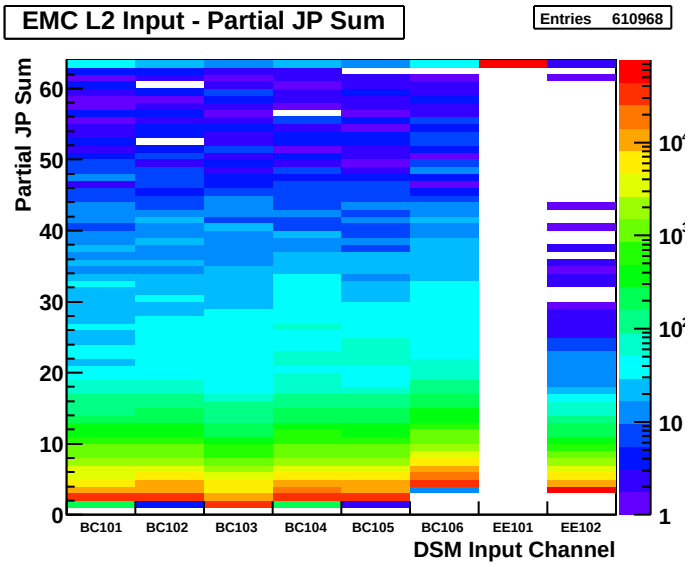

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

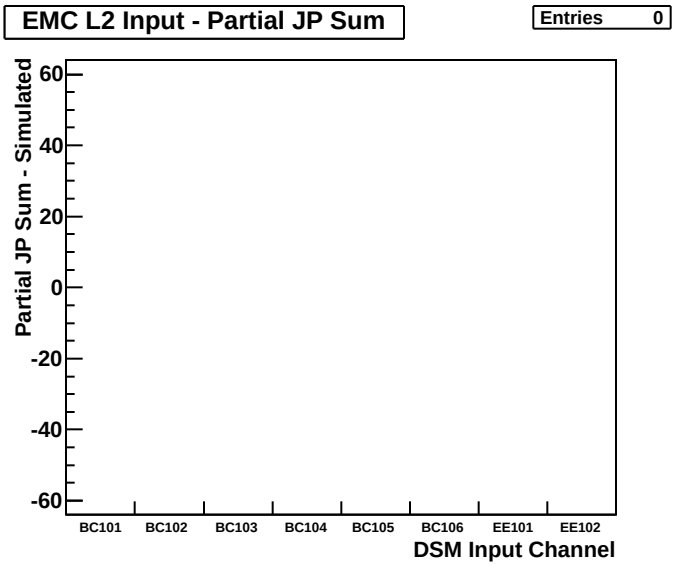

EMC L2 Input - JPY/JPB bits
Entries 610968
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
85 66 51 45 44 89 76371 3
279 195 229 189 223 244 5
479 376 392 345 496 500 16
75528 75734 75699 75792 75608 75538 76347

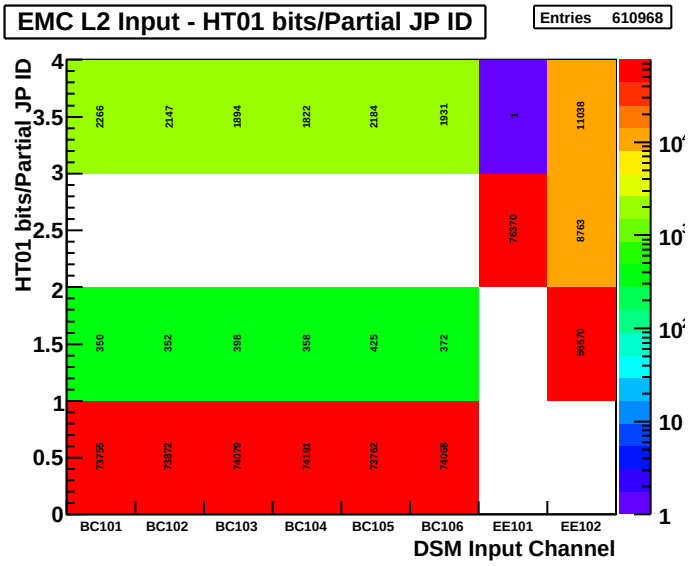

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

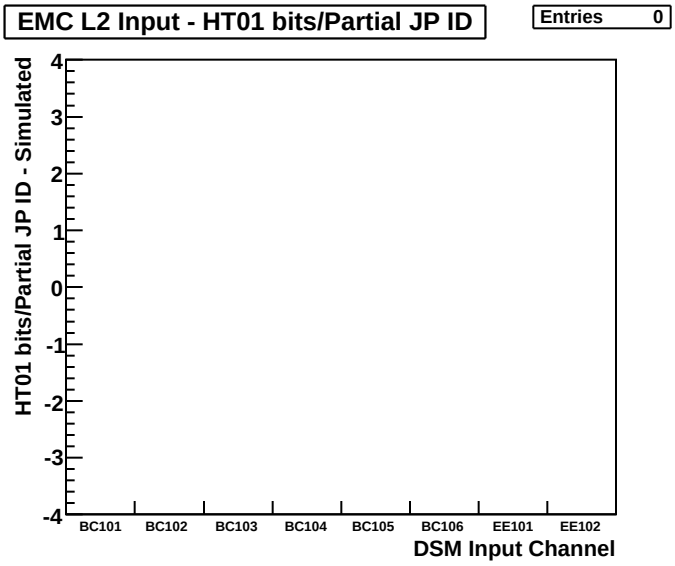

EMC L2 Input - JPZ/JPC bits
Entries 610968
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
67 61 36 56 38 72 1 2
237 192 210 189 208 243 1 5
383 331 339 384 489 497 16 11
75684 75787 75786 75742 75636 75559 76353 76353

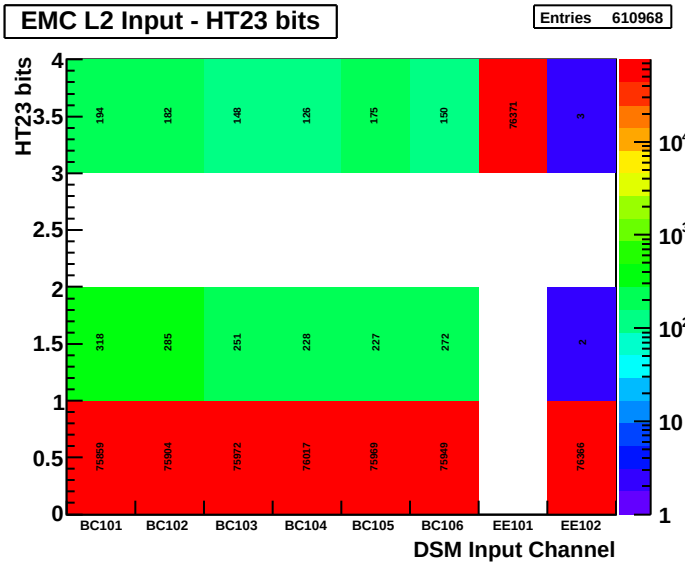

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

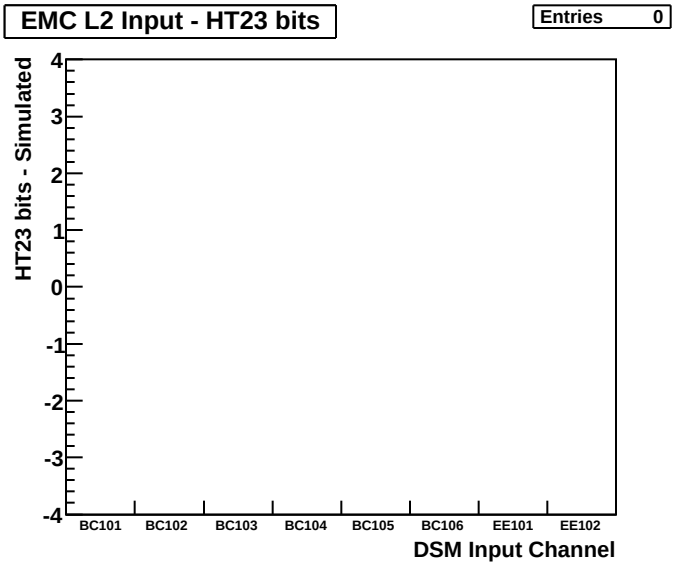

EMC L2 Input - Partial JP Sum
Entries 610968
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

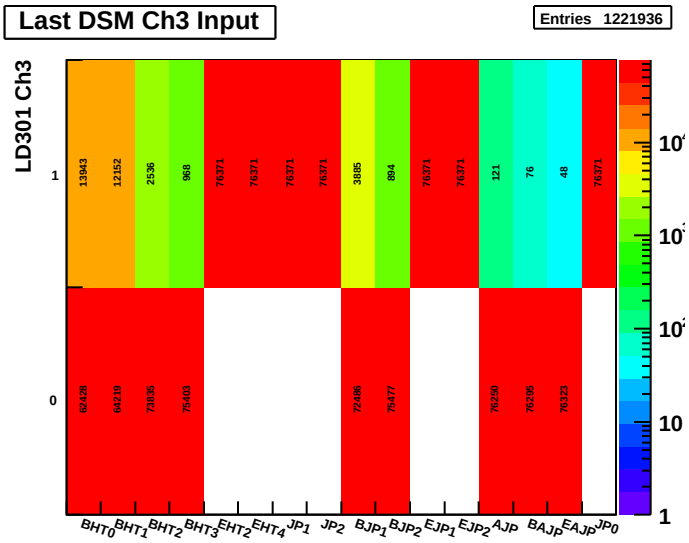

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

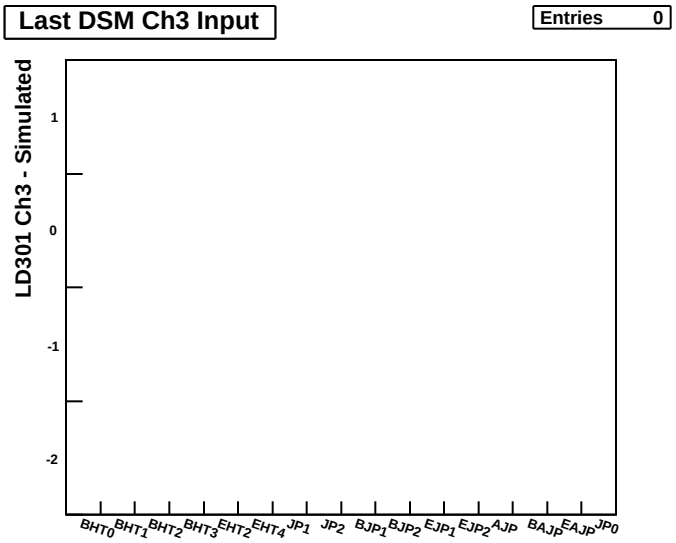

EMC L2 Input - HT01 bits/Partial JP ID
Entries 610968
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

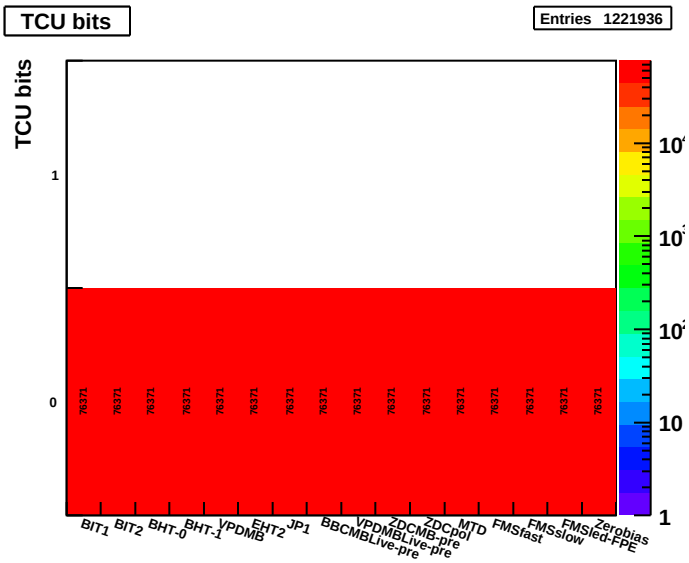

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

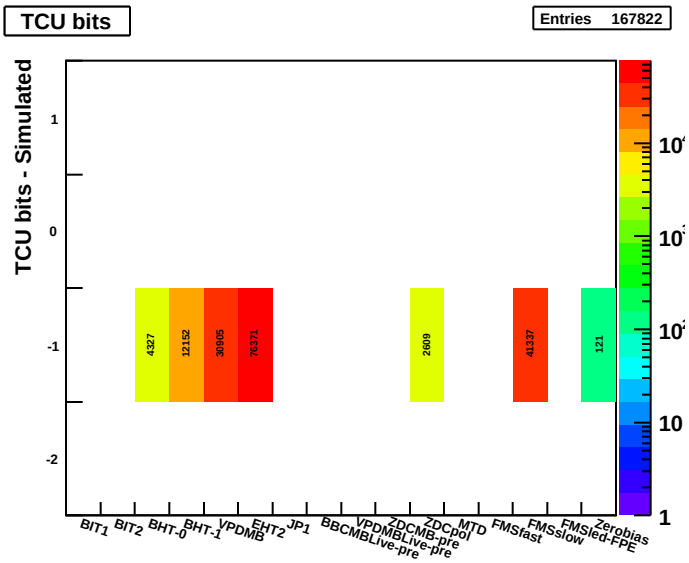

EMC L2 Input - HT23 bits
Entries 610968
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

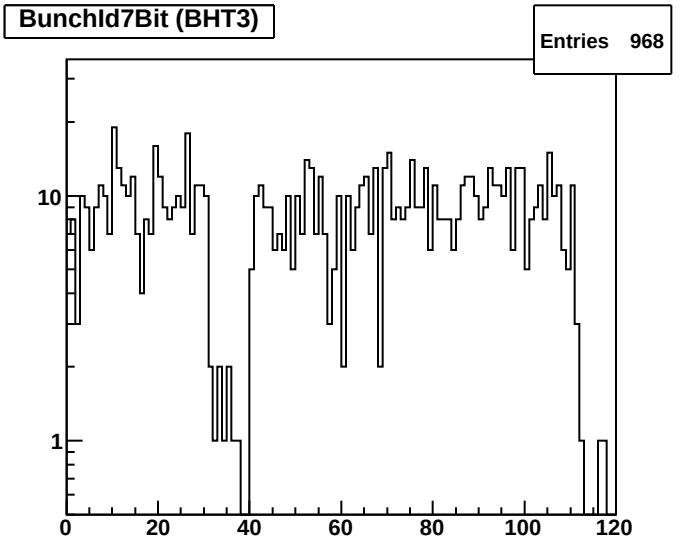

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

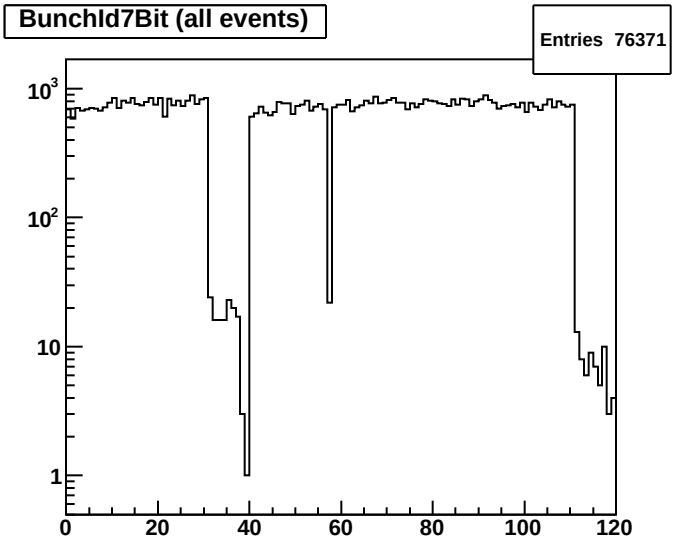

Last DSM Ch3 Input
Entries 1221936
LD301 Ch3
13943 12152 2536 968 76371 76371 76371 76371 3885 894 76371 76371 121 76 48 76371
62428 64219 73835 75403 72486 75477 76250 76295 76323
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

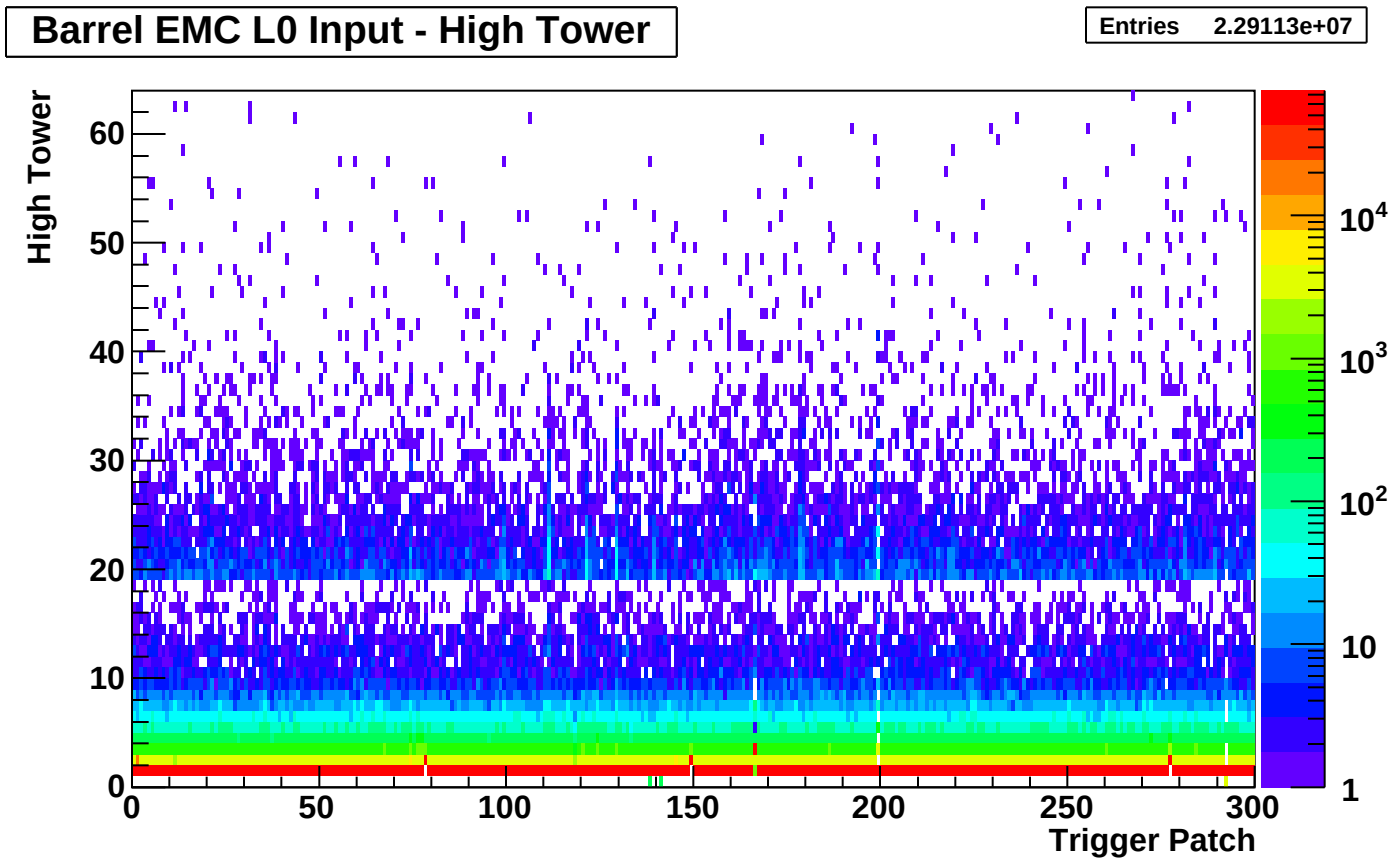

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

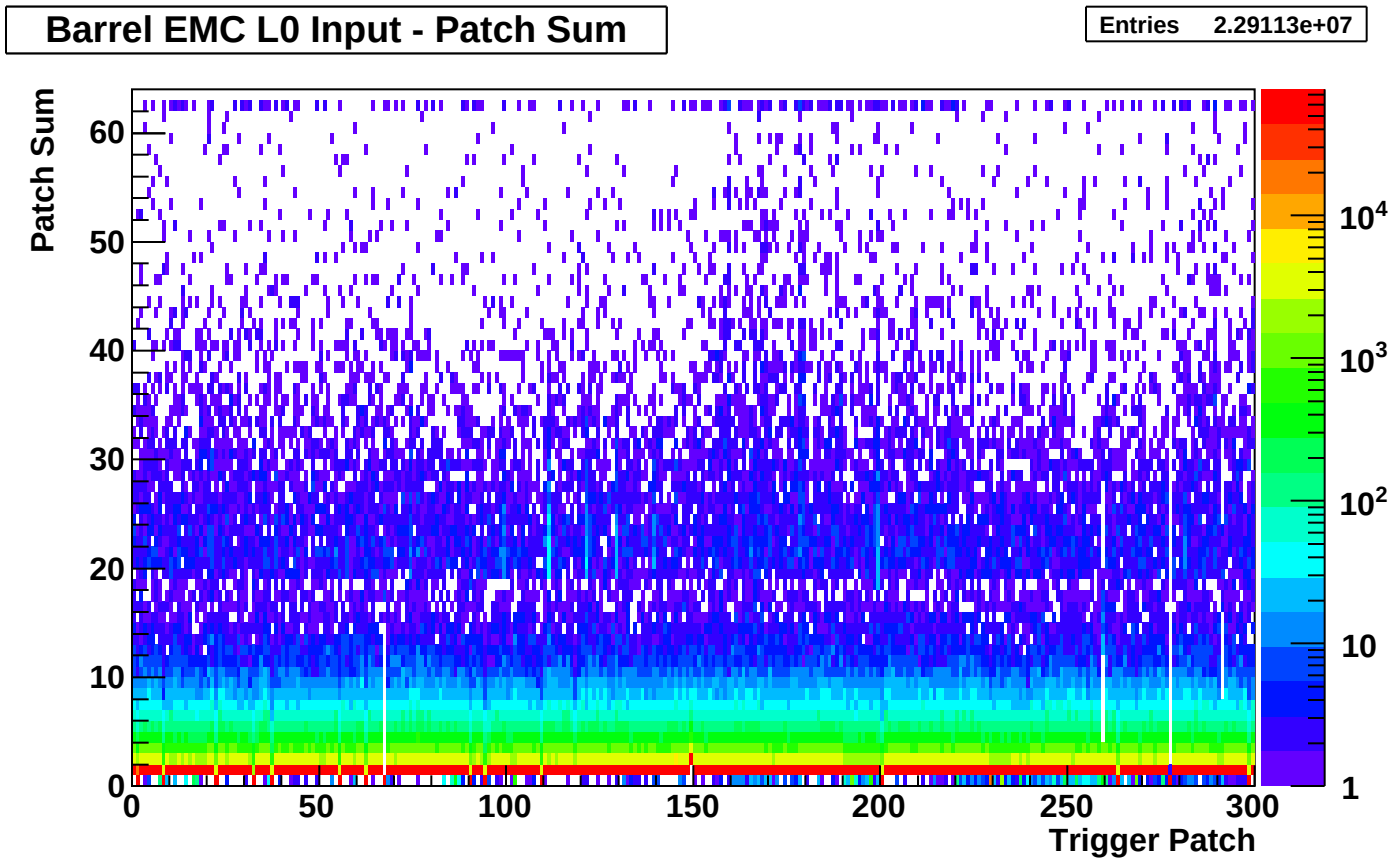

TCU bits
Entries 1221936
TCU bits
76371 76371 76371 76371 76371 76371 76371 76371 76371 76371 76371 76371 76371 76371 76371 76371
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

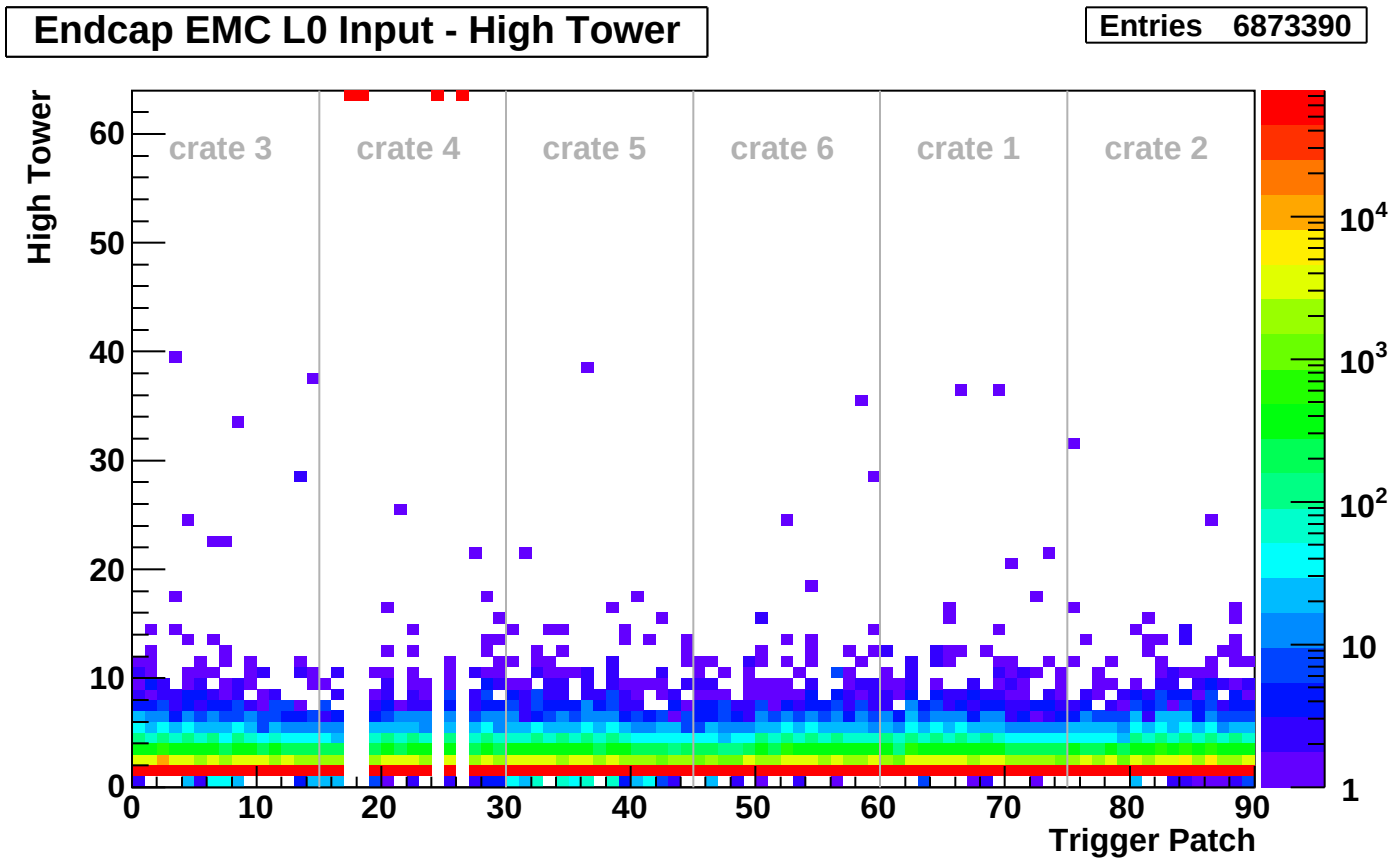

TCU bits
Entries 167822
TCU bits - Simulated
4327 12152 30905 76371 2609 41337 121
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

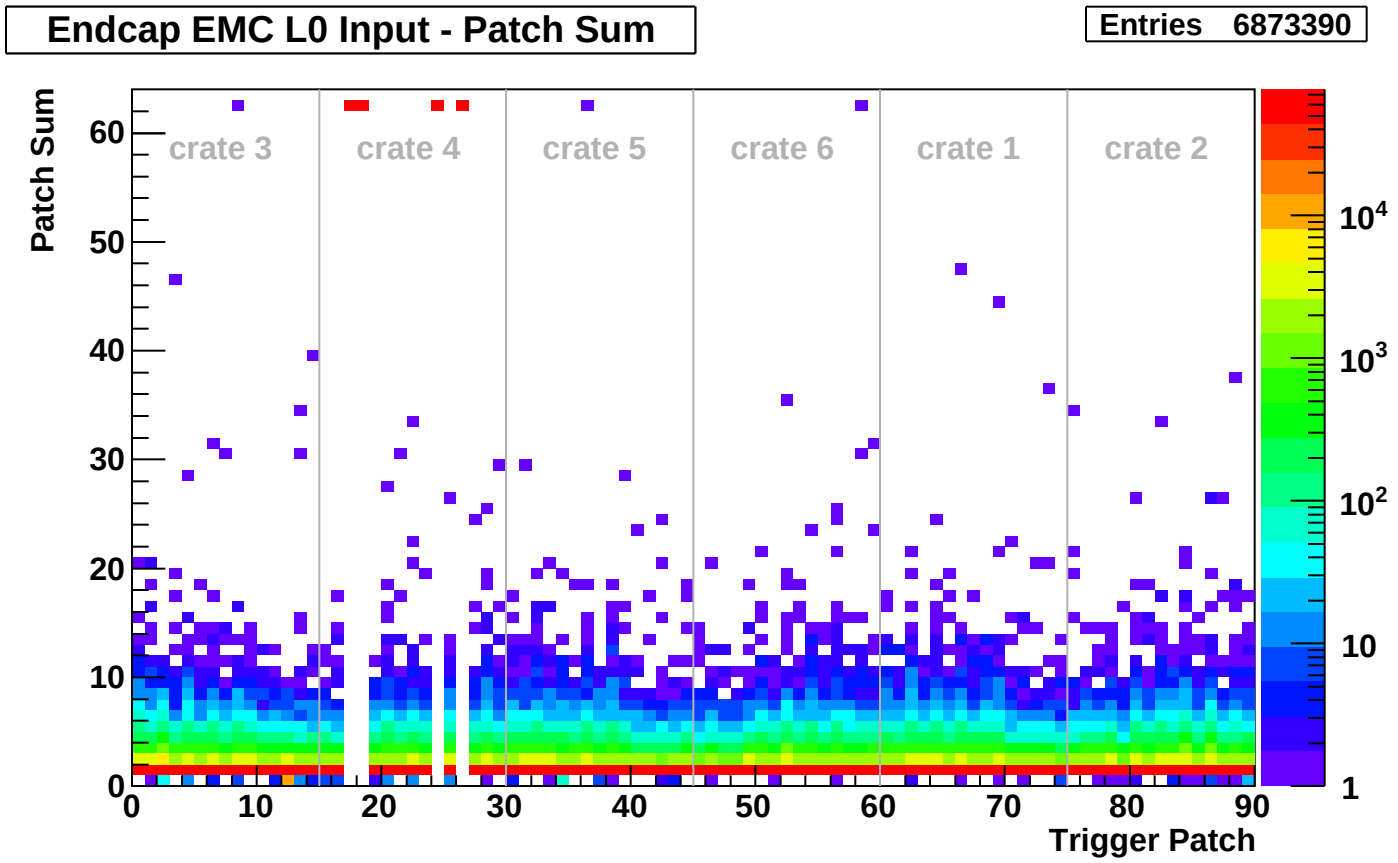

BunchId7Bit (BHT3)
Entries 968

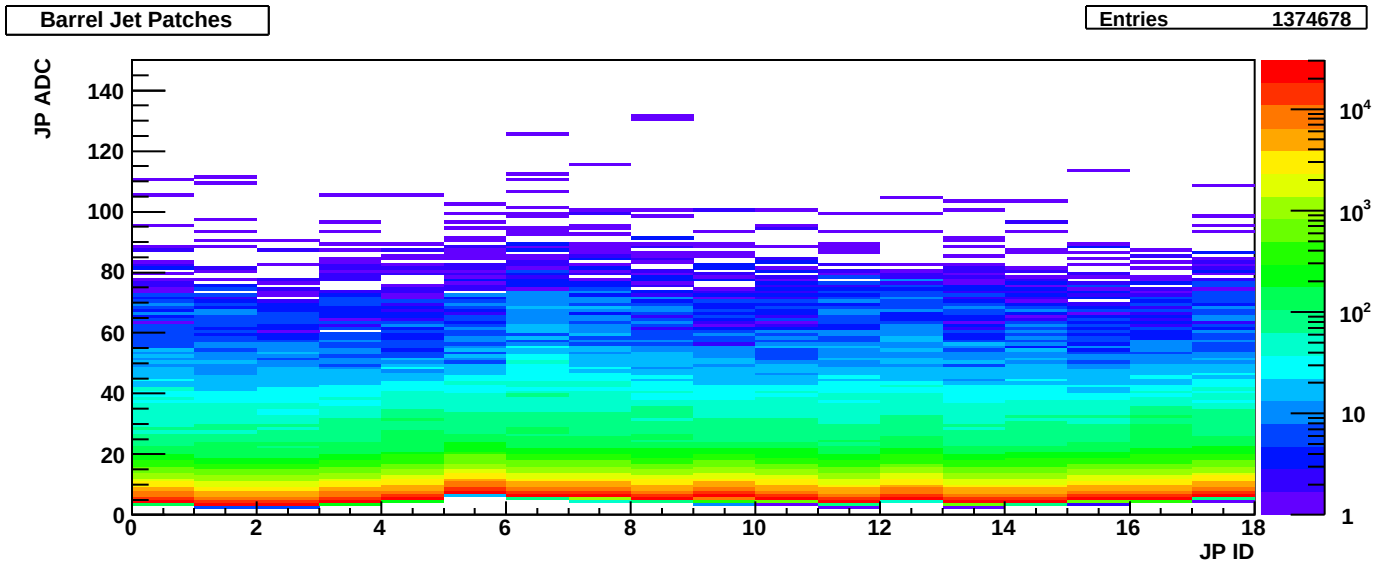

BunchId7Bit (all events)
Entries 76371

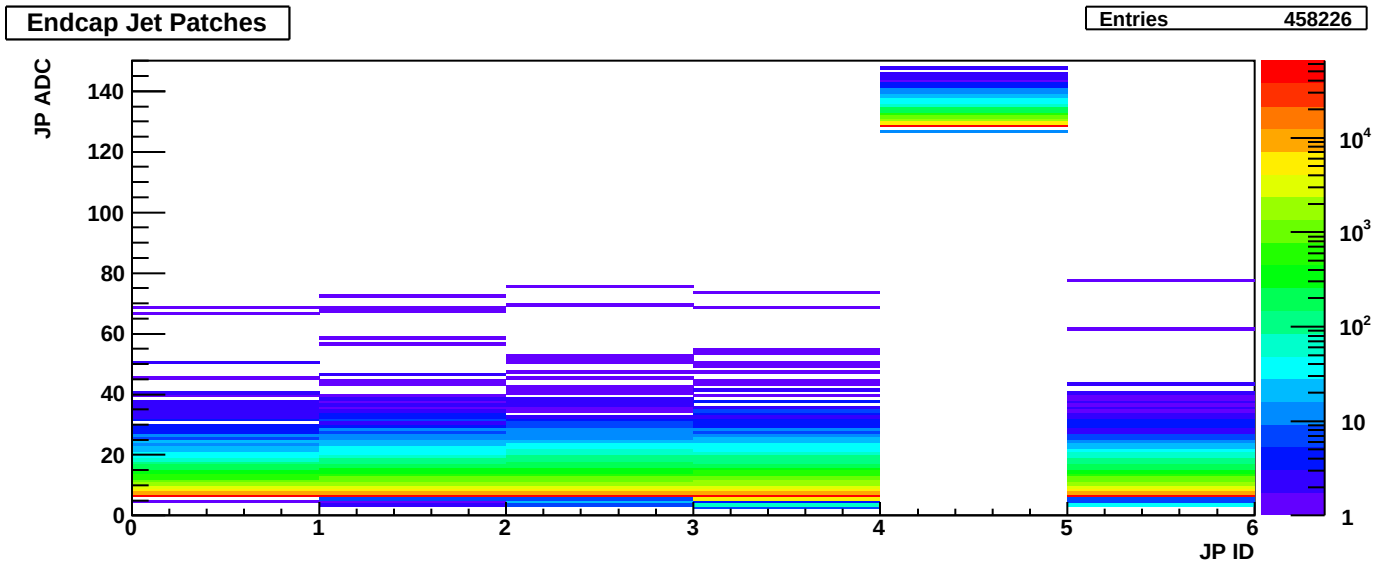

Barrel EMC L0 Input - High Tower
Entries 2.29113e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

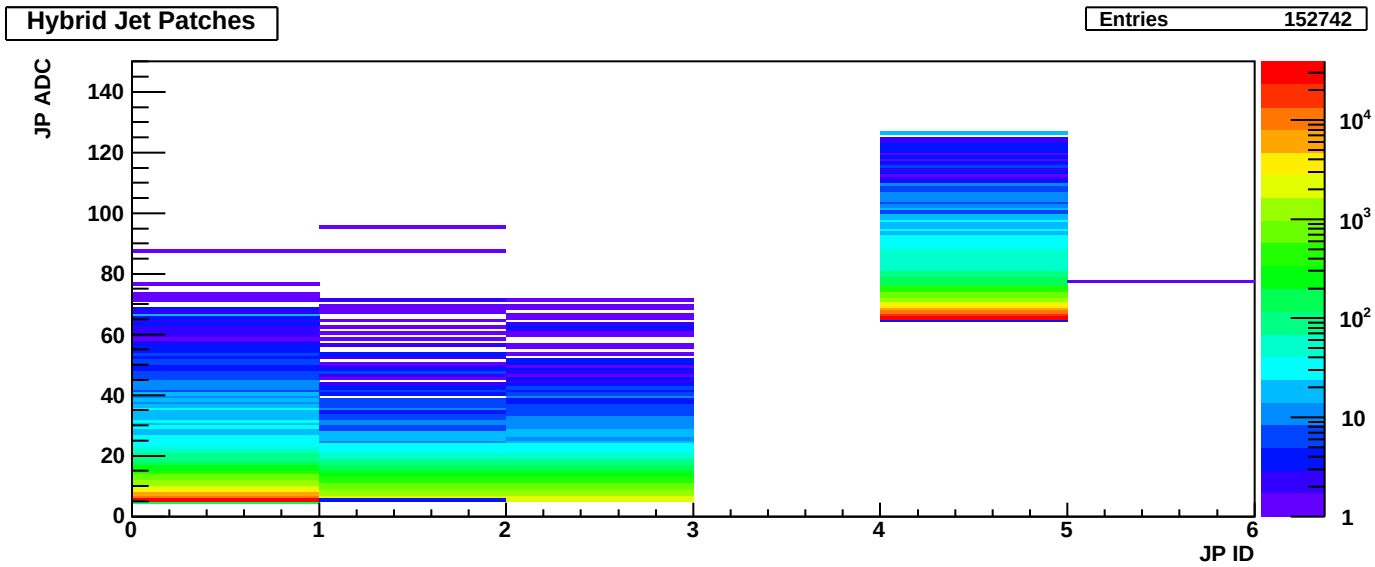

Barrel EMC L0 Input - Patch Sum
Entries 2.29113e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

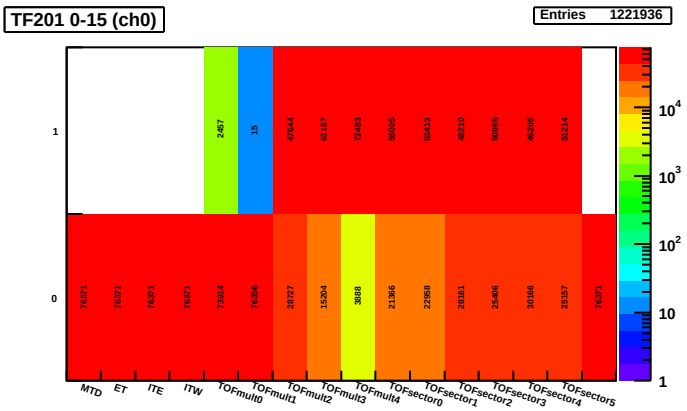

Endcap EMC L0 Input - High Tower
Entries 6873390
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

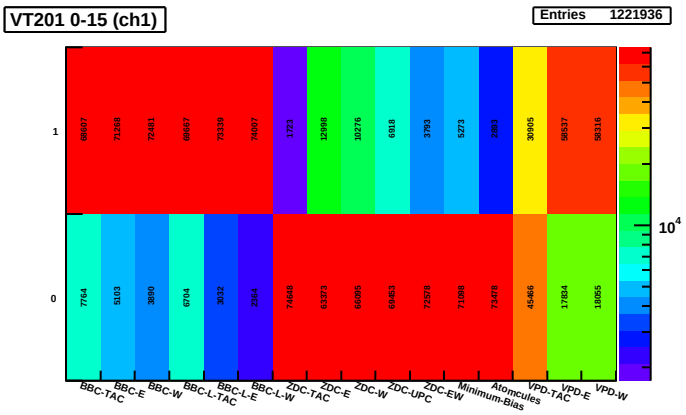

Endcap EMC L0 Input - Patch Sum
Entries 6873390
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

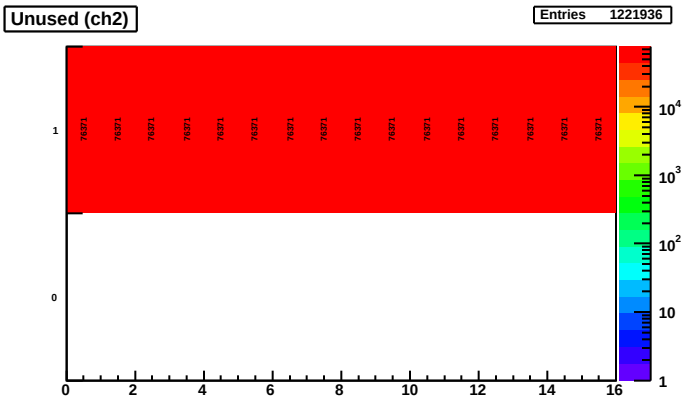

Barrel Jet Patches
Entries 1374678
JP ADC
JP ID

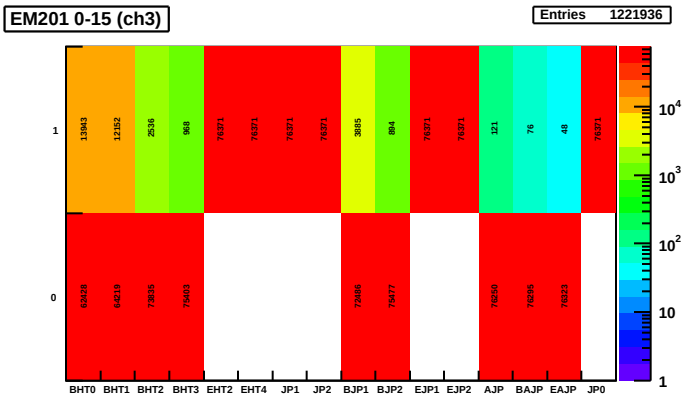

Endcap Jet Patches
Entries 458226
JP ADC
JP ID

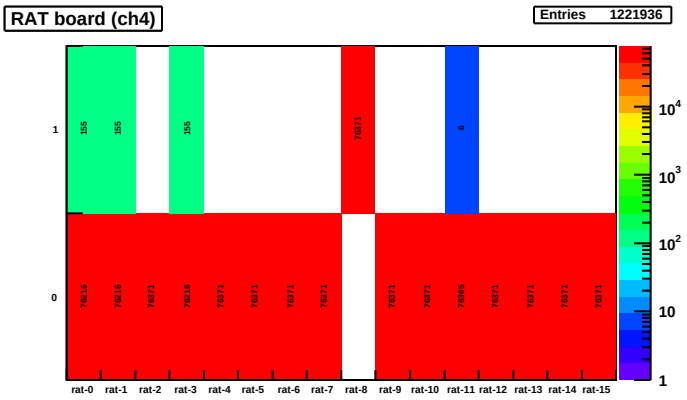

Hybrid Jet Patches
Entries 152742
JP ADC
JP ID

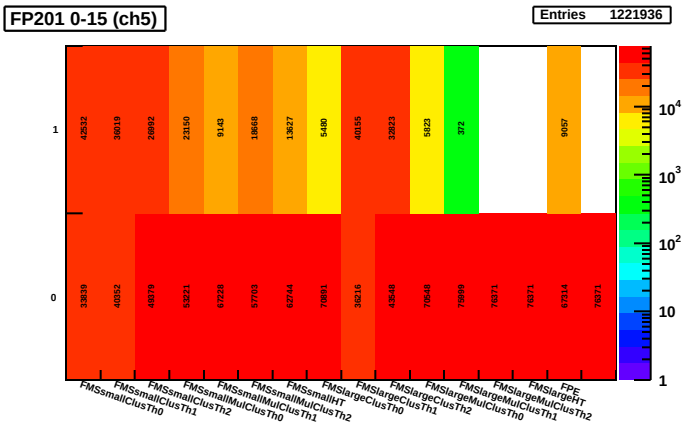

TF201 0-15 (ch0)
Entries 1221936
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

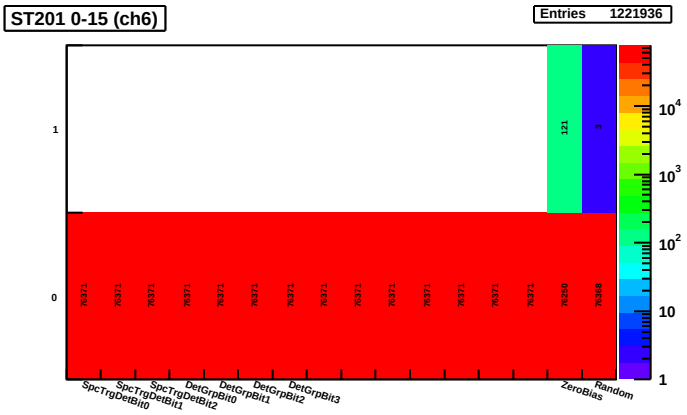

VT201 0-15 (ch1)
Entries 1221936
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

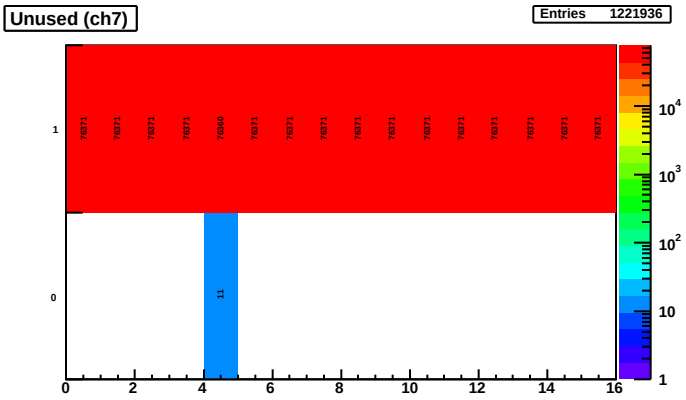

Unused (ch2)
Entries 1221936

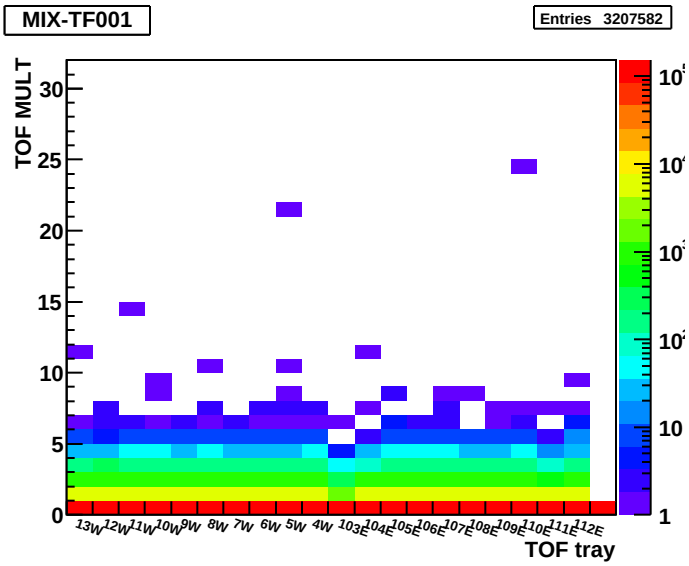

EM201 0-15 (ch3)
Entries 1221936
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

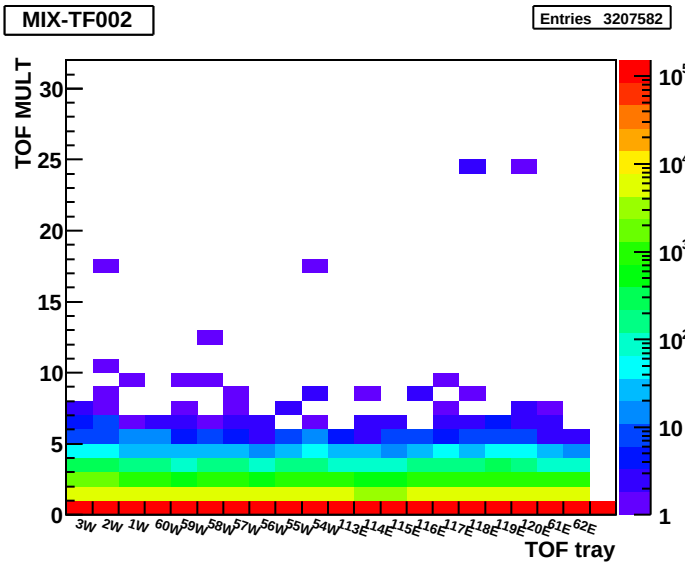

RAT board (ch4)
Entries 1221936
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

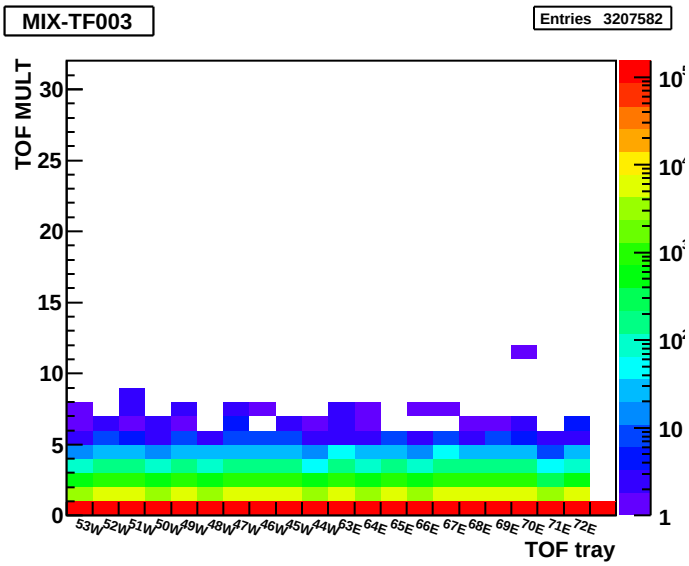

FP201 0-15 (ch5)
Entries 1221936
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

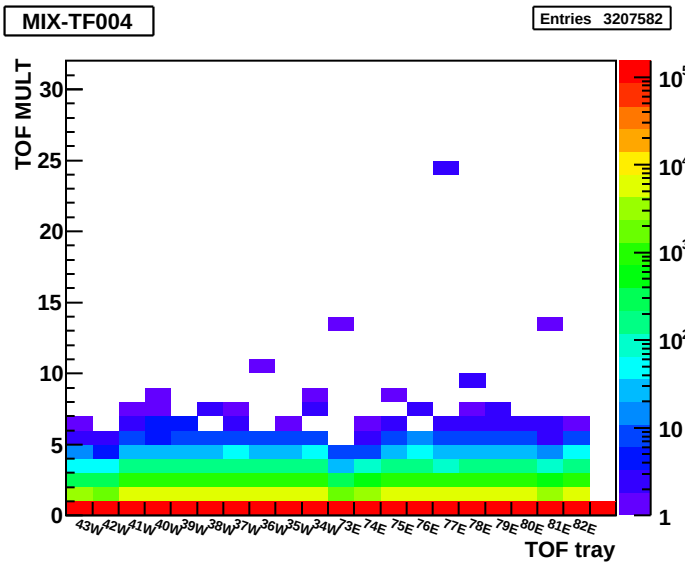

ST201 0-15 (ch6)
Entries 1221936
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

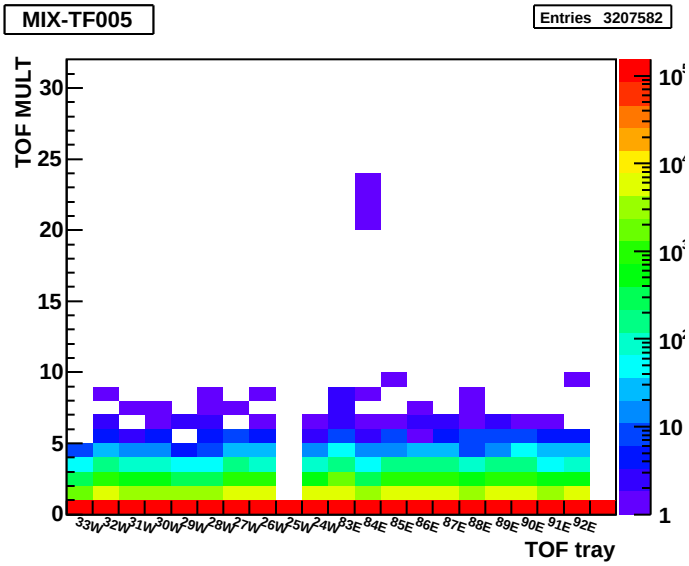

Unused (ch7)
Entries 1221936

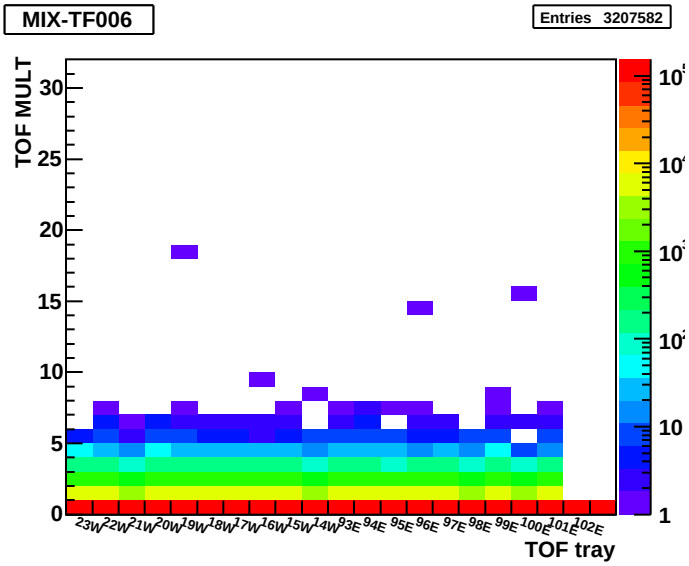

MIX-TF001
Entries 3207582
TOF MULT
TOF tray

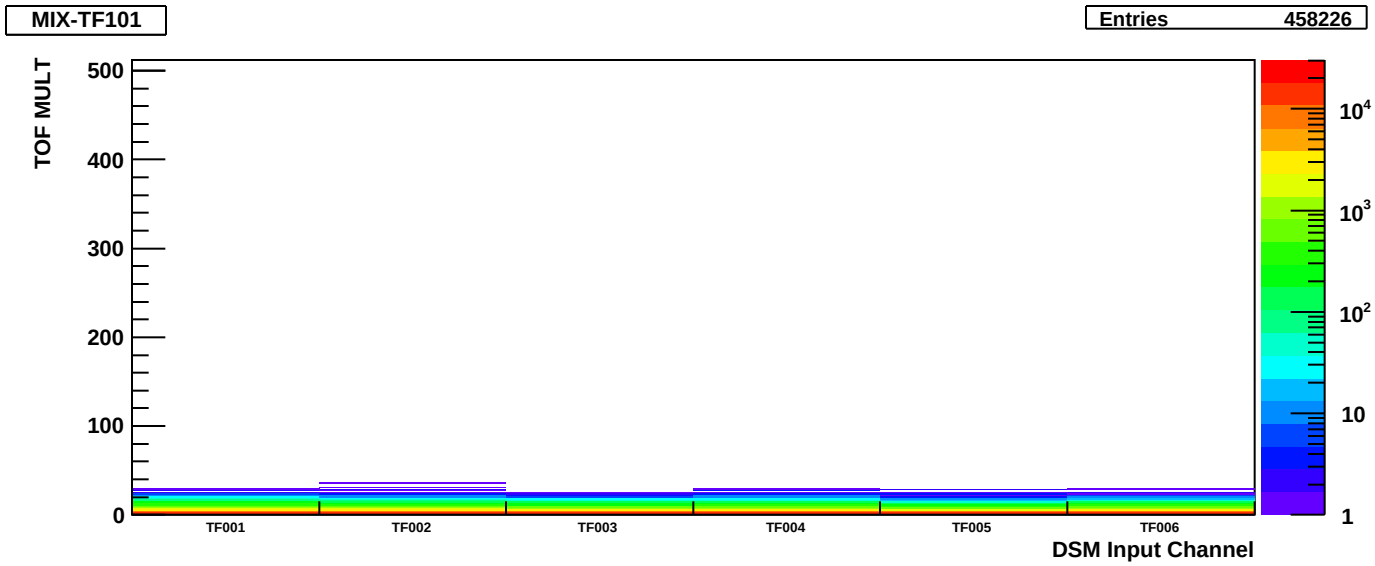

MIX-TF002
Entries 3207582
TOF MULT
TOF tray

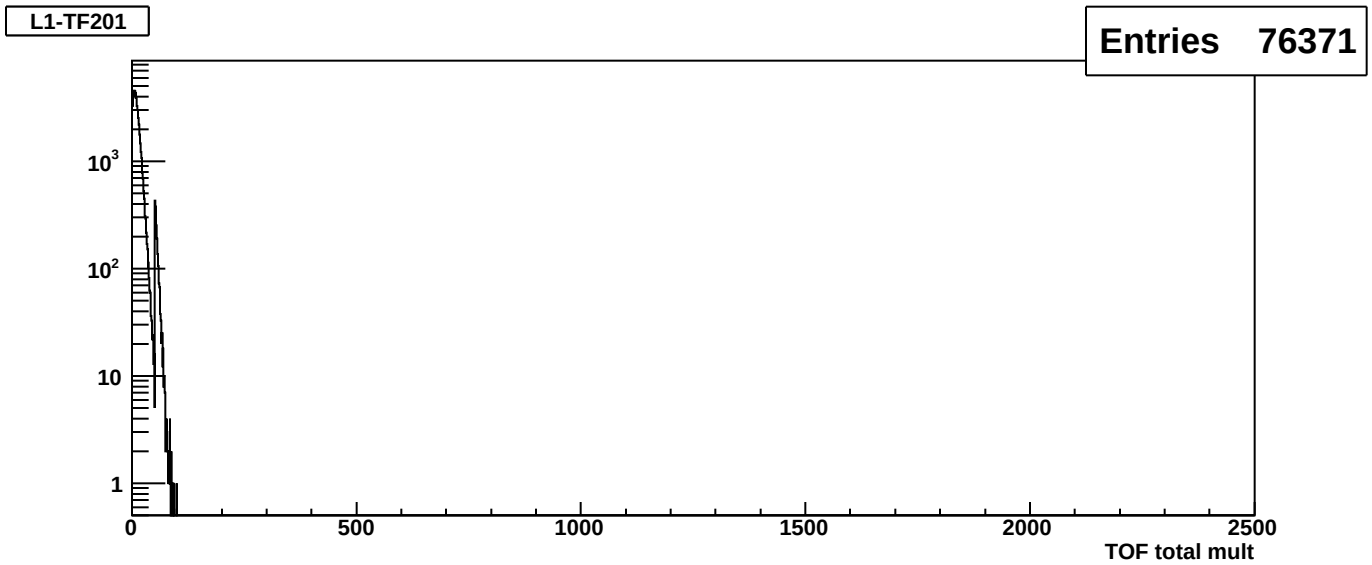

MIX-TF003
Entries 3207582
TOF MULT
TOF tray

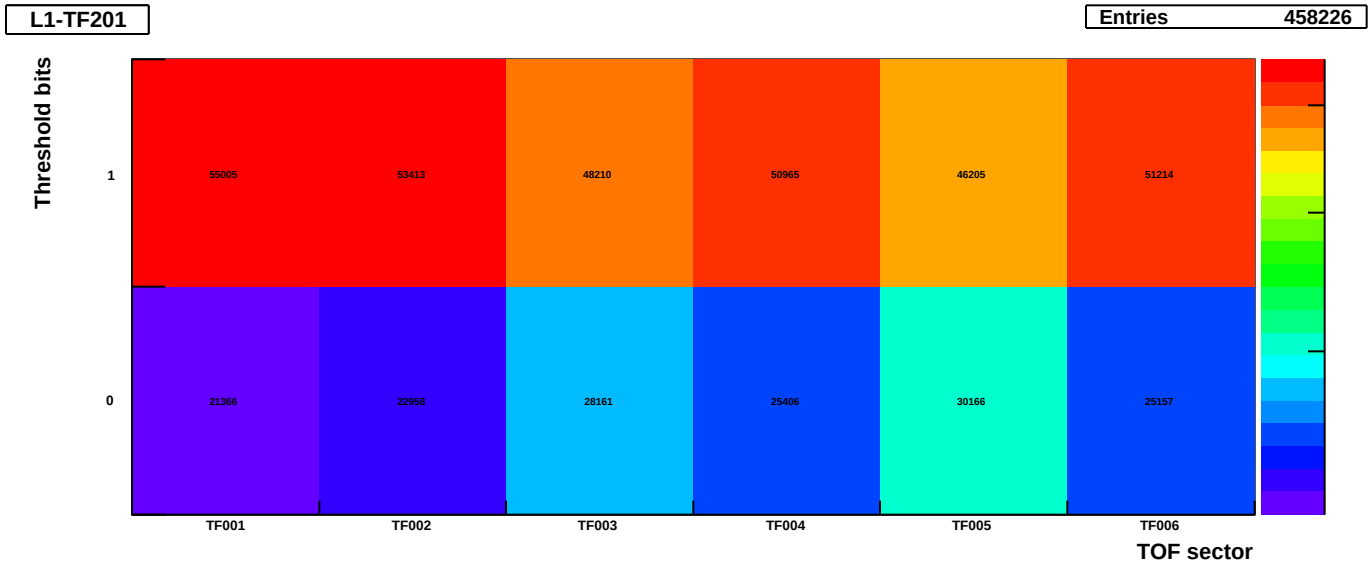

MIX-TF004
Entries 3207582
TOF MULT
TOF tray

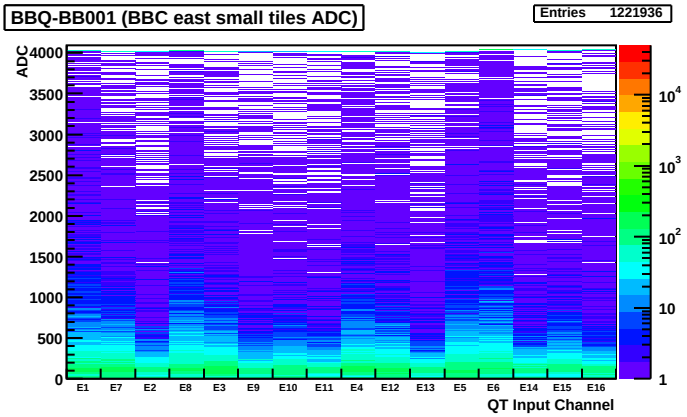

MIX-TF005
Entries 3207582
TOF MULT
TOF tray

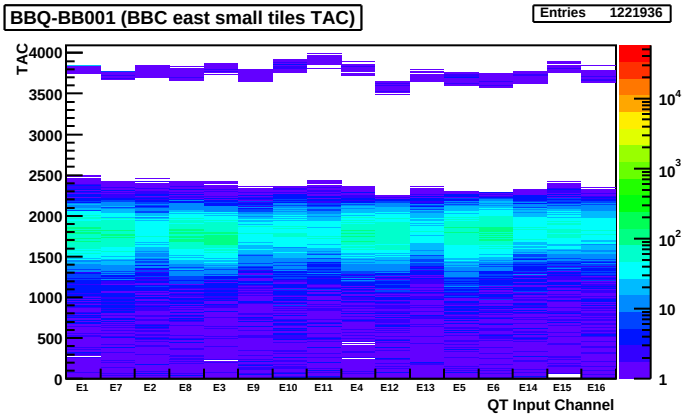

MIX-TF006
Entries 3207582
TOF MULT
TOF tray

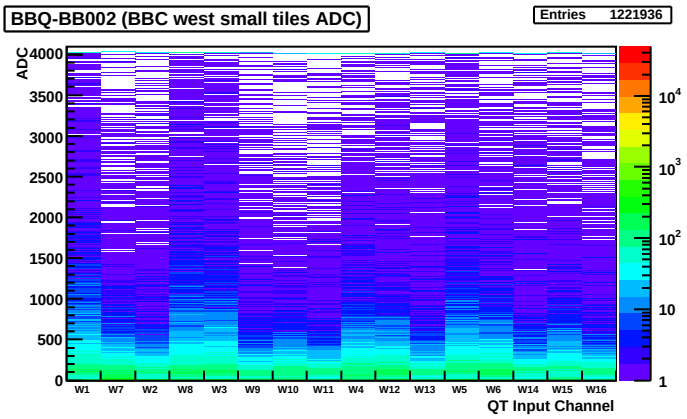

MIX-TF101
Entries 458226
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

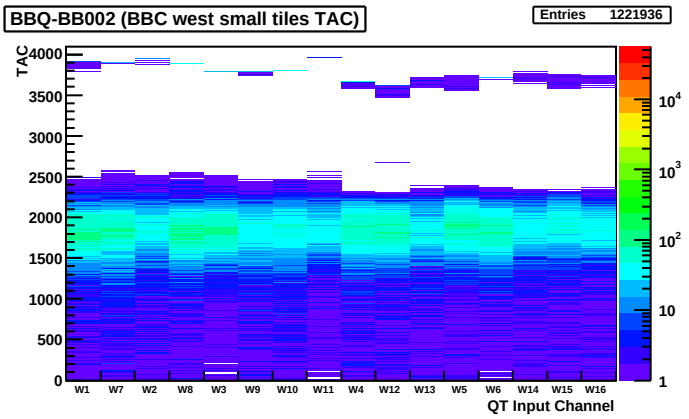

L1-TF201
Entries 76371
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

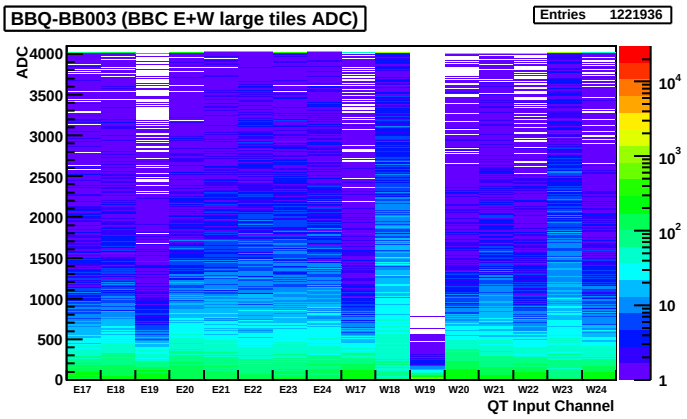

L1-TF201
Entries 458226
Threshold bits
1
0
55005
53413
48210
50965
46205
51214
21366
22958
28161
25406
30166
25157
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

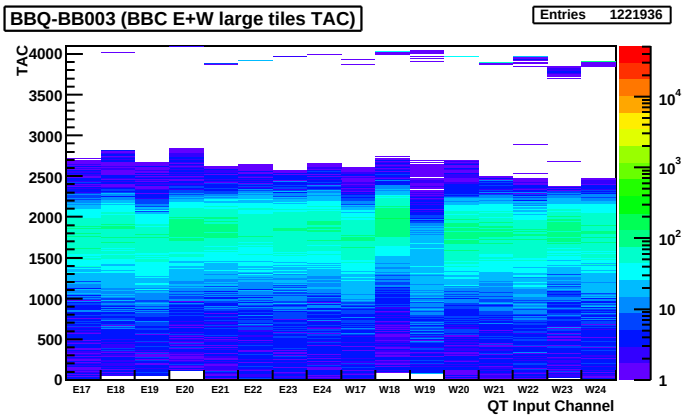

BBQ-BB001 (BBC east small tiles ADC)
Entries 1221936
ADC
QT Input Channel

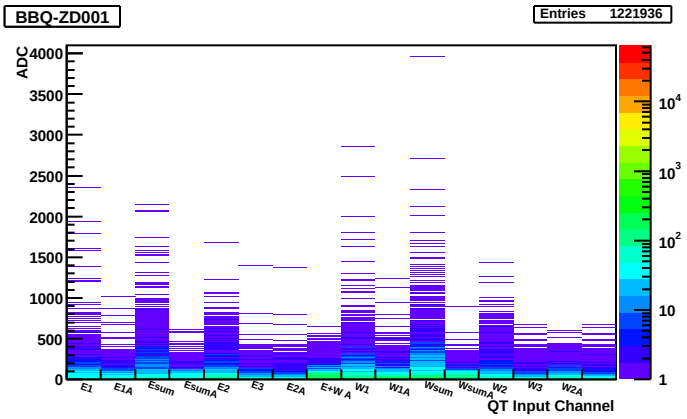

BBQ-BB001 (BBC east small tiles TAC)
Entries 1221936
TAC
QT Input Channel

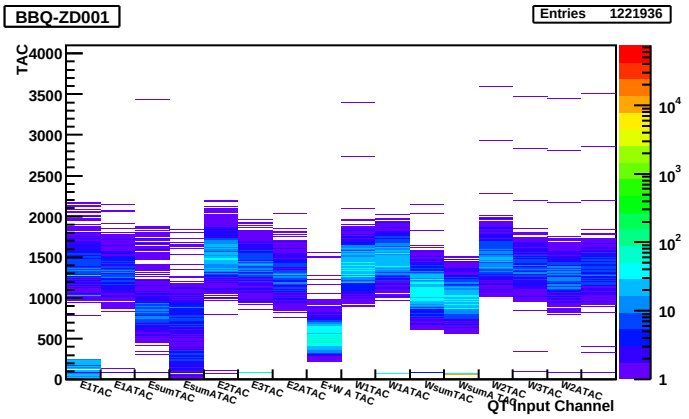

BBQ-BB002 (BBC west small tiles ADC)
Entries 1221936
ADC
QT Input Channel

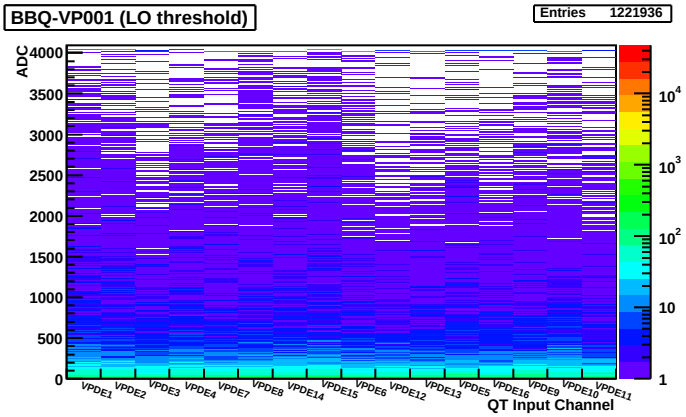

BBQ-BB002 (BBC west small tiles TAC)
Entries 1221936
TAC
QT Input Channel

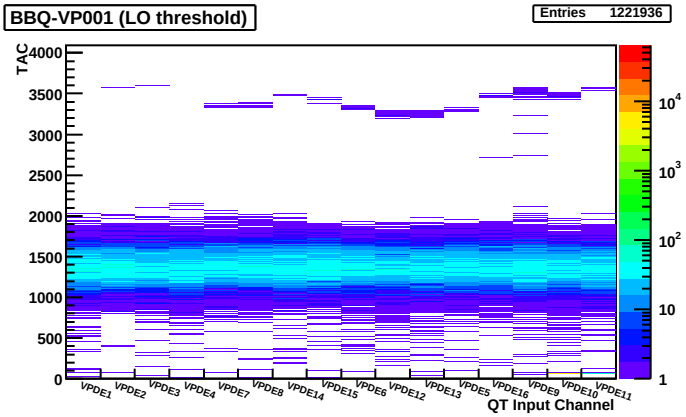

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1221936
ADC
QT Input Channel

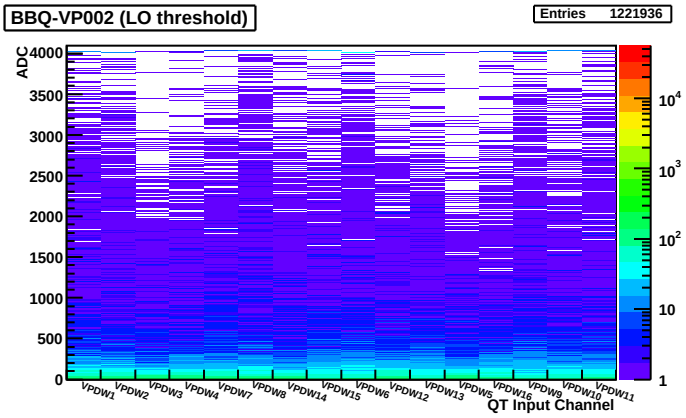

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1221936
TAC
QT Input Channel

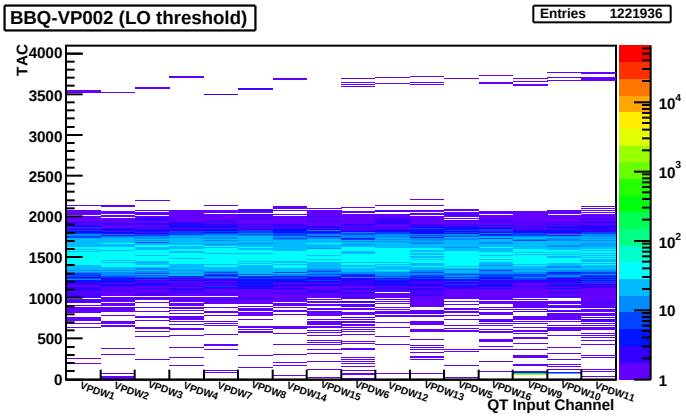

BBQ-ZD001
Entries 1221936
ADC
QT Input Channel

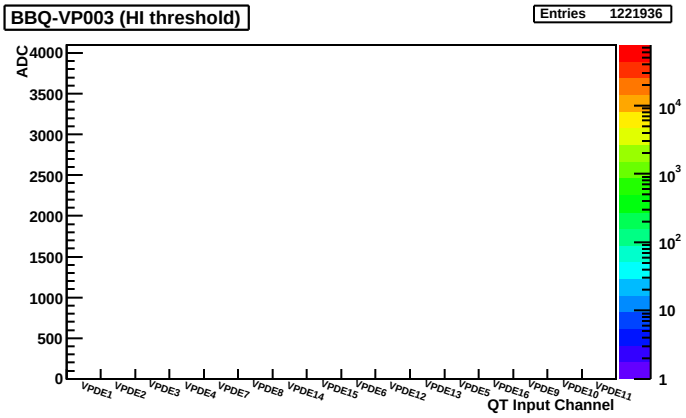

BBQ-ZD001
Entries 1221936
TAC
QT Input Channel

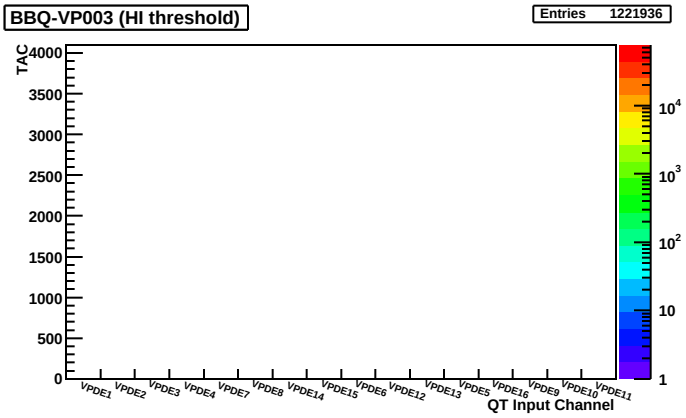

BBQ-VP001 (LO threshold)
Entries 1221936
ADC
QT Input Channel

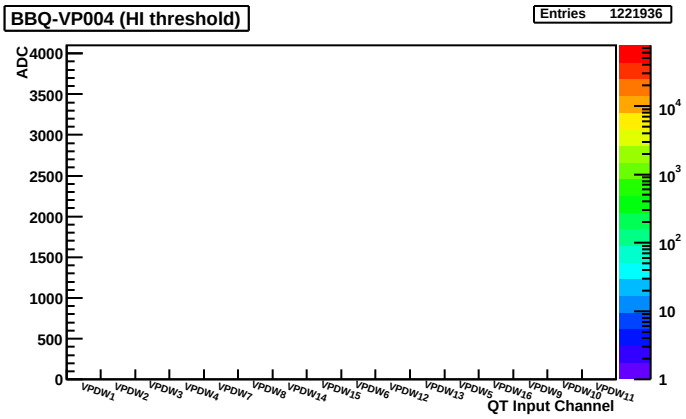

BBQ-VP001 (LO threshold)
Entries 1221936
TAC
QT Input Channel

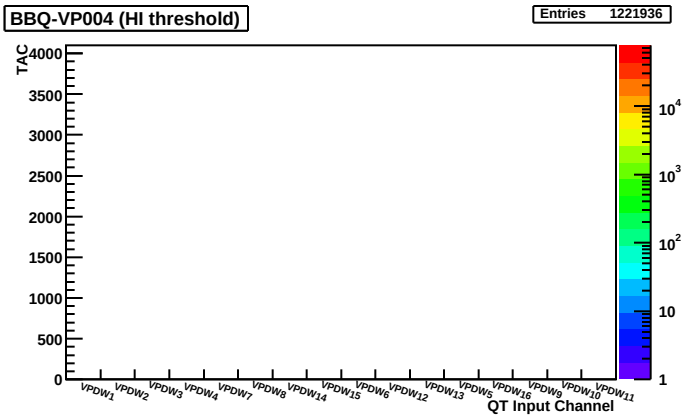

BBQ-VP002 (LO threshold)
Entries 1221936
ADC
QT Input Channel

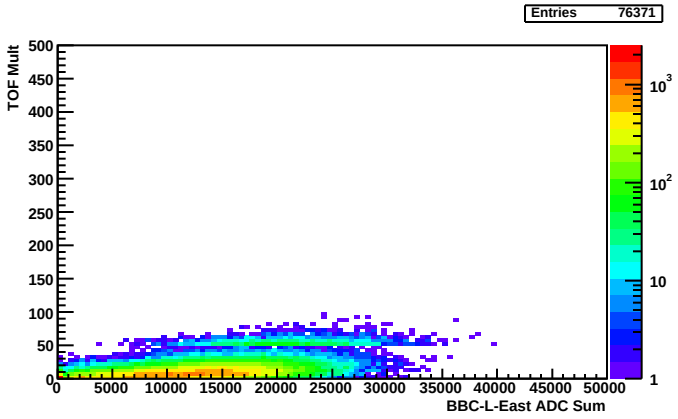

BBQ-VP002 (LO threshold)
Entries 1221936
TAC
QT Input Channel

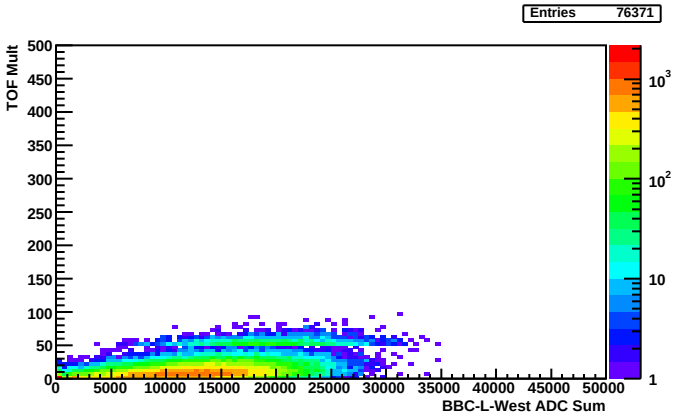

BBQ-VP003 (HI threshold)
Entries 1221936
ADC
QT Input Channel

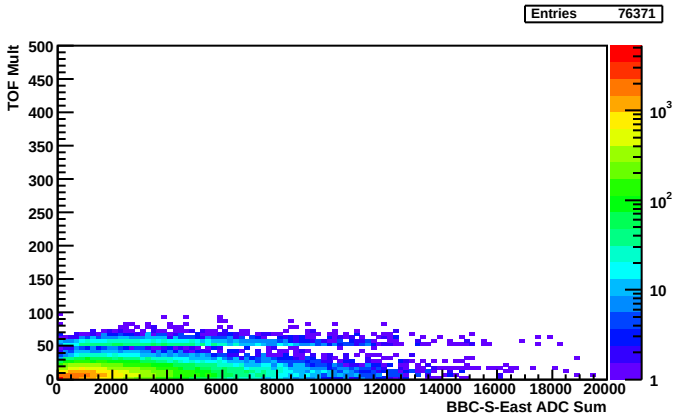

BBQ-VP003 (HI threshold)
Entries 1221936
TAC
QT Input Channel

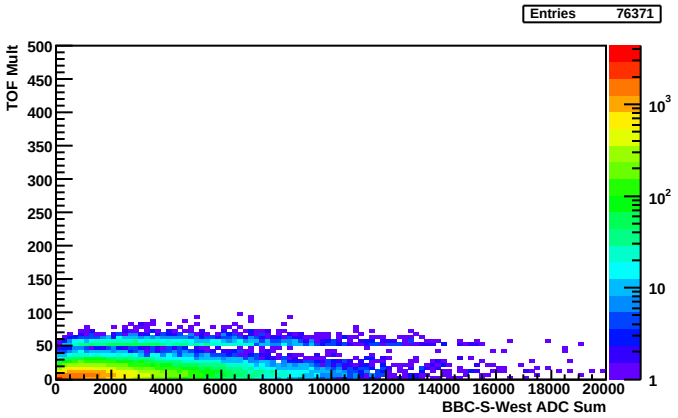

BBQ-VP004 (HI threshold)
Entries 1221936
ADC
QT Input Channel

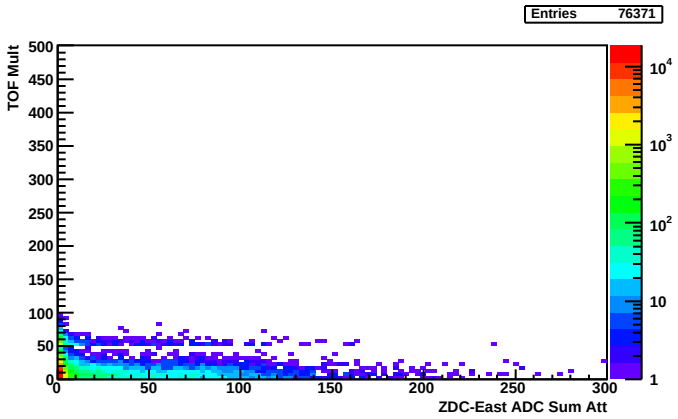

BBQ-VP004 (HI threshold)
Entries 1221936
TAC
QT Input Channel

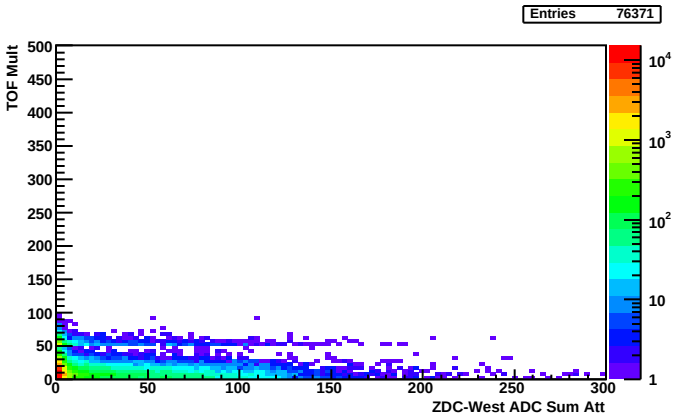

Entries 76371
TOF Mult
BBC-L-East ADC Sum

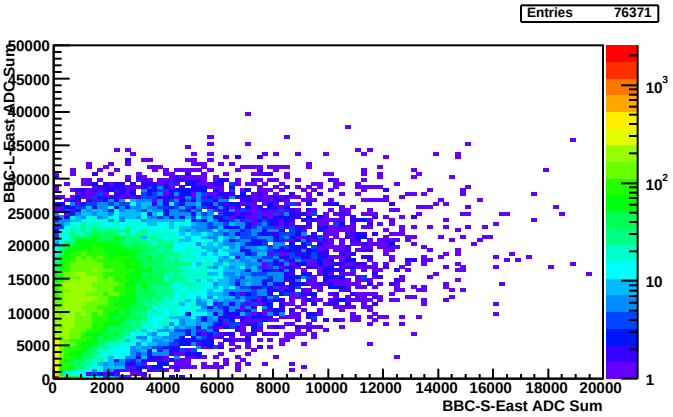

Entries 76371
TOF Mult
BBC-L-West ADC Sum

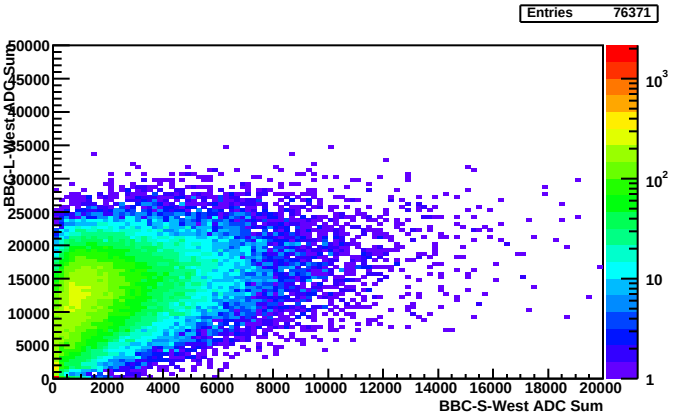

Entries 76371
TOF Mult
BBC-S-East ADC Sum

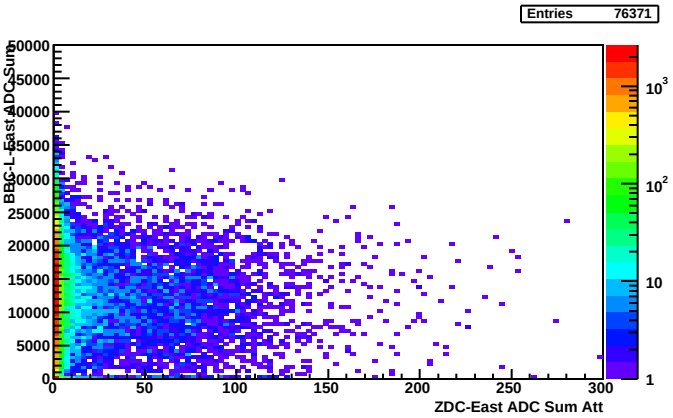

Entries 76371
TOF Mult
BBC-S-West ADC Sum

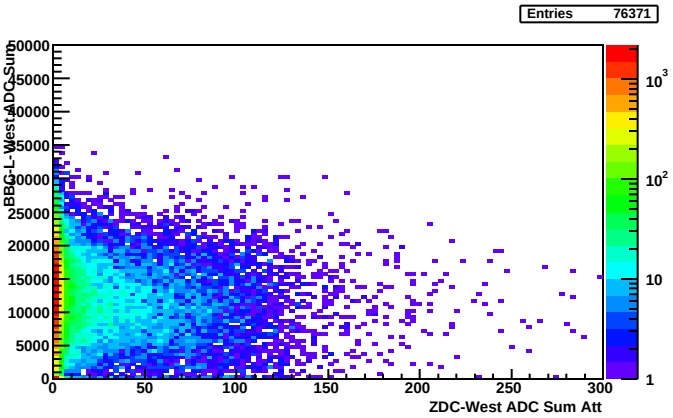

Entries 76371
TOF Mult
ZDC-East ADC Sum Att

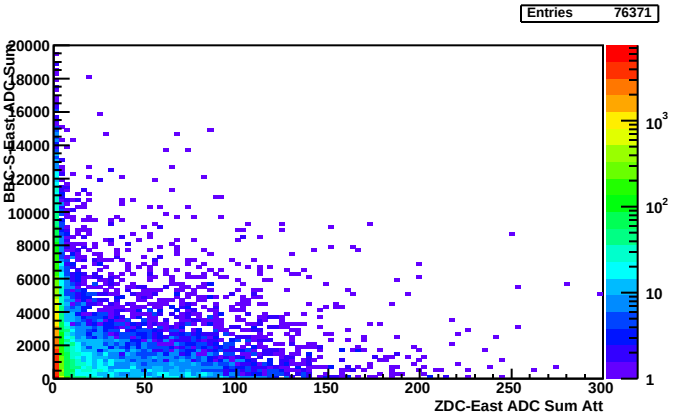

Entries 76371
TOF Mult
ZDC-West ADC Sum Att

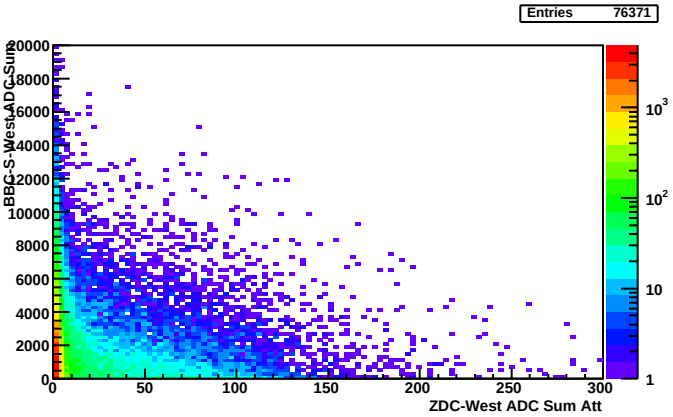

Entries 76371
BBC-L-East ADC Sum
BBC-S-East ADC Sum

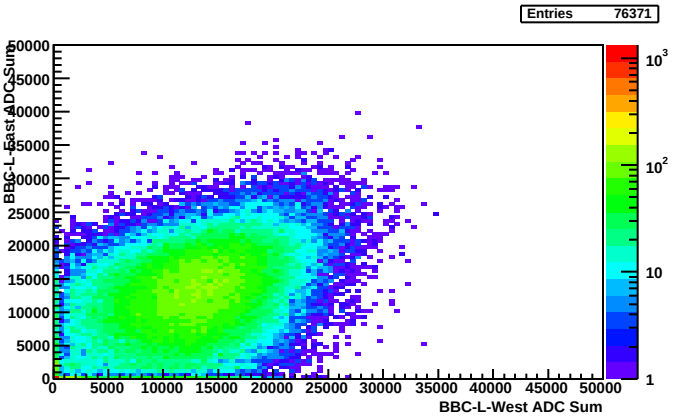

Entries 76371
BBC-L-West ADC Sum
BBC-S-West ADC Sum

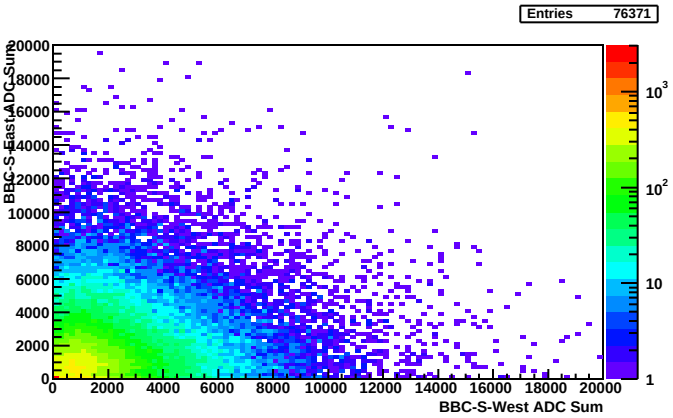

Entries 76371
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

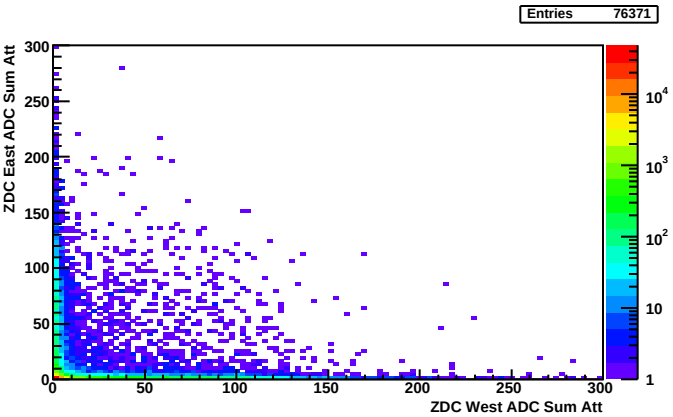

Entries 76371
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

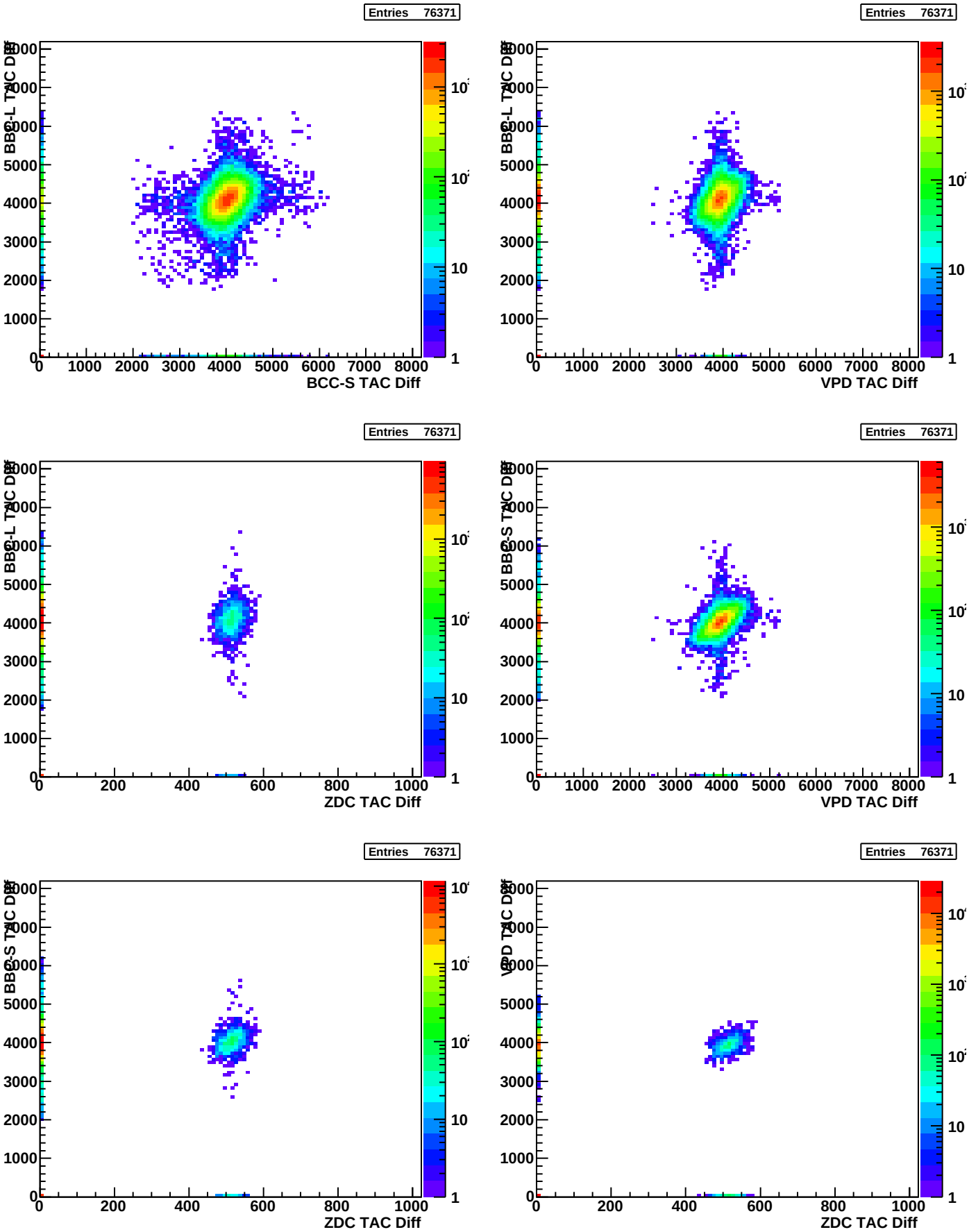

Entries 76371
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

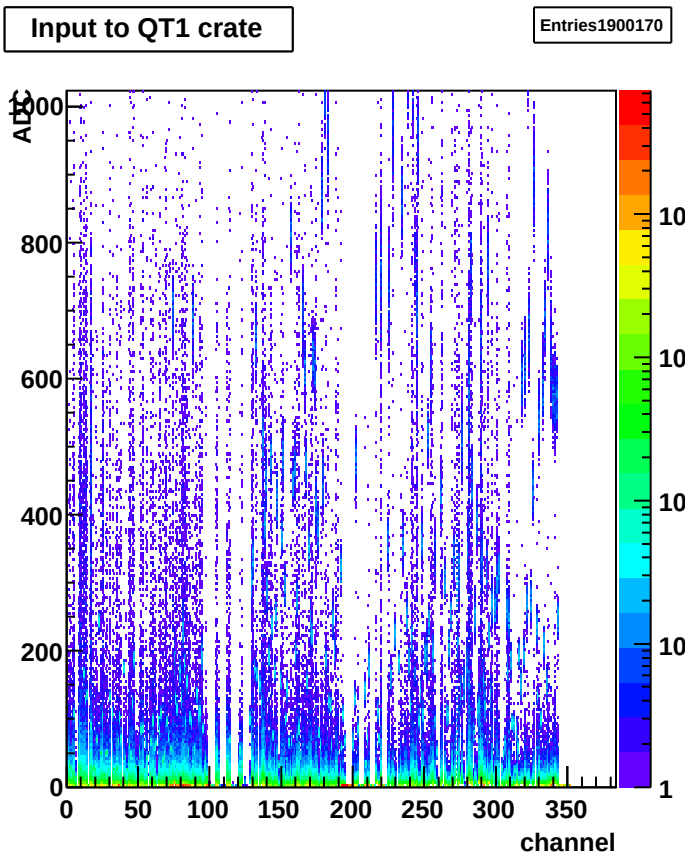

Entries 76371
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

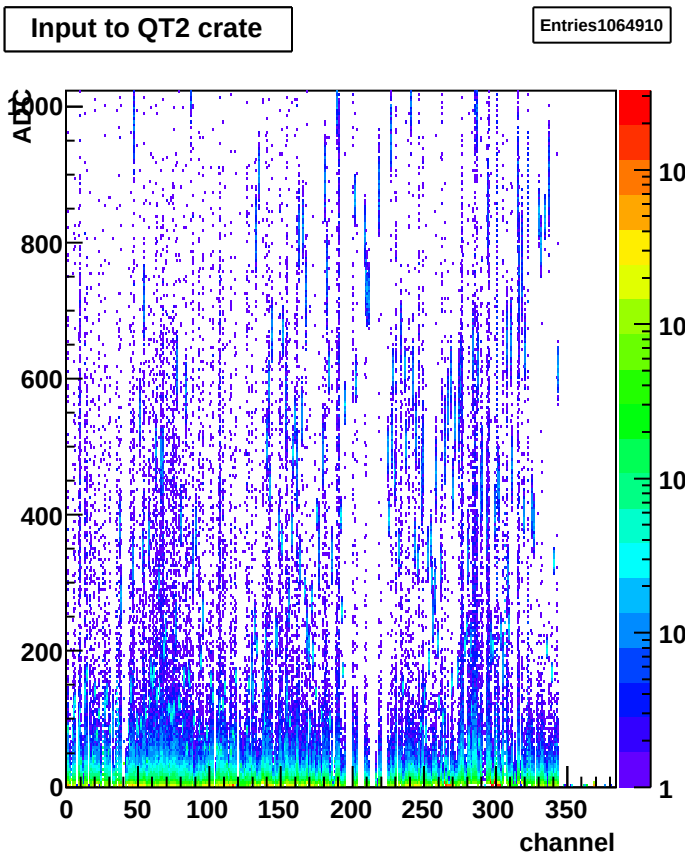

Entries 76371
BBC-L-East ADC Sum
BBC-L-West ADC Sum

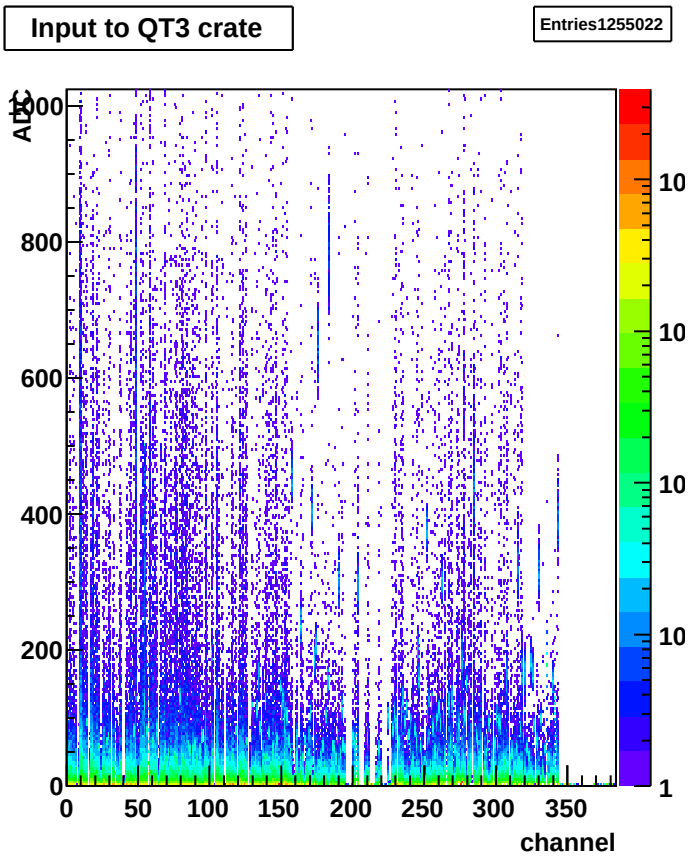

Entries 76371
BBC-S-East ADC Sum
BBC-S-West ADC Sum

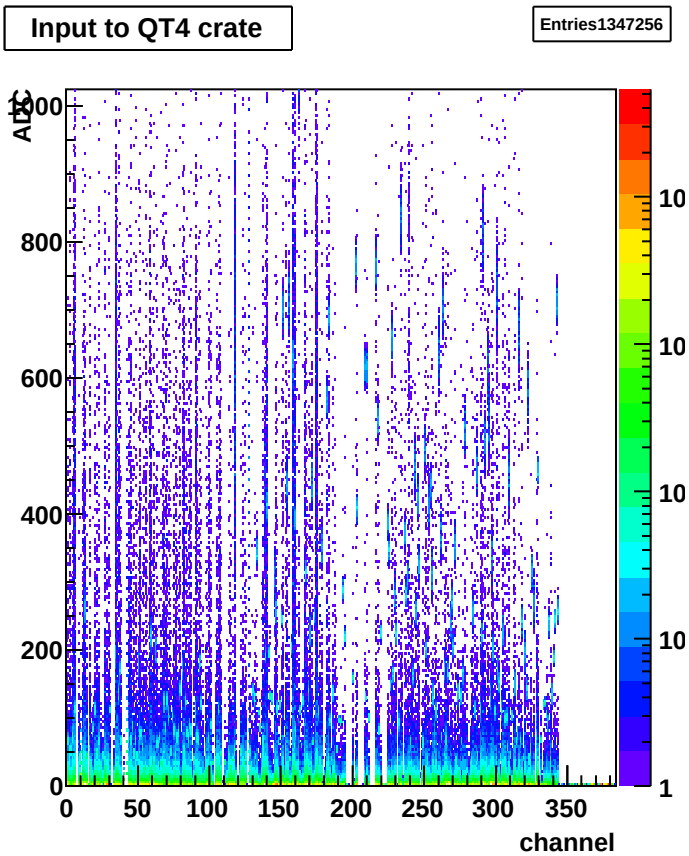

Entries 76371
ZDC East ADC Sum Att
ZDC West ADC Sum Att

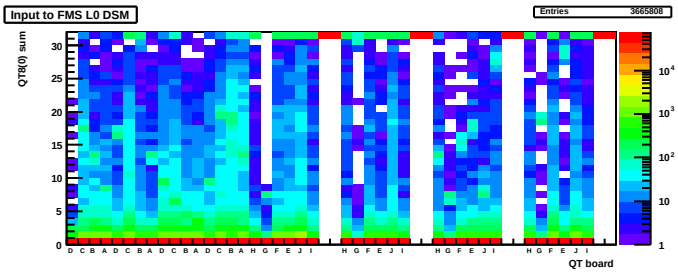

Entries 76371
BBC-L TAC Diff
BCC-S TAC Diff
Entries 76371
BBC-L TAC Diff
VPD TAC Diff
Entries 76371
BBC-L TAC Diff
ZDC TAC Diff
Entries 76371
BBC-S TAC Diff
VPD TAC Diff
Entries 76371
BBC-S TAC Diff
ZDC TAC Diff
Entries 76371
VPD TAC Diff
ZDC TAC Diff

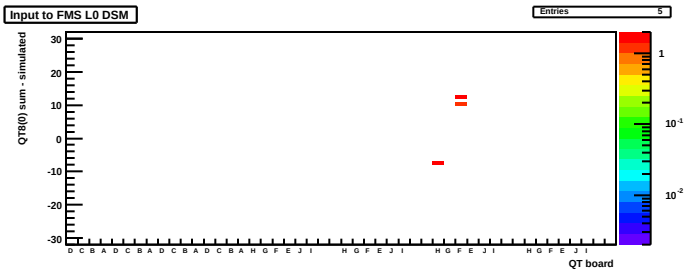

Input to QT1 crate
Entries1900170
ADC
channel

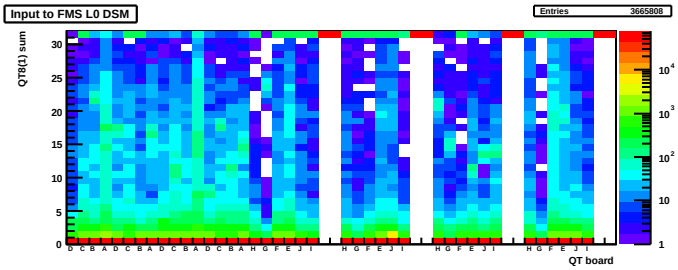

Input to QT2 crate
Entries1064910
ADC
channel

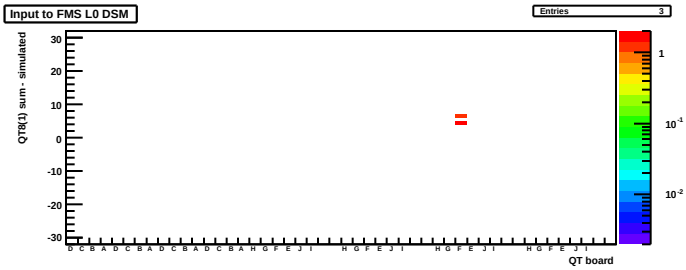

Input to QT3 crate
Entries1255022
ADC
channel

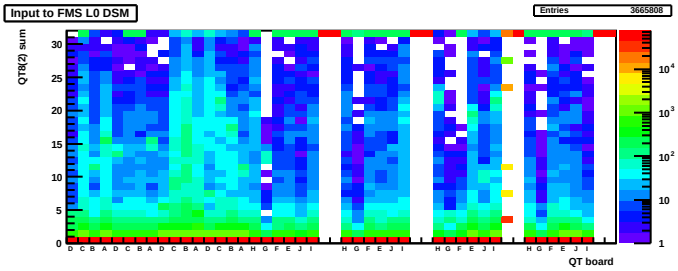

Input to QT4 crate
Entries1347256
ADC
channel

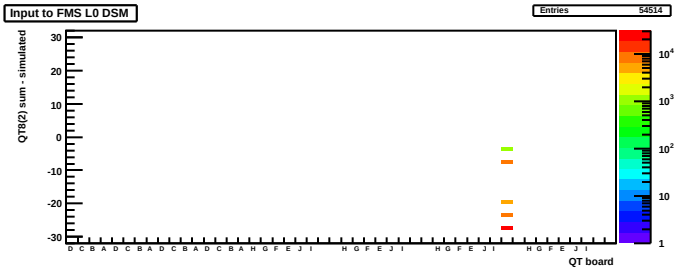

Input to FMS L0 DSM
Entries 3665808
QT8(0) sum
QT board

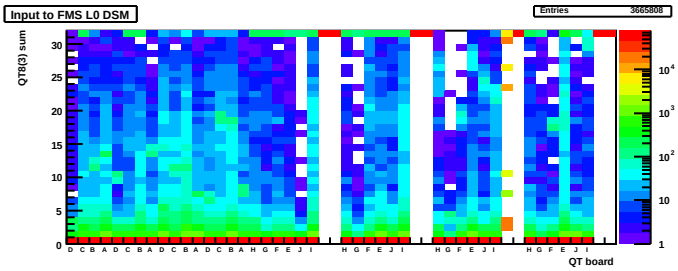

Input to FMS L0 DSM
Entries 5
QT8(0) sum - simulated
QT board

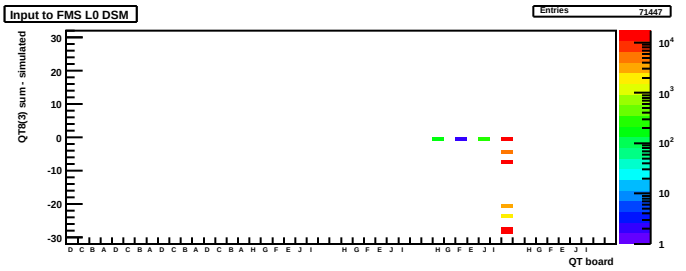

Input to FMS L0 DSM
Entries 3665808
QT8(1) sum
QT board

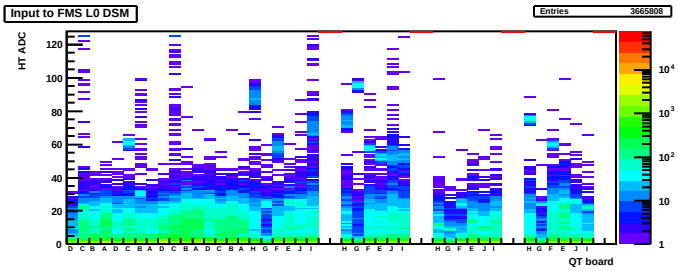

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

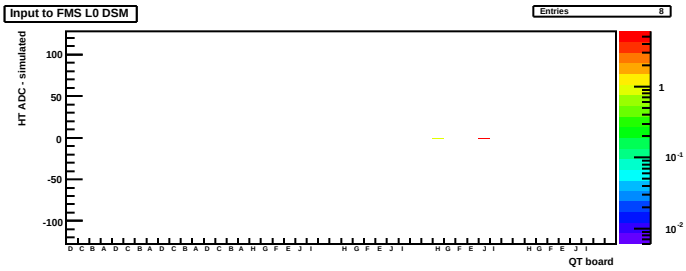

Input to FMS L0 DSM
Entries 3665808
QT8(2) sum
QT board

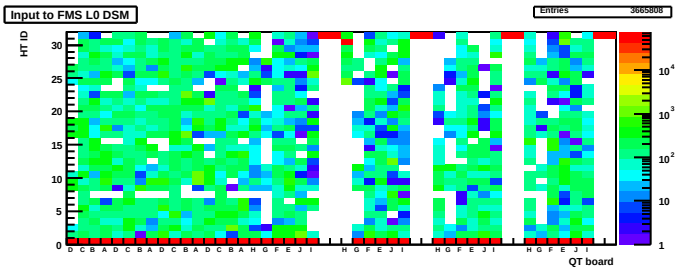

Input to FMS L0 DSM
Entries 54514
QT8(2) sum - simulated
QT board

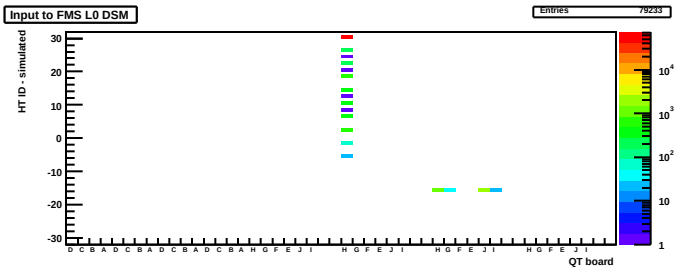

Input to FMS L0 DSM
Entries 3665808
QT8(3) sum
QT board

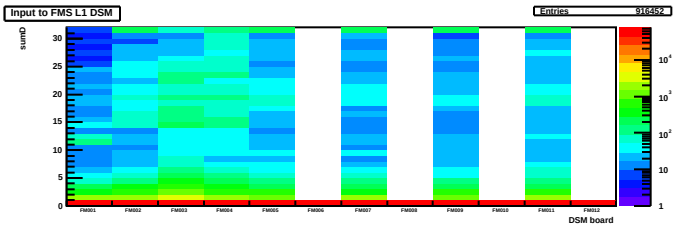

Input to FMS L0 DSM
Entries 71447
QT8(3) sum - simulated
QT board

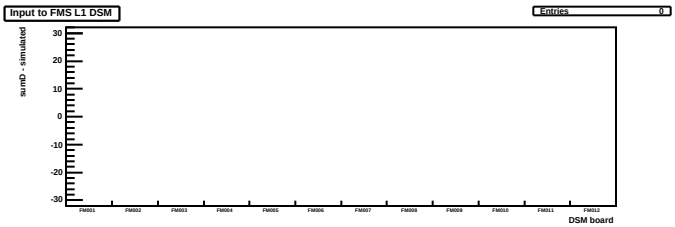

Input to FMS L0 DSM
Entries 3665808
HT ADC
QT board

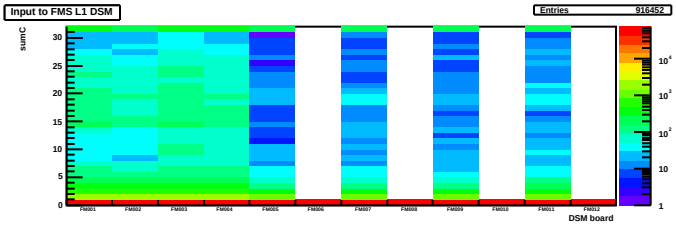

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

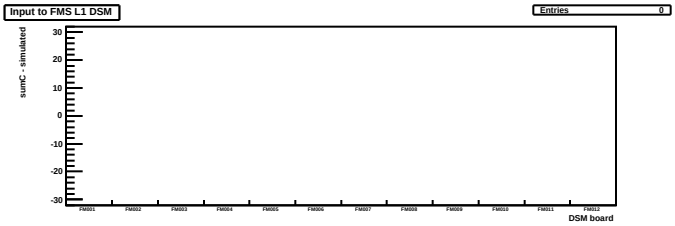

Input to FMS L0 DSM
Entries 3665808
HT ID
QT board

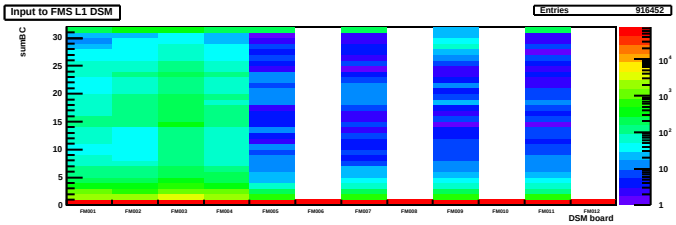

Input to FMS L0 DSM
Entries 79233
HT ID - simulated
QT board

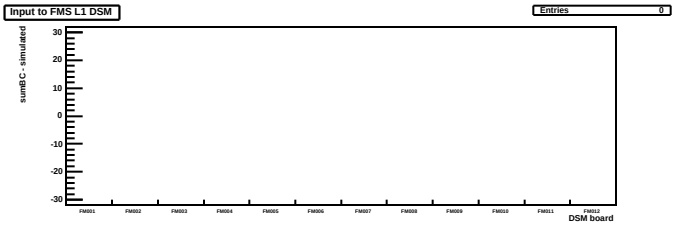

Input to FMS L1 DSM
Entries 916452
sumD
DSM board

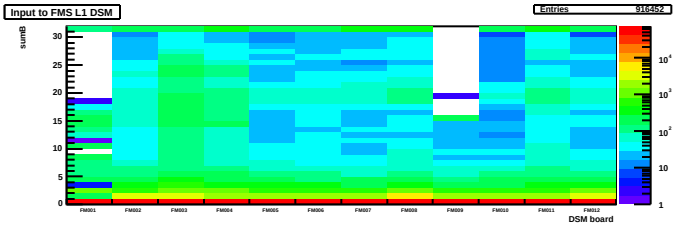

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

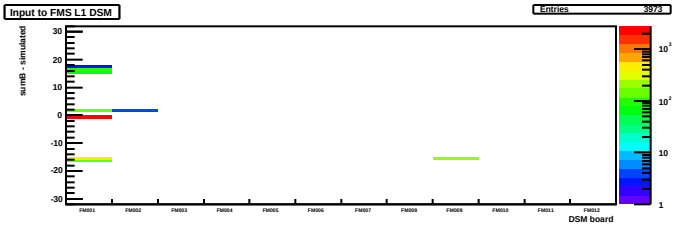

Input to FMS L1 DSM
Entries 916452
sumC
DSM board

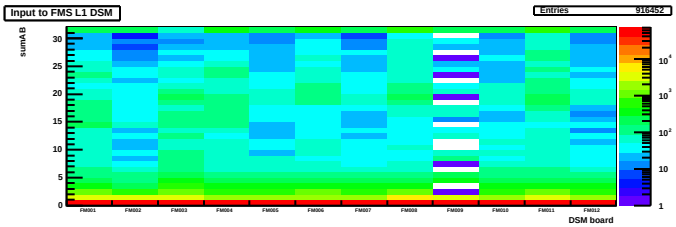

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

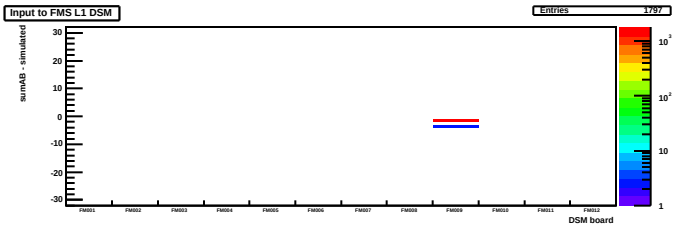

Input to FMS L1 DSM
Entries 916452
sumBC
DSM board

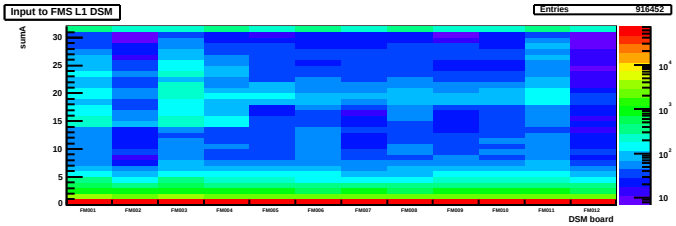

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

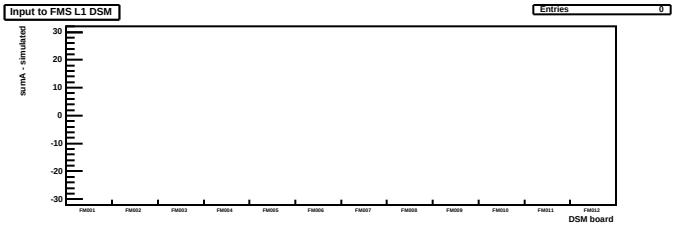

Input to FMS L1 DSM
Entries 916452
sumB
DSM board

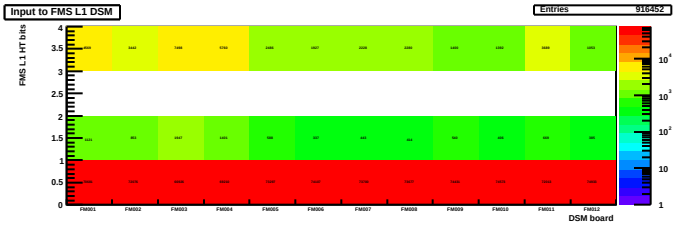

Input to FMS L1 DSM
Entries 3973
sumB - simulated
DSM board

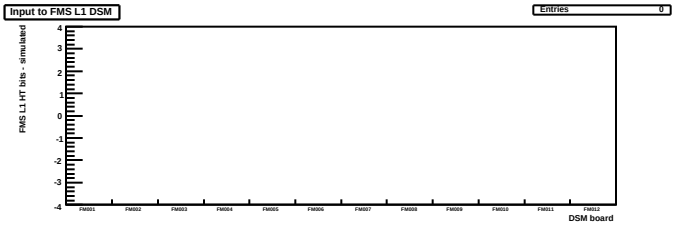

Input to FMS L1 DSM
Entries 916452
sumAB
DSM board

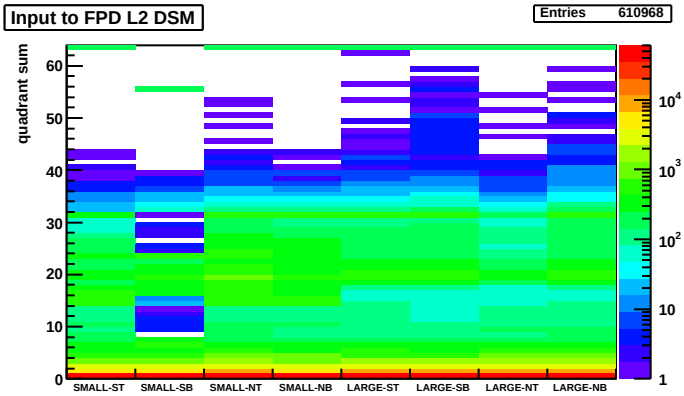

Input to FMS L1 DSM
Entries 1797
sumAB - simulated
DSM board

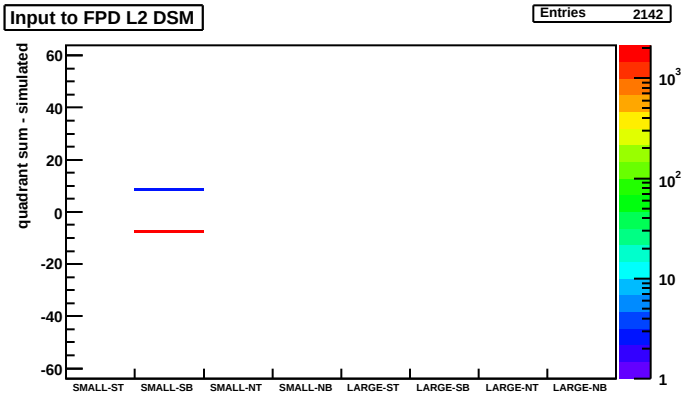

Input to FMS L1 DSM
Entries 916452
sumA
DSM board

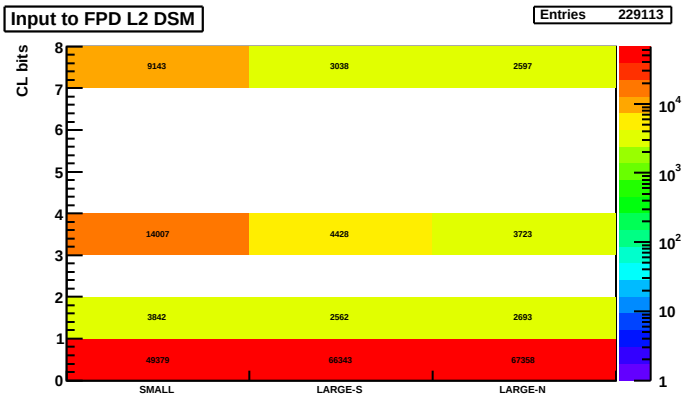

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

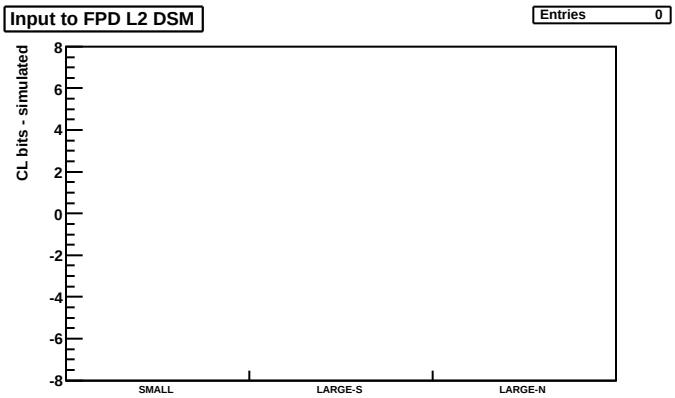

Input to FMS L1 DSM
Entries 916452
FMS L1 HT bits
DSM board

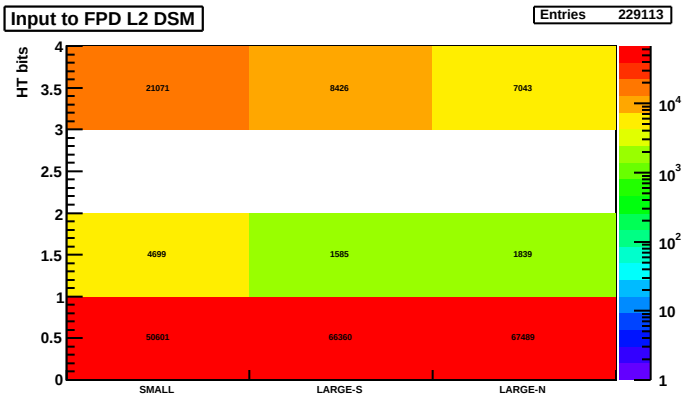

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

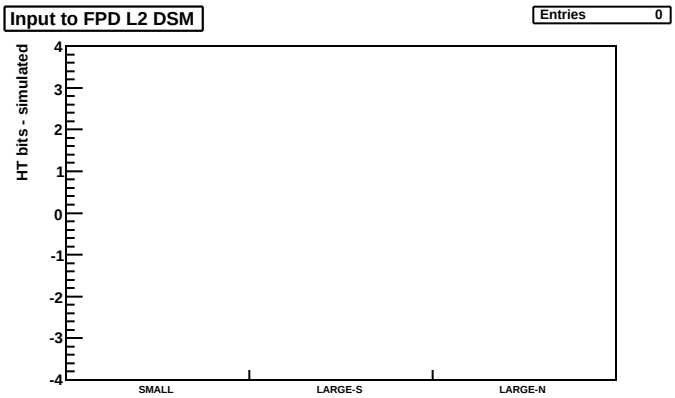

Input to FPD L2 DSM
Entries 610968
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

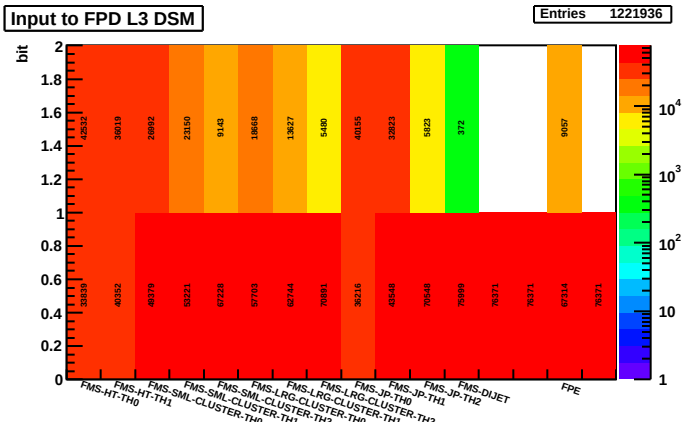

Input to FPD L2 DSM
Entries 2142
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

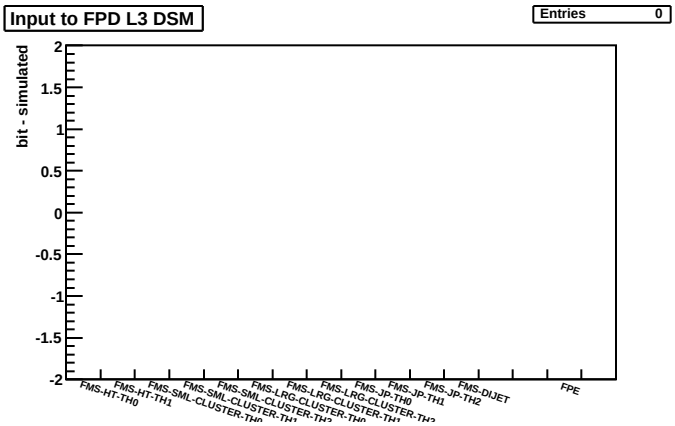

Input to FPD L2 DSM
Entries 229113
CL bits
9143 3038 2597
14007 4428 3723
3842 2562 2693
49379 66343 67358
SMALL LARGE-S LARGE-N

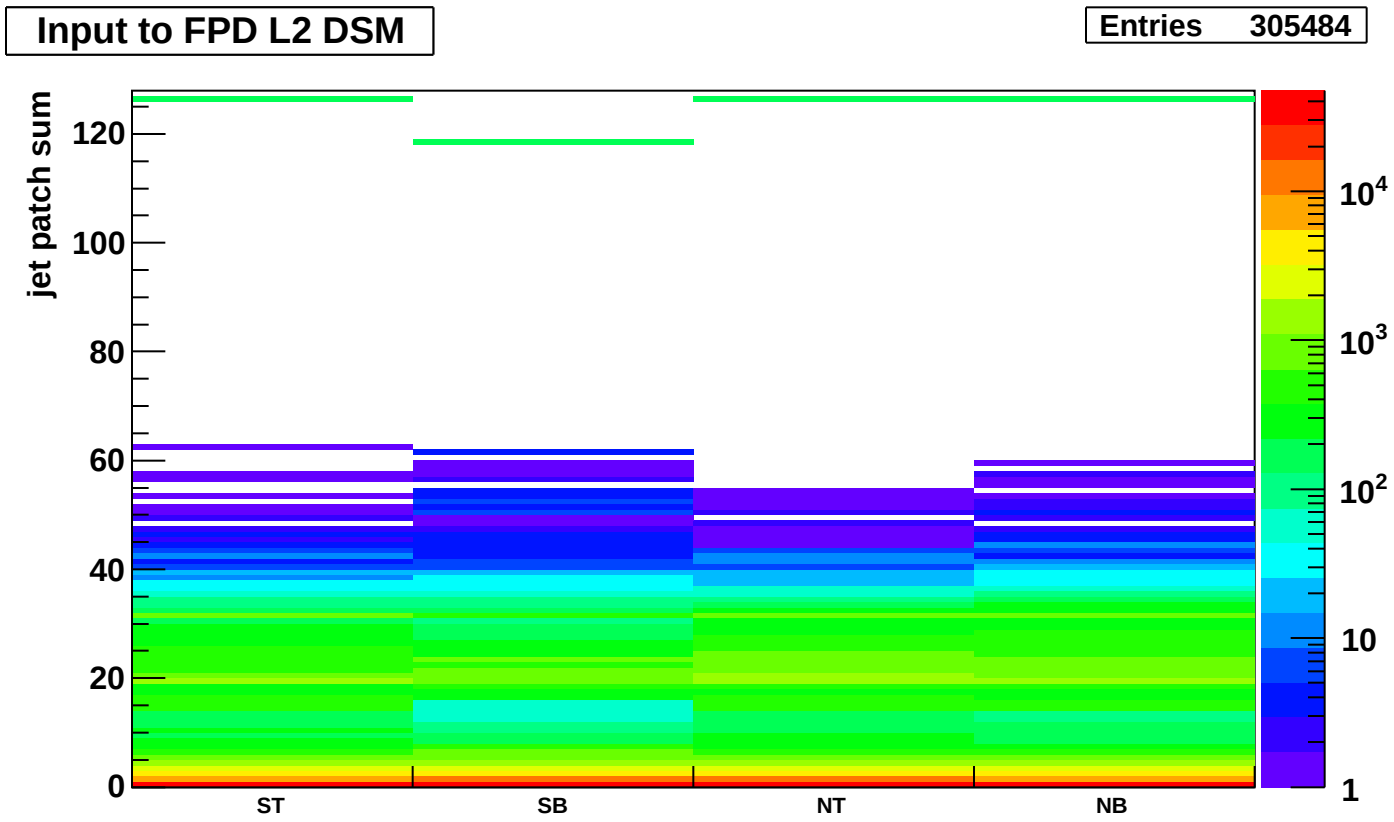

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

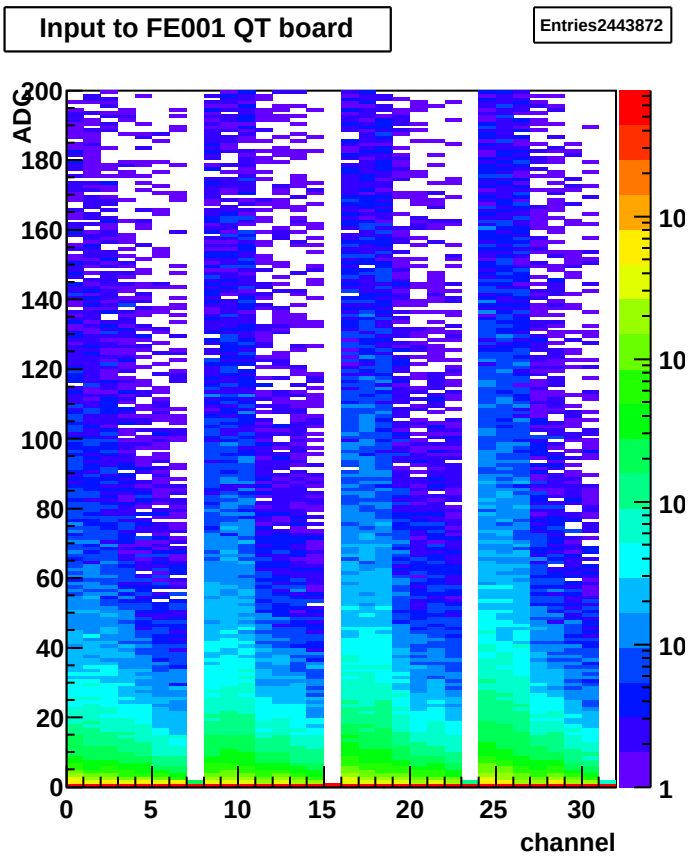

Input to FPD L2 DSM
Entries 229113
HT bits
21071 8426 7043
4699 1585 1839
50601 66360 67489
SMALL LARGE-S LARGE-N

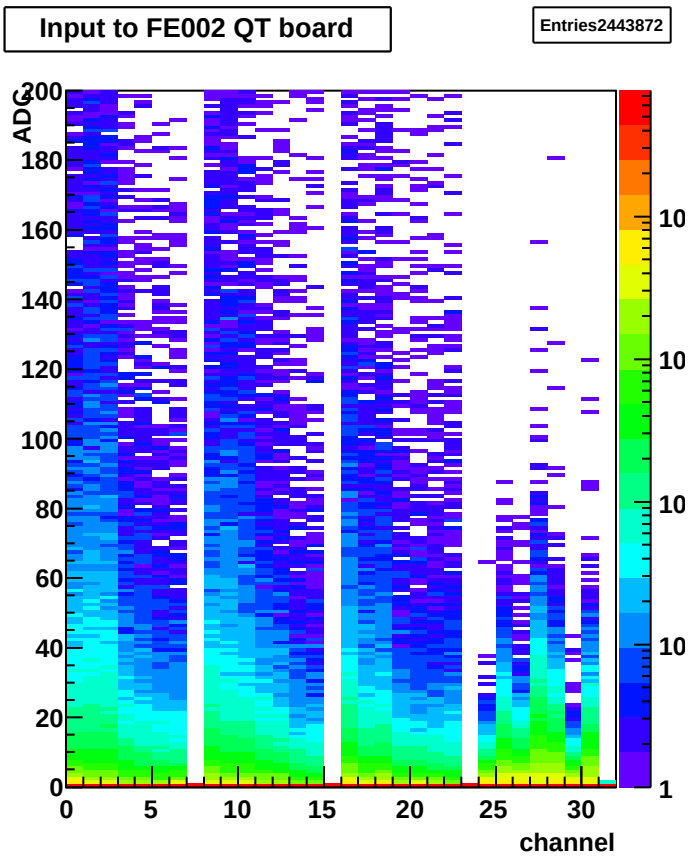

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

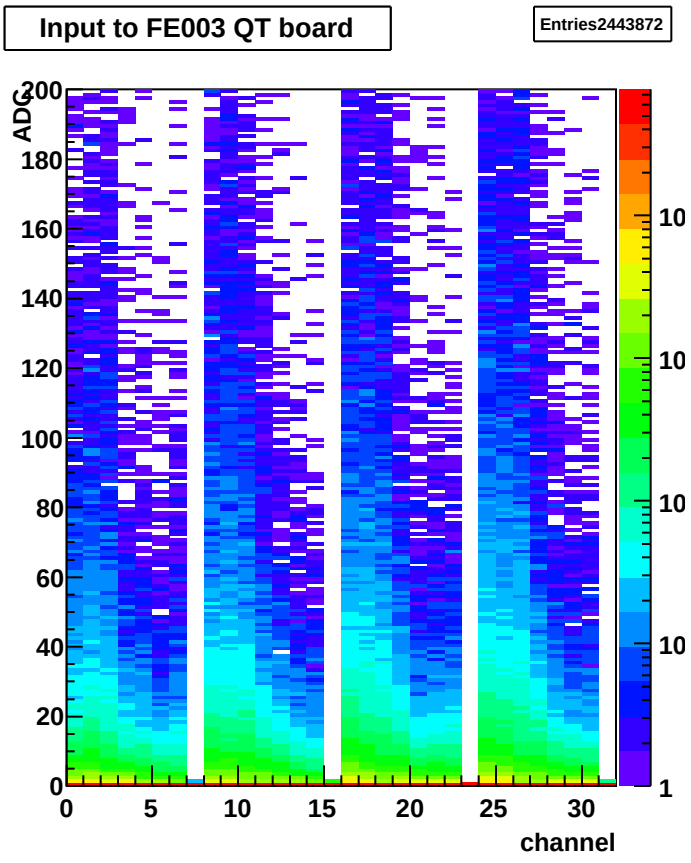

Input to FPD L3 DSM
Entries 1221936
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

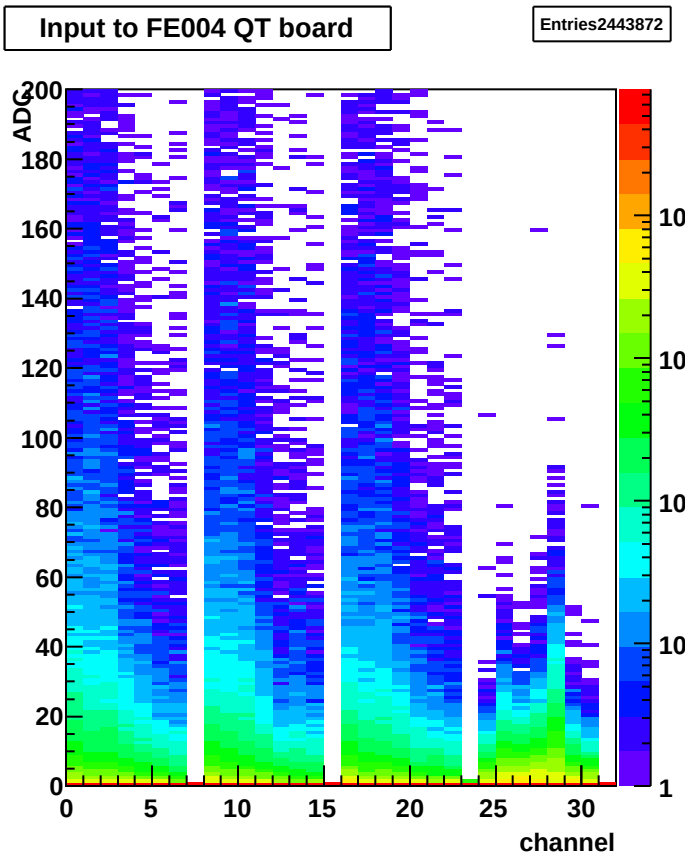

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

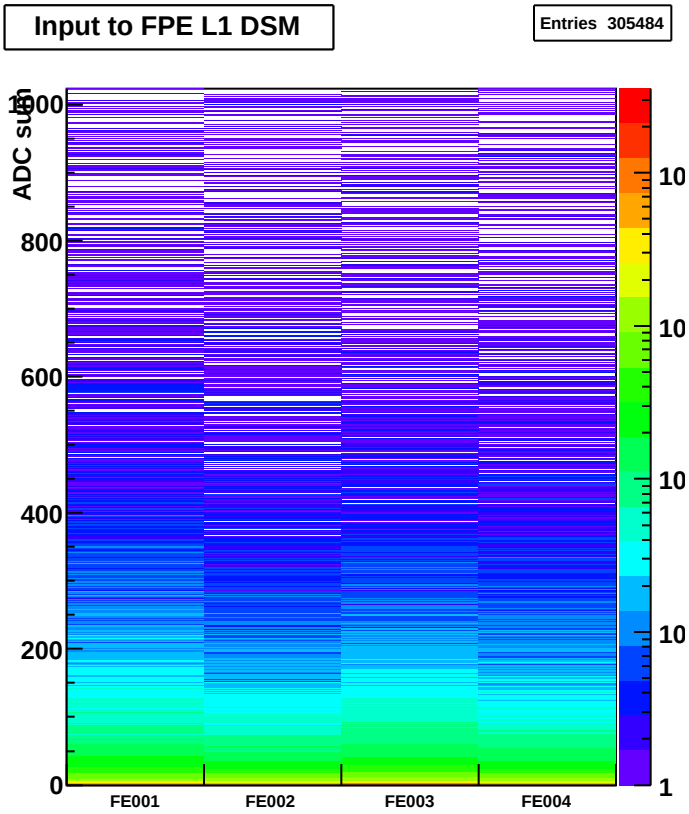

Input to FPD L2 DSM
Entries 305484
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

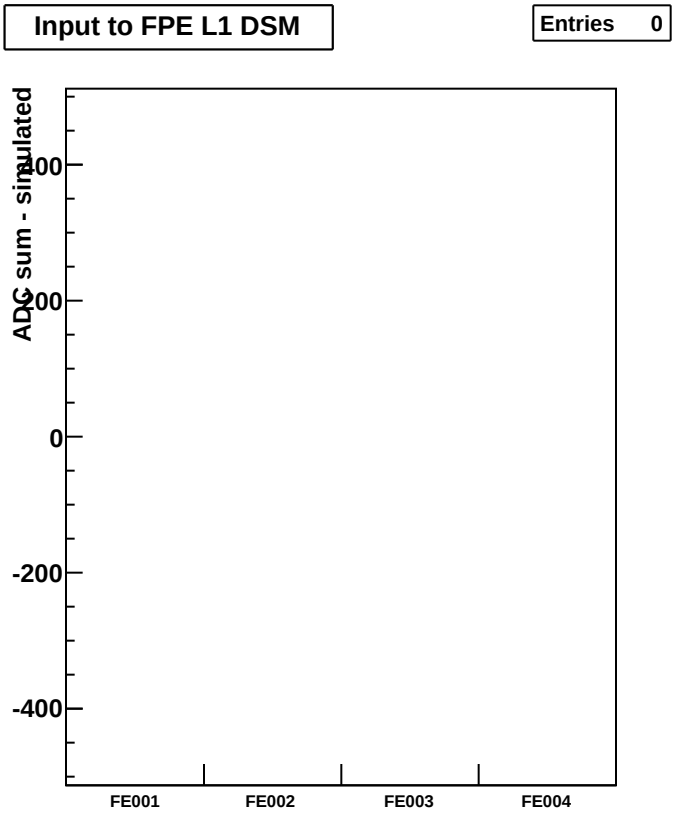

Input to FE001 QT board
Entries2443872
ADC
channel

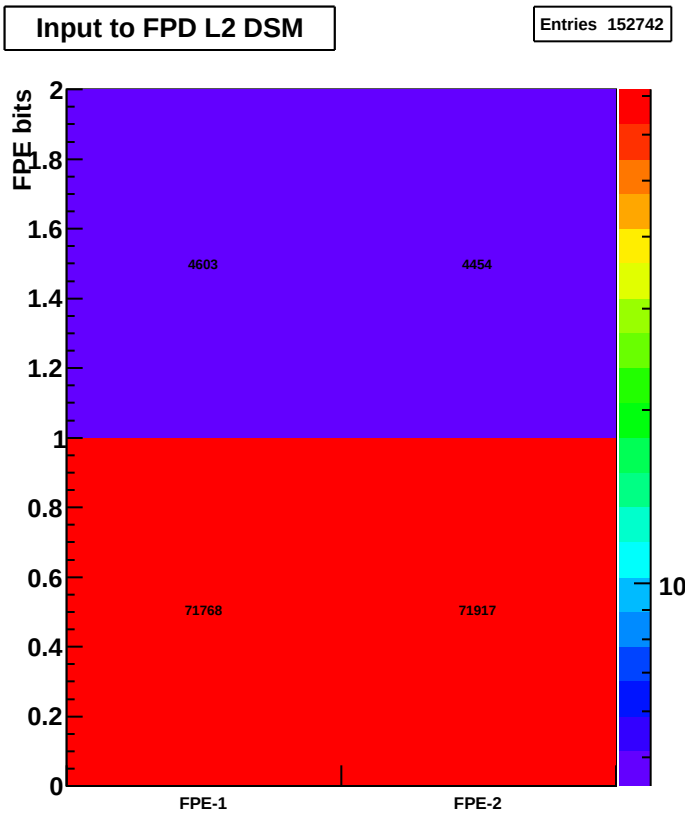

Input to FE002 QT board
Entries2443872
ADC
channel

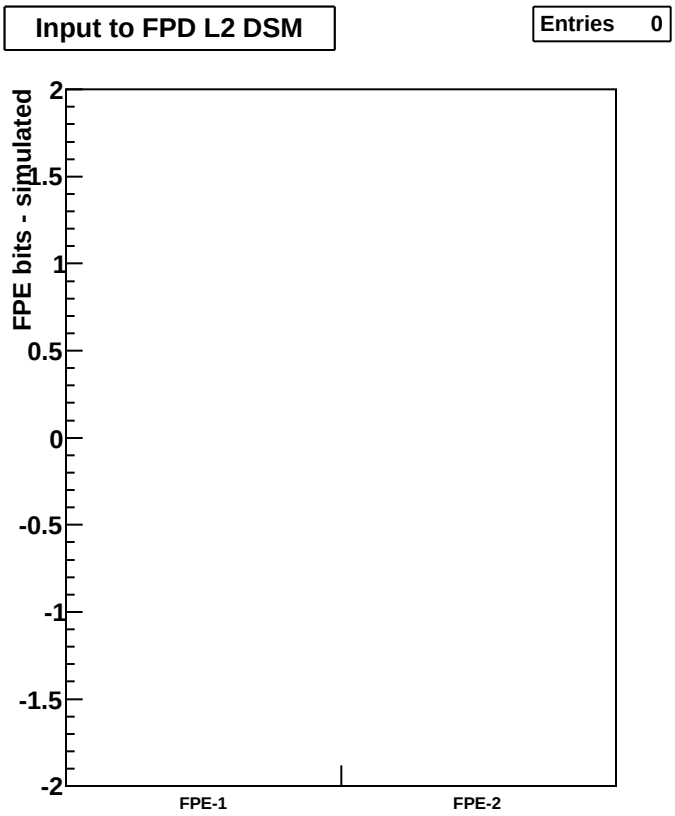

Input to FE003 QT board
Entries2443872
ADC
channel


Input to FE004 QT board
Entries2443872
ADC
channel


Input to FPE L1 DSM
Entries 305484
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 152742
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
4603
4454
71768
71917
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2