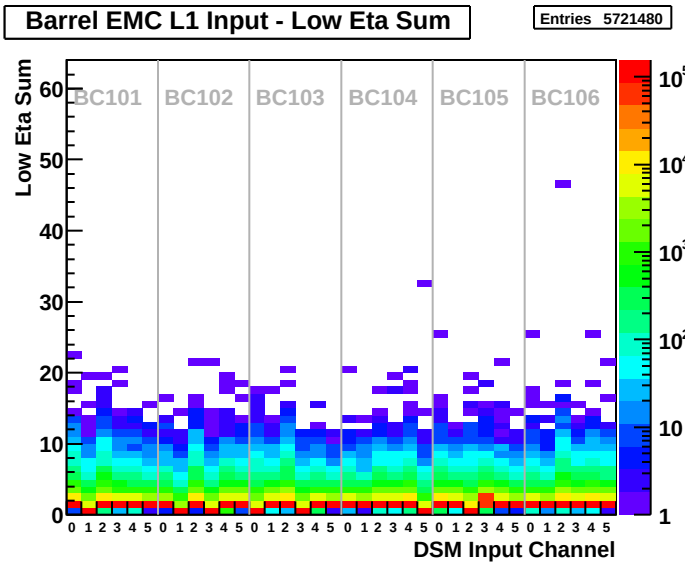
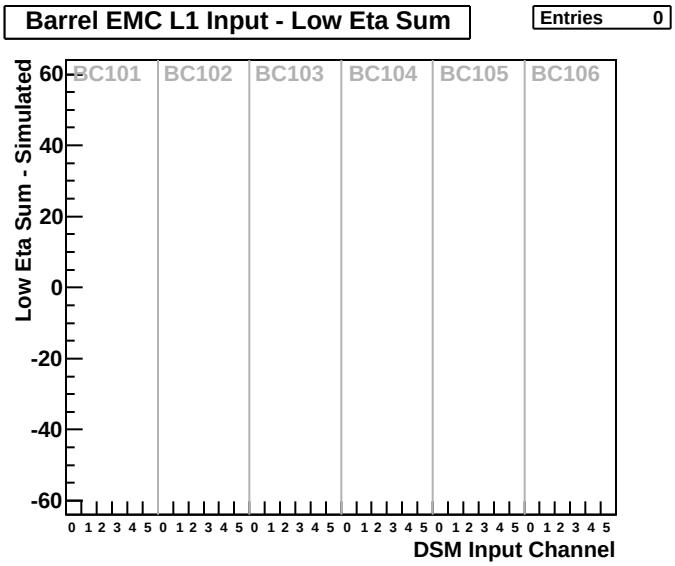
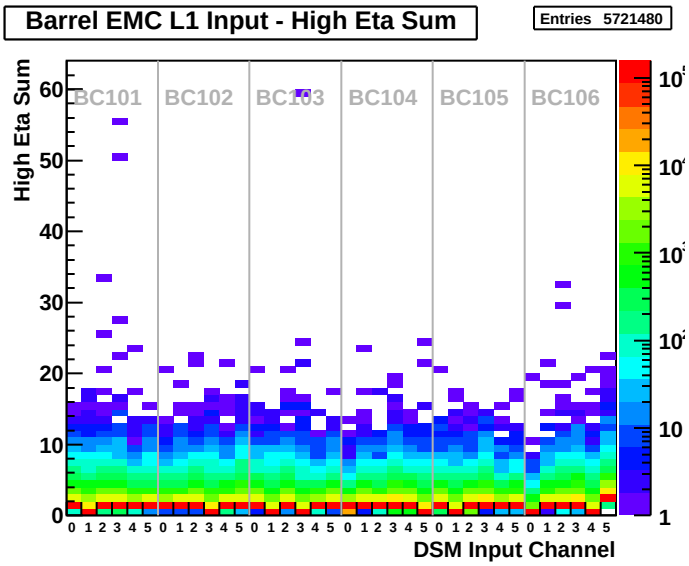
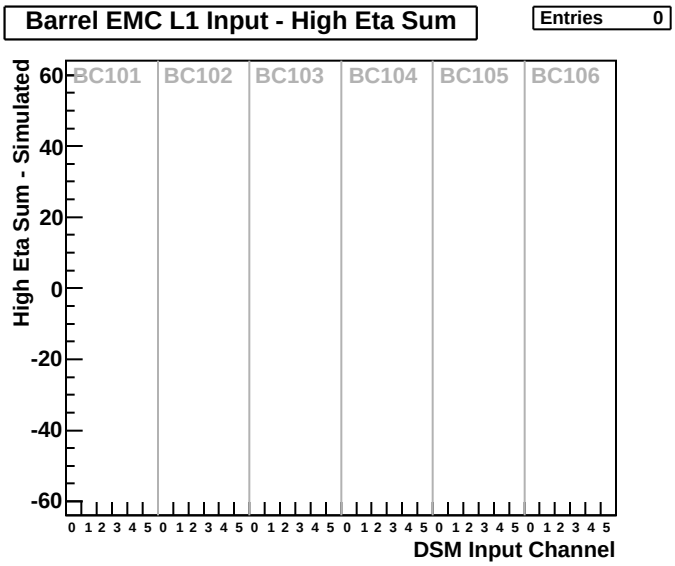
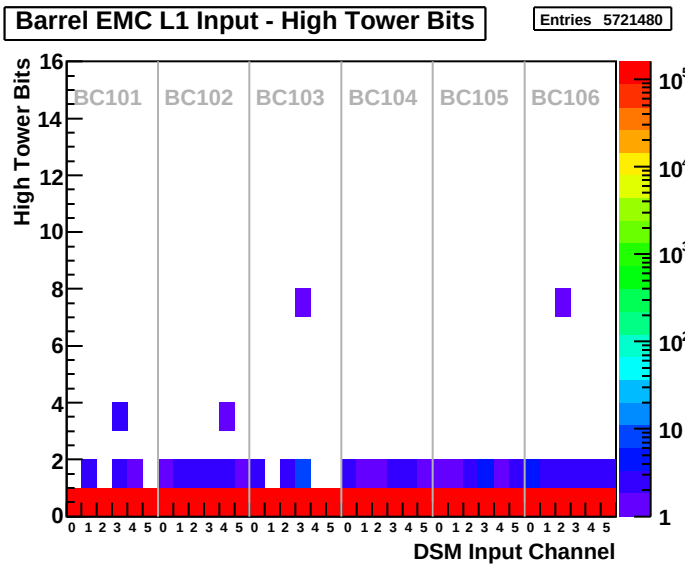
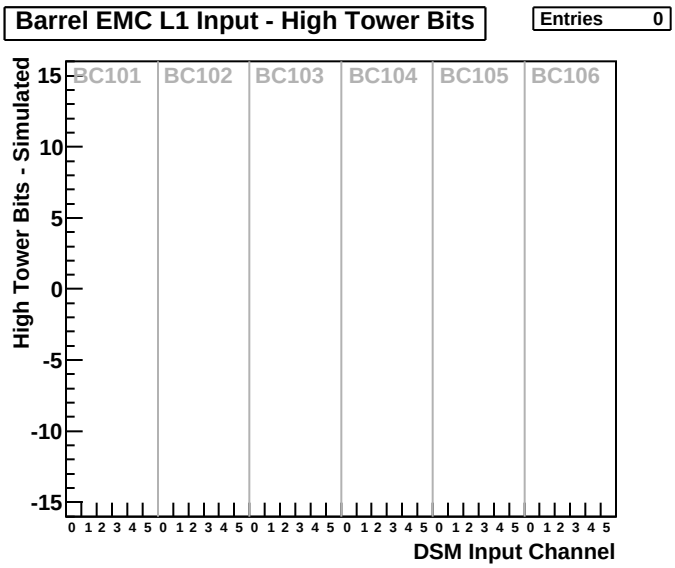

Barrel EMC L1 Input - Low Eta Sum
Entries 5721480
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

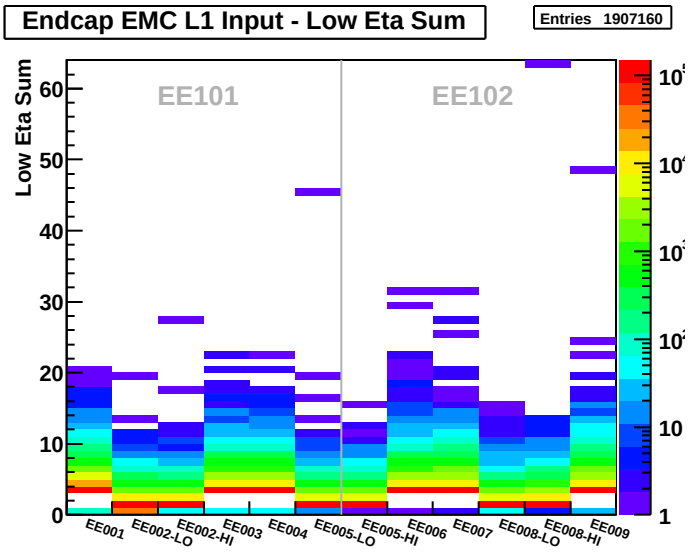

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

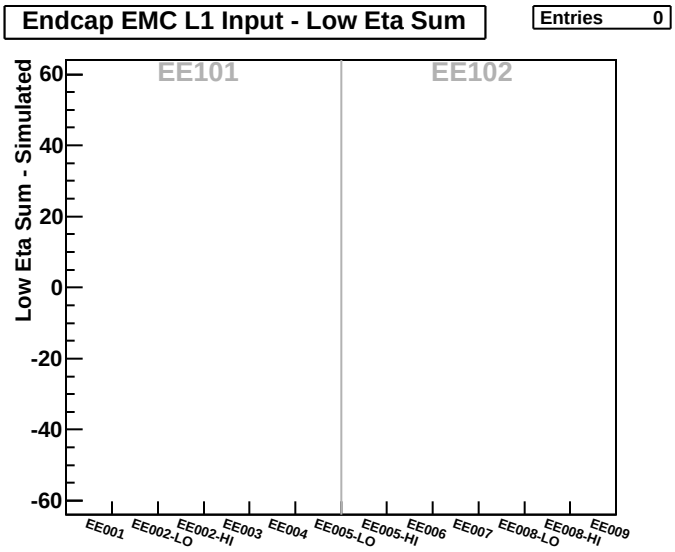

Barrel EMC L1 Input - High Eta Sum
Entries 5721480
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

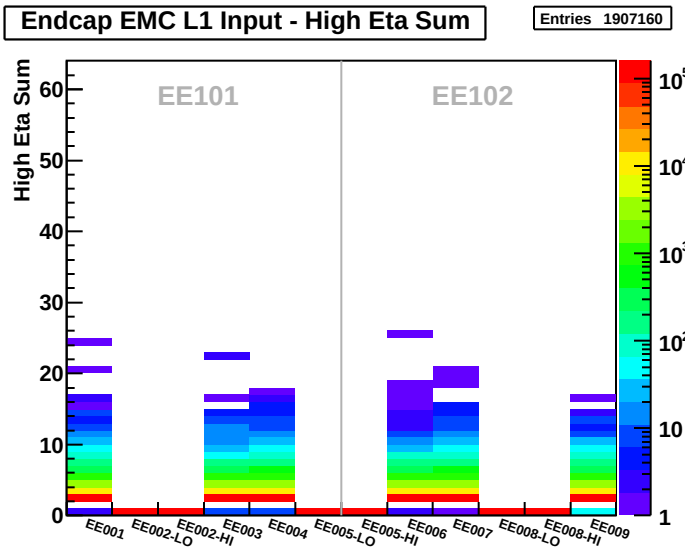

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

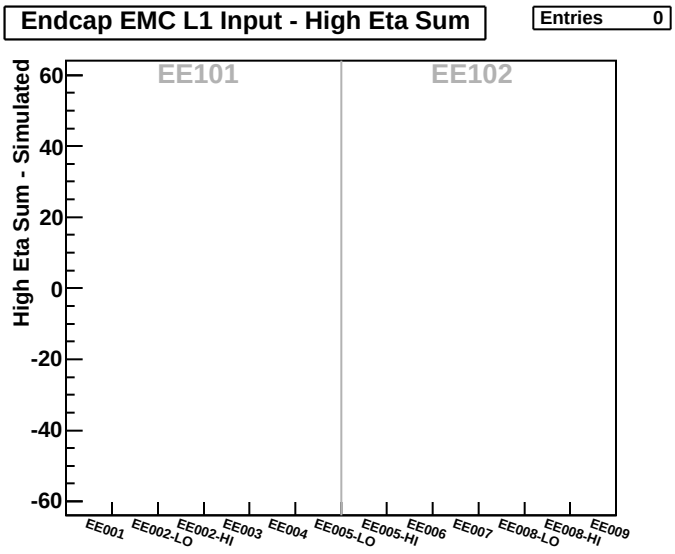

Barrel EMC L1 Input - High Tower Bits
Entries 5721480
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

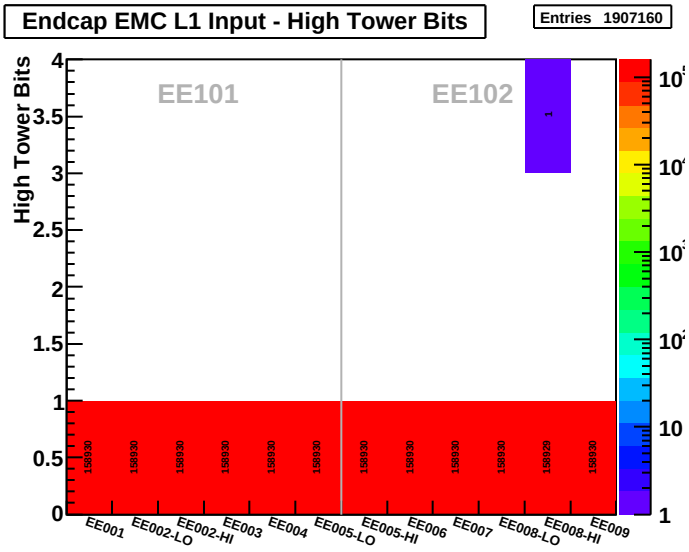

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

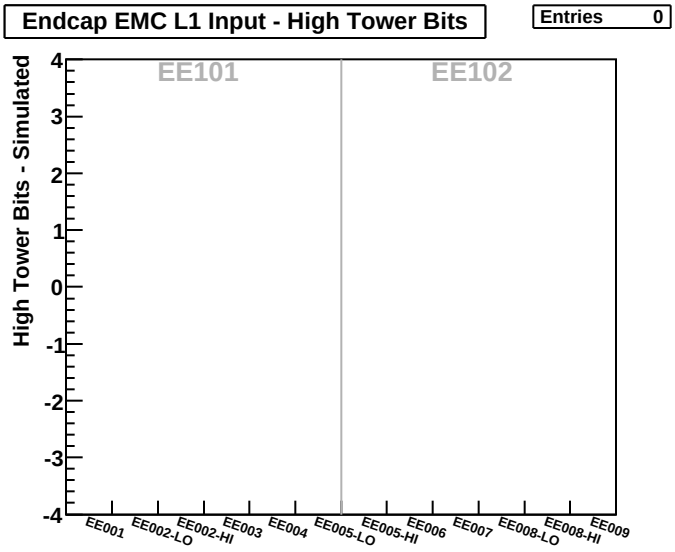

Endcap EMC L1 Input - Low Eta Sum
Entries 1907160
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

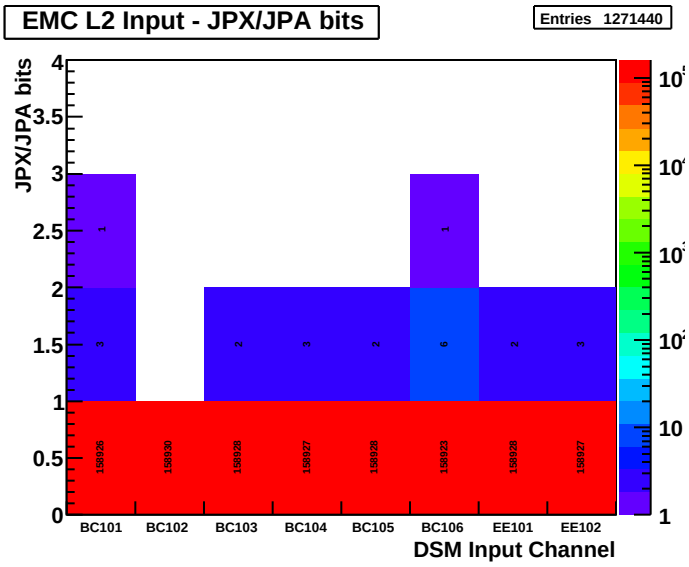

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

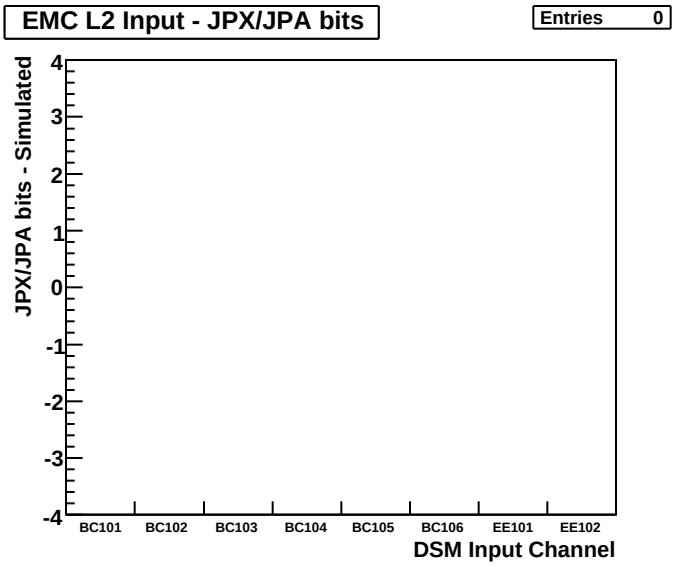

Endcap EMC L1 Input - High Eta Sum
Entries 1907160
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

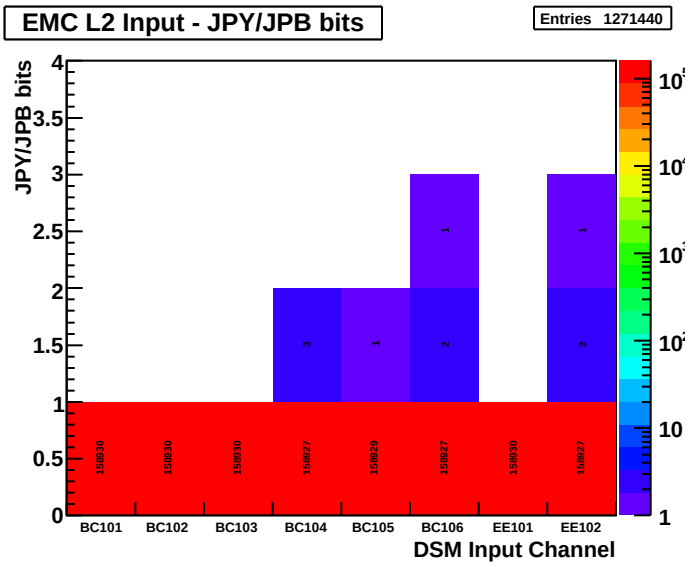

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

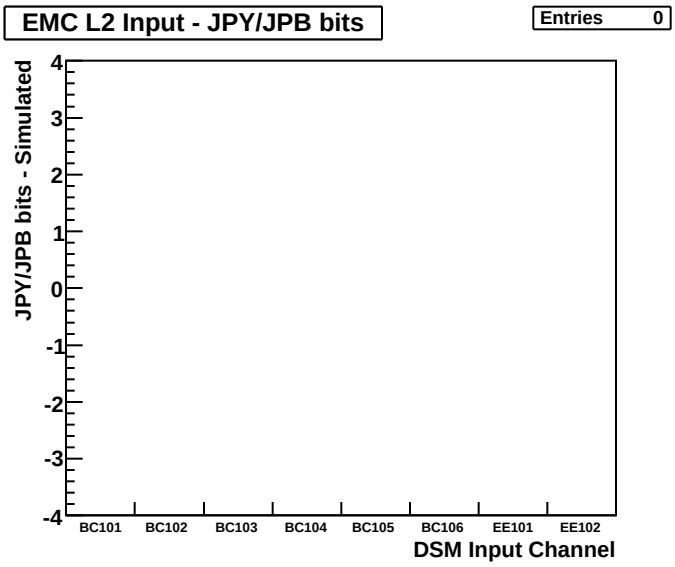

Endcap EMC L1 Input - High Tower Bits
Entries 1907160
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

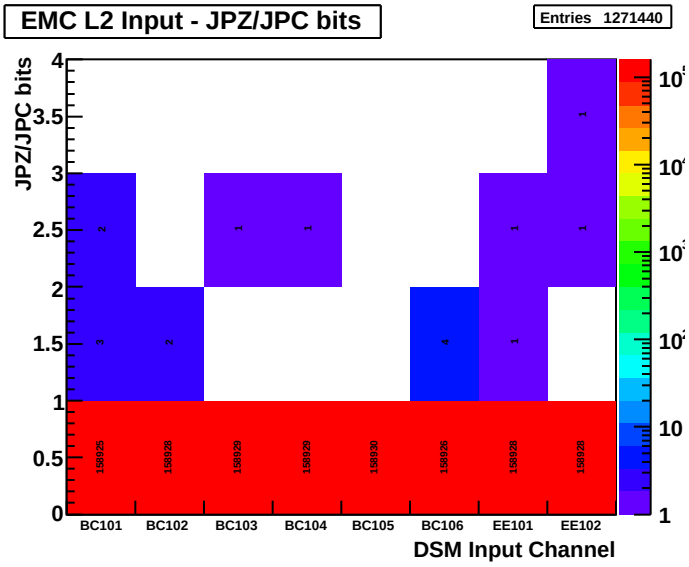

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

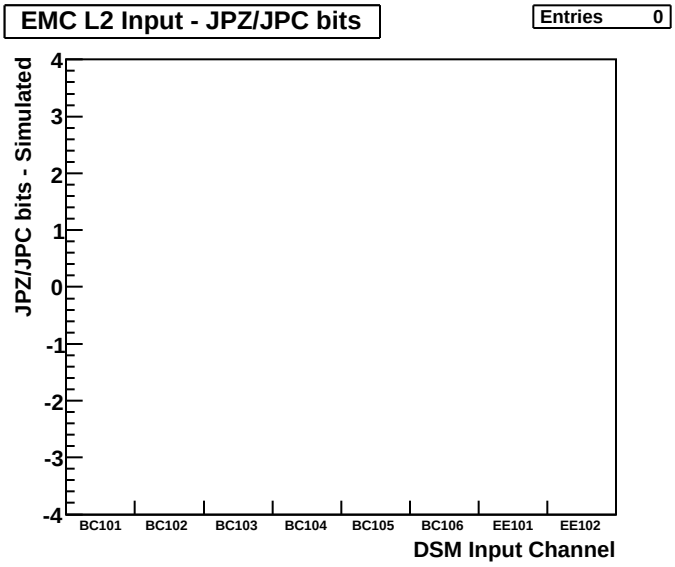

EMC L2 Input - JPX/JPA bits
Entries 1271440
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

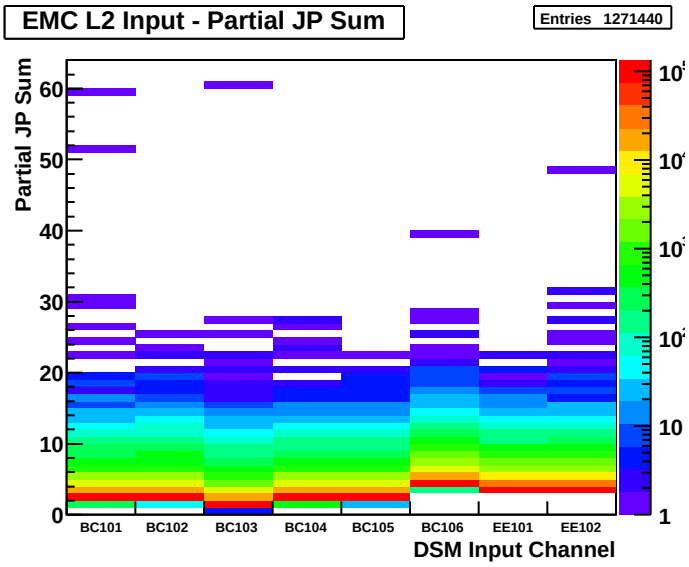

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

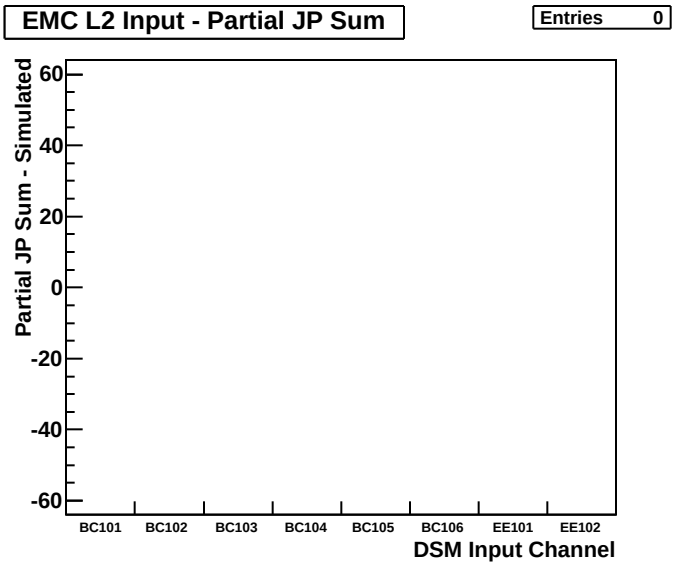

EMC L2 Input - JPY/JPB bits
Entries 1271440
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

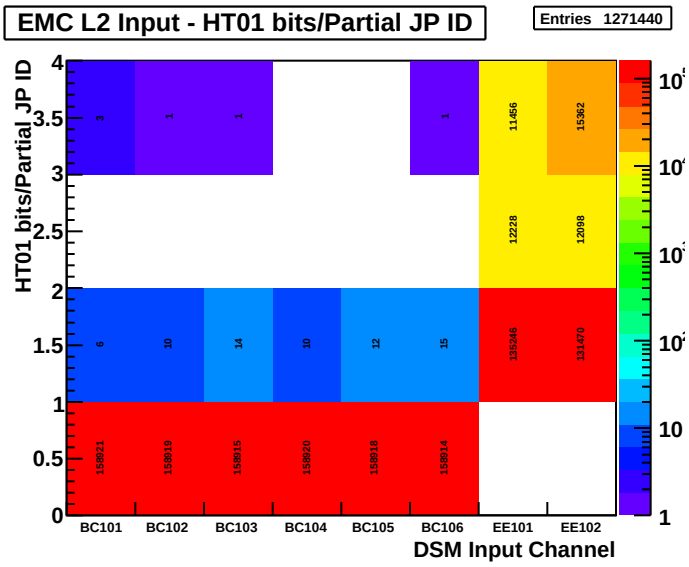

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

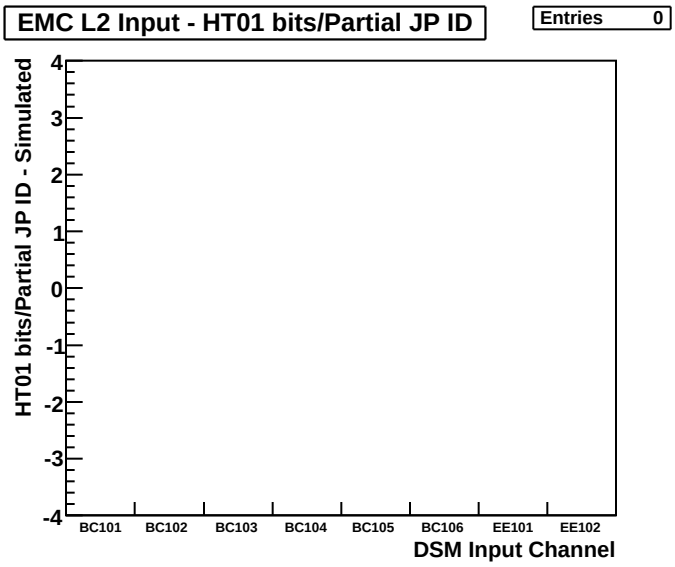

EMC L2 Input - JPZ/JPC bits
Entries 1271440
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

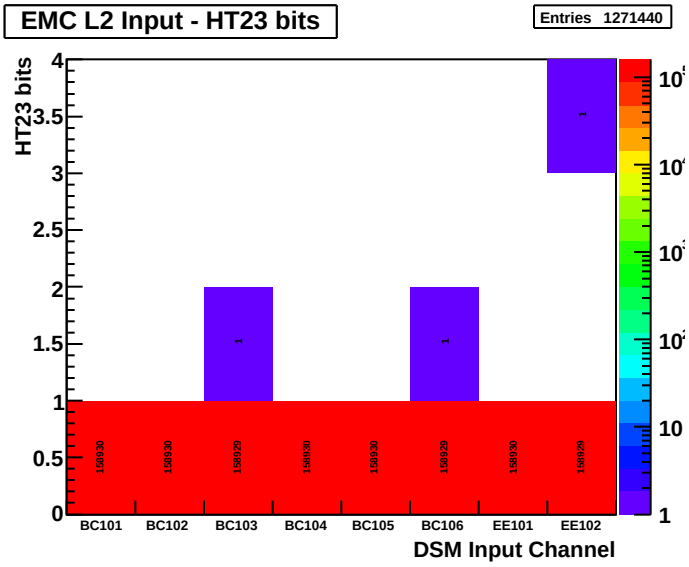

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

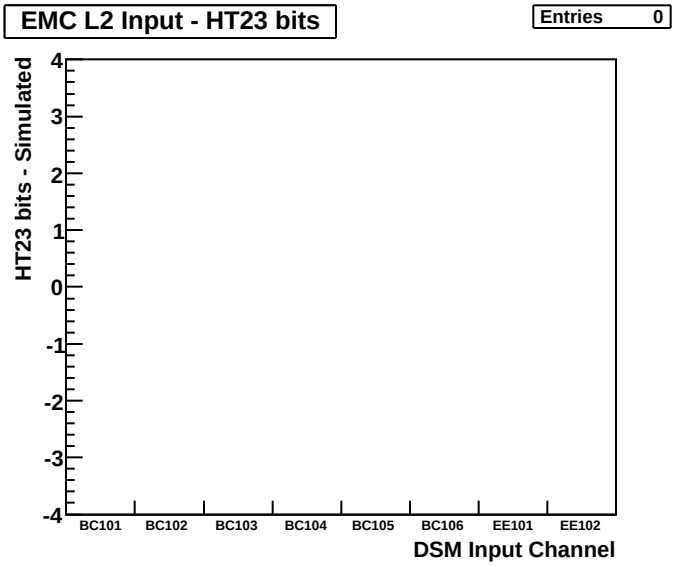

EMC L2 Input - Partial JP Sum
Entries 1271440
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

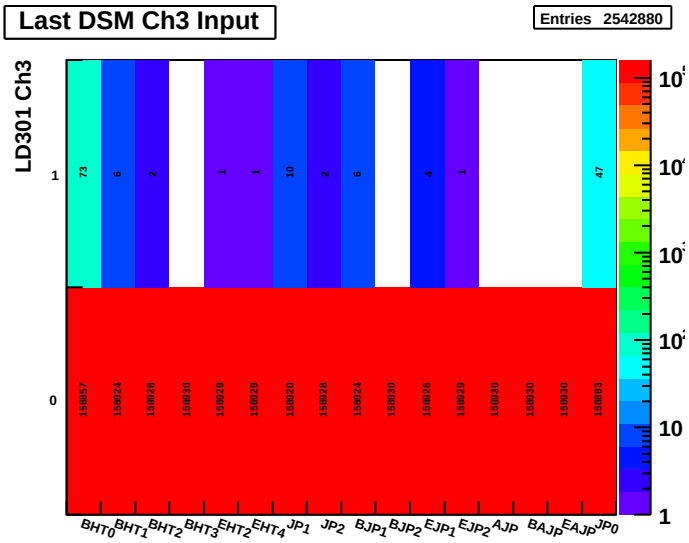

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

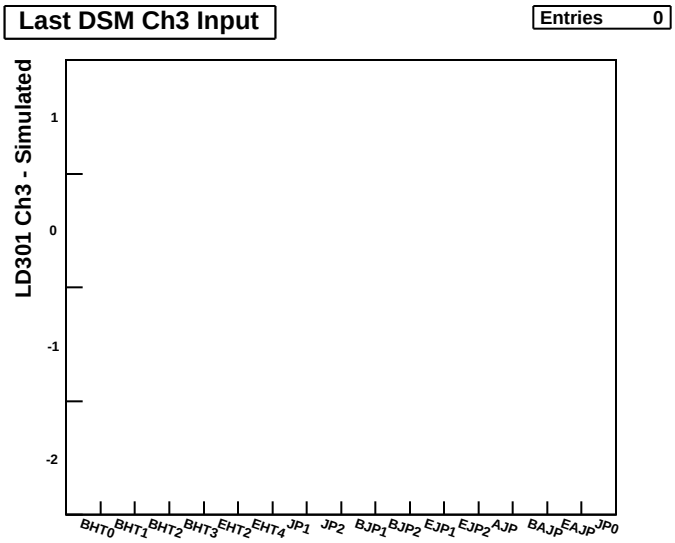

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1271440
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

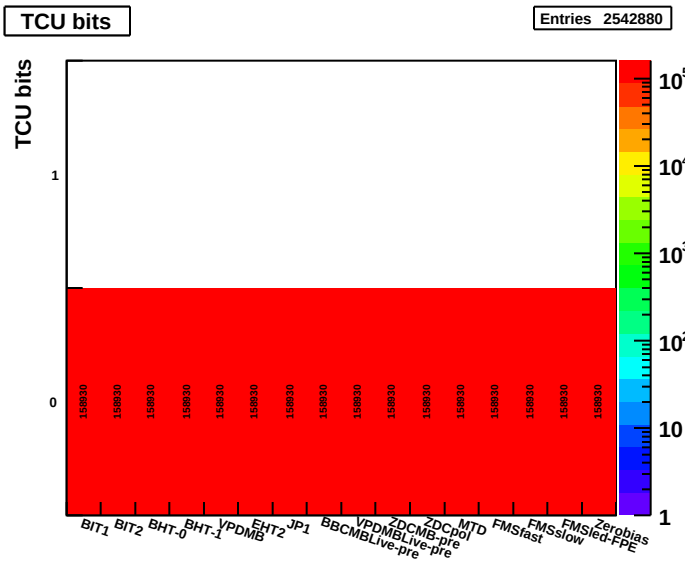

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

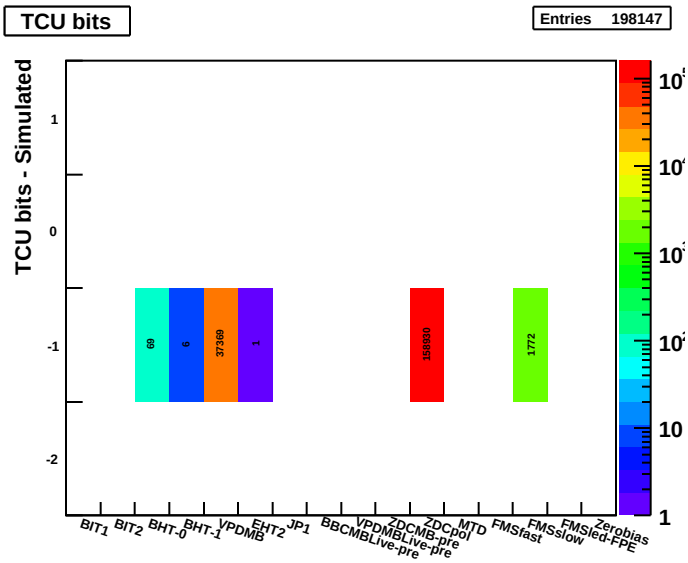

EMC L2 Input - HT23 bits
Entries 1271440
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

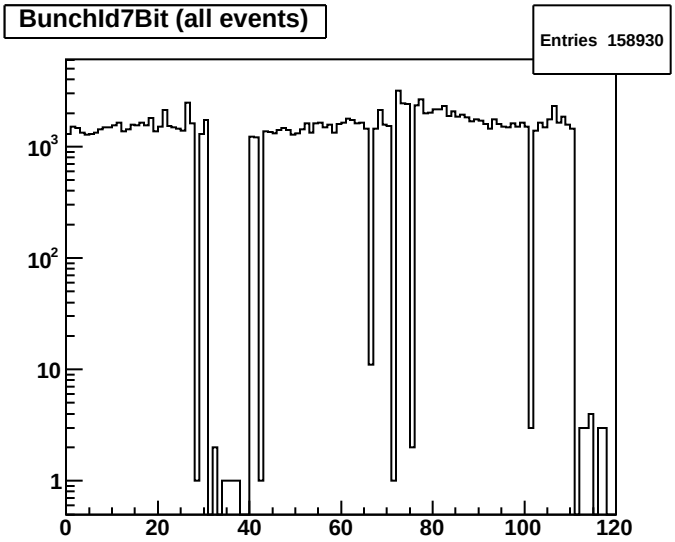

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

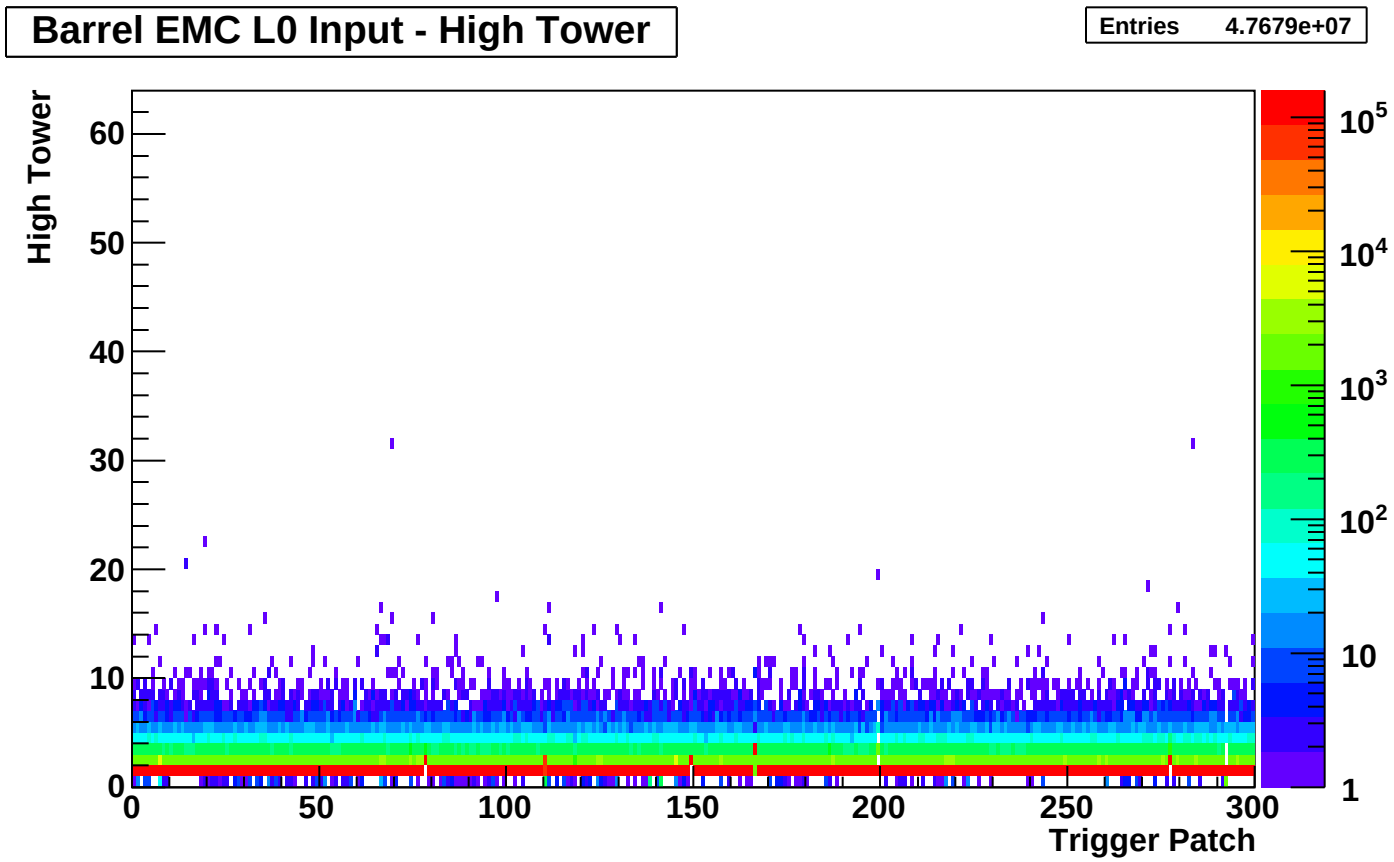

Last DSM Ch3 Input
Entries 2542880
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

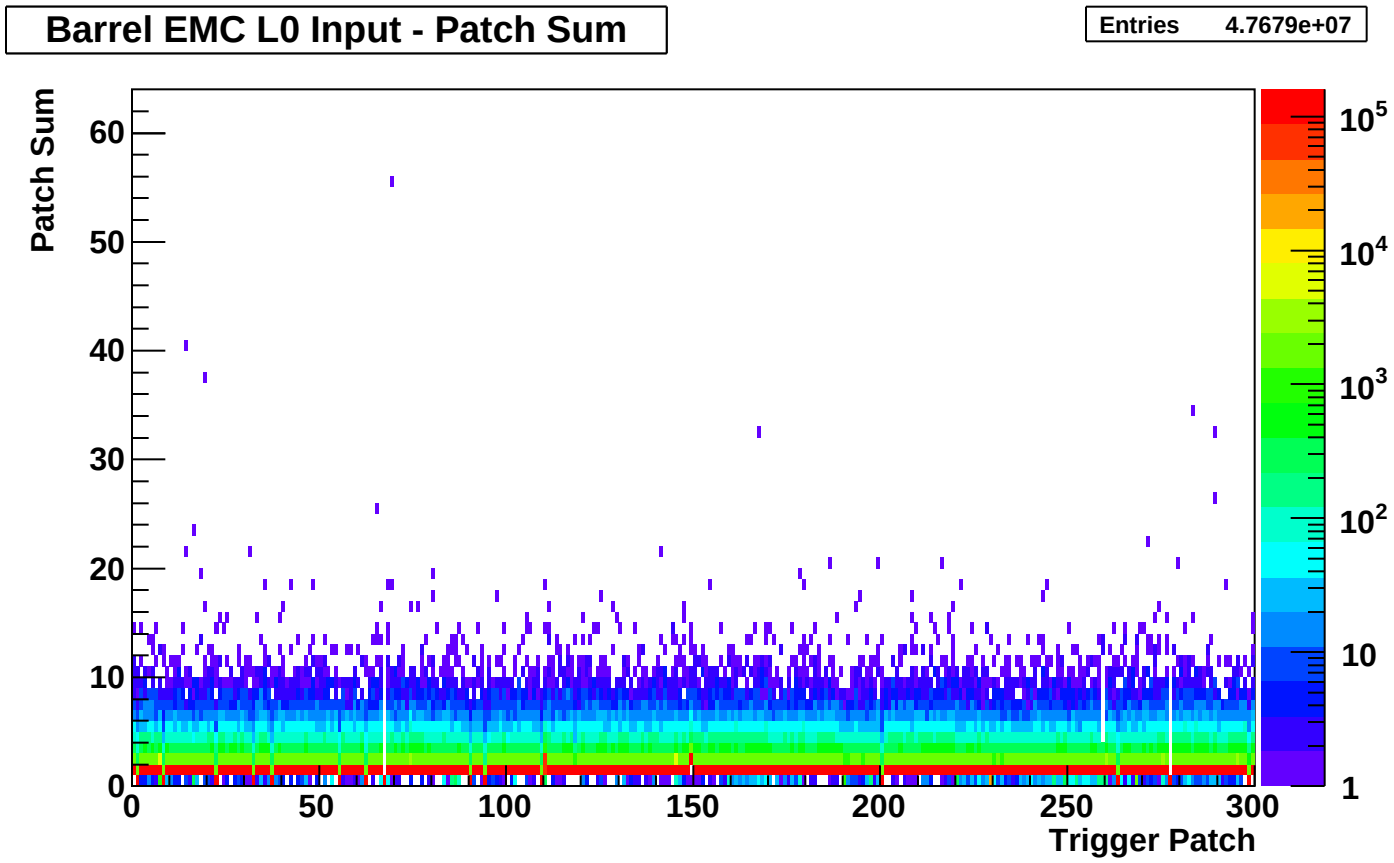

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

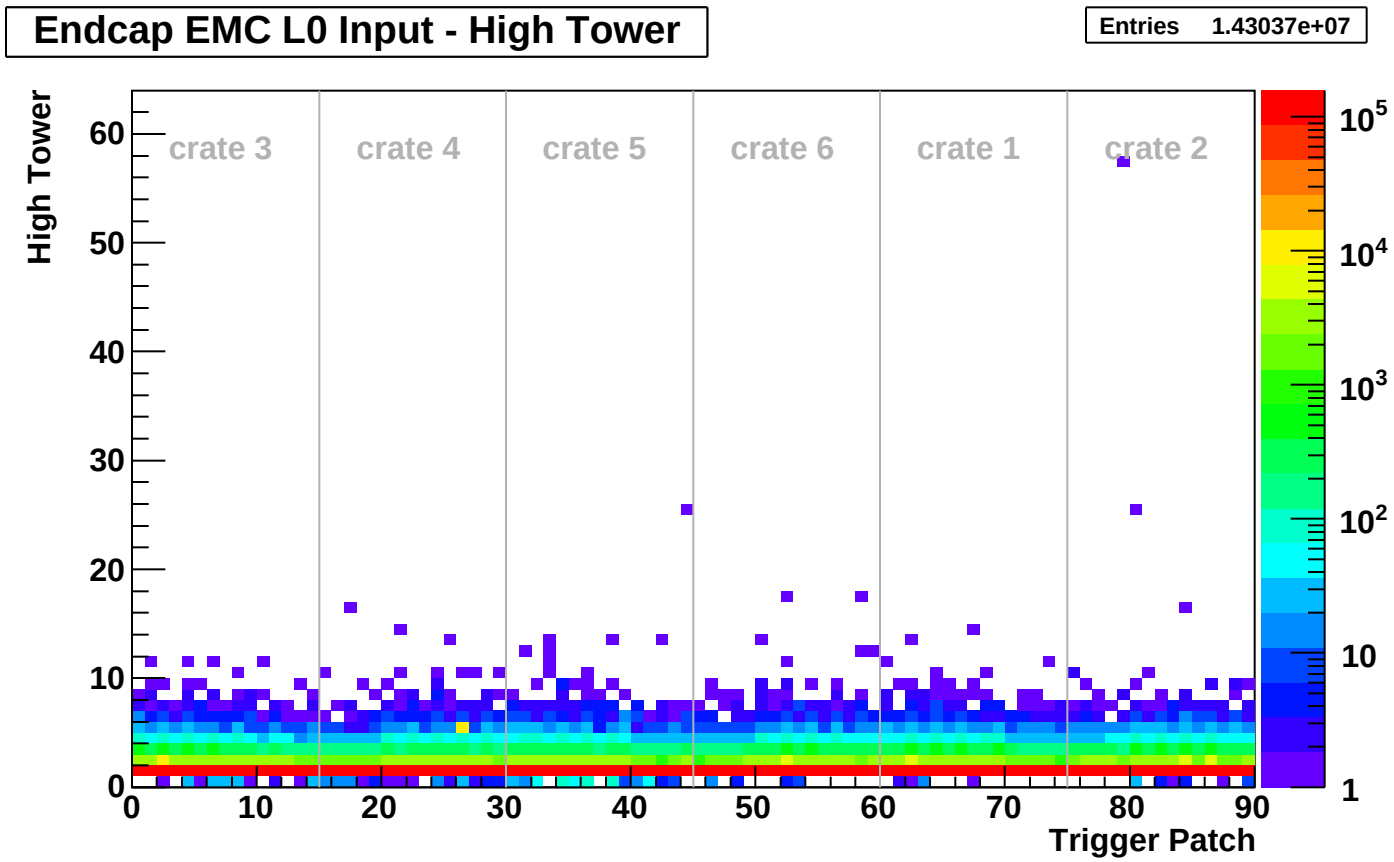

TCU bits
Entries 2542880
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

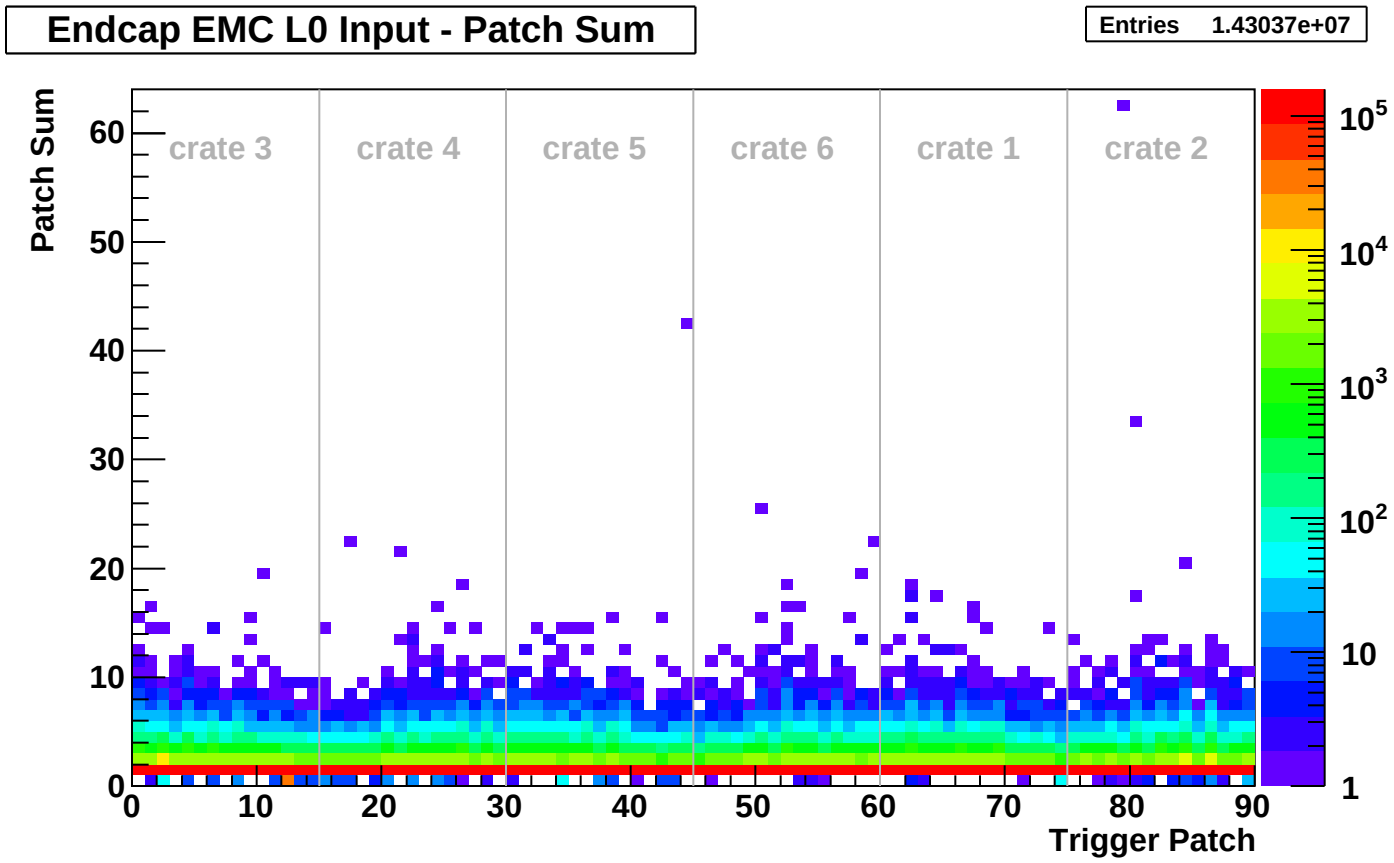

TCU bits
Entries 198147
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

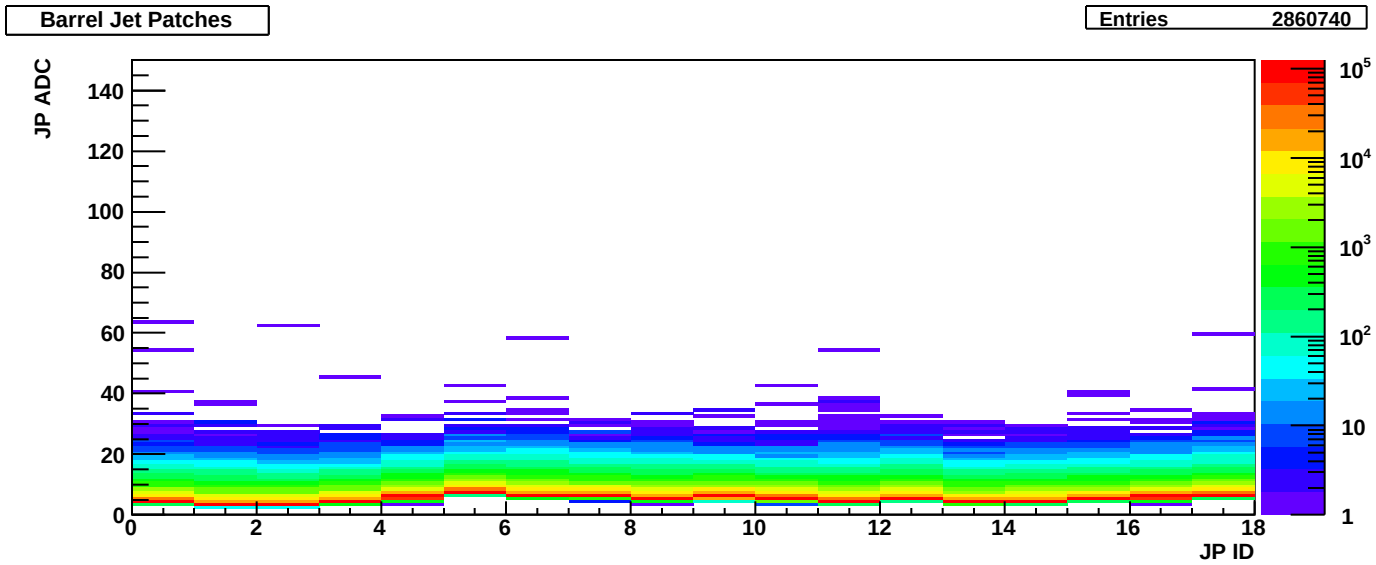

BunchId7Bit (all events)
Entries 158930

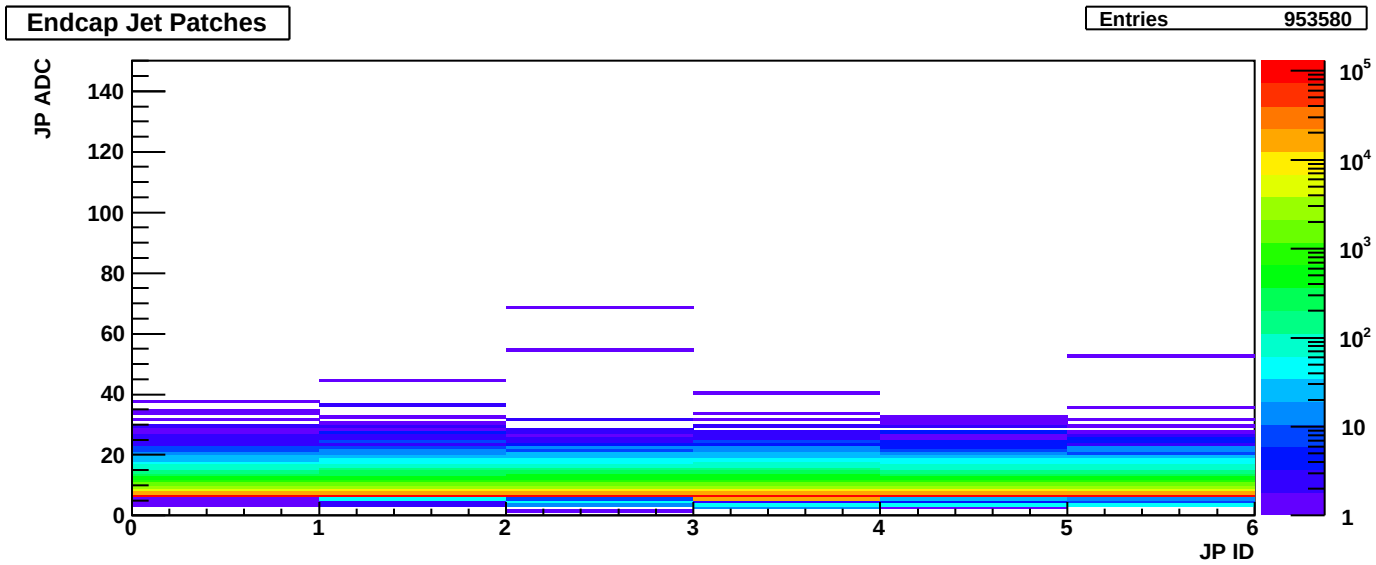

Barrel EMC L0 Input - High Tower
Entries 4.7679e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

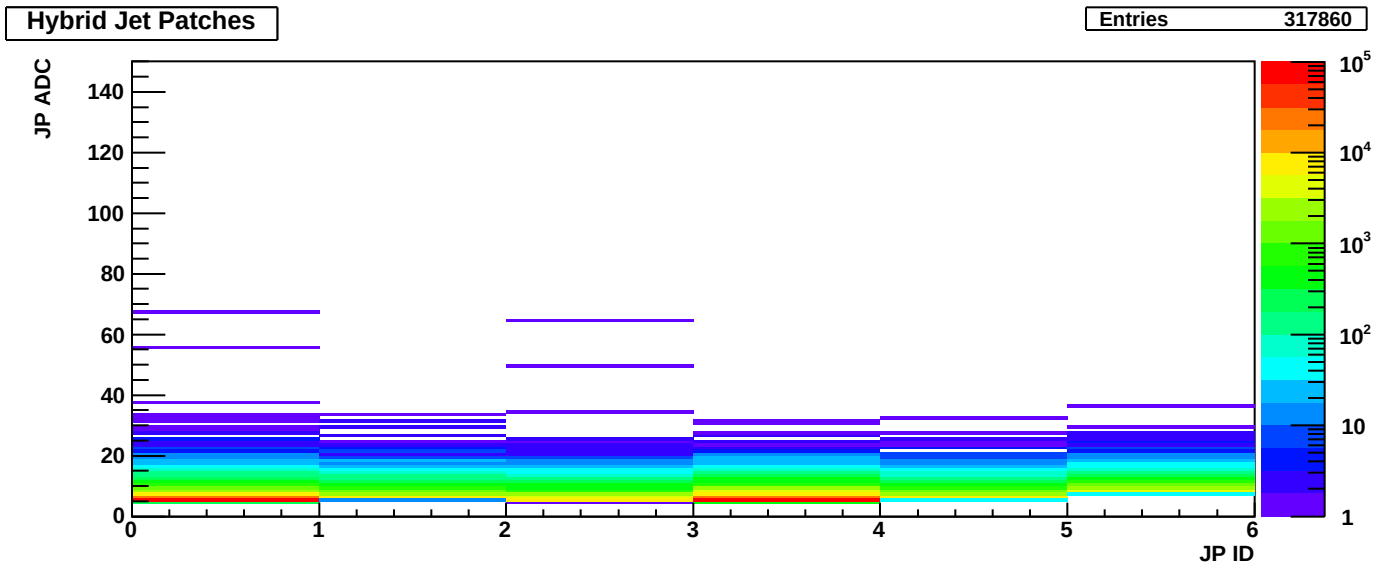

Barrel EMC L0 Input - Patch Sum
Entries 4.7679e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

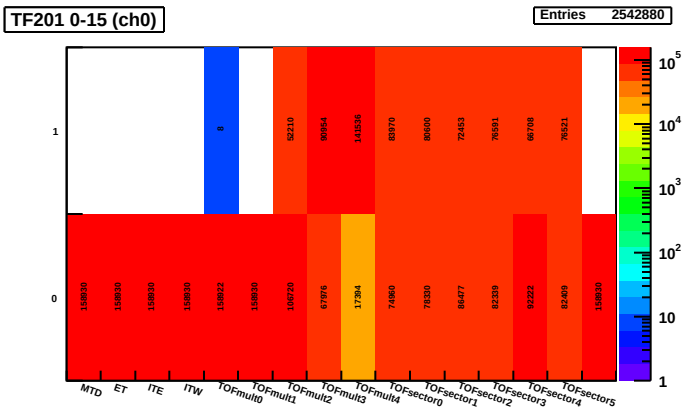

Endcap EMC L0 Input - High Tower
Entries 1.43037e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

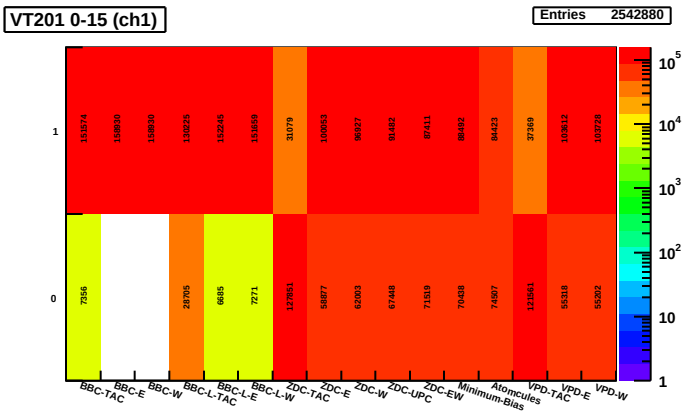

Endcap EMC L0 Input - Patch Sum
Entries 1.43037e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

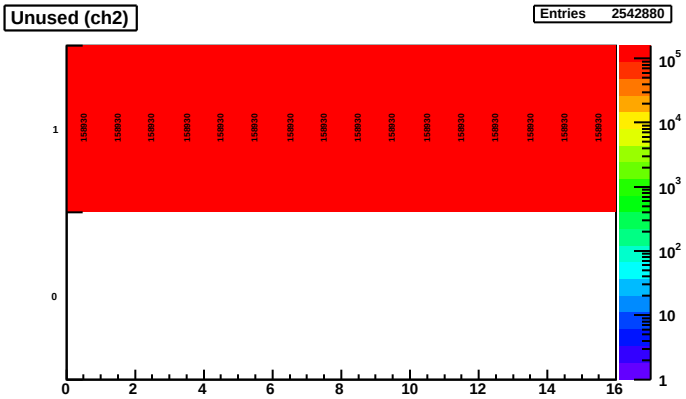

Barrel Jet Patches
Entries 2860740
JP ADC
JP ID

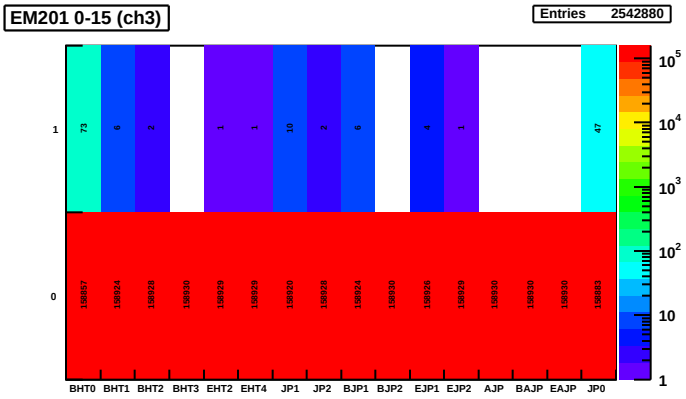

Endcap Jet Patches
Entries 953580
JP ADC
JP ID

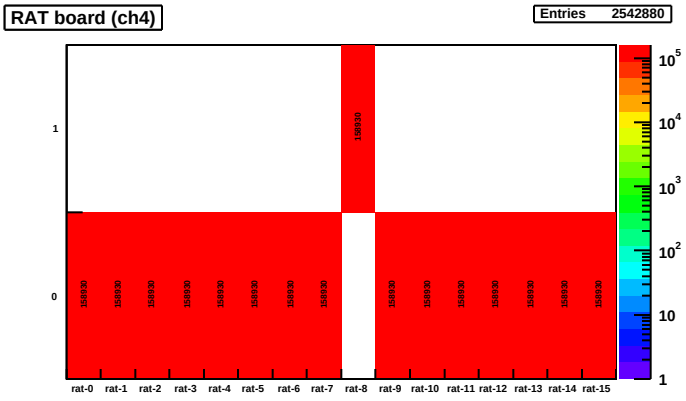

Hybrid Jet Patches
Entries 317860
JP ADC
JP ID

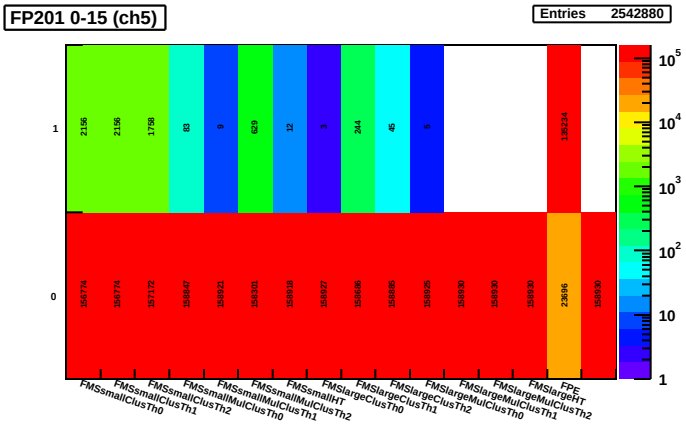

TF201 0-15 (ch0)
Entries 2542880
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

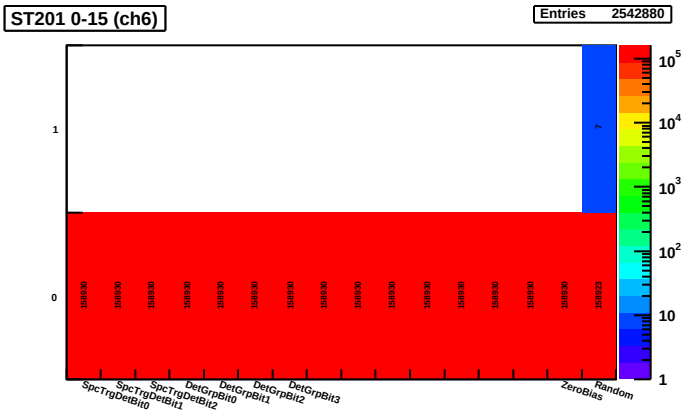

VT201 0-15 (ch1)
Entries 2542880
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

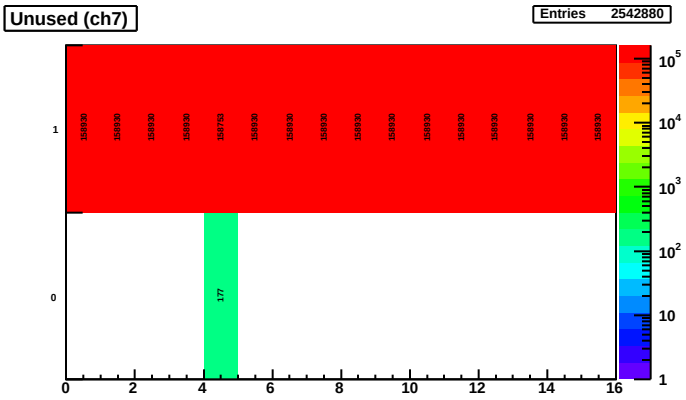

Unused (ch2)
Entries 2542880

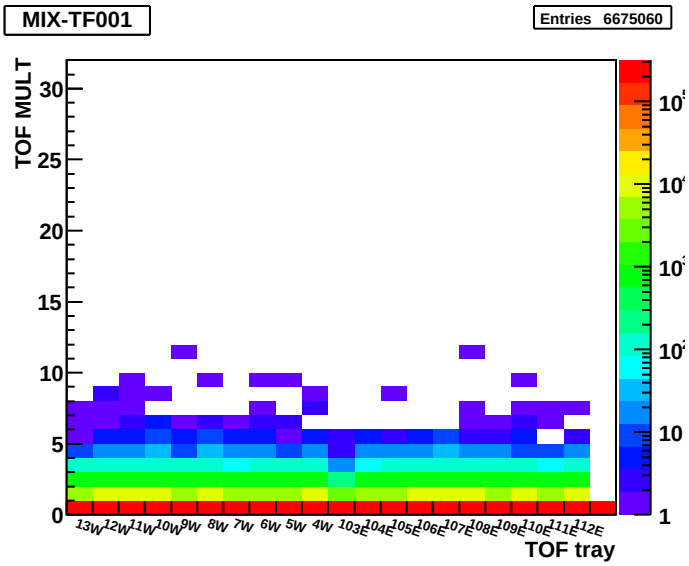

EM201 0-15 (ch3)
Entries 2542880
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

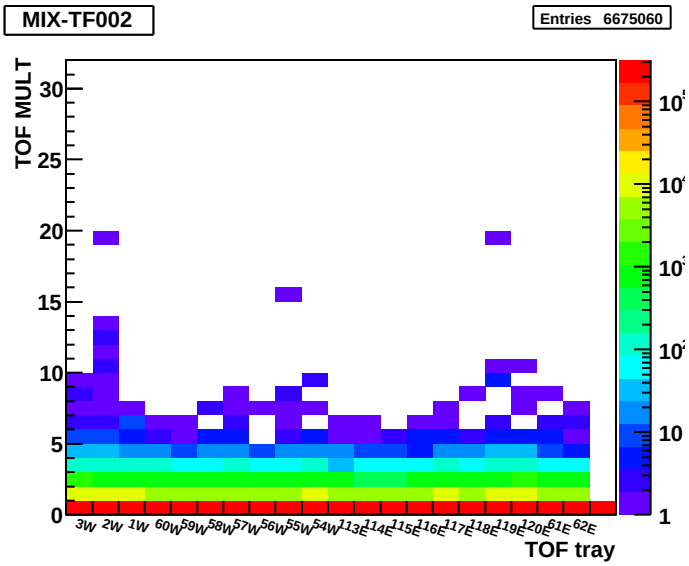

RAT board (ch4)
Entries 2542880
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

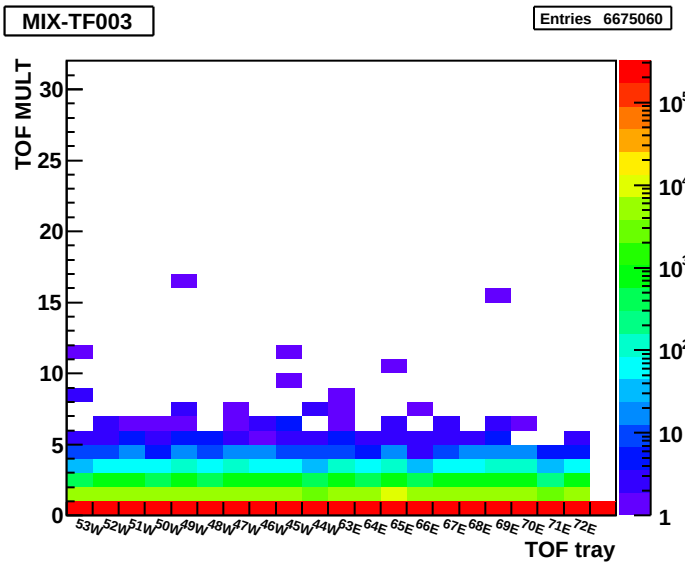

FP201 0-15 (ch5)
Entries 2542880
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

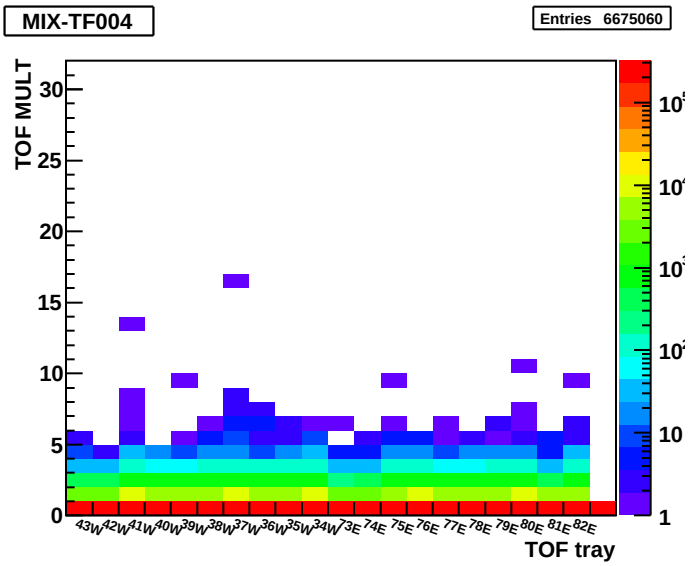

ST201 0-15 (ch6)
Entries 2542880
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

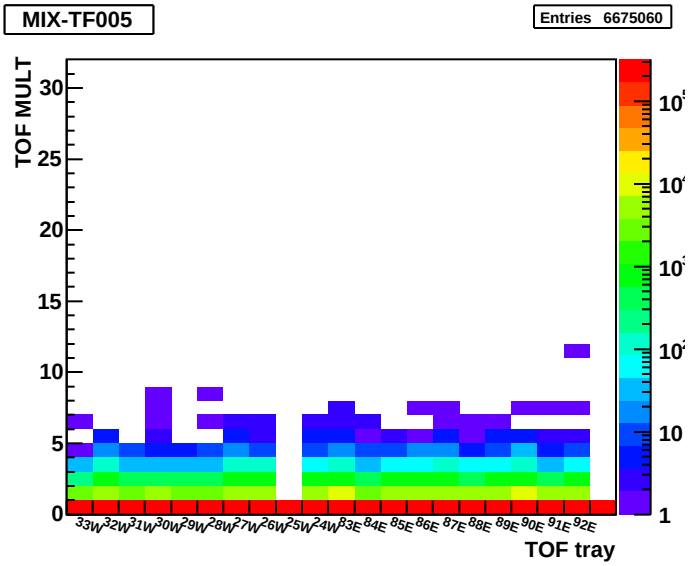

Unused (ch7)
Entries 2542880

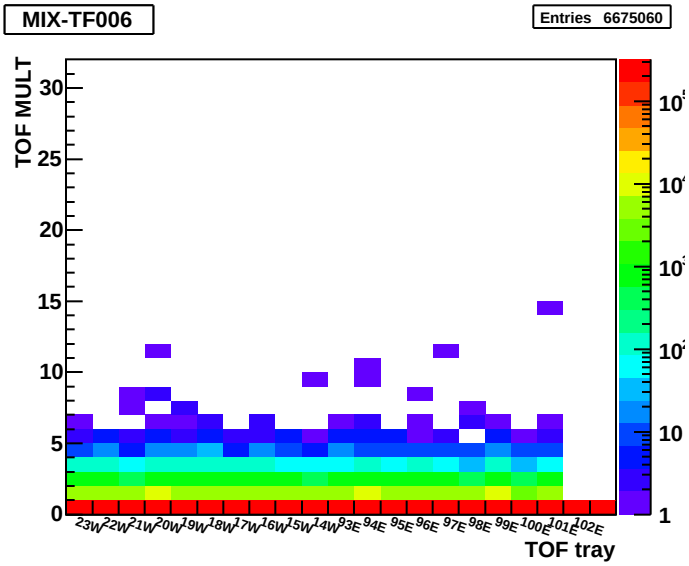

MIX-TF001
Entries 6675060
TOF MULT
TOF tray

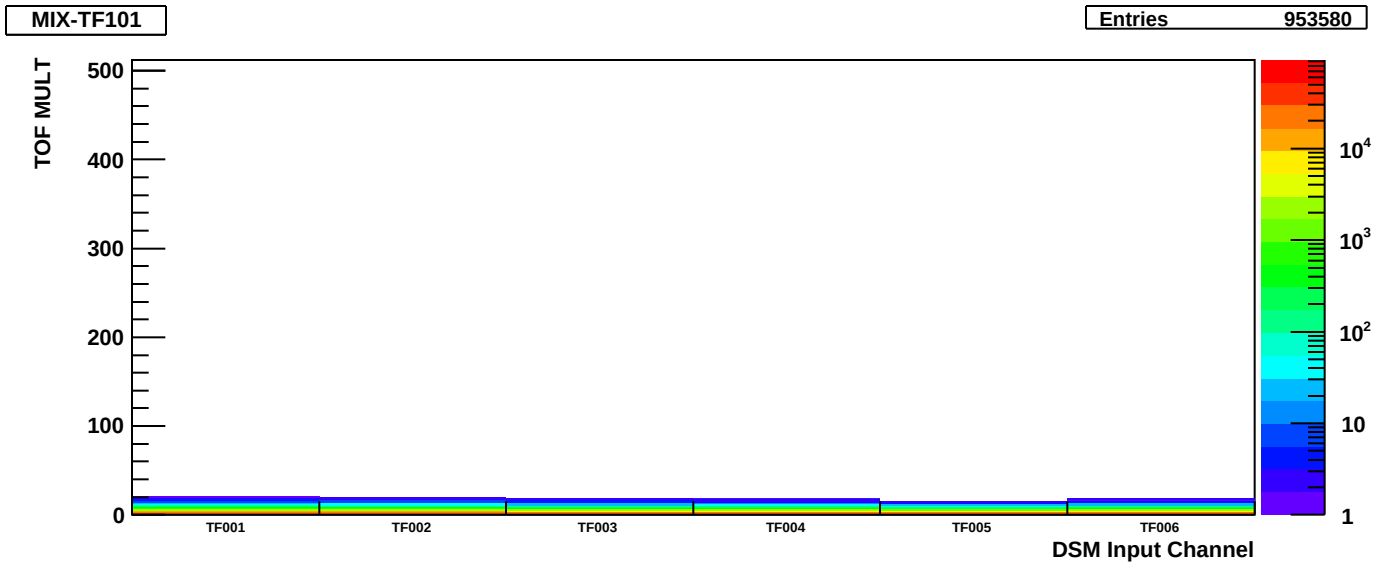

MIX-TF002
Entries 6675060
TOF MULT
TOF tray

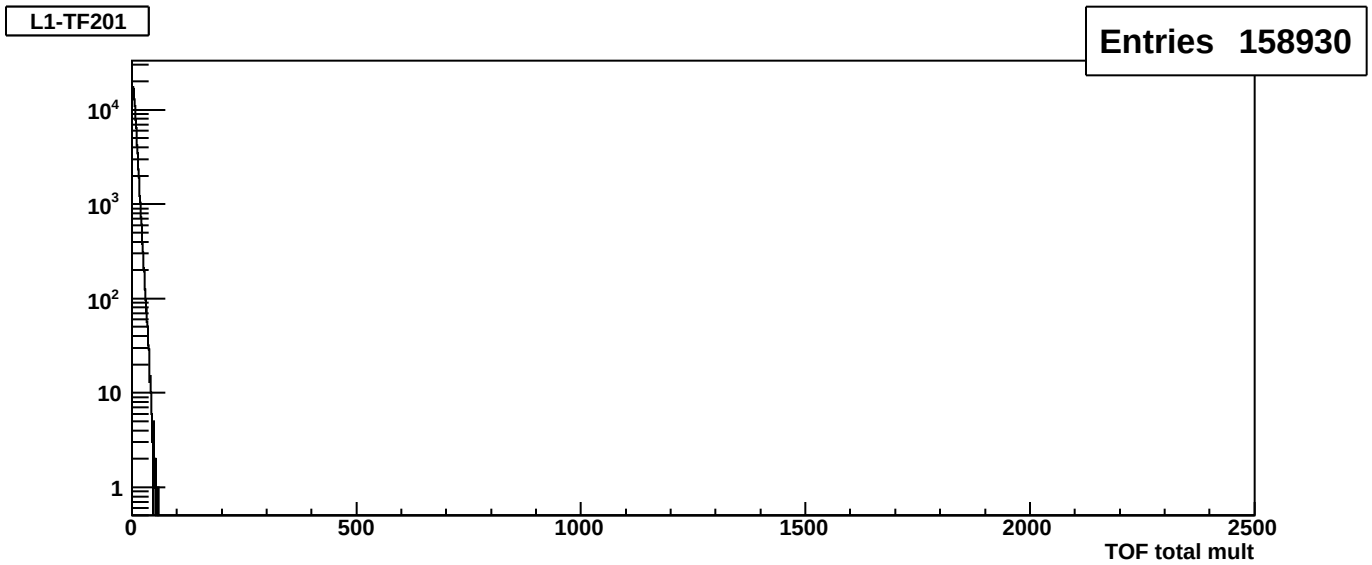

MIX-TF003
Entries 6675060
TOF MULT
TOF tray

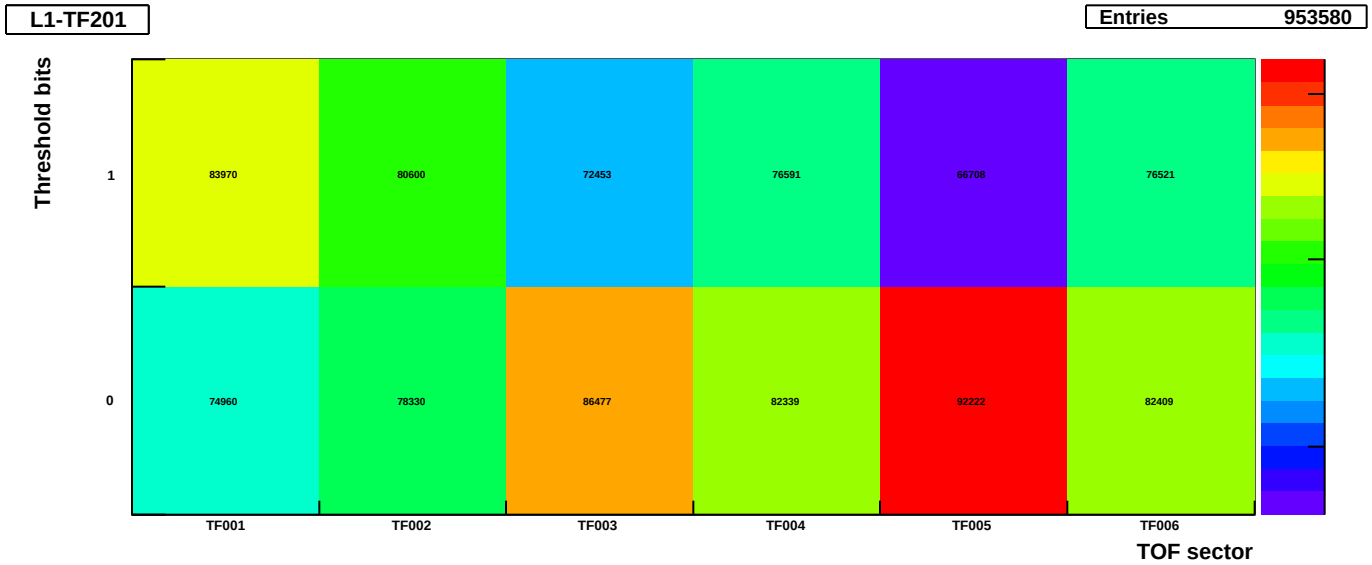

MIX-TF004
Entries 6675060
TOF MULT
TOF tray

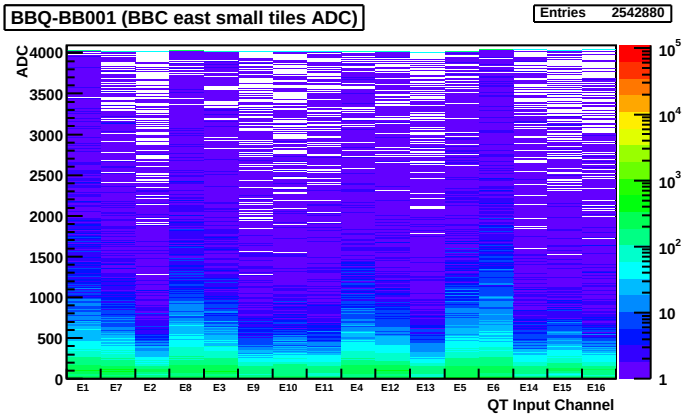

MIX-TF005
Entries 6675060
TOF MULT
TOF tray

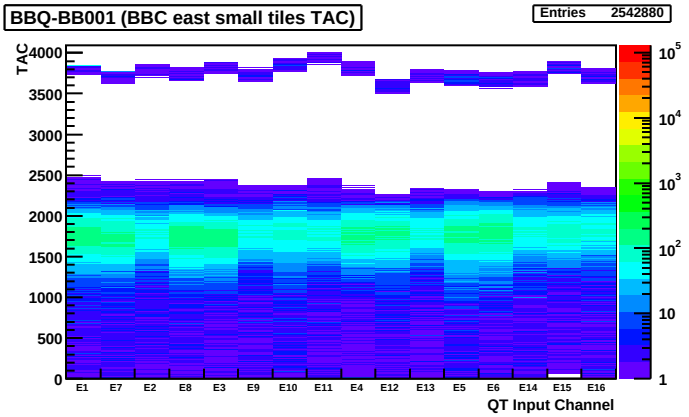

MIX-TF006
Entries 6675060
TOF MULT
TOF tray

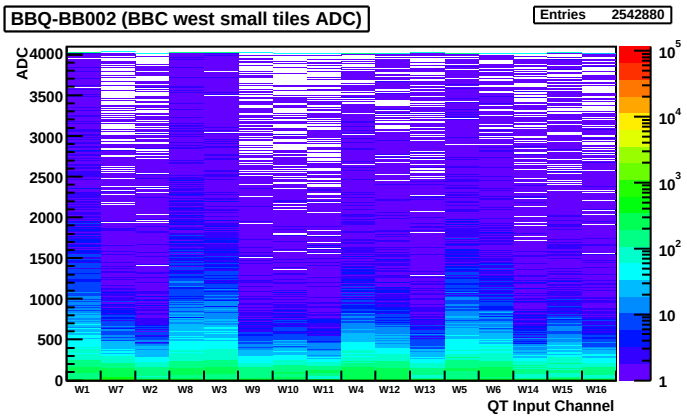

MIX-TF101
Entries 953580
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

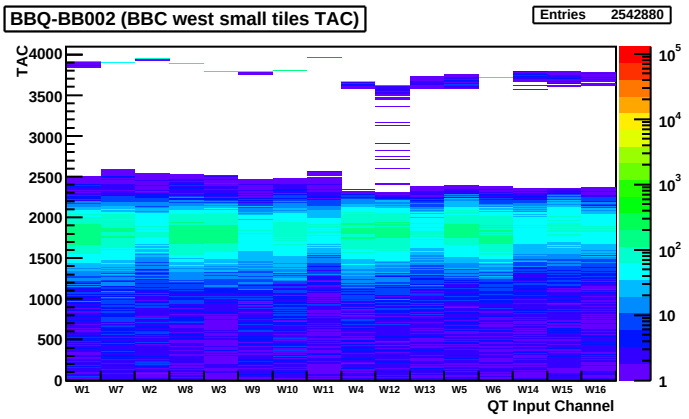

L1-TF201
Entries 158930
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

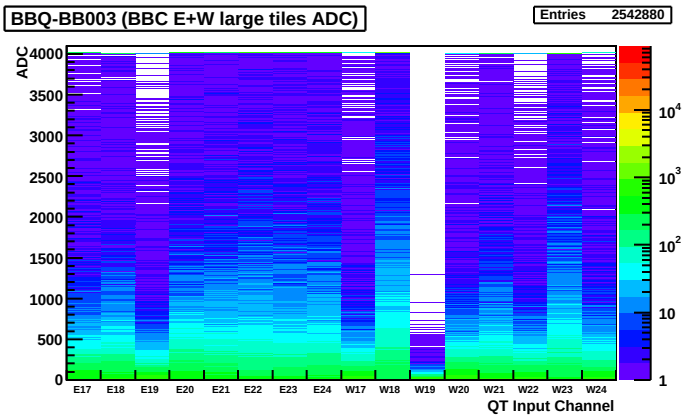

L1-TF201
Entries 953580
Threshold bits
1
0
83970
80600
72453
76591
66708
76521
74960
78330
86477
82339
92222
82409
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

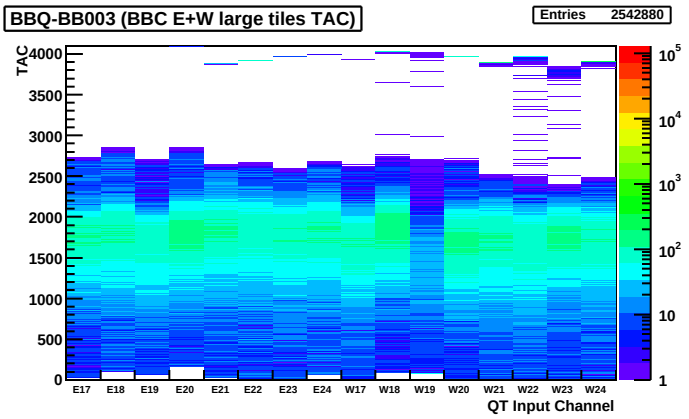

BBQ-BB001 (BBC east small tiles ADC)
Entries 2542880
ADC
QT Input Channel

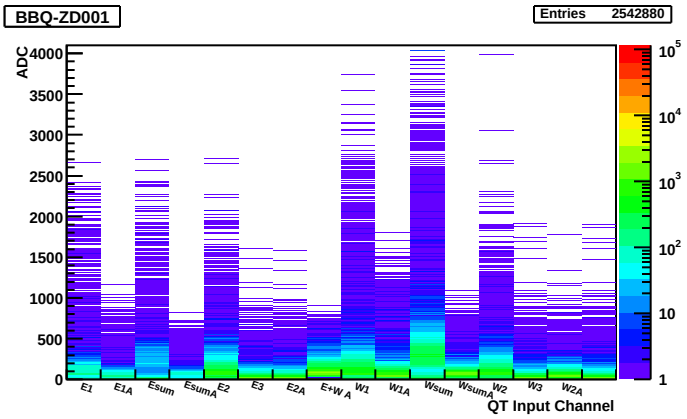

BBQ-BB001 (BBC east small tiles TAC)
Entries 2542880
TAC
QT Input Channel

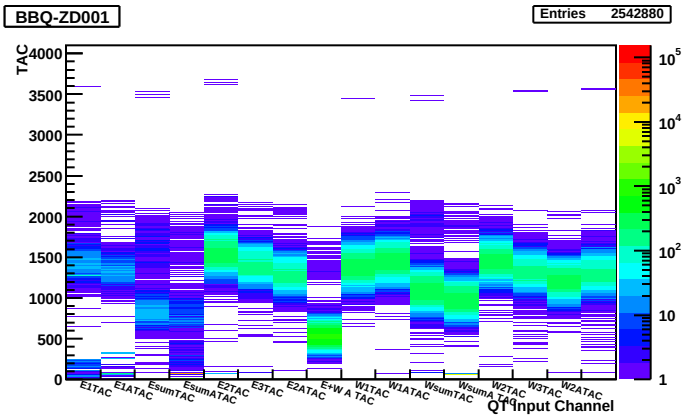

BBQ-BB002 (BBC west small tiles ADC)
Entries 2542880
ADC
QT Input Channel

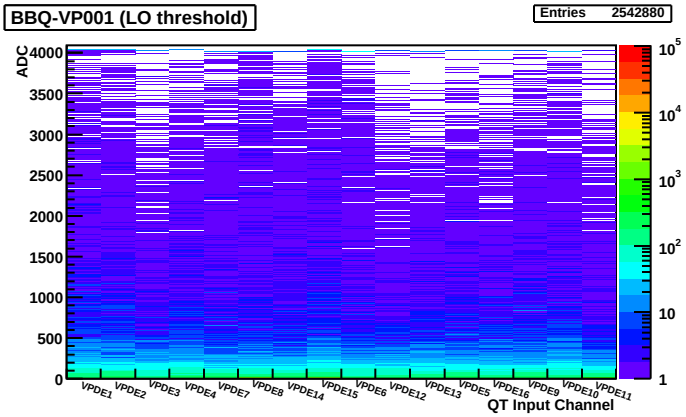

BBQ-BB002 (BBC west small tiles TAC)
Entries 2542880
TAC
QT Input Channel

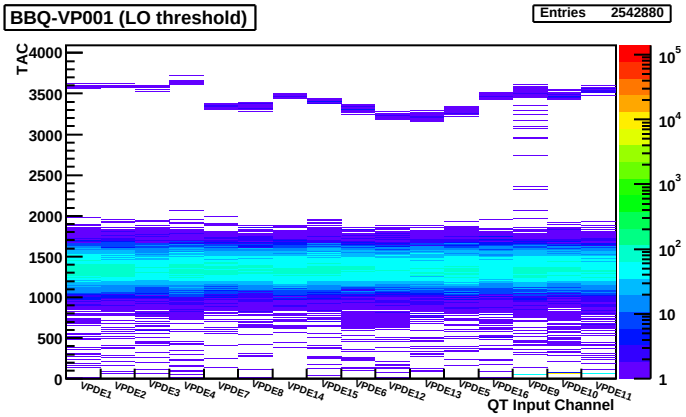

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2542880
ADC
QT Input Channel

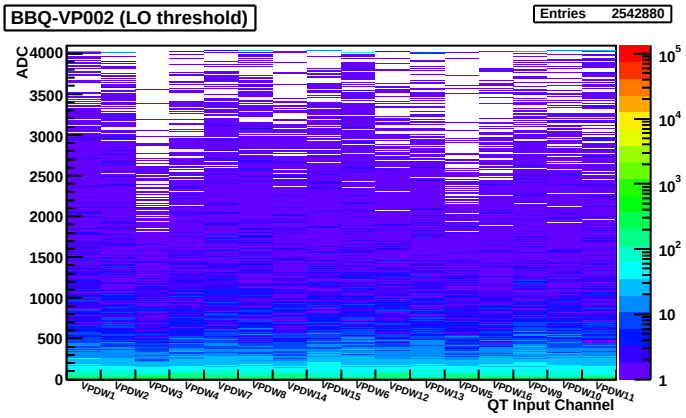

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2542880
TAC
QT Input Channel

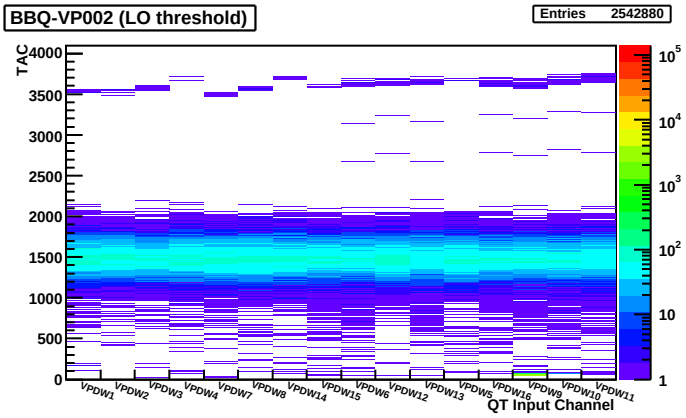

BBQ-ZD001
Entries 2542880
ADC
QT Input Channel

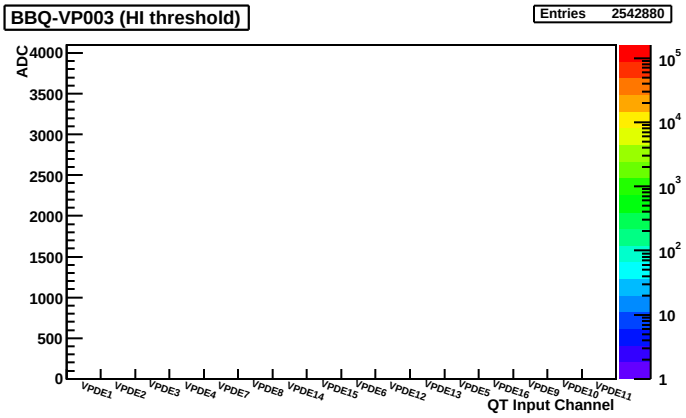

BBQ-ZD001
Entries 2542880
TAC
QT Input Channel

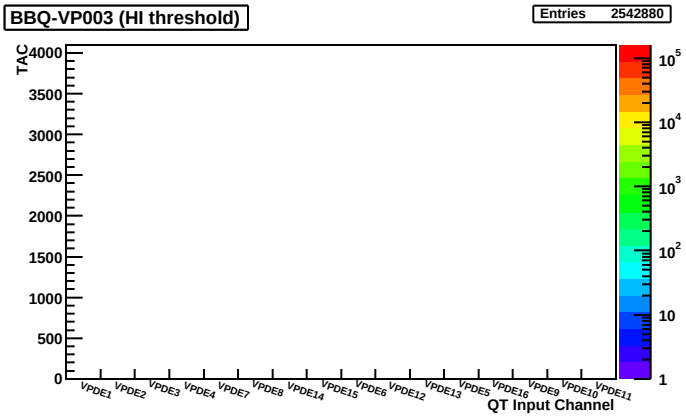

BBQ-VP001 (LO threshold)
Entries 2542880
ADC
QT Input Channel

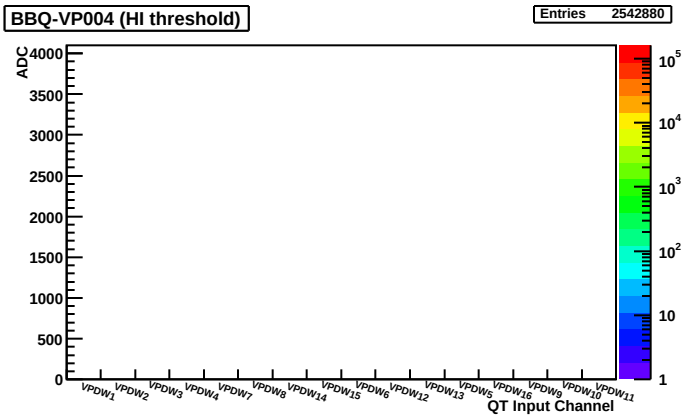

BBQ-VP001 (LO threshold)
Entries 2542880
TAC
QT Input Channel

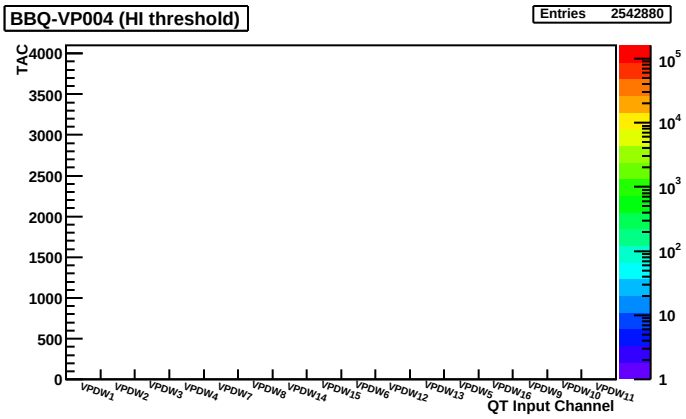

BBQ-VP002 (LO threshold)
Entries 2542880
ADC
QT Input Channel

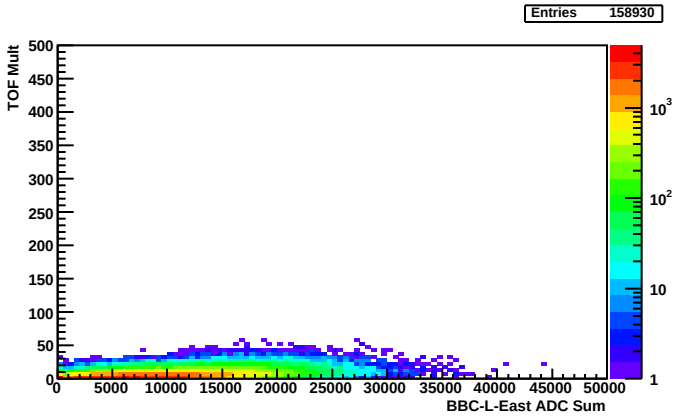

BBQ-VP002 (LO threshold)
Entries 2542880
TAC
QT Input Channel

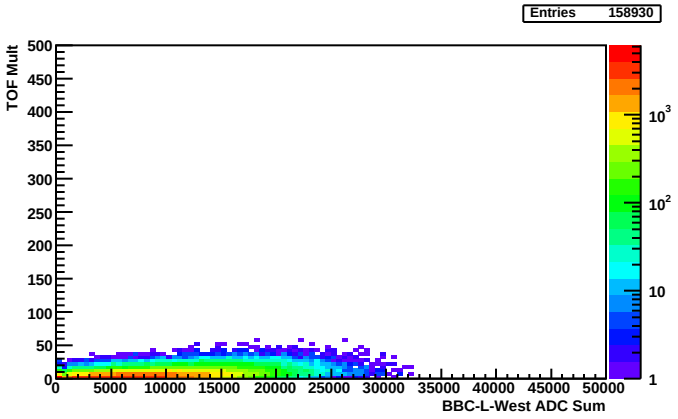

BBQ-VP003 (HI threshold)
Entries 2542880
ADC
QT Input Channel

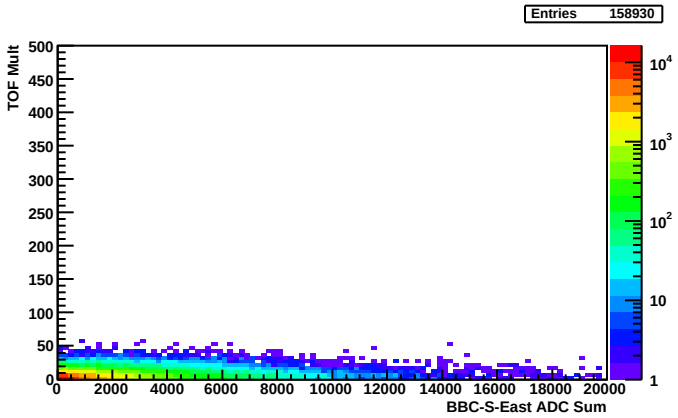

BBQ-VP003 (HI threshold)
Entries 2542880
TAC
QT Input Channel

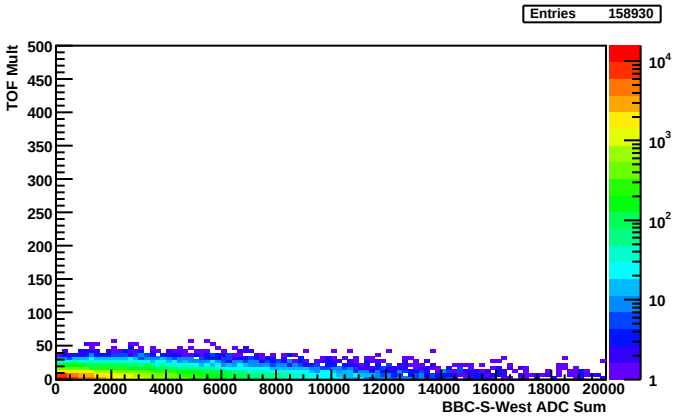

BBQ-VP004 (HI threshold)
Entries 2542880
ADC
QT Input Channel

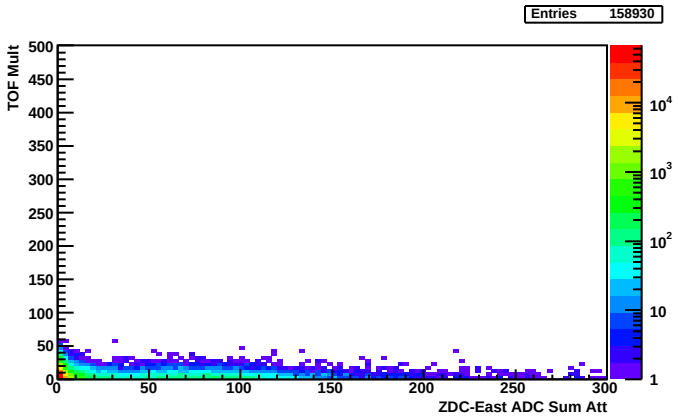

BBQ-VP004 (HI threshold)
Entries 2542880
TAC
QT Input Channel

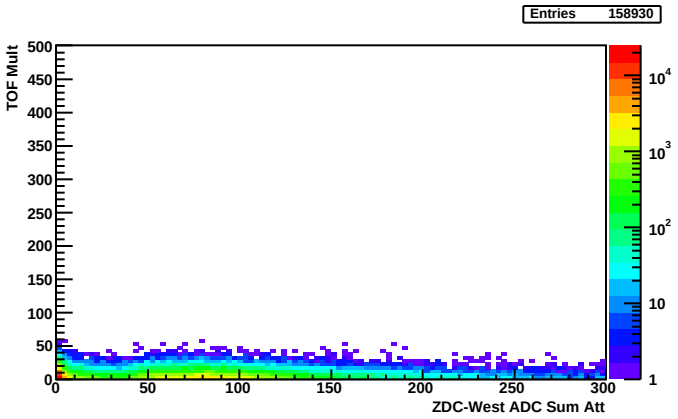

Entries 158930
TOF Mult
BBC-L-East ADC Sum

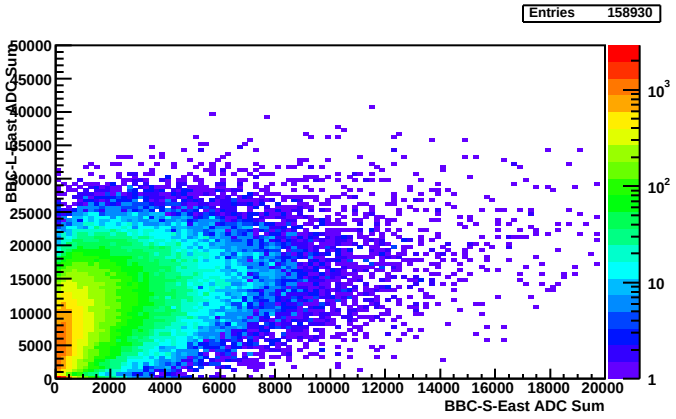

Entries 158930
TOF Mult
BBC-L-West ADC Sum

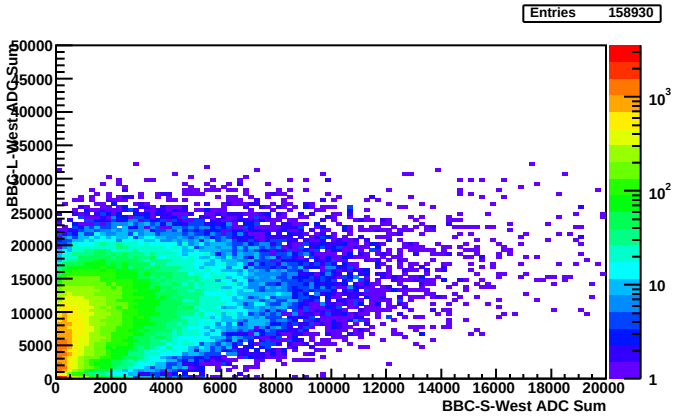

Entries 158930
TOF Mult
BBC-S-East ADC Sum

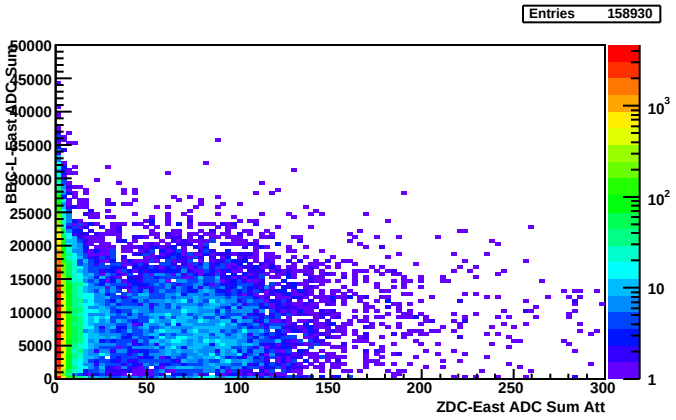

Entries 158930
TOF Mult
BBC-S-West ADC Sum

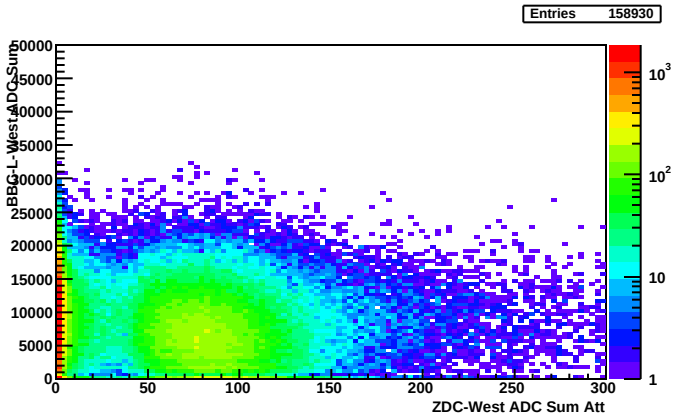

Entries 158930
TOF Mult
ZDC-East ADC Sum Att

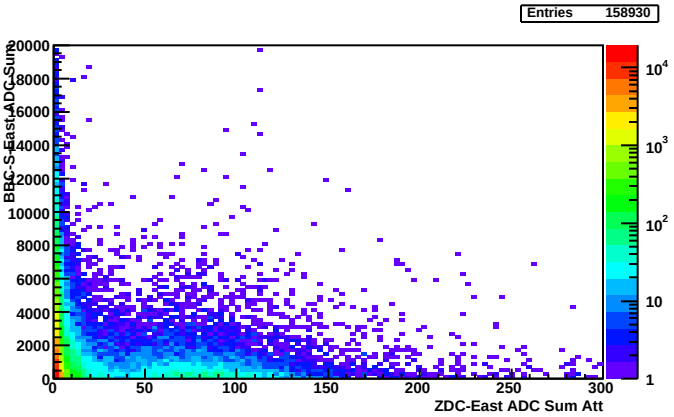

Entries 158930
TOF Mult
ZDC-West ADC Sum Att

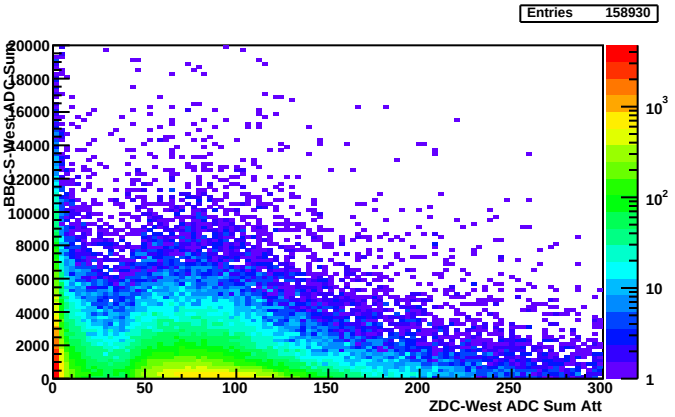

Entries 158930
BBC-L-East ADC Sum
BBC-S-East ADC Sum

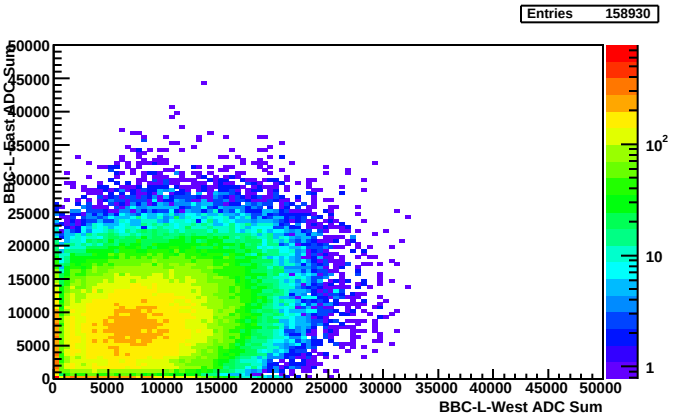

Entries 158930
BBC-L-West ADC Sum
BBC-S-West ADC Sum

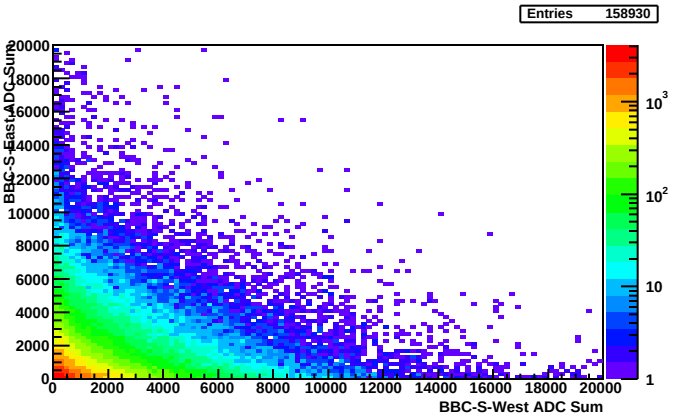

Entries 158930
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

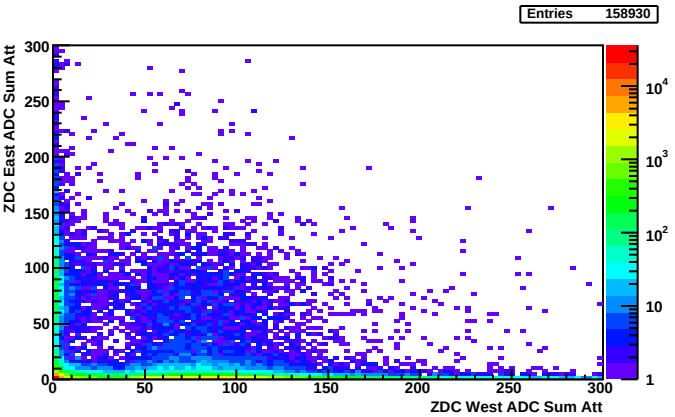

Entries 158930
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

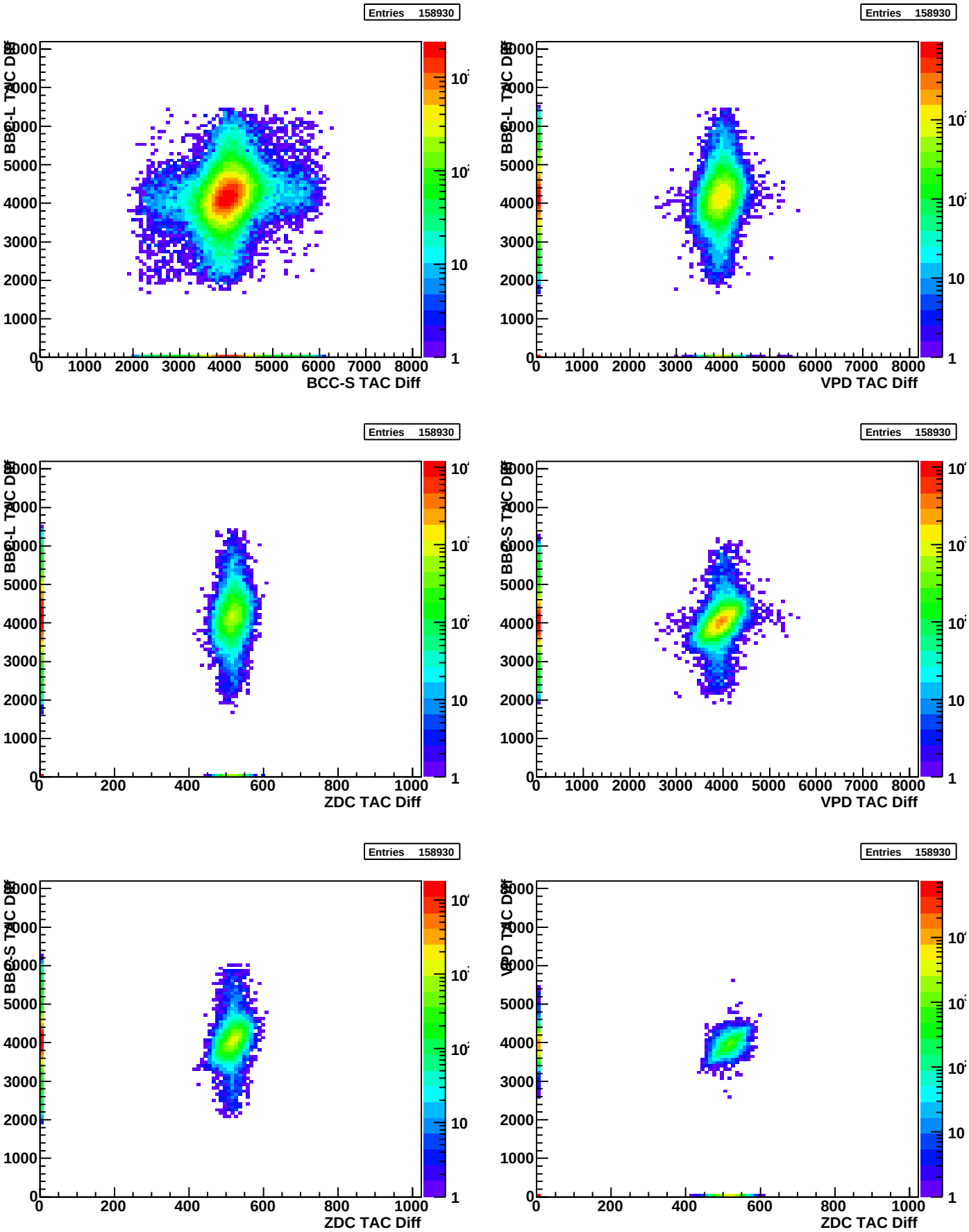

Entries 158930
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

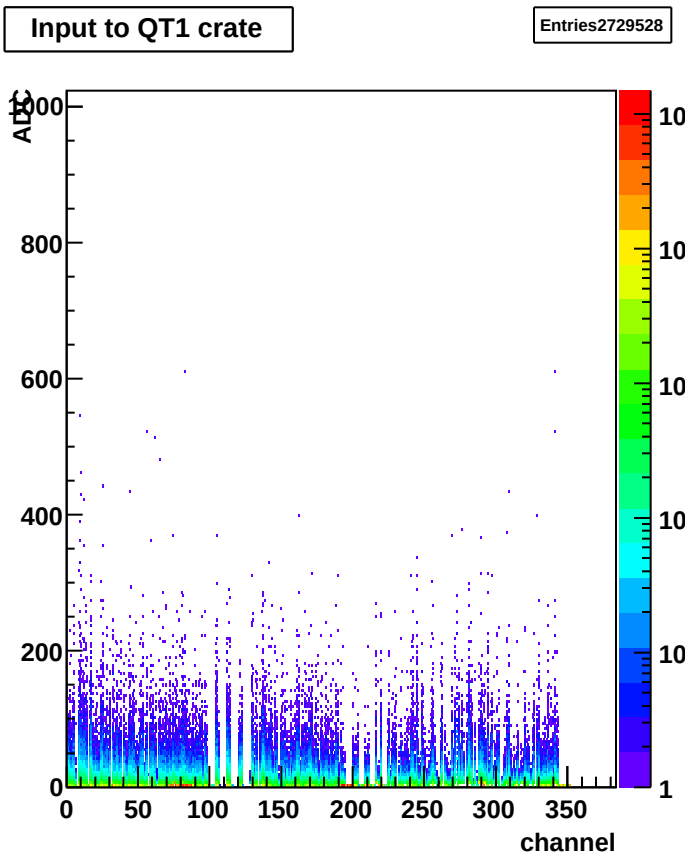

Entries 158930
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

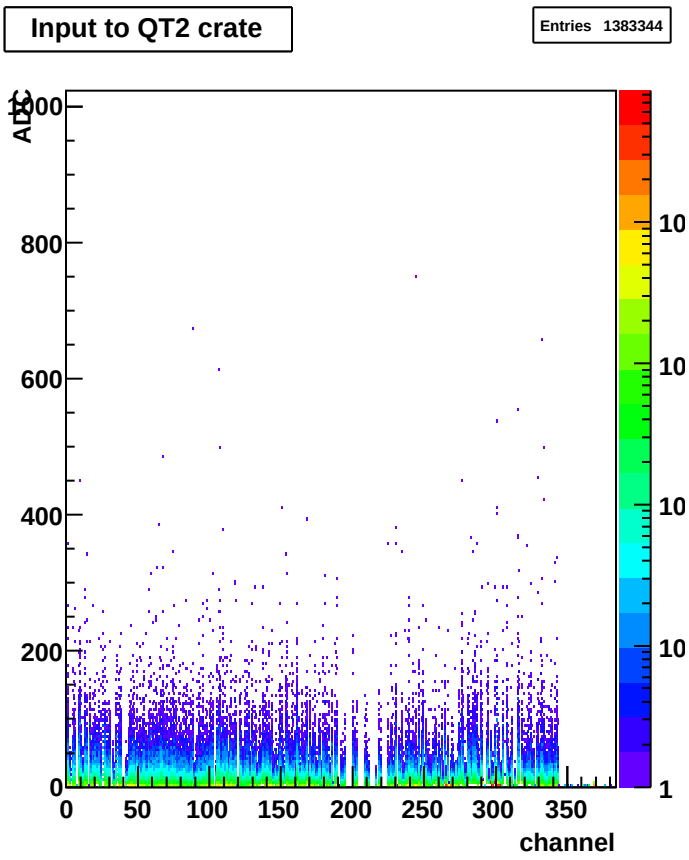

Entries 158930
BBC-L-East ADC Sum
BBC-L-West ADC Sum

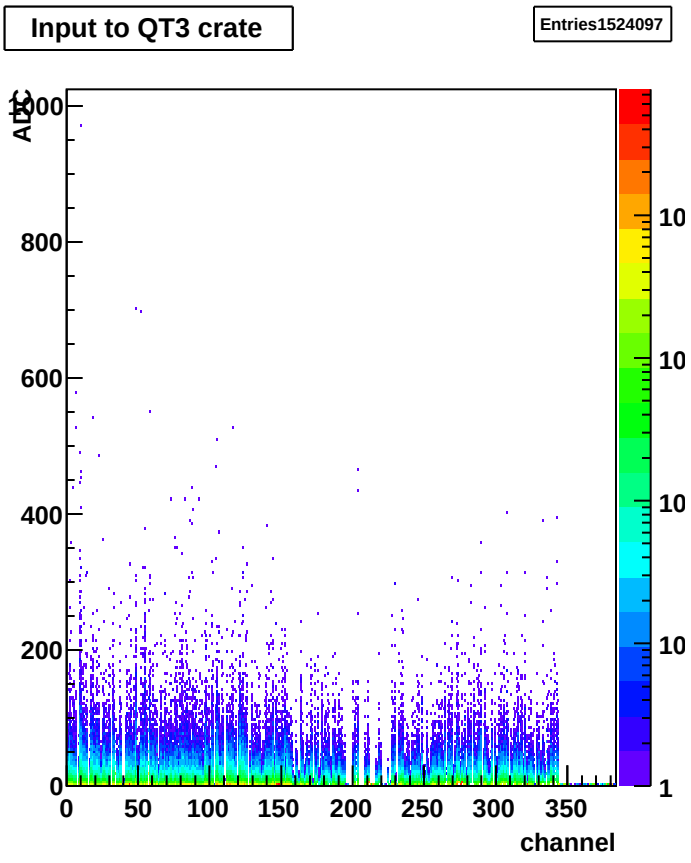

Entries 158930
BBC-S-East ADC Sum
BBC-S-West ADC Sum

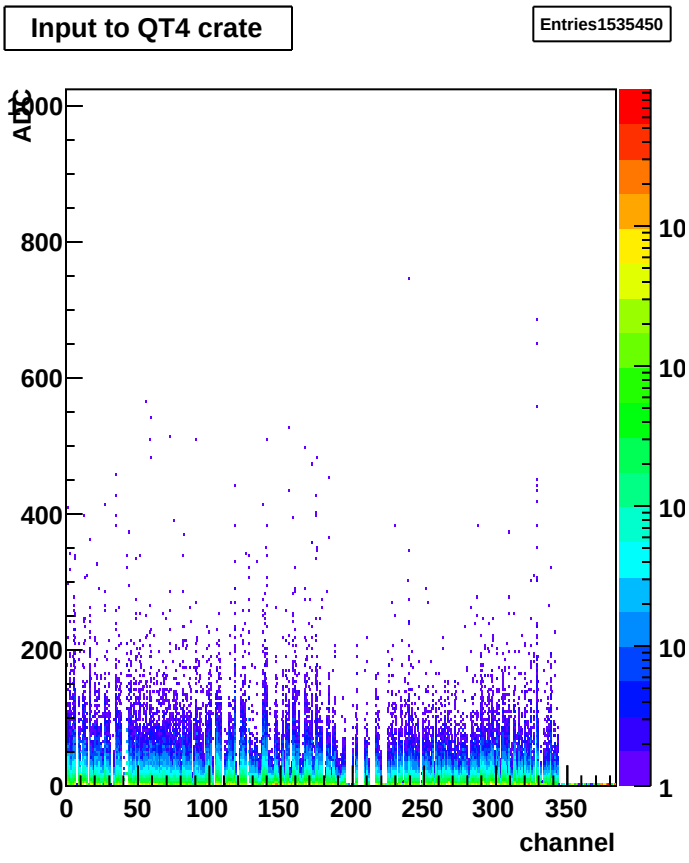

Entries 158930
ZDC East ADC Sum Att
ZDC West ADC Sum Att

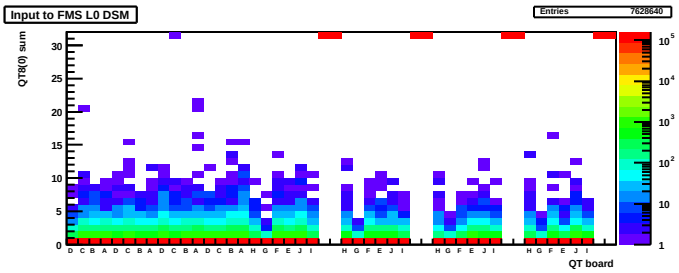

Entries 158930
BBC-L TAC Diff
BCC-S TAC Diff
Entries 158930
BBC-L TAC Diff
VPD TAC Diff
Entries 158930
BBC-L TAC Diff
ZDC TAC Diff
Entries 158930
BBC-S TAC Diff
VPD TAC Diff
Entries 158930
BBC-S TAC Diff
ZDC TAC Diff
Entries 158930
VPD TAC Diff
ZDC TAC Diff

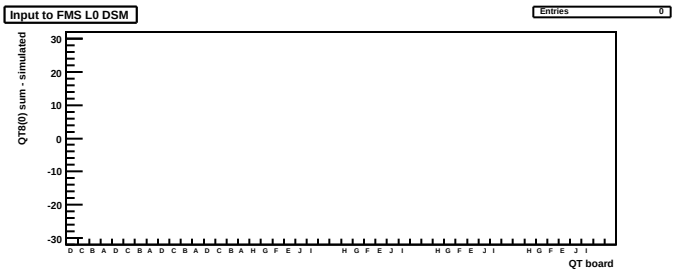

Input to QT1 crate
Entries2729528
ADC
channel

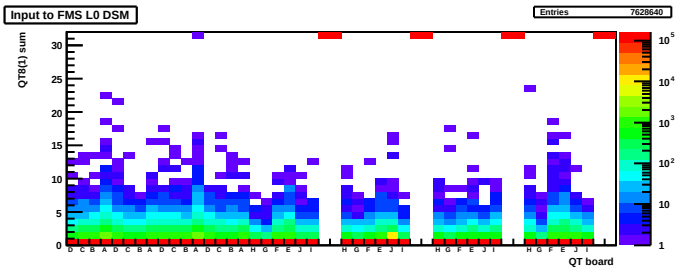

Input to QT2 crate
Entries 1383344
ADC
channel

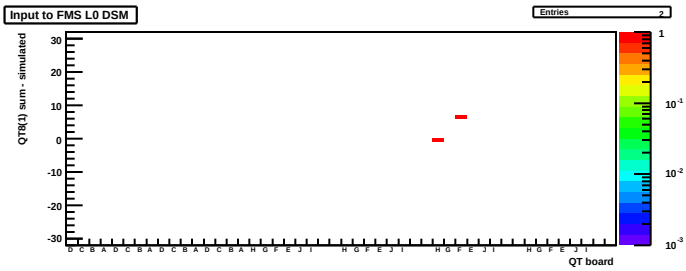

Input to QT3 crate
Entries1524097
ADC
channel

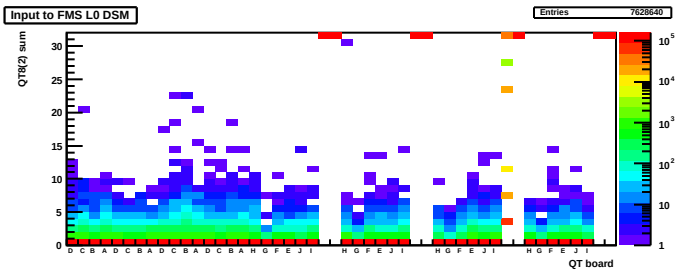

Input to QT4 crate
Entries1535450
ADC
channel

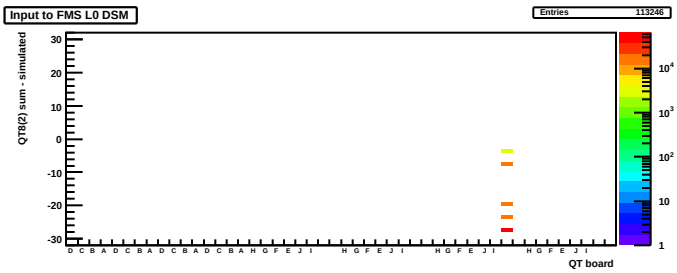

Input to FMS L0 DSM
Entries 7628640
QT8(0) sum
QT board

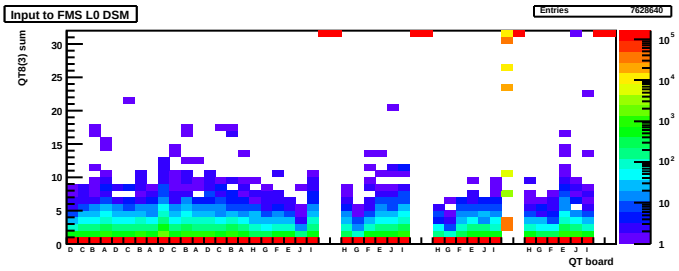

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

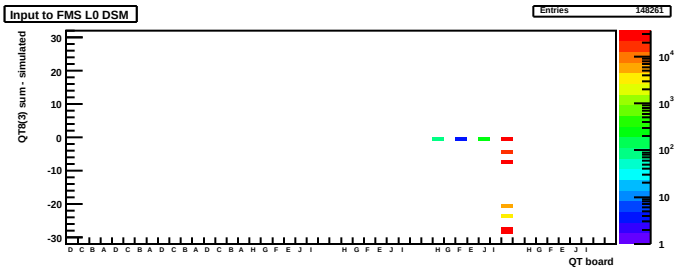

Input to FMS L0 DSM
Entries 7628640
QT8(1) sum
QT board

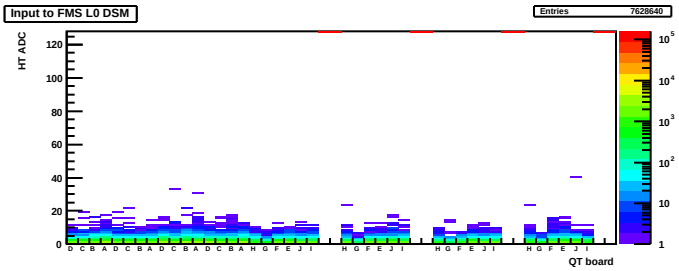

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

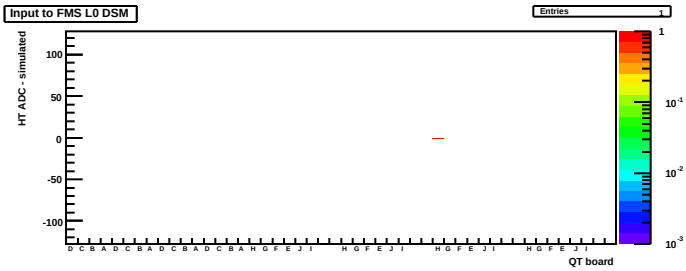

Input to FMS L0 DSM
Entries 7628640
QT8(2) sum
QT board

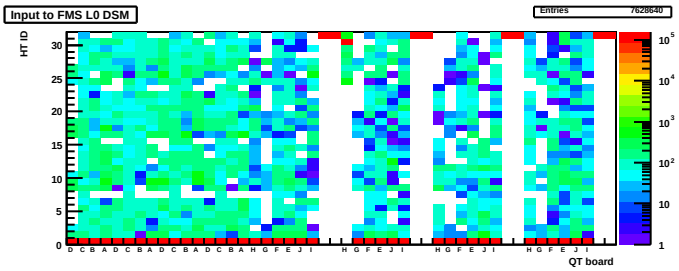

Input to FMS L0 DSM
Entries 113246
QT8(2) sum - simulated
QT board

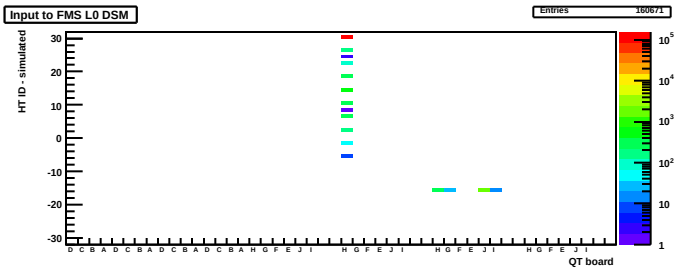

Input to FMS L0 DSM
Entries 7628640
QT8(3) sum
QT board

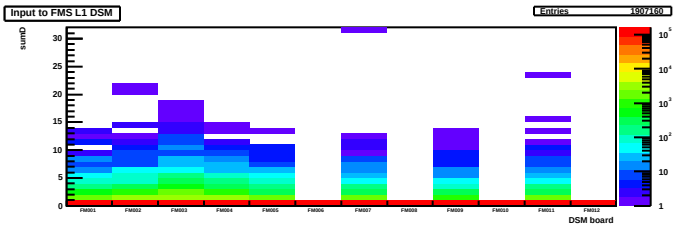

Input to FMS L0 DSM
Entries 148261
QT8(3) sum - simulated
QT board

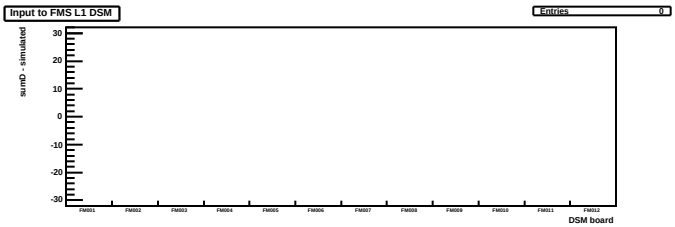

Input to FMS L0 DSM
Entries 7628640
HT ADC
QT board

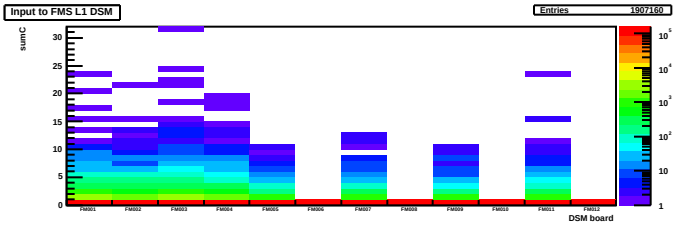

Input to FMS L0 DSM
Entries 1
HT ADC - simulated
QT board

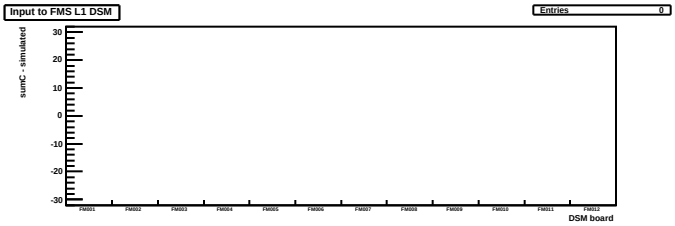

Input to FMS L0 DSM
Entries 7628640
HT ID
QT board

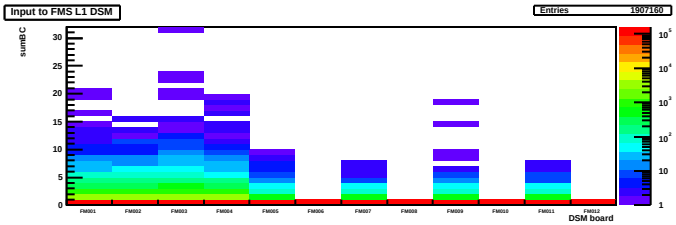

Input to FMS L0 DSM
Entries 160671
HT ID - simulated
QT board

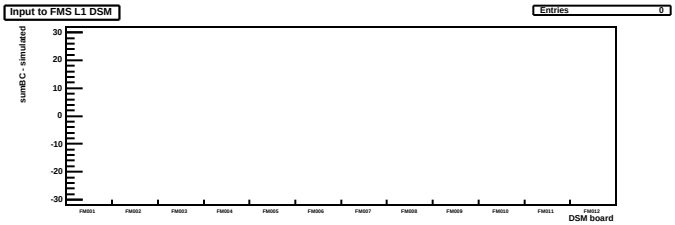

Input to FMS L1 DSM
Entries 1907160
sumD
DSM board

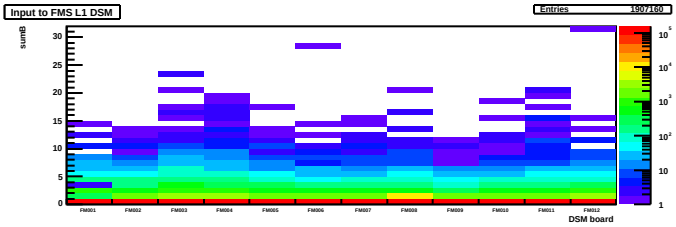

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

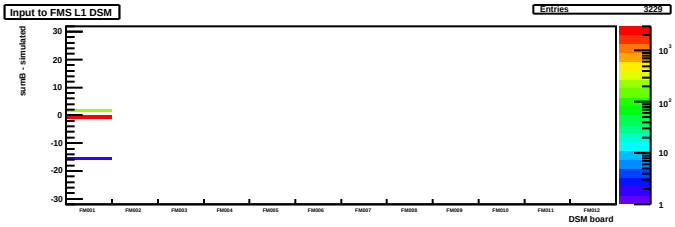

Input to FMS L1 DSM
Entries 1907160
sumC
DSM board

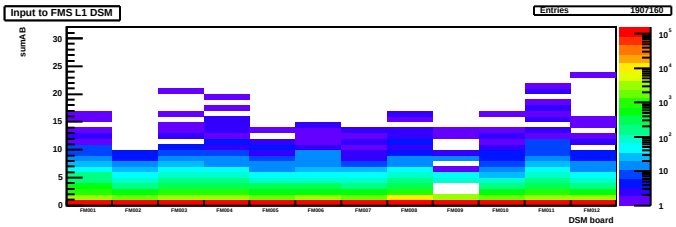

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

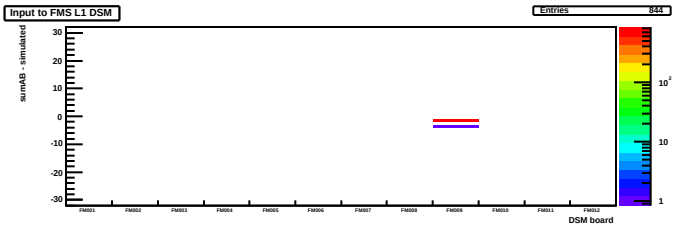

Input to FMS L1 DSM
Entries 1907160
sumBC
DSM board

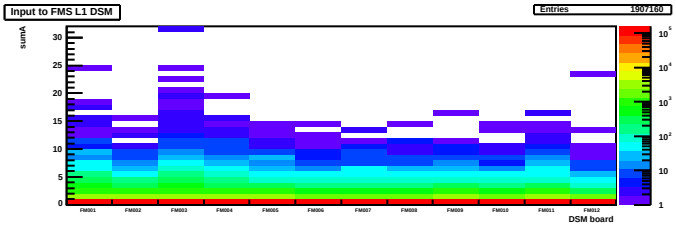

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

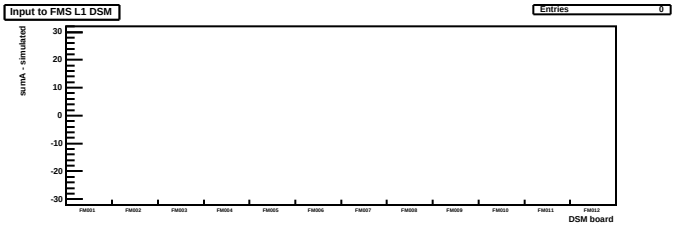

Input to FMS L1 DSM
Entries 1907160
sumB
DSM board

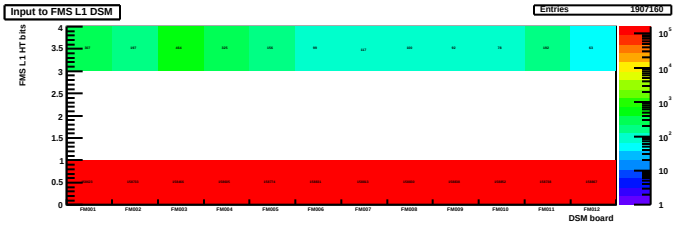

Input to FMS L1 DSM
Entries 3229
sumB - simulated
DSM board

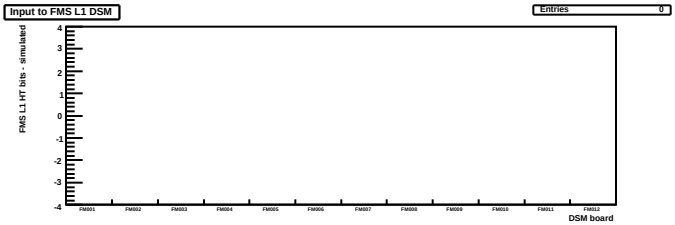

Input to FMS L1 DSM
Entries 1907160
sumAB
DSM board

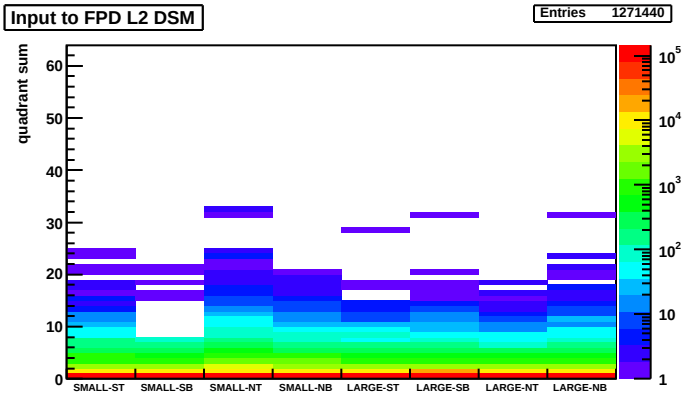

Input to FMS L1 DSM
Entries 844
sumAB - simulated
DSM board

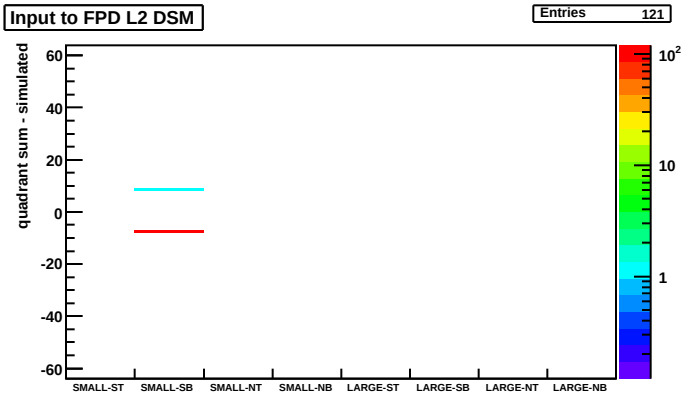

Input to FMS L1 DSM
Entries 1907160
sumA
DSM board

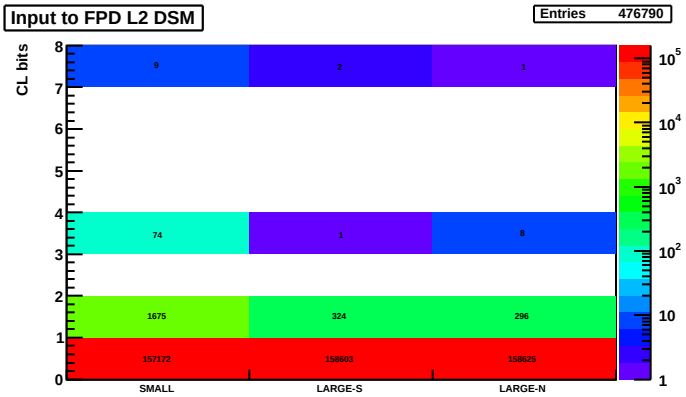

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

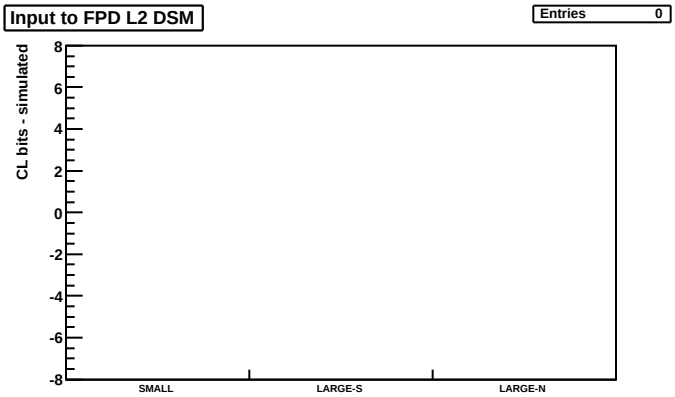

Input to FMS L1 DSM
Entries 1907160
FMS L1 HT bits
DSM board

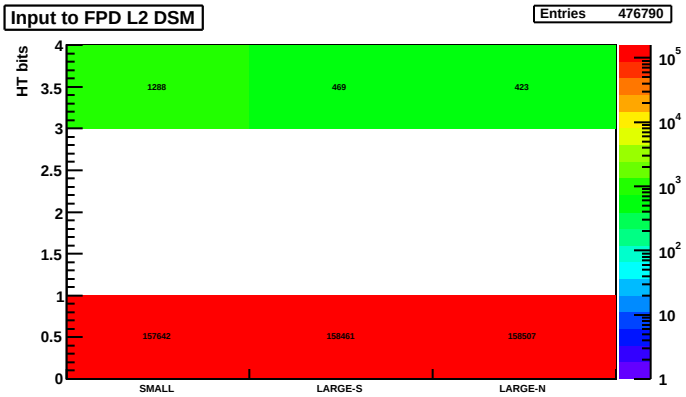

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

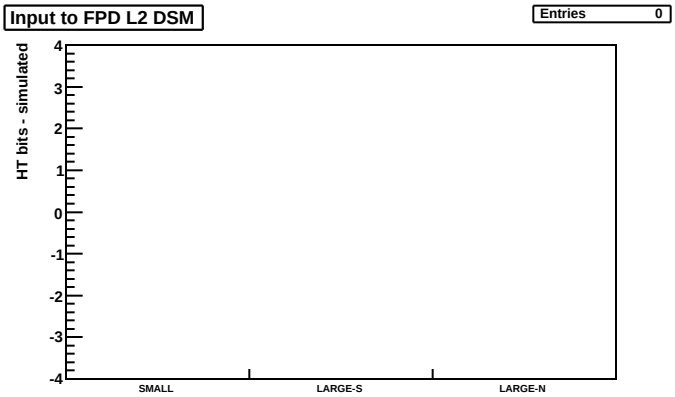

Input to FPD L2 DSM
Entries 1271440
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

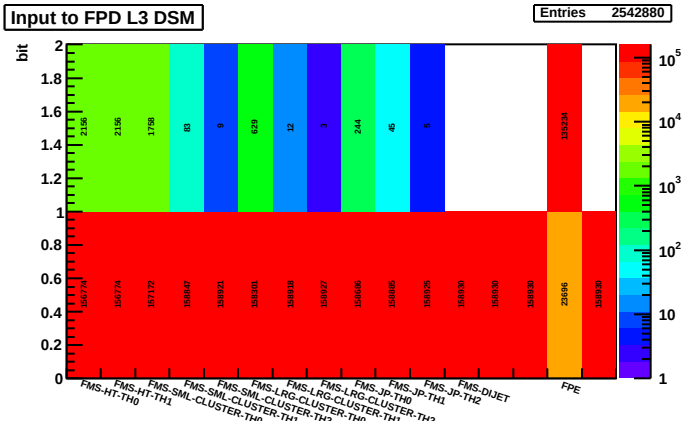

Input to FPD L2 DSM
Entries 121
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

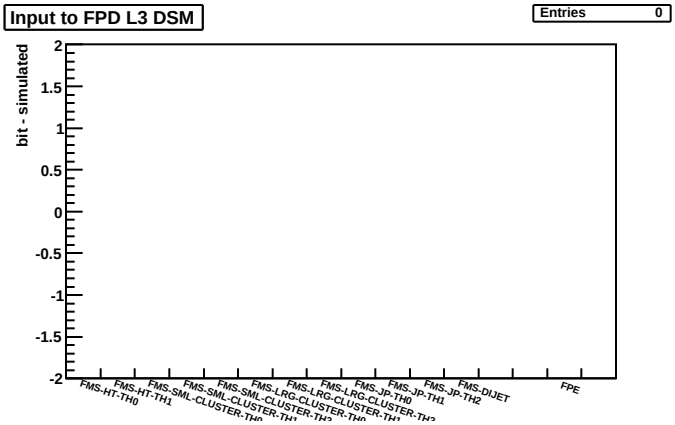

Input to FPD L2 DSM
Entries 476790
CL bits
9
2
1
74
1
8
1675
324
296
157172
158603
158625
SMALL
LARGE-S
LARGE-N

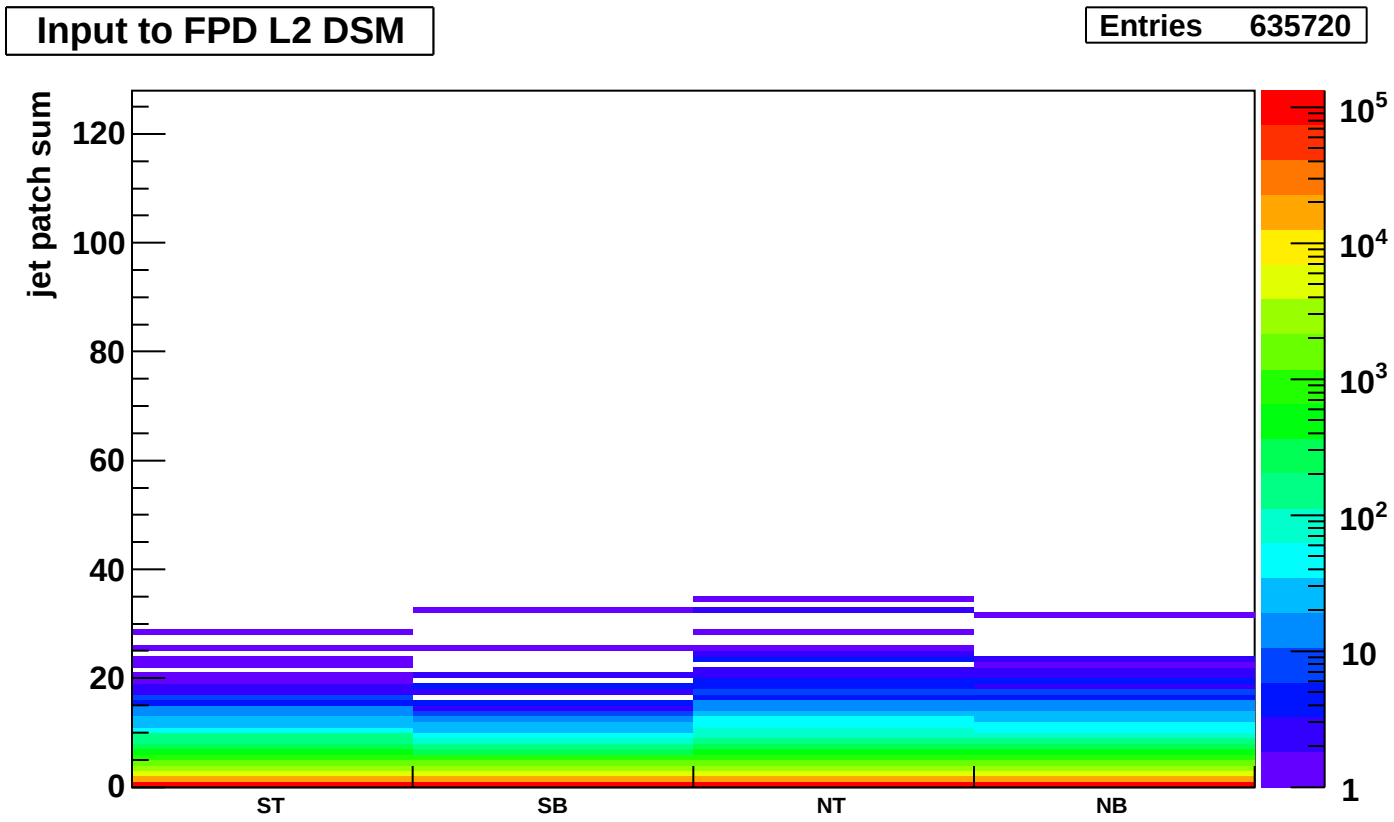

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

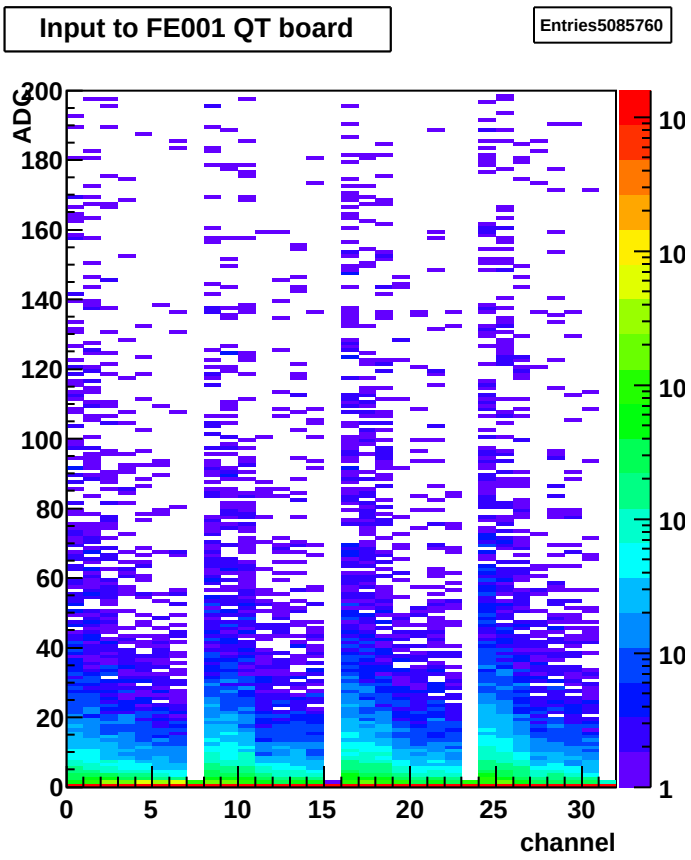

Input to FPD L2 DSM
Entries 476790
HT bits
1288
469
423
157642
158461
158507
SMALL
LARGE-S
LARGE-N

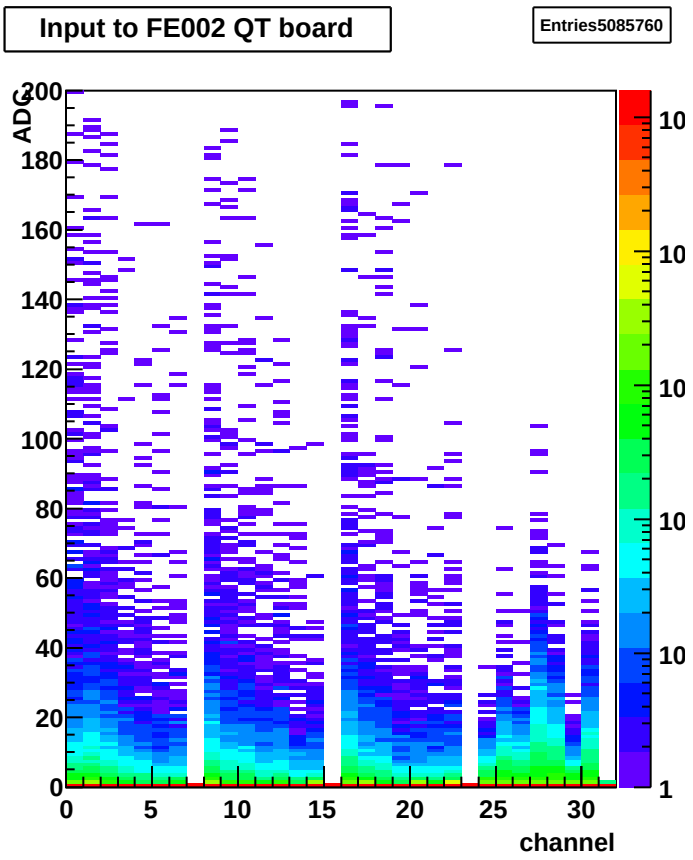

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

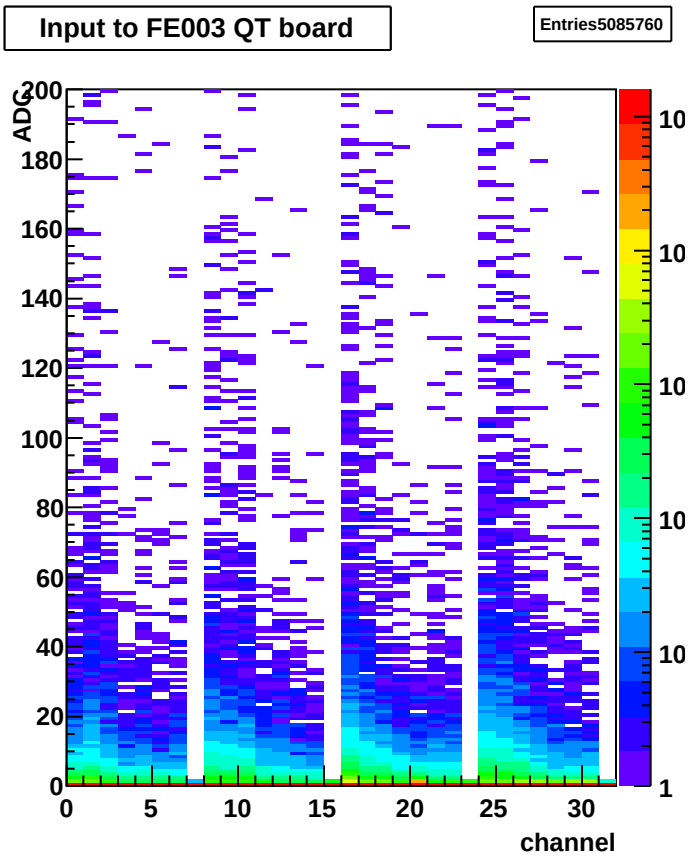

Input to FPD L3 DSM
Entries 2542880
bit
2136
2156
1798
83
9
629
12
3
244
45
5
135234
155174
156774
157172
158847
158921
158301
158918
158927
158686
158085
158925
158930
158930
158930
22696
158930
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

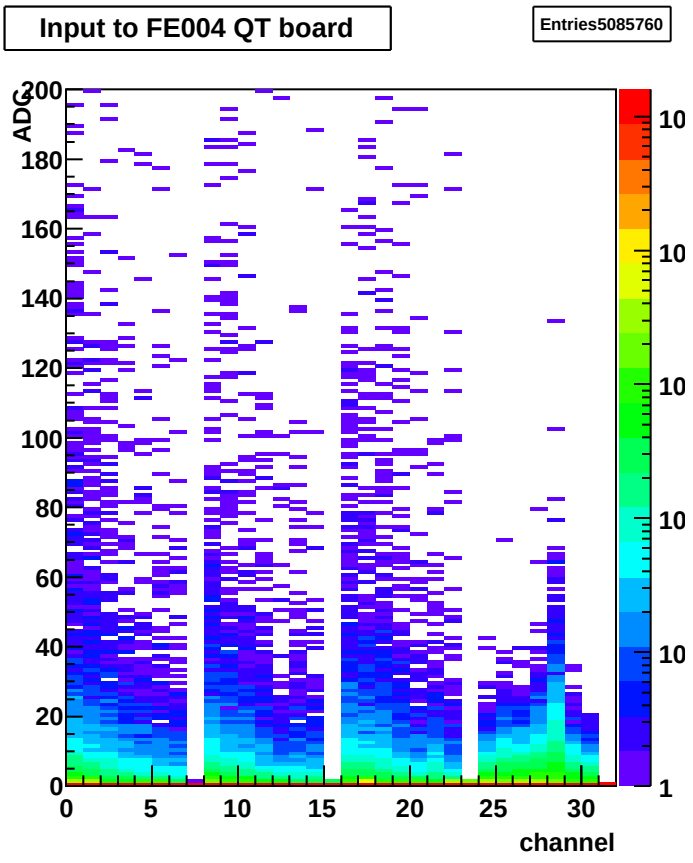

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

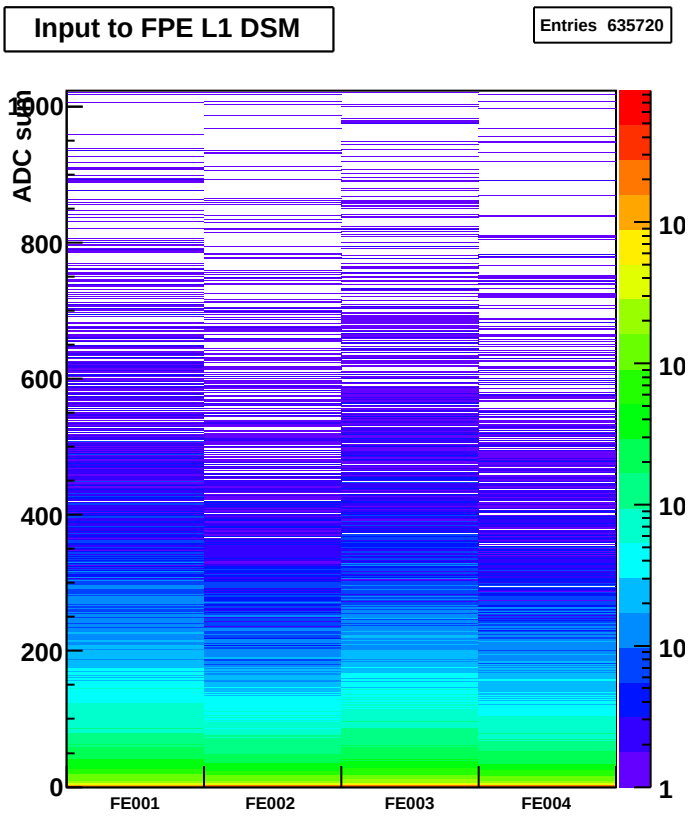

Input to FPD L2 DSM
Entries 635720
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

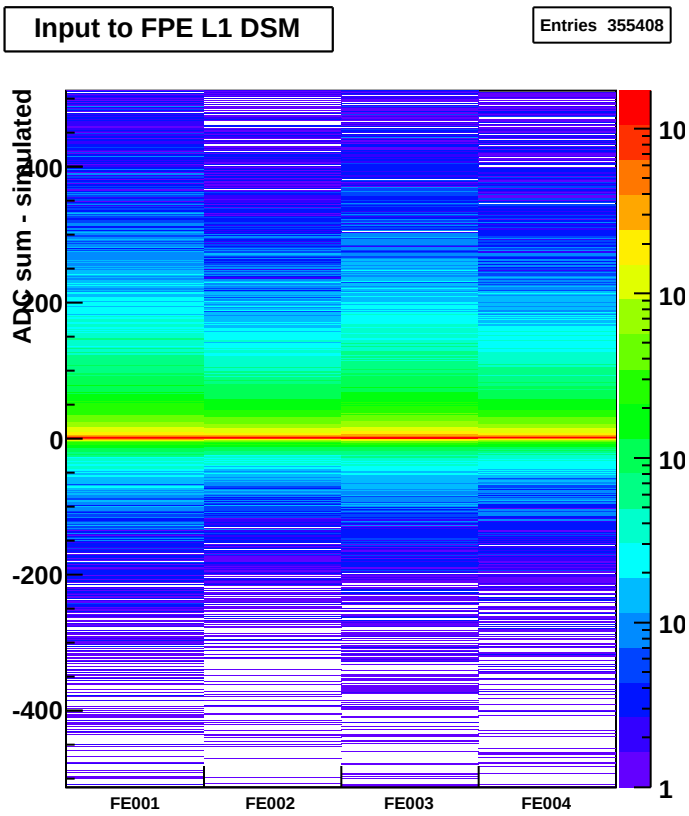

Input to FE001 QT board
Entries5085760
ADC
channel

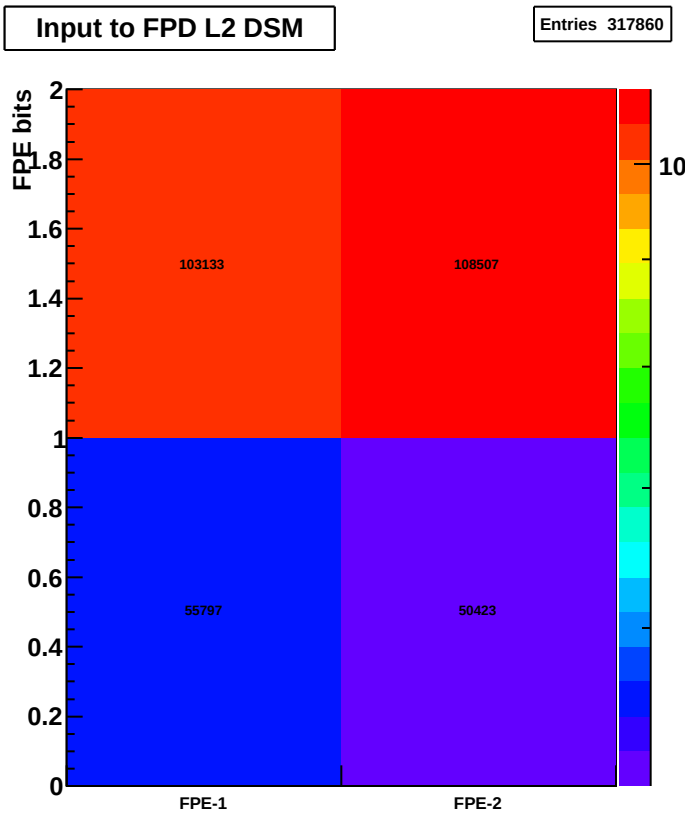

Input to FE002 QT board
Entries5085760
ADC
channel

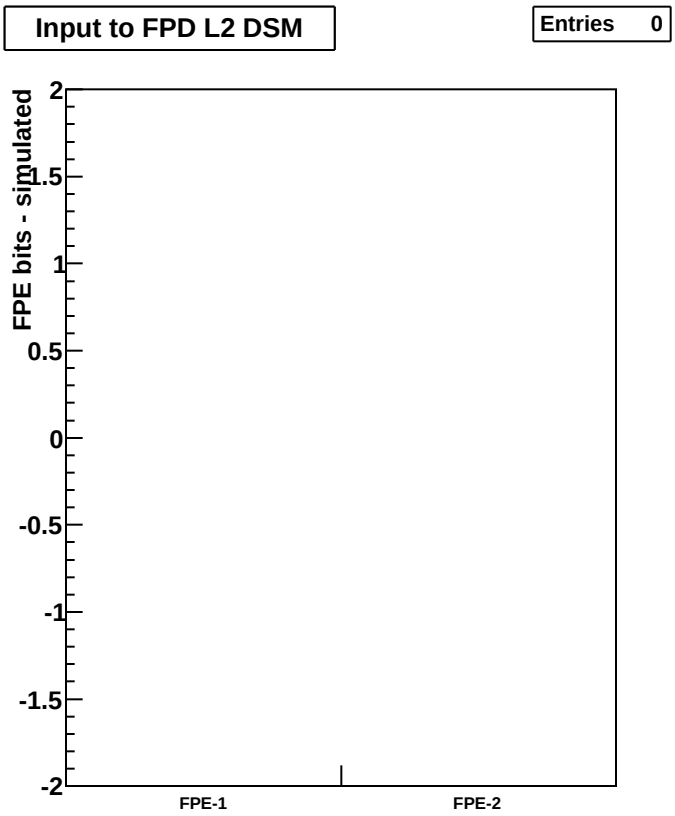

Input to FE003 QT board
Entries5085760
ADC
channel


Input to FE004 QT board
Entries5085760
ADC
channel


Input to FPE L1 DSM
Entries 635720
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 355408
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 317860
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
103133
108507
55797
50423
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2