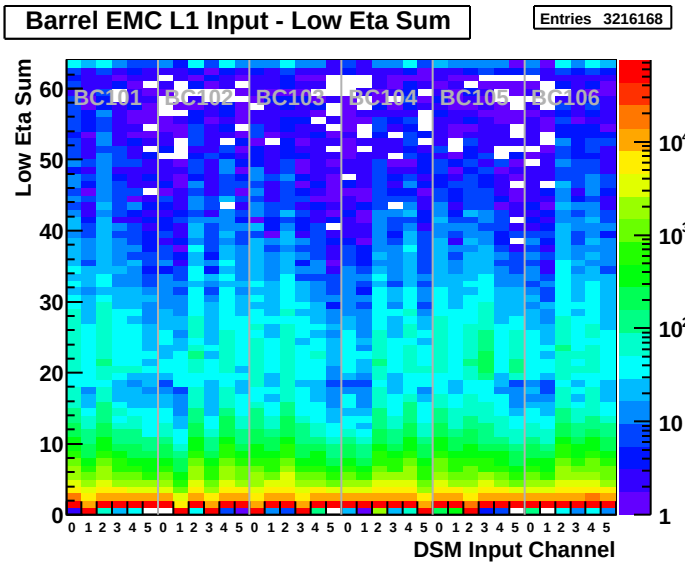
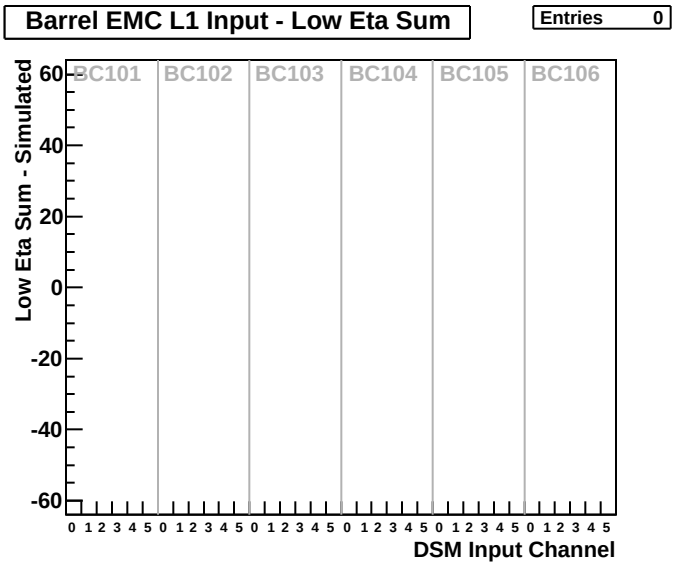
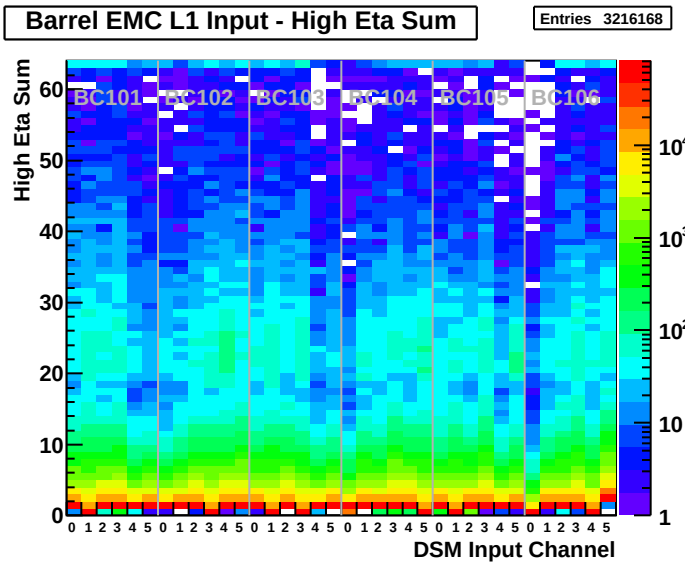
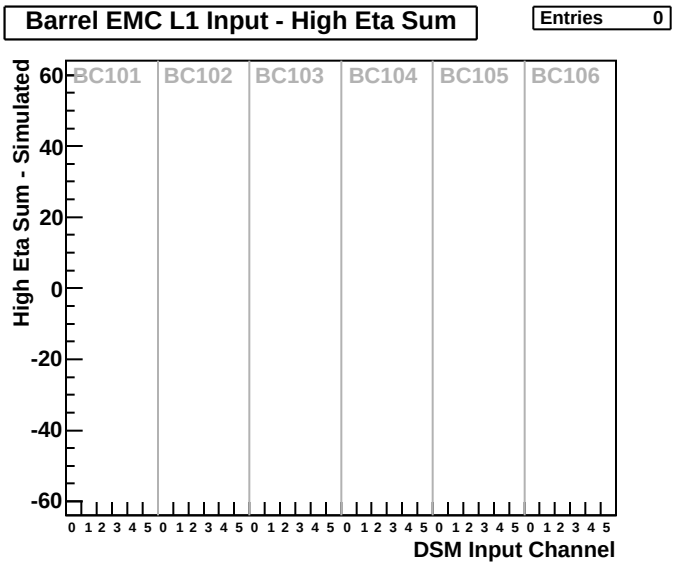
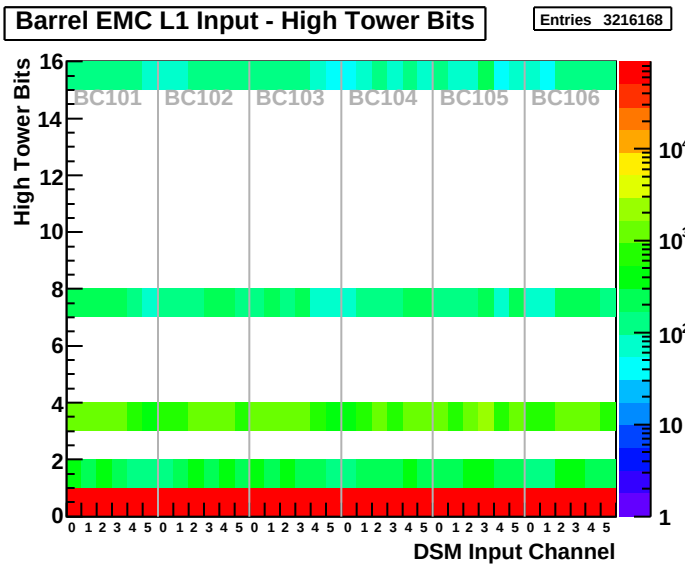
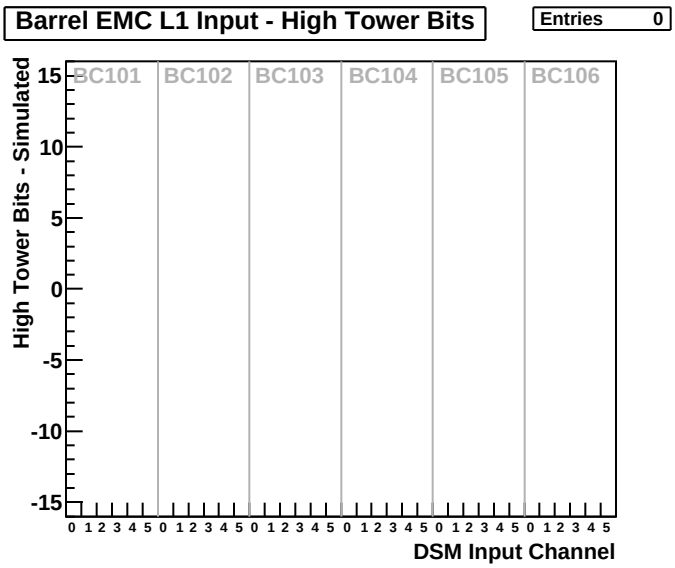

Barrel EMC L1 Input - Low Eta Sum
Entries 3216168
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

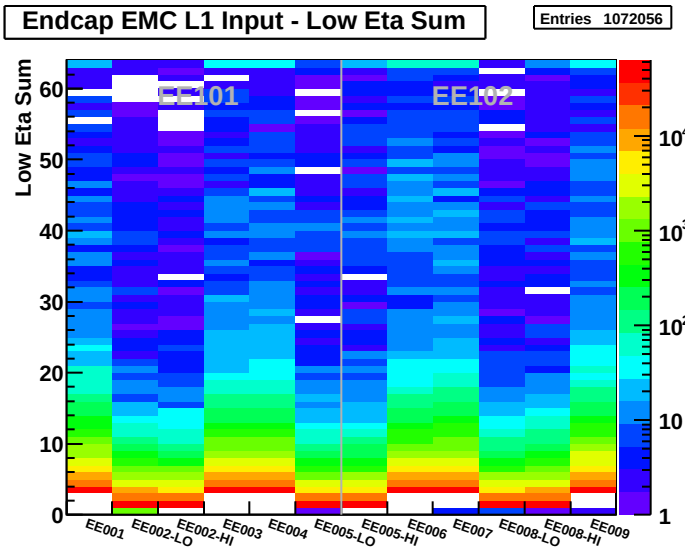

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

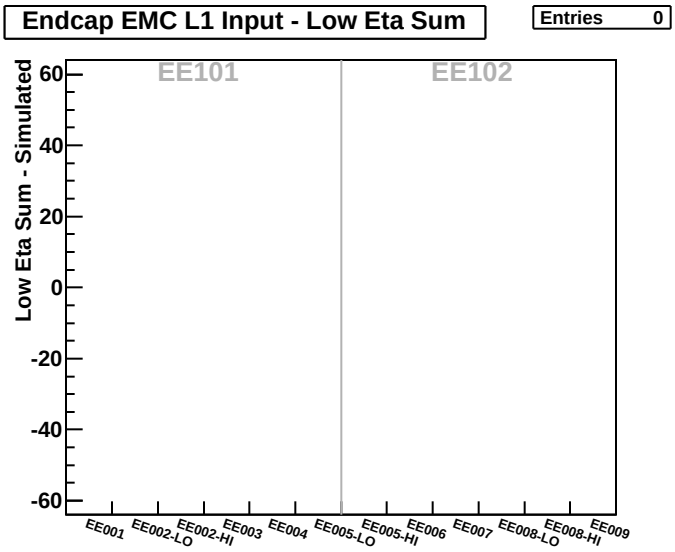

Barrel EMC L1 Input - High Eta Sum
Entries 3216168
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

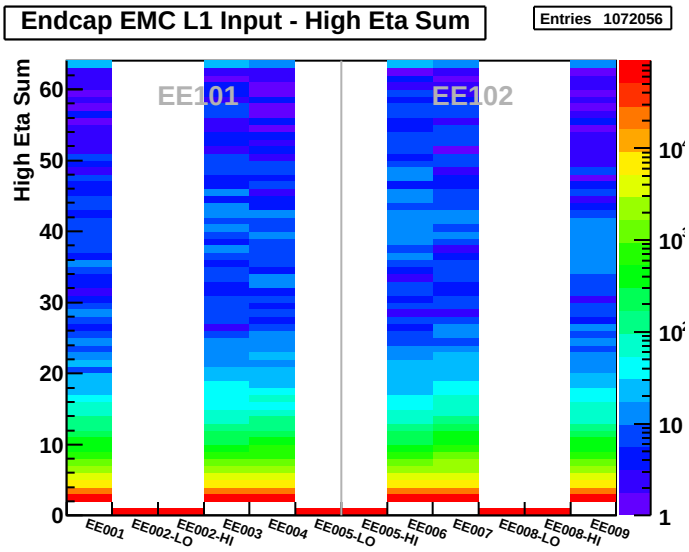

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

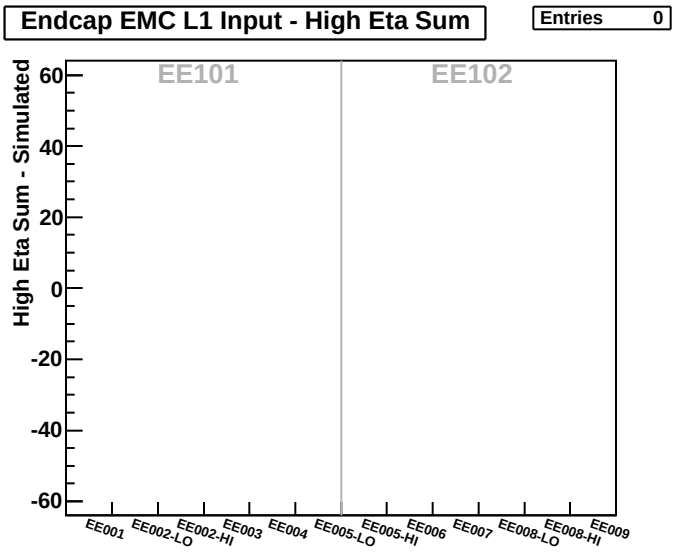

Barrel EMC L1 Input - High Tower Bits
Entries 3216168
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

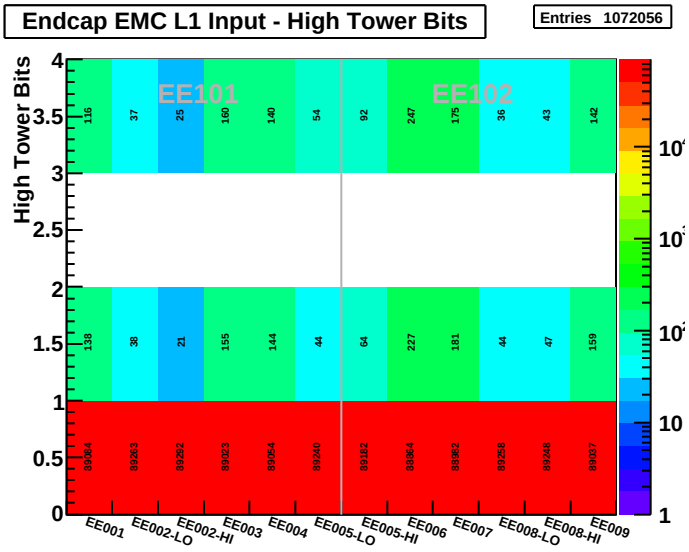

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

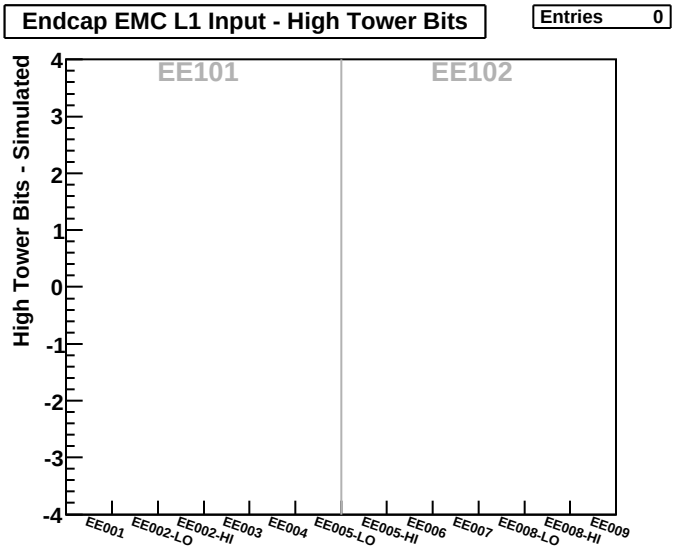

Endcap EMC L1 Input - Low Eta Sum
Entries 1072056
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

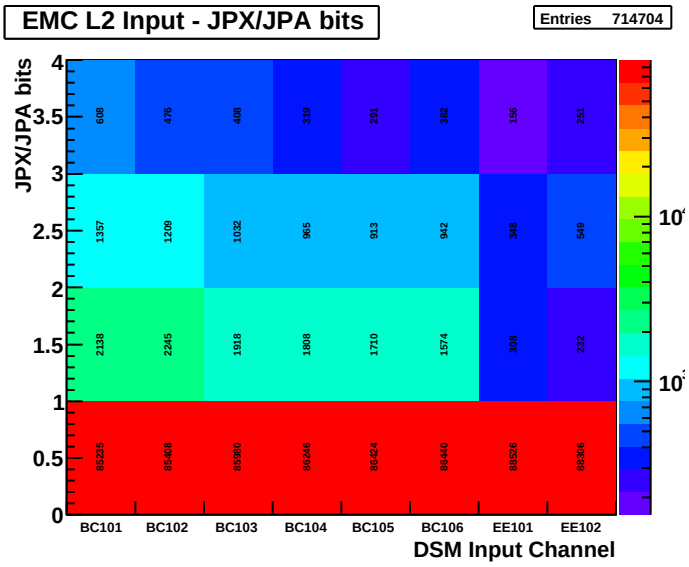

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

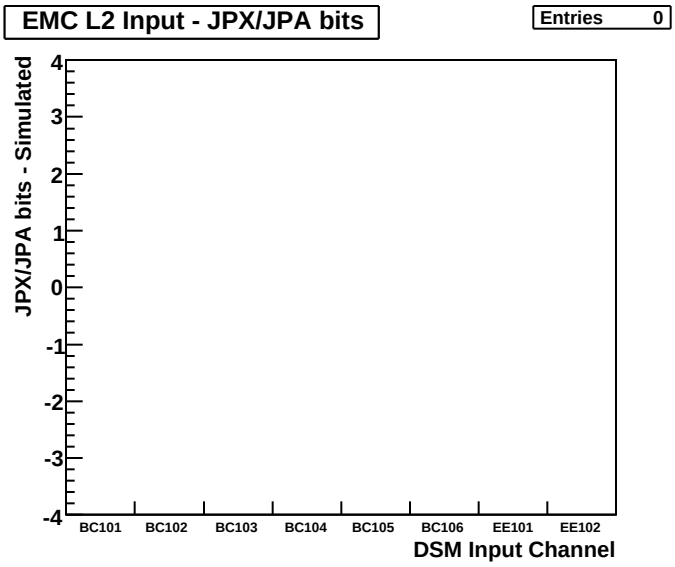

Endcap EMC L1 Input - High Eta Sum
Entries 1072056
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

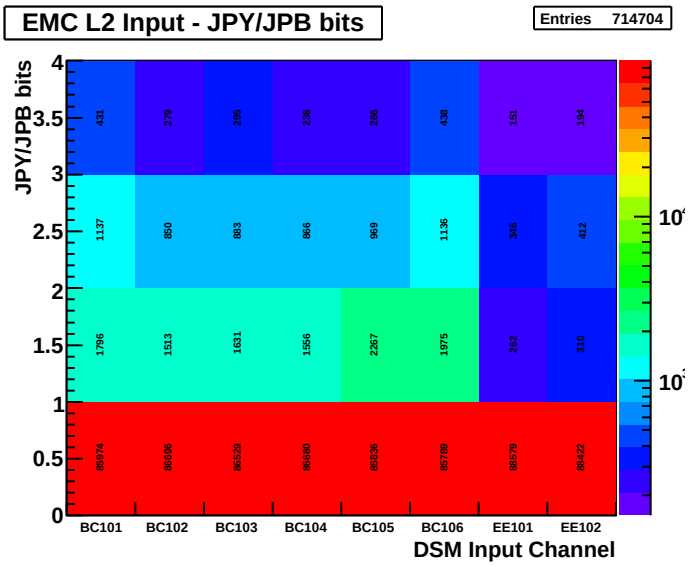

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

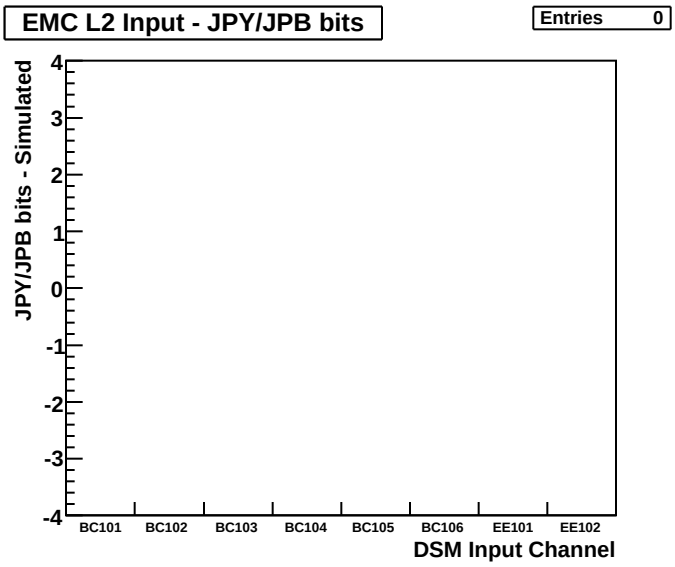

Endcap EMC L1 Input - High Tower Bits
Entries 1072056
High Tower Bits
EE101
EE102
116 37 25 160 140 54 92 247 175 36 43 142
138 38 21 155 144 44 64 227 181 44 47 159
89084 89263 89292 89023 89054 89240 89182 88864 88862 89258 89248 89037
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

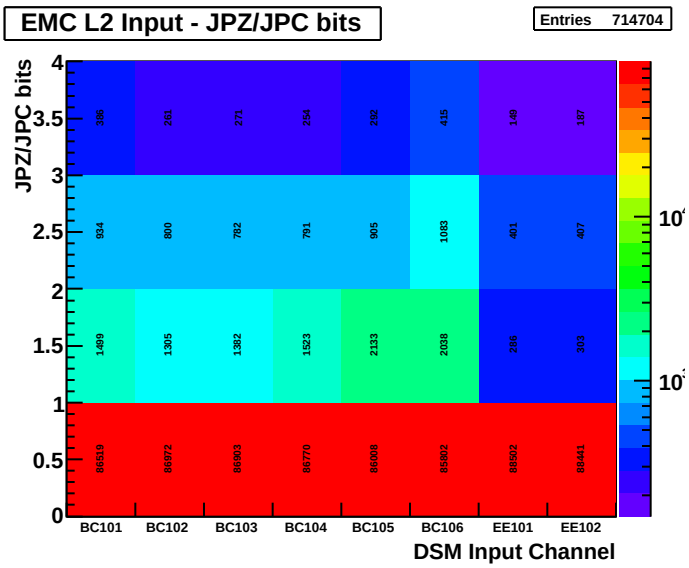

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

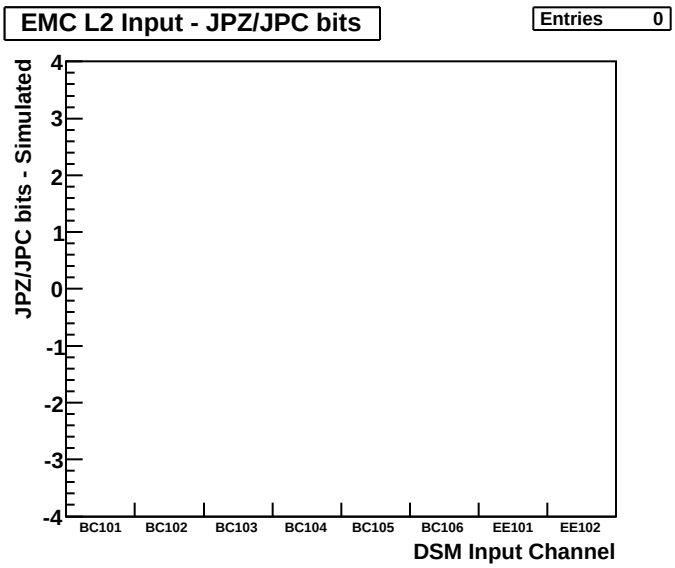

EMC L2 Input - JPX/JPA bits
Entries 714704
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
608 476 408 319 291 382 156 251
1357 1209 1032 965 913 942 348 549
2138 2245 1918 1808 1710 1574 308 232
85235 85408 85980 86246 86424 86440 88526 88306

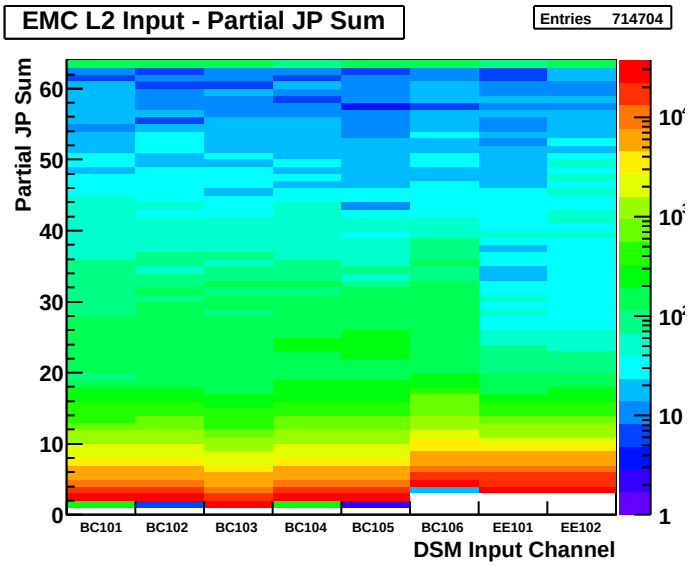

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

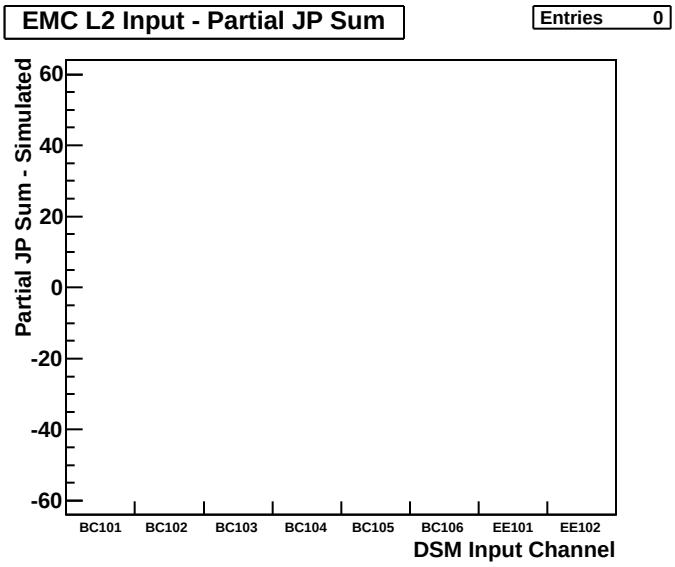

EMC L2 Input - JPY/JPB bits
Entries 714704
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
431 279 205 236 266 438 151 194
1137 850 883 866 969 1136 346 412
1796 1513 1631 1556 2267 1975 262 310
85974 86696 86529 86680 85836 85789 88579 88422

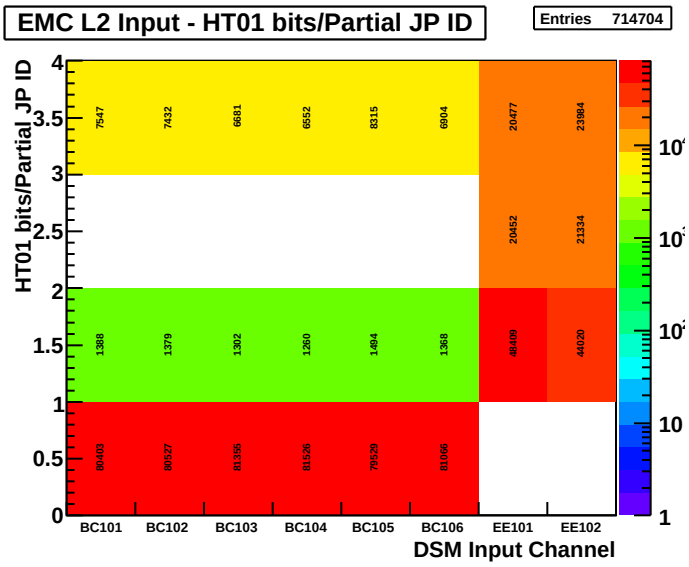

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

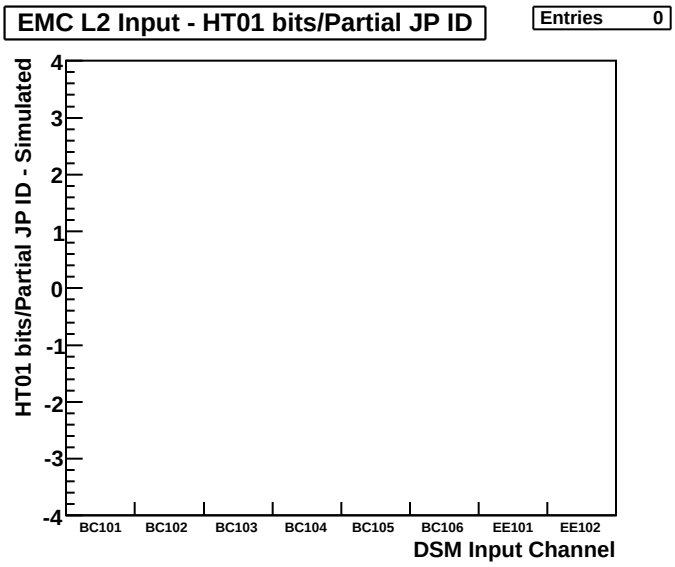

EMC L2 Input - JPZ/JPC bits
Entries 714704
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
386 261 271 254 292 415 149 187
934 800 782 791 905 1083 401 407
1489 1305 1382 1523 2133 2038 266 303
86519 86972 86903 86770 86008 85802 88502 88441

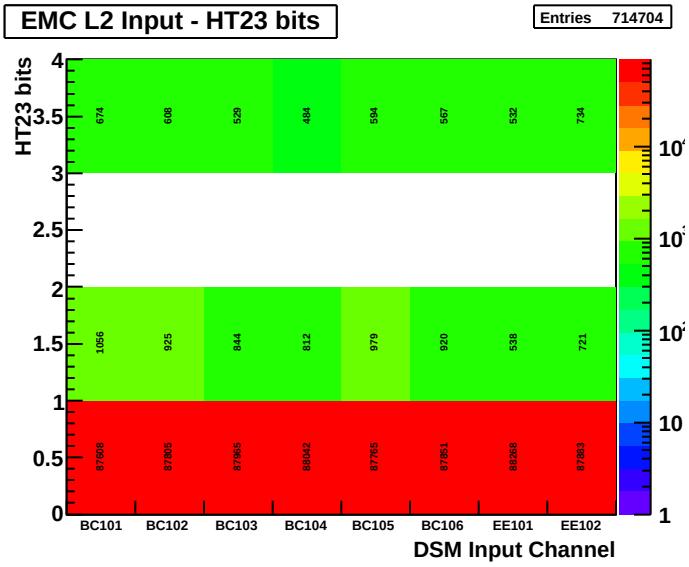

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

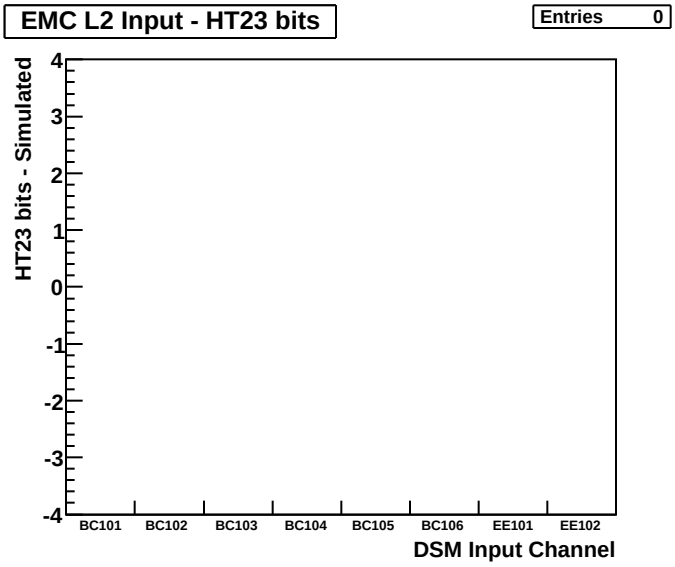

EMC L2 Input - Partial JP Sum
Entries 714704
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

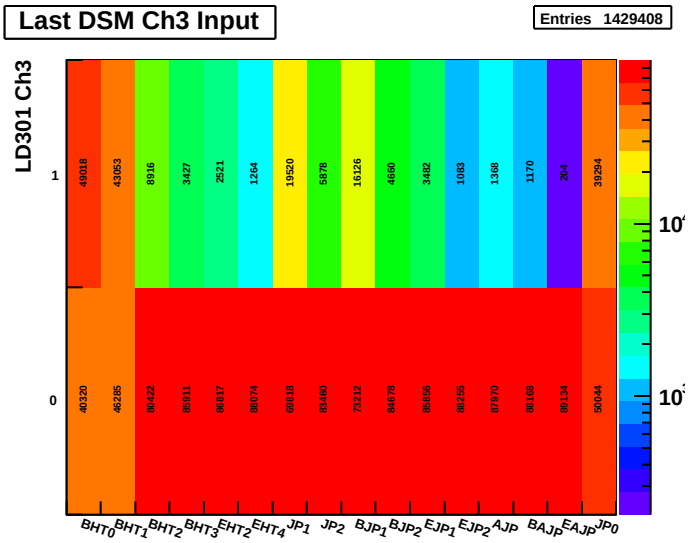

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

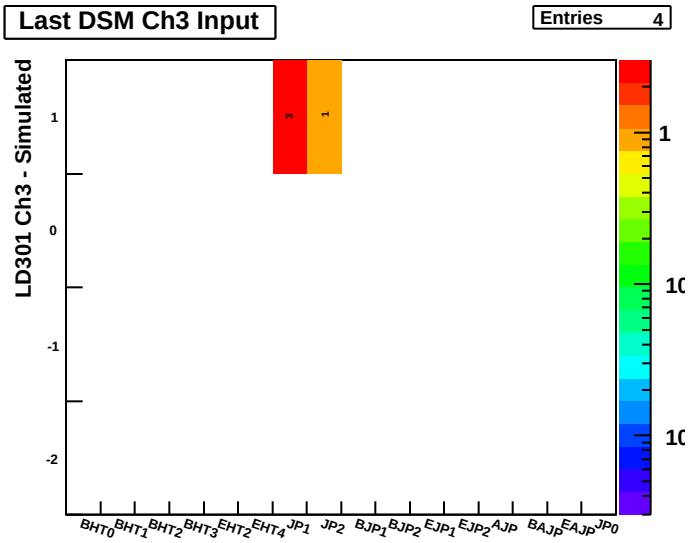

EMC L2 Input - HT01 bits/Partial JP ID
Entries 714704
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

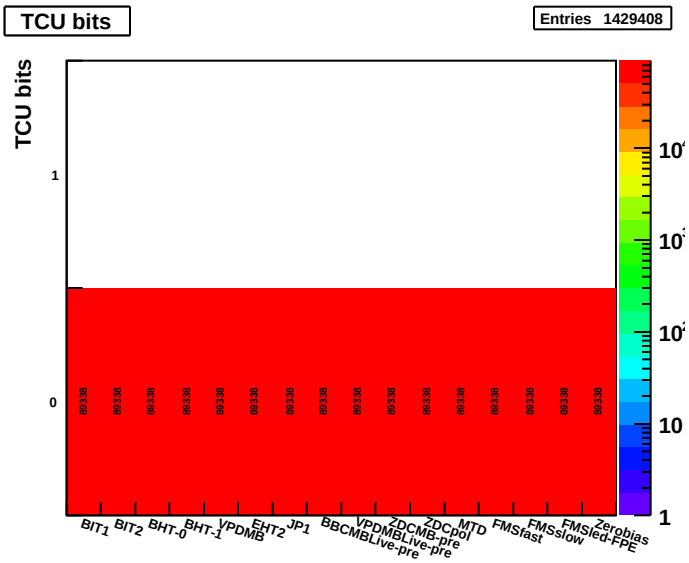

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

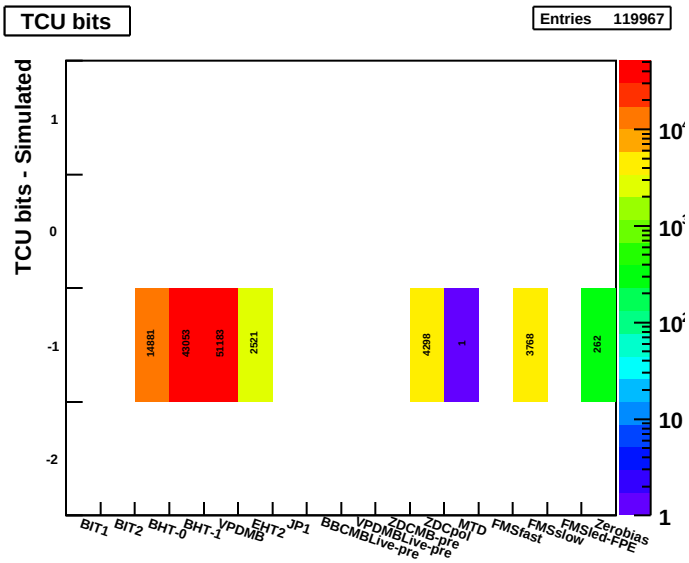

EMC L2 Input - HT23 bits
Entries 714704
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

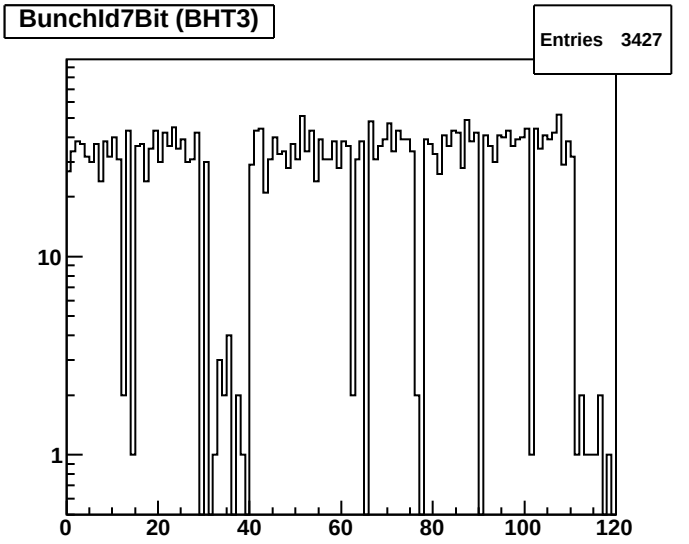

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

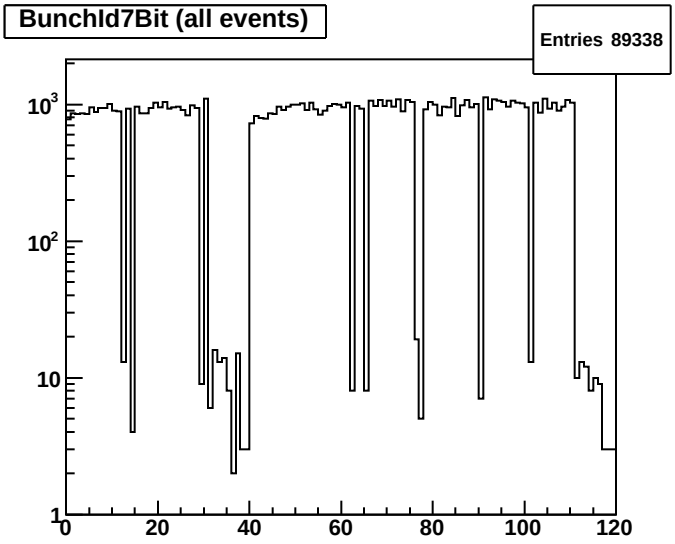

Last DSM Ch3 Input
Entries 1429408
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

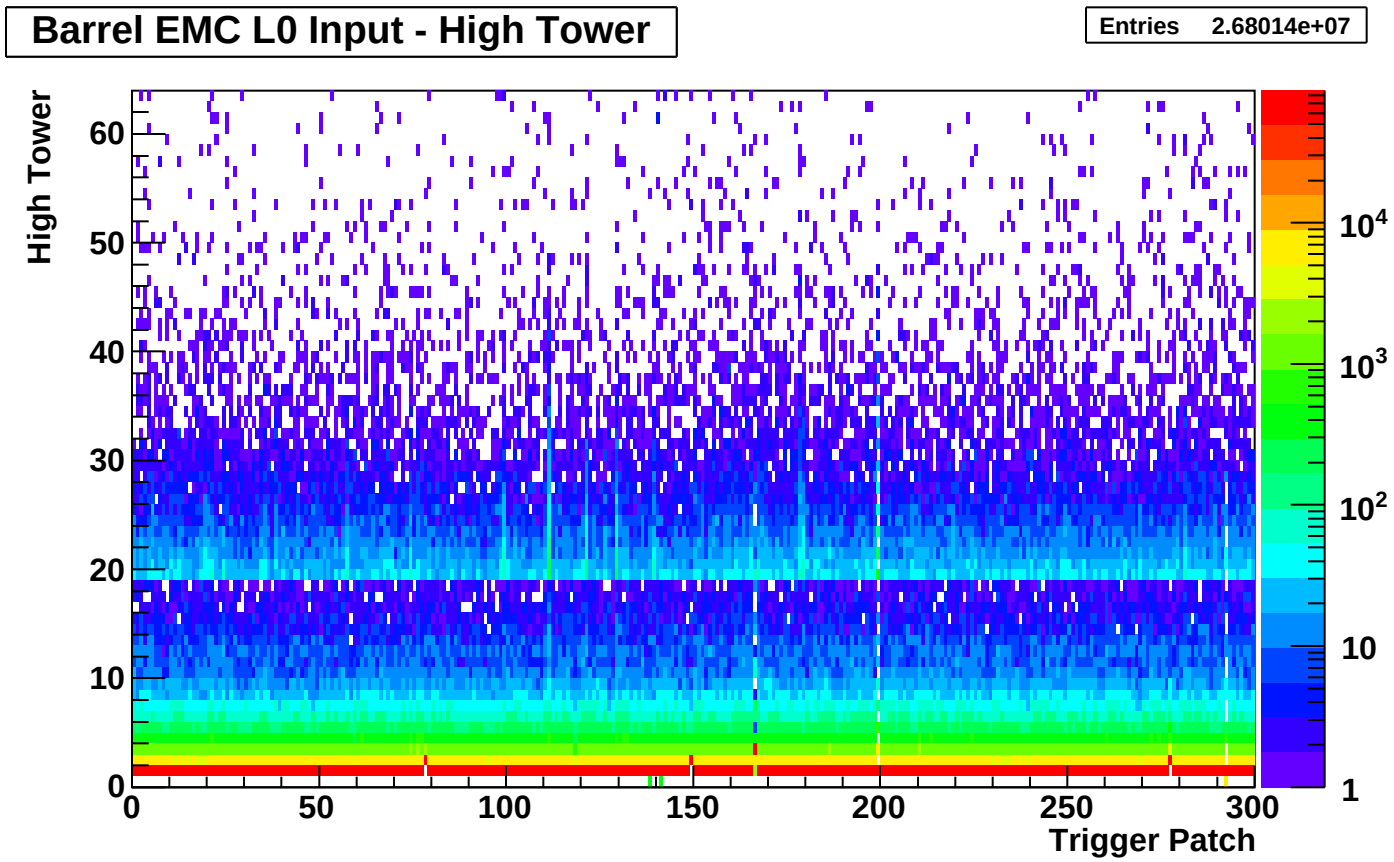

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

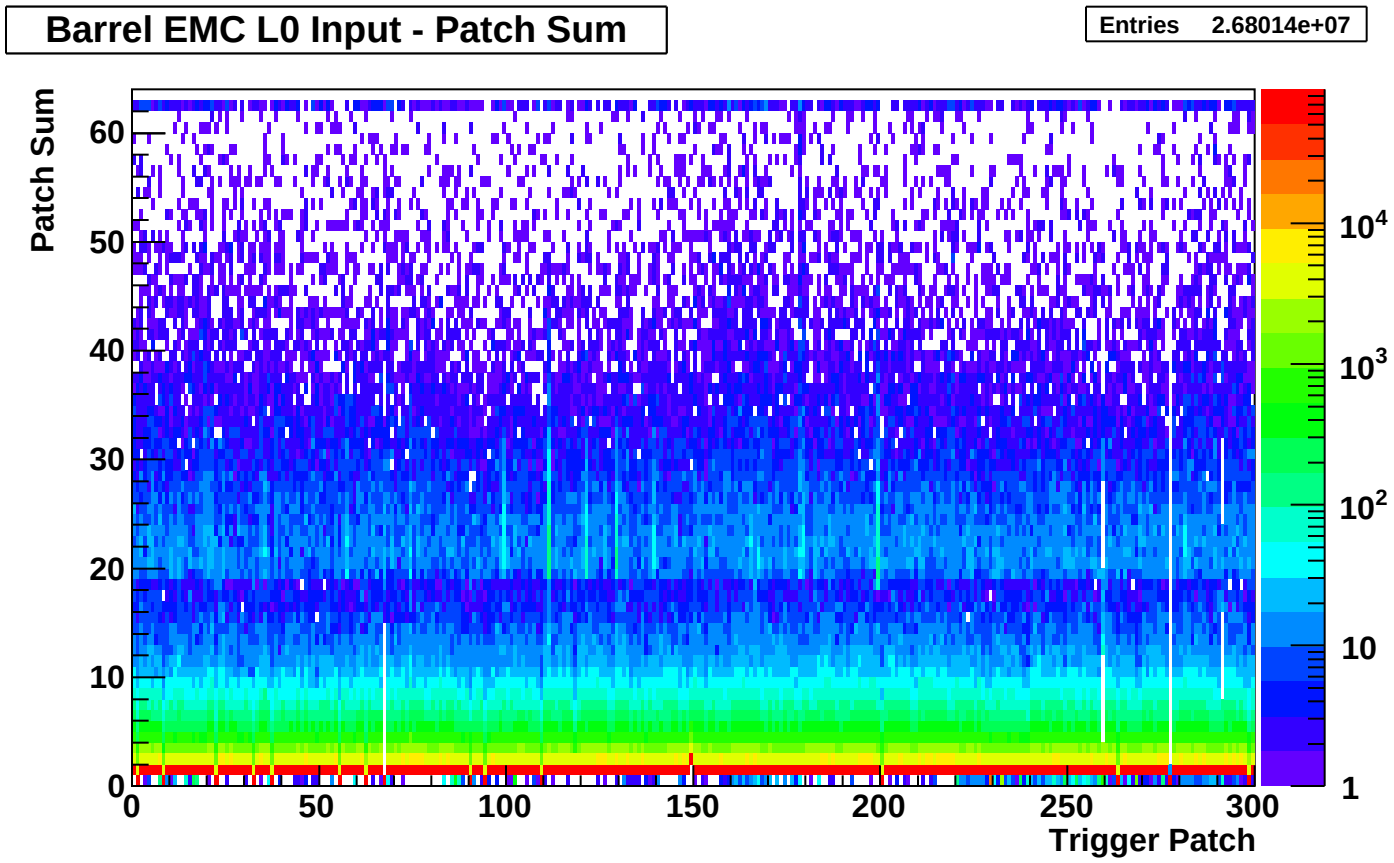

TCU bits
Entries 1429408
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

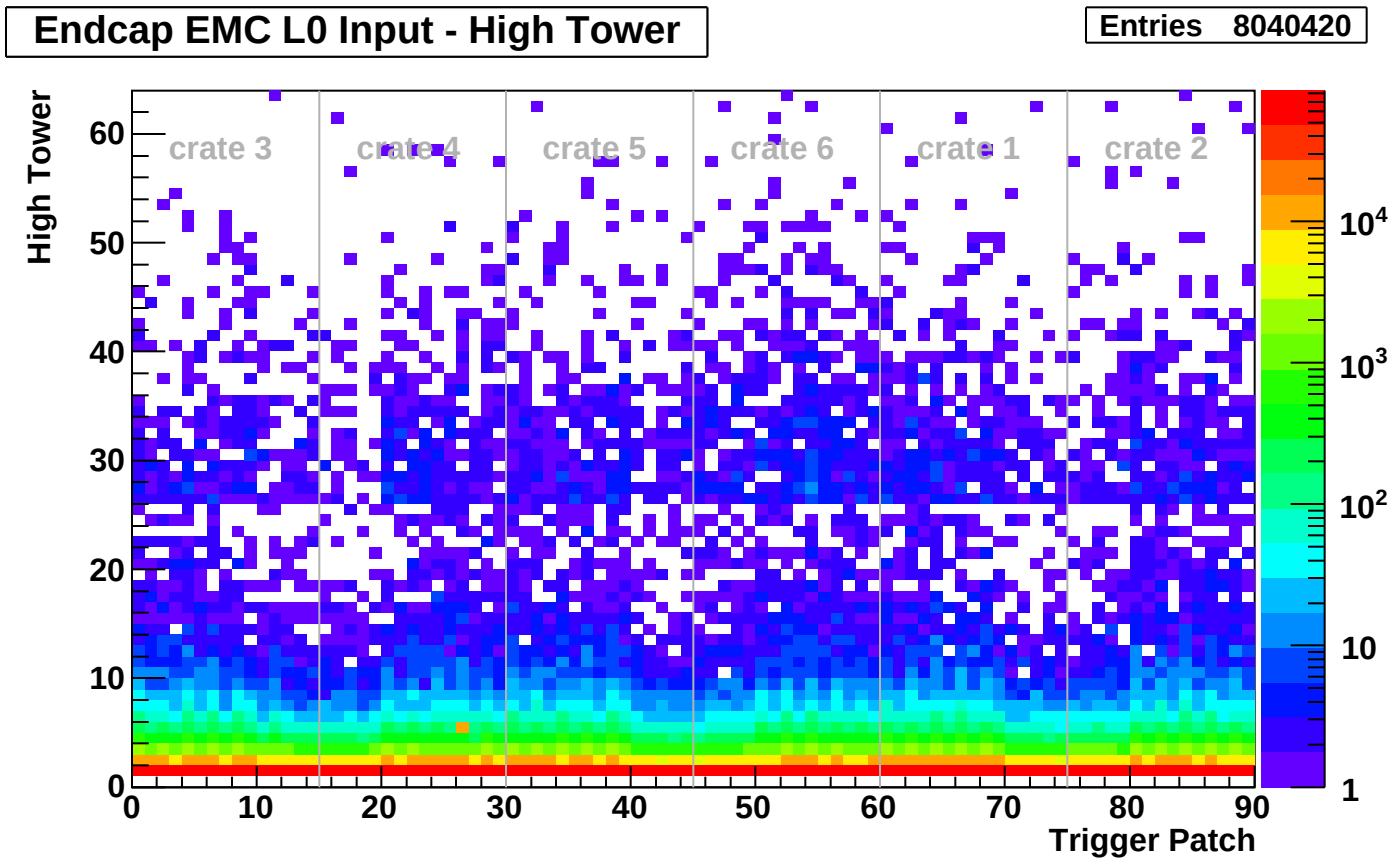

TCU bits
Entries 119967
TCU bits - Simulated
14881
43053
51183
2521
4288
1
3768
262
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

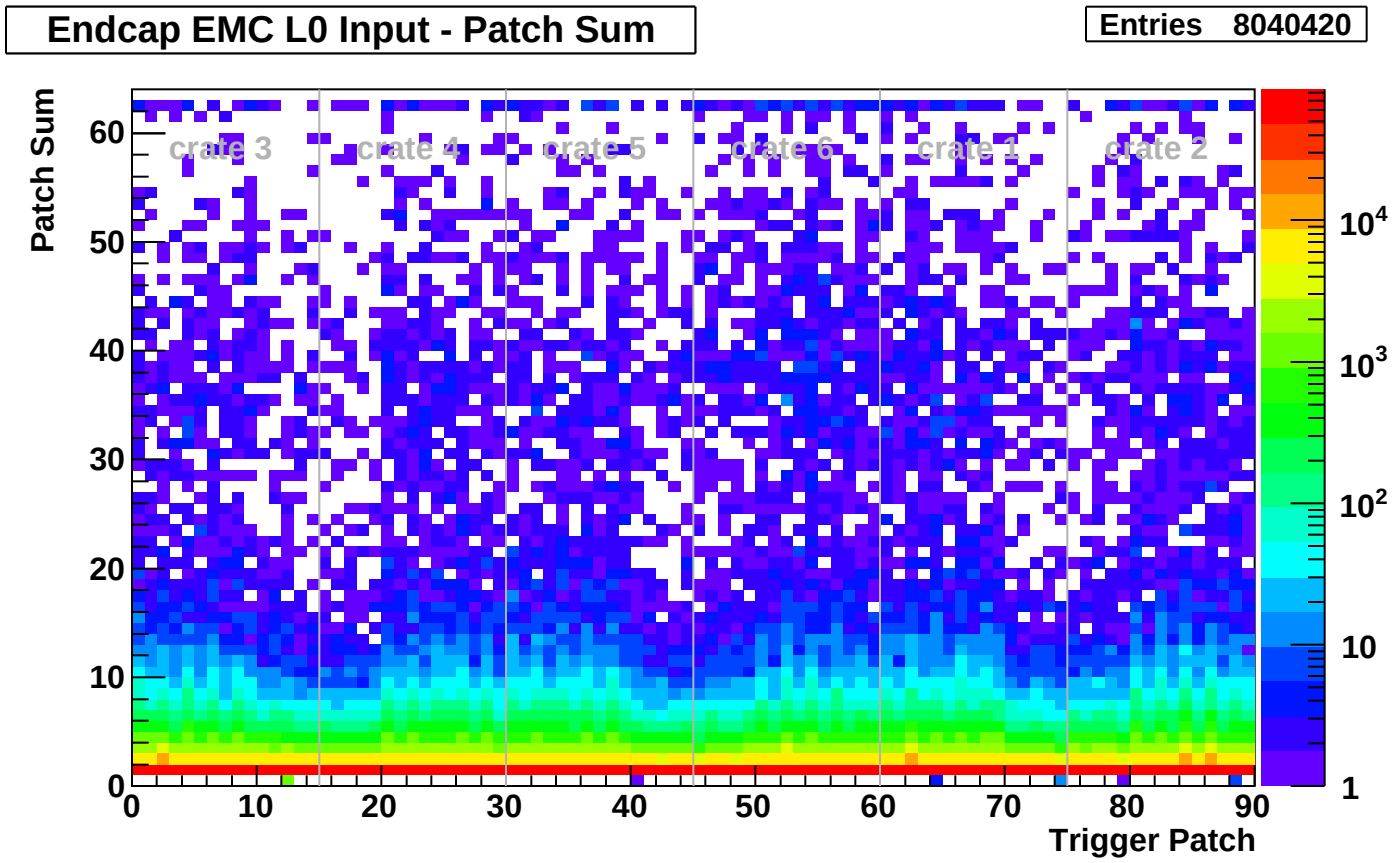

BunchId7Bit (BHT3)
Entries 3427

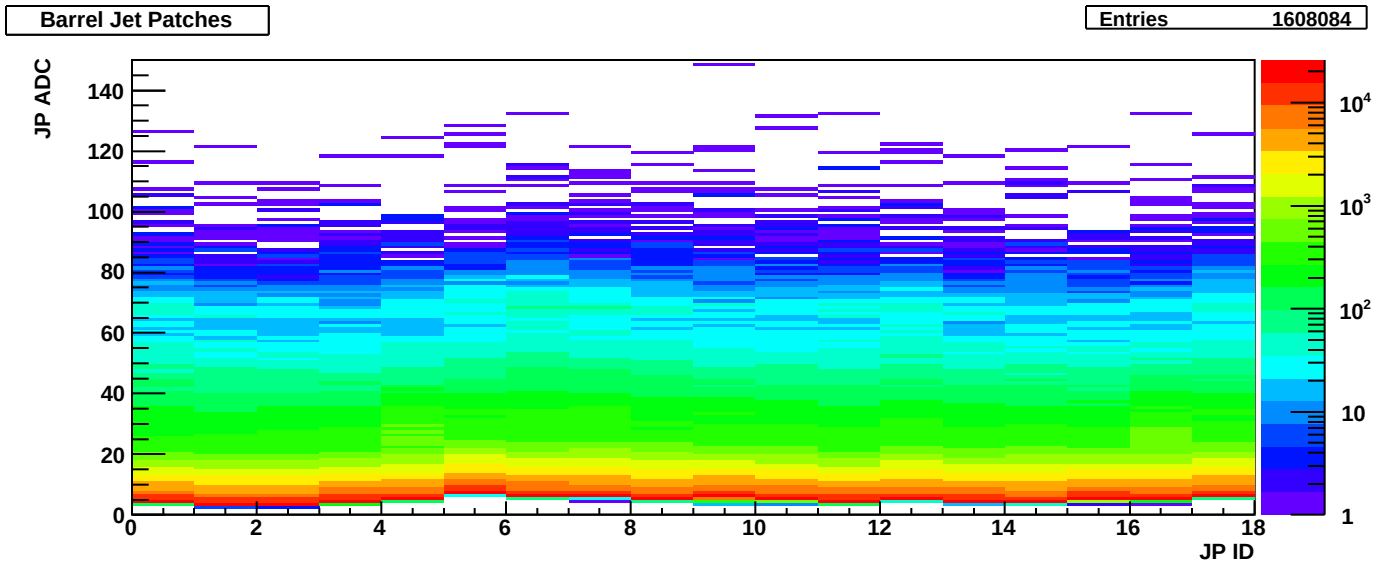

BunchId7Bit (all events)
Entries 89338

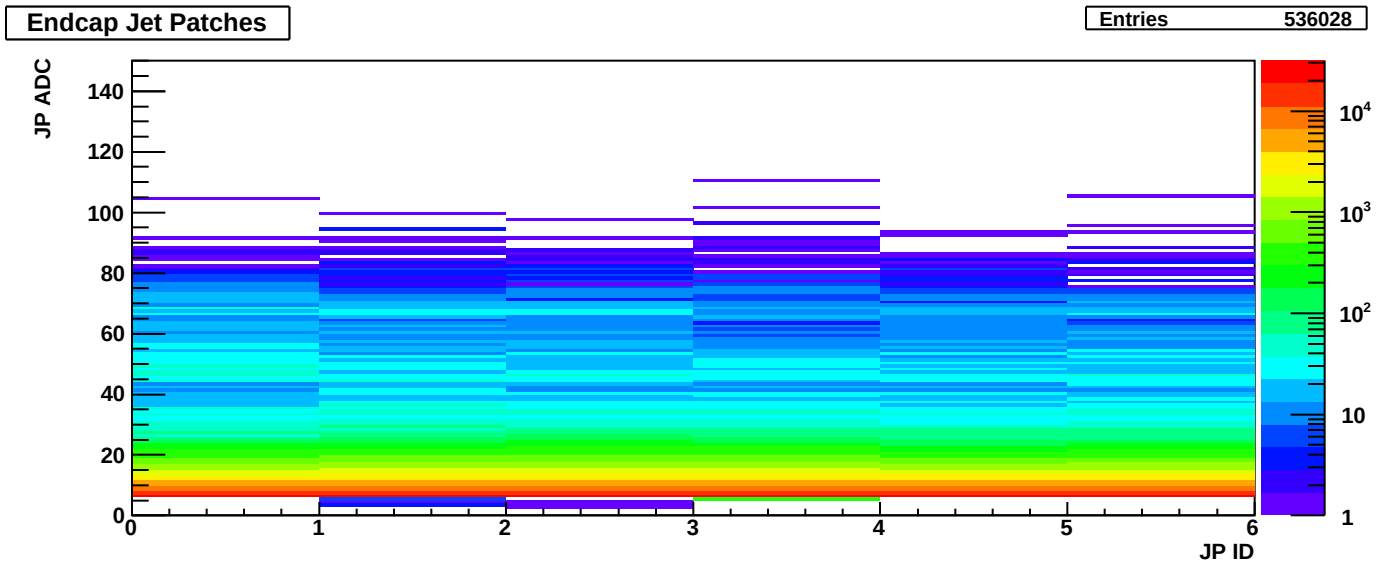

Barrel EMC L0 Input - High Tower
Entries 2.68014e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

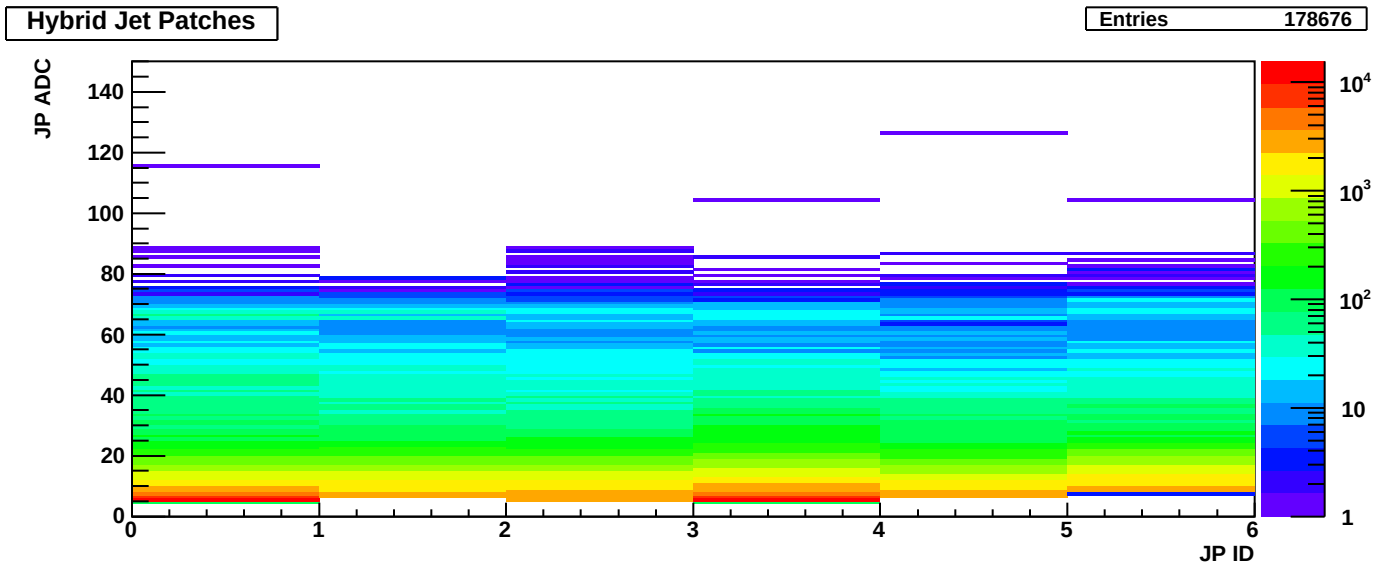

Barrel EMC L0 Input - Patch Sum
Entries 2.68014e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

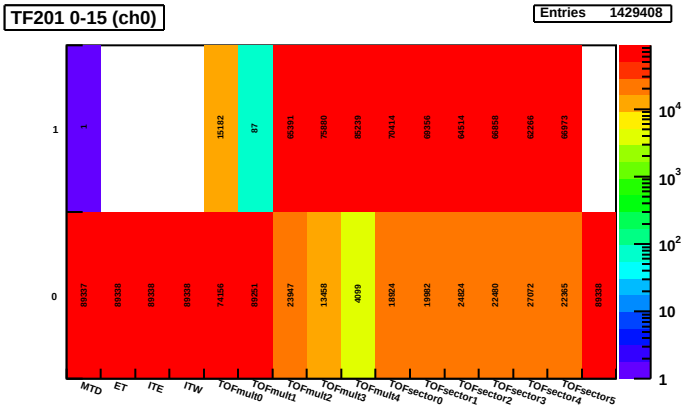

Endcap EMC L0 Input - High Tower
Entries 8040420
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

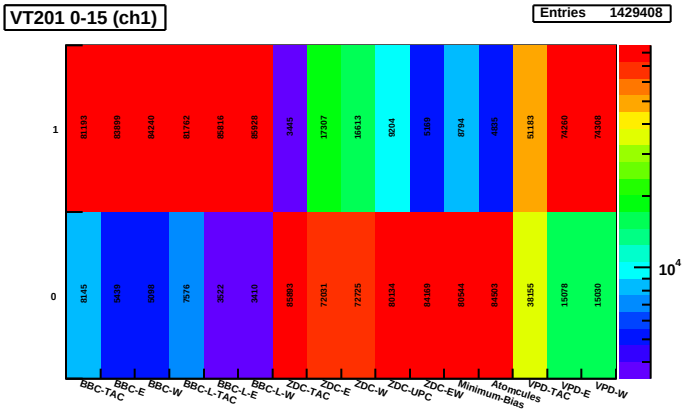

Endcap EMC L0 Input - Patch Sum
Entries 8040420
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

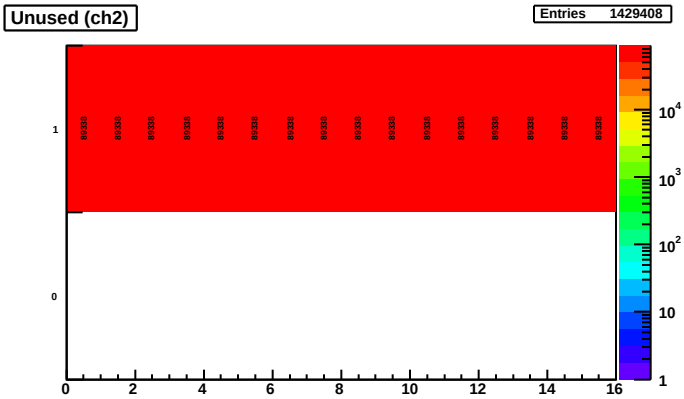

Barrel Jet Patches
Entries 1608084
JP ADC
JP ID

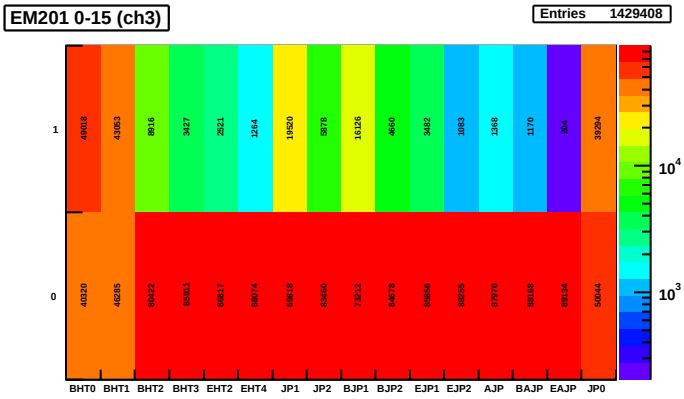

Endcap Jet Patches
Entries 536028
JP ADC
JP ID

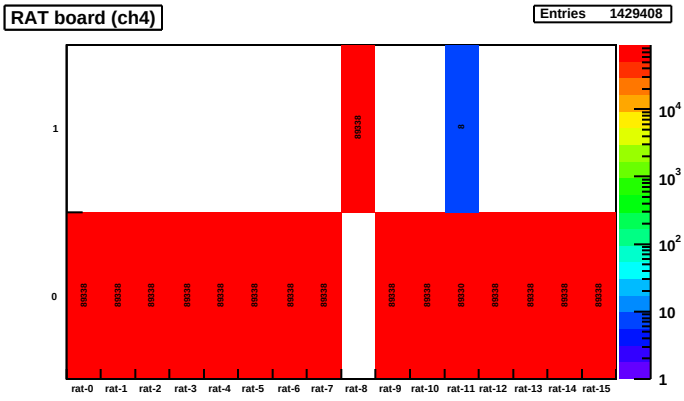

Hybrid Jet Patches
Entries 178676
JP ADC
JP ID

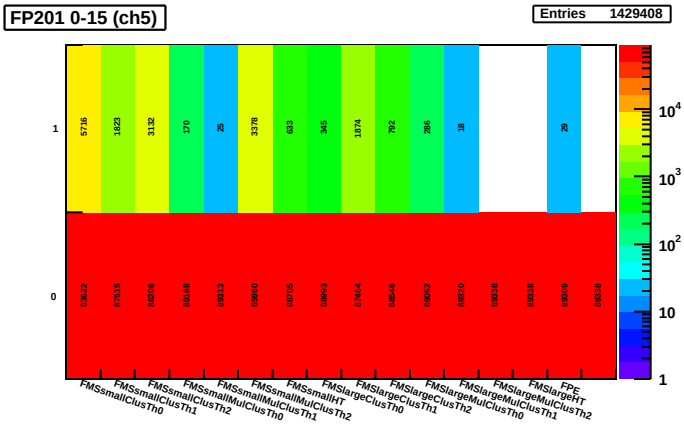

TF201 0-15 (ch0)
Entries 1429408
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

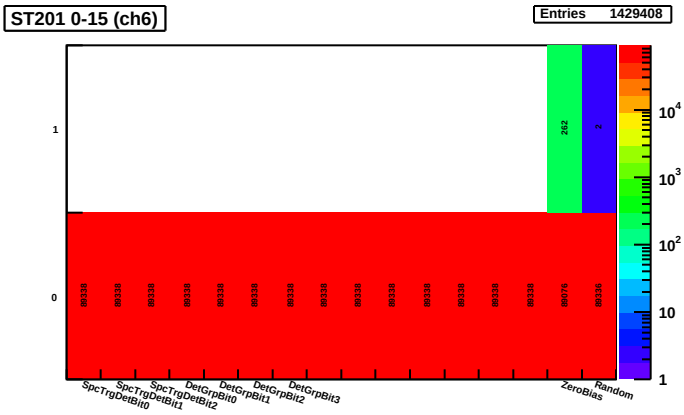

VT201 0-15 (ch1)
Entries 1429408
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

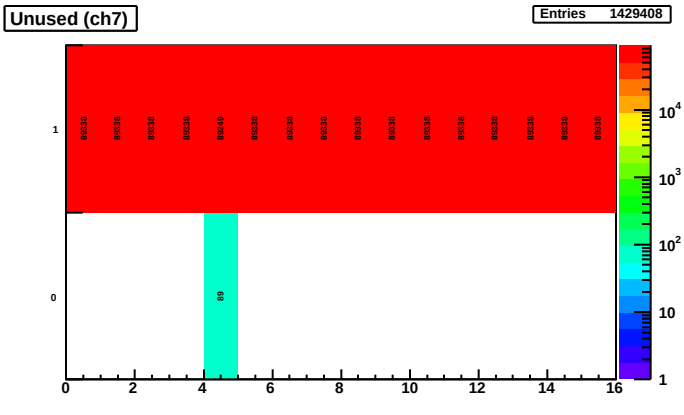

Unused (ch2)
Entries 1429408

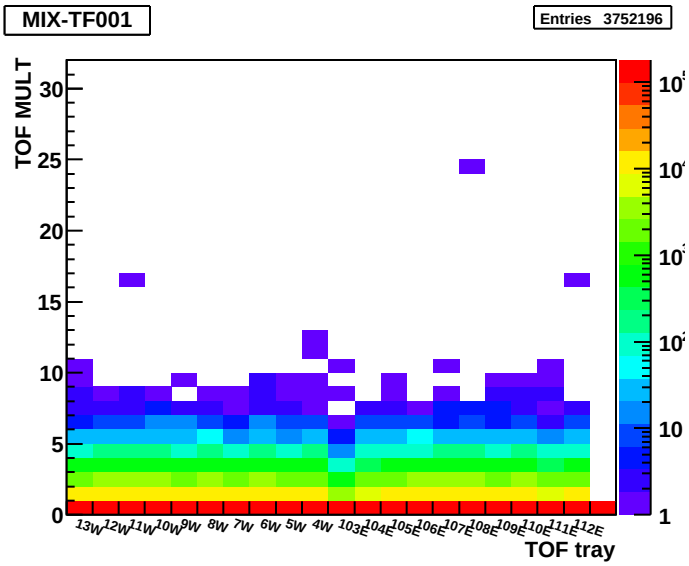

EM201 0-15 (ch3)
Entries 1429408
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

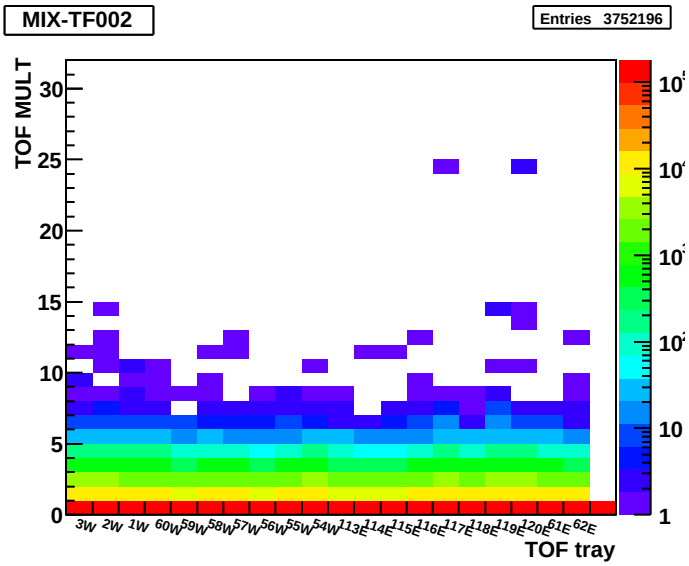

RAT board (ch4)
Entries 1429408
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

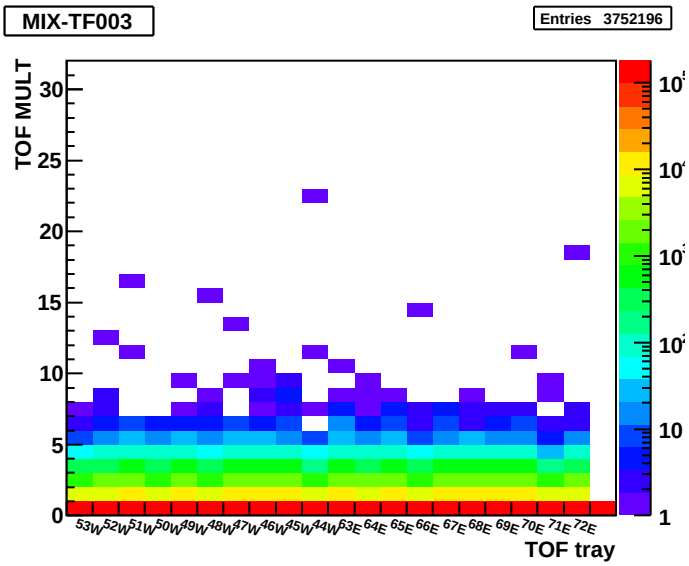

FP201 0-15 (ch5)
Entries 1429408
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

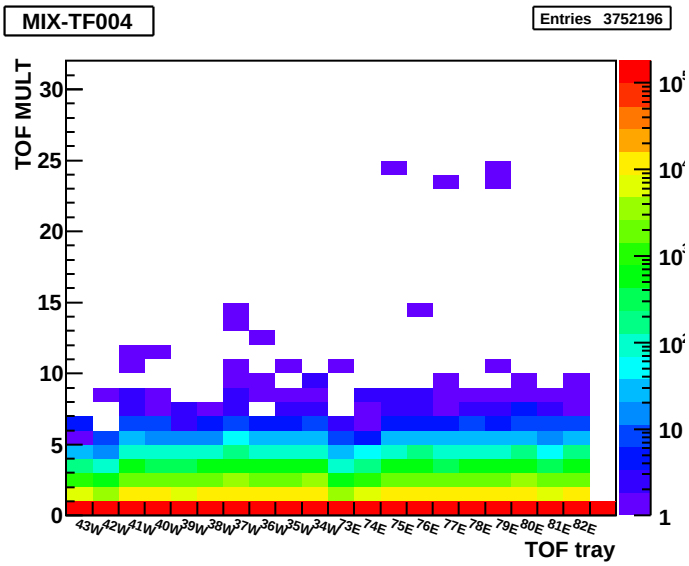

ST201 0-15 (ch6)
Entries 1429408
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

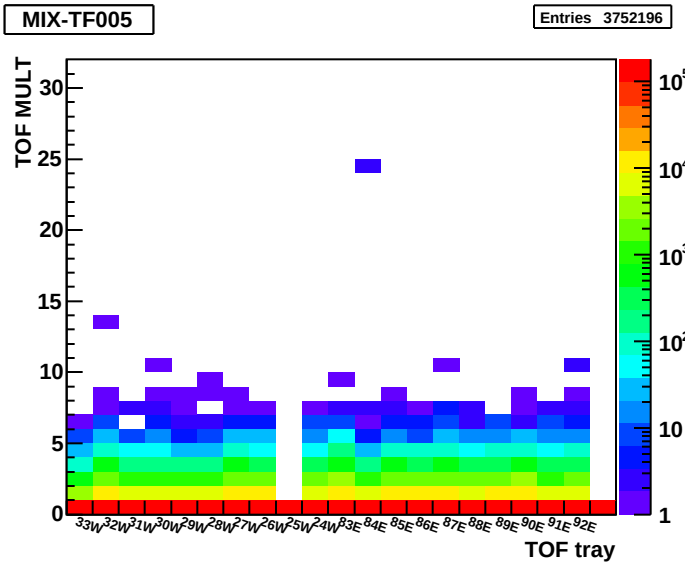

Unused (ch7)
Entries 1429408

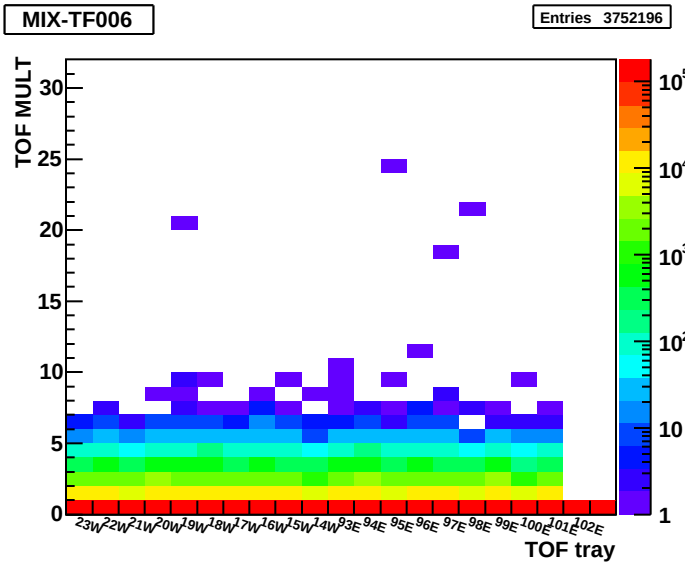

MIX-TF001
Entries 3752196
TOF MULT
TOF tray

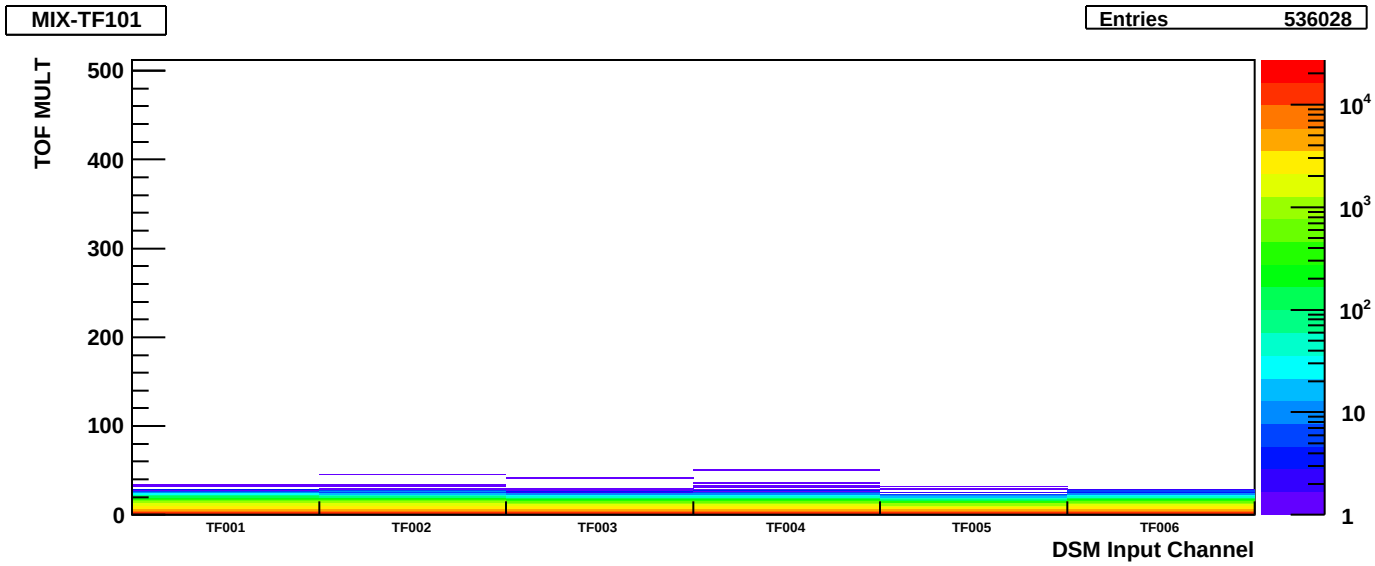

MIX-TF002
Entries 3752196
TOF MULT
TOF tray

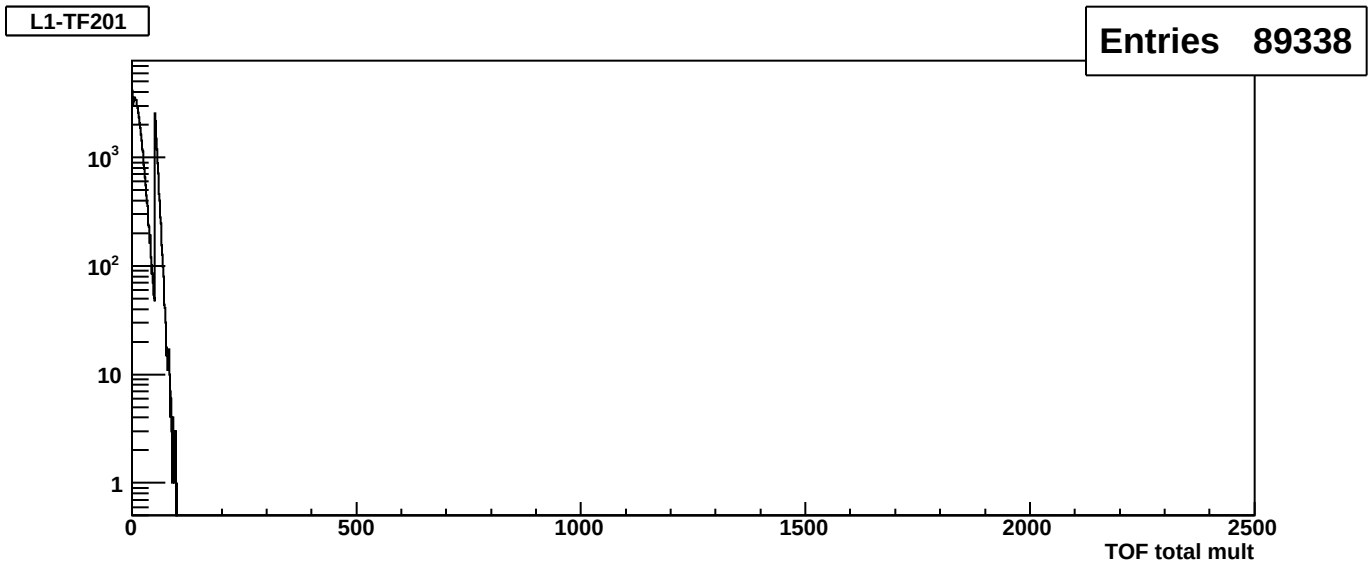

MIX-TF003
Entries 3752196
TOF MULT
TOF tray

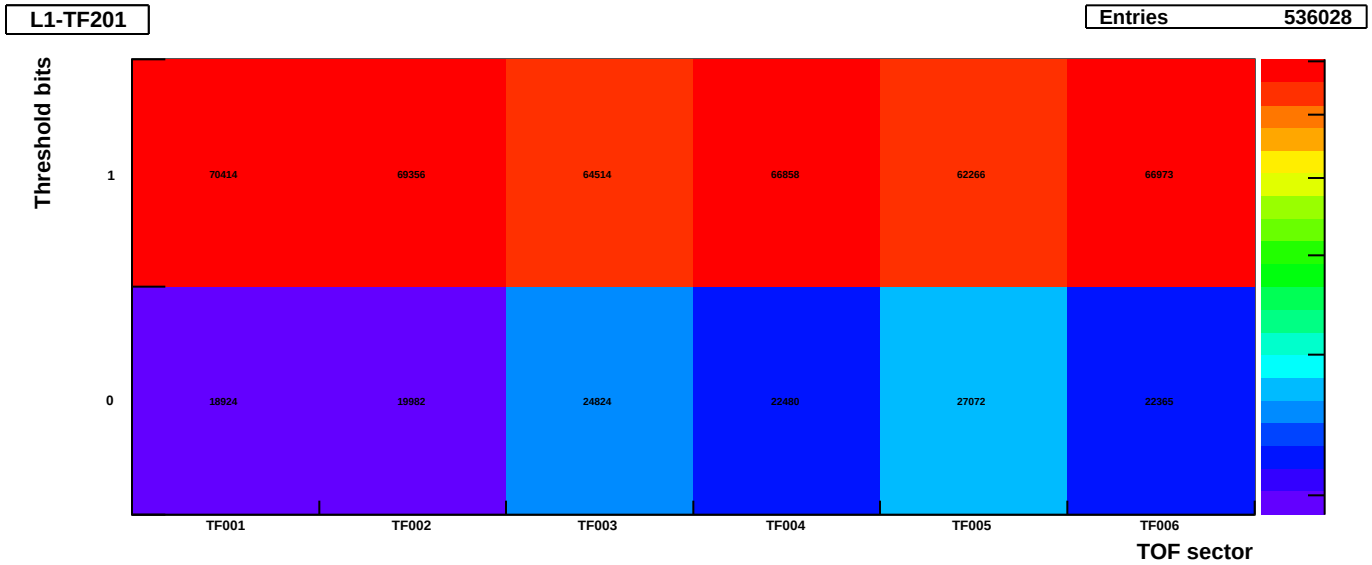

MIX-TF004
Entries 3752196
TOF MULT
TOF tray

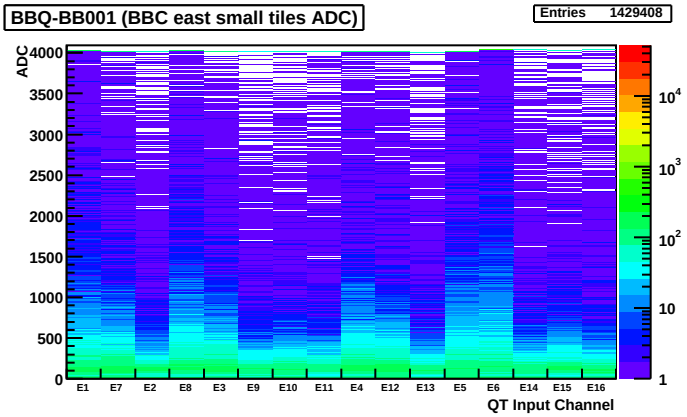

MIX-TF005
Entries 3752196
TOF MULT
TOF tray

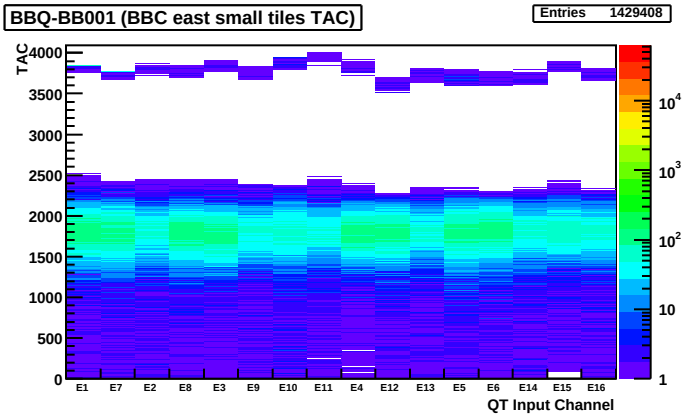

MIX-TF006
Entries 3752196
TOF MULT
TOF tray

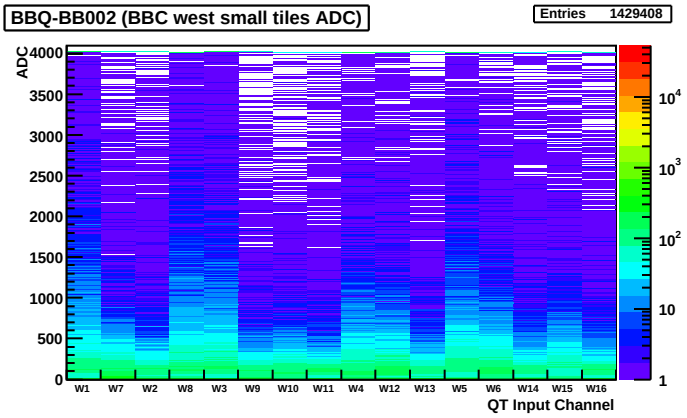

MIX-TF101
Entries 536028
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

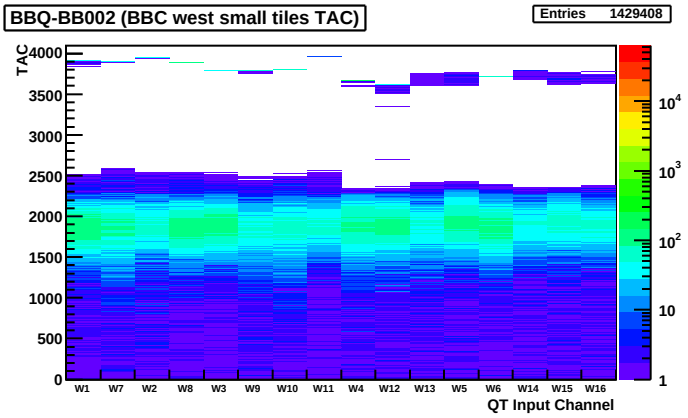

L1-TF201
Entries 89338
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

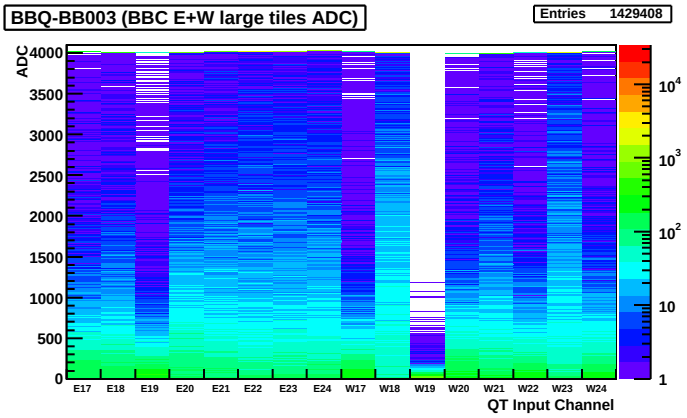

L1-TF201
Entries 536028
Threshold bits
1
0
70414
69356
64514
66858
62266
66973
18924
19982
24824
22480
27072
22365
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

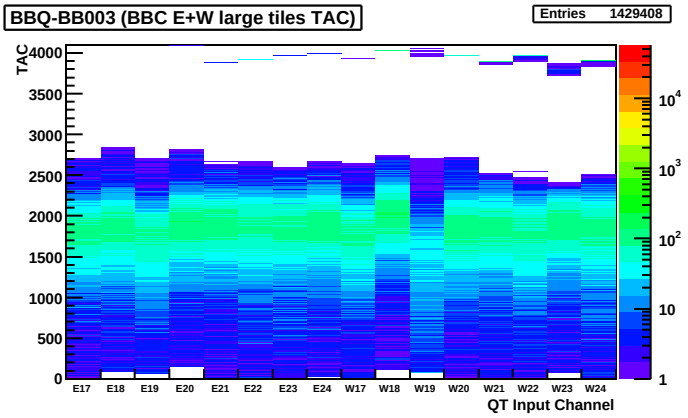

BBQ-BB001 (BBC east small tiles ADC)
Entries 1429408
ADC
QT Input Channel

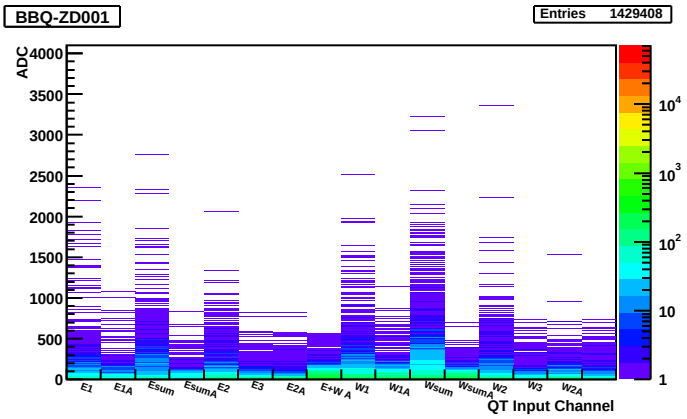

BBQ-BB001 (BBC east small tiles TAC)
Entries 1429408
TAC
QT Input Channel

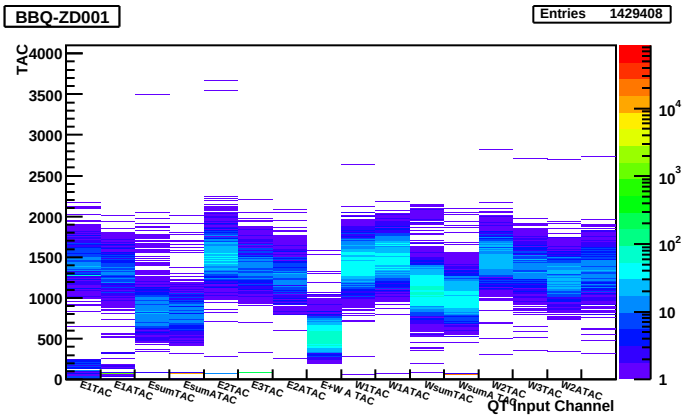

BBQ-BB002 (BBC west small tiles ADC)
Entries 1429408
ADC
QT Input Channel

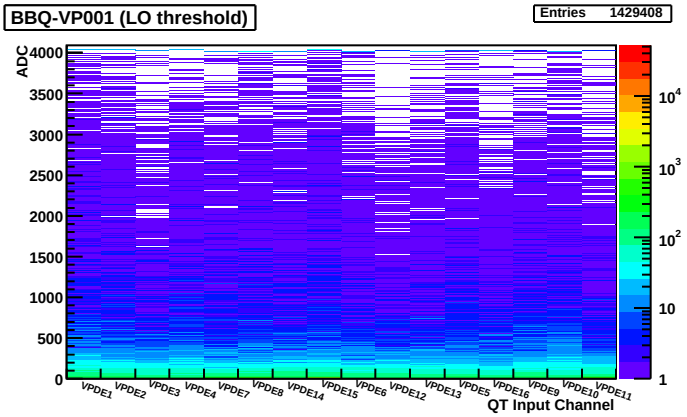

BBQ-BB002 (BBC west small tiles TAC)
Entries 1429408
TAC
QT Input Channel

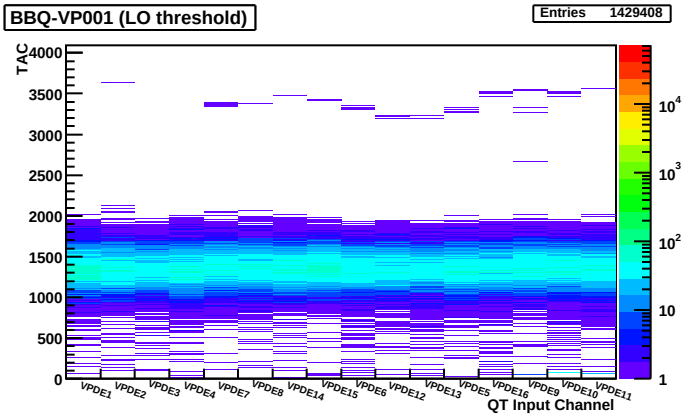

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1429408
ADC
QT Input Channel

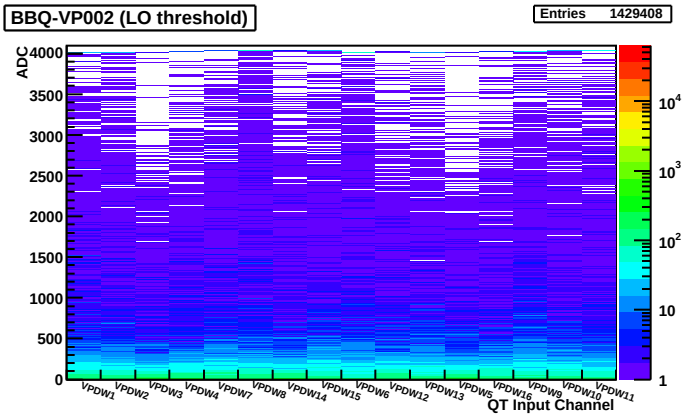

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1429408
TAC
QT Input Channel

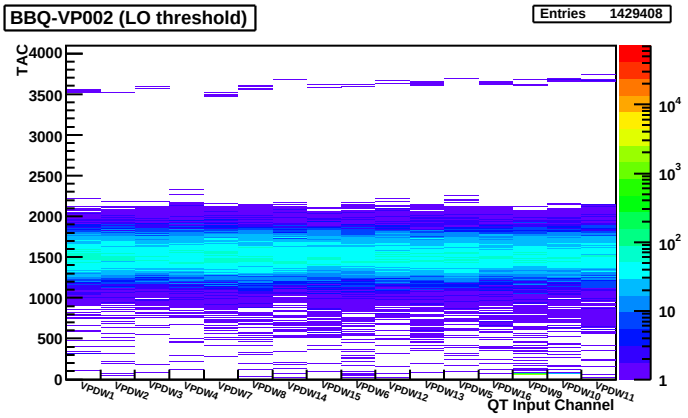

BBQ-ZD001
Entries 1429408
ADC
QT Input Channel

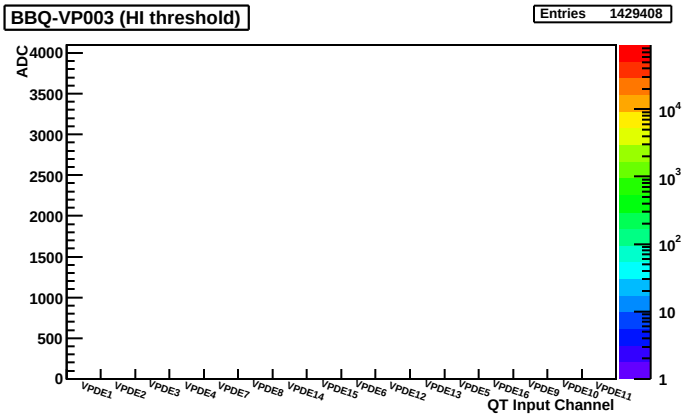

BBQ-ZD001
Entries 1429408
TAC
QT Input Channel

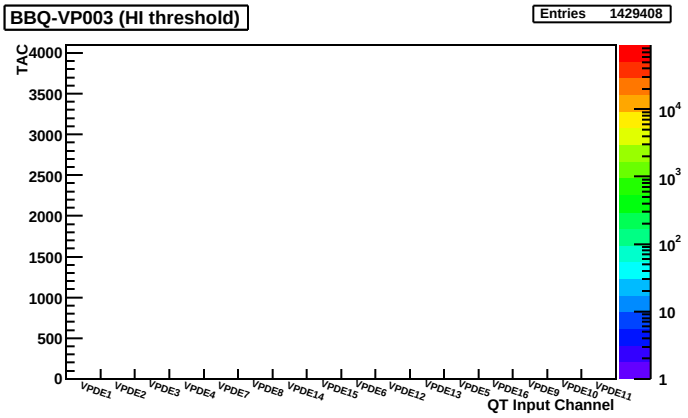

BBQ-VP001 (LO threshold)
Entries 1429408
ADC
QT Input Channel

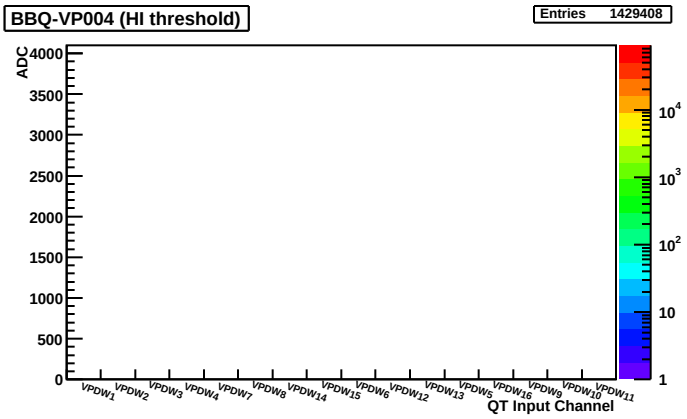

BBQ-VP001 (LO threshold)
Entries 1429408
TAC
QT Input Channel

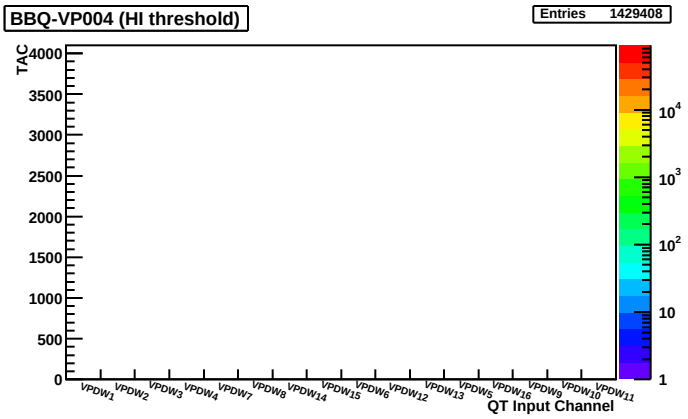

BBQ-VP002 (LO threshold)
Entries 1429408
ADC
QT Input Channel

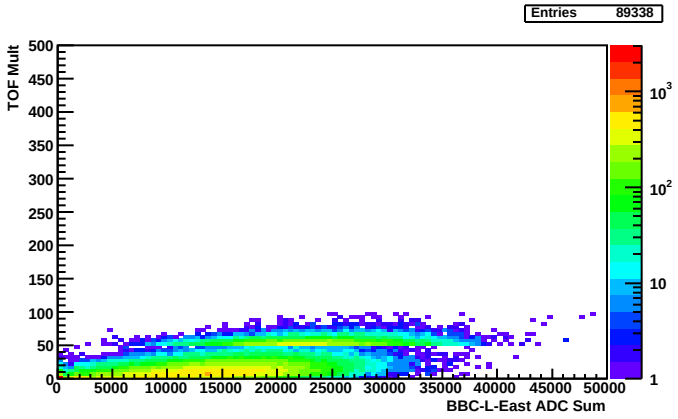

BBQ-VP002 (LO threshold)
Entries 1429408
TAC
QT Input Channel

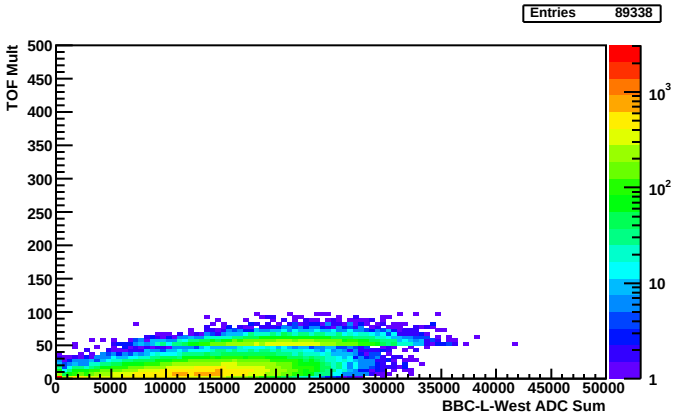

BBQ-VP003 (HI threshold)
Entries 1429408
ADC
QT Input Channel

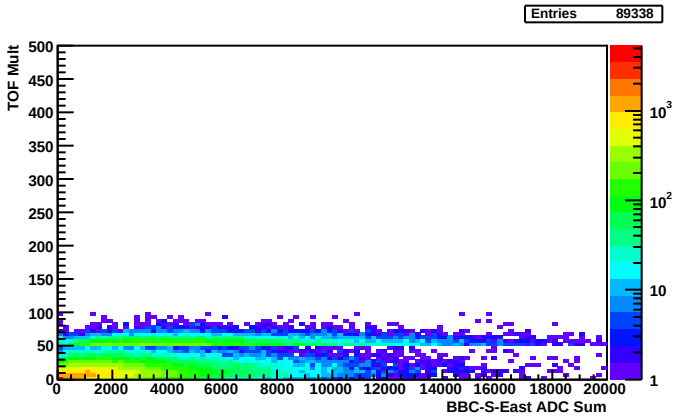

BBQ-VP003 (HI threshold)
Entries 1429408
TAC
QT Input Channel

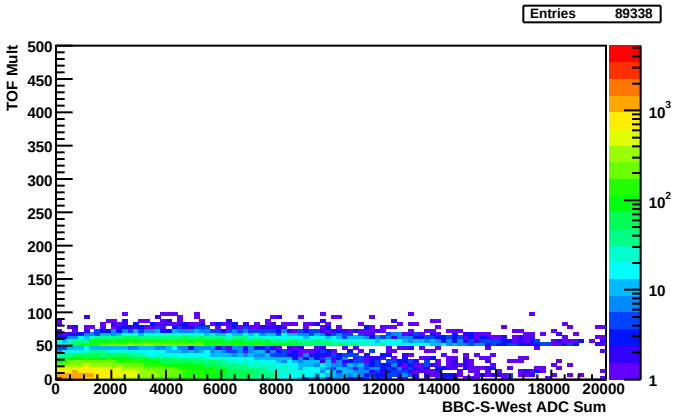

BBQ-VP004 (HI threshold)
Entries 1429408
ADC
QT Input Channel

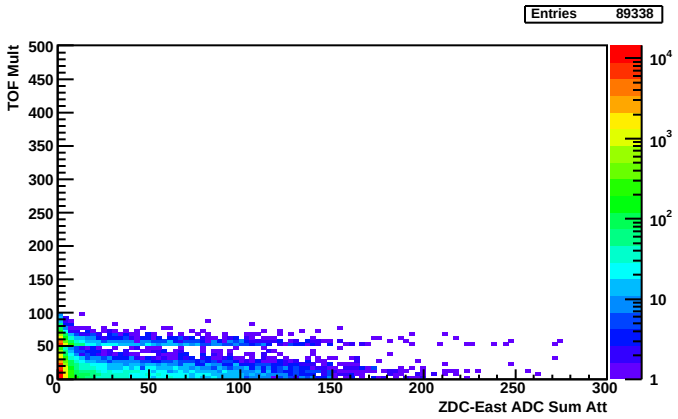

BBQ-VP004 (HI threshold)
Entries 1429408
TAC
QT Input Channel

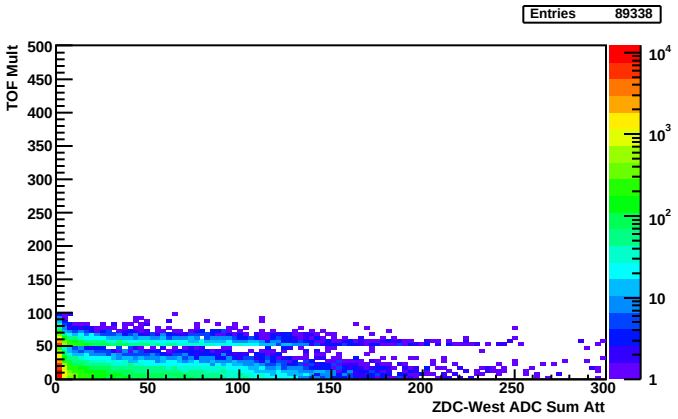

Entries 89338
TOF Mult
BBC-L-East ADC Sum

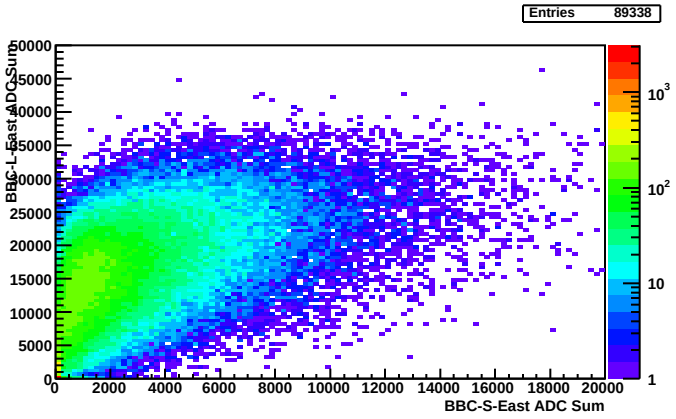

Entries 89338
TOF Mult
BBC-L-West ADC Sum

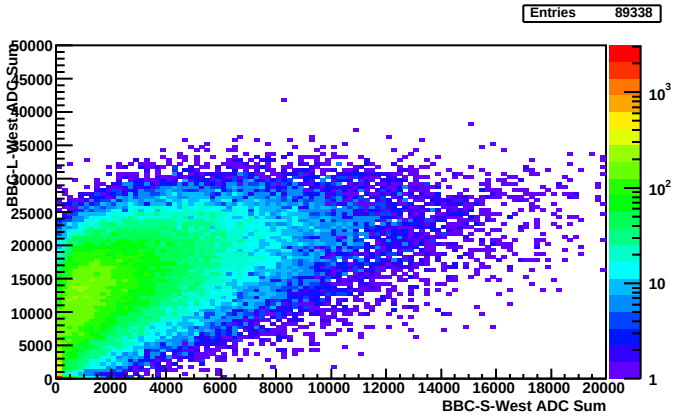

Entries 89338
TOF Mult
BBC-S-East ADC Sum

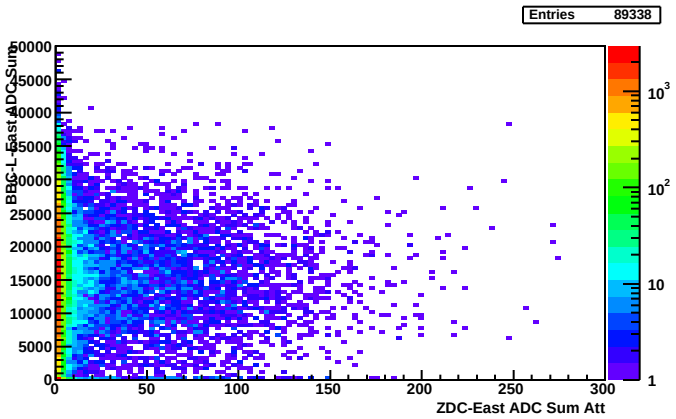

Entries 89338
TOF Mult
BBC-S-West ADC Sum

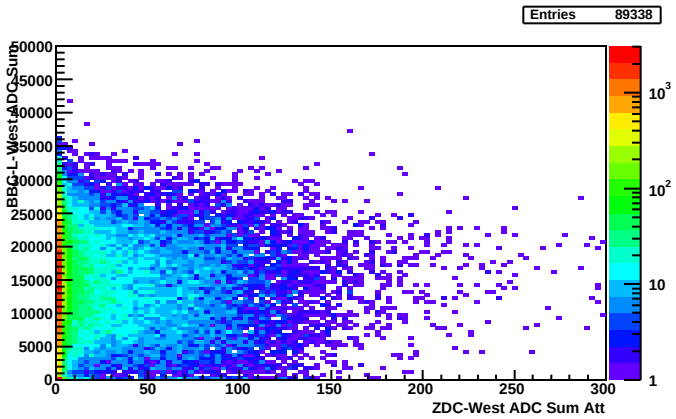

Entries 89338
TOF Mult
ZDC-East ADC Sum Att

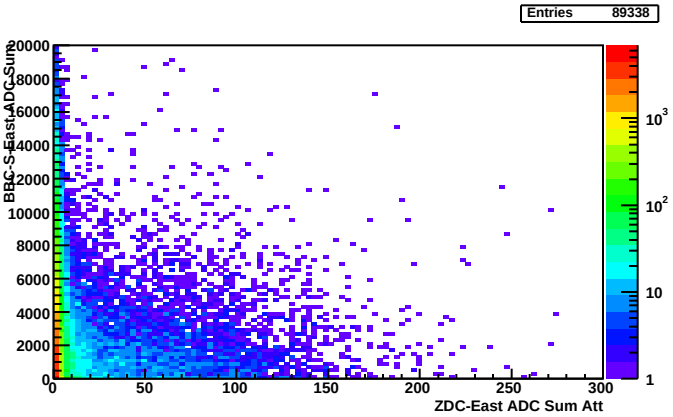

Entries 89338
TOF Mult
ZDC-West ADC Sum Att

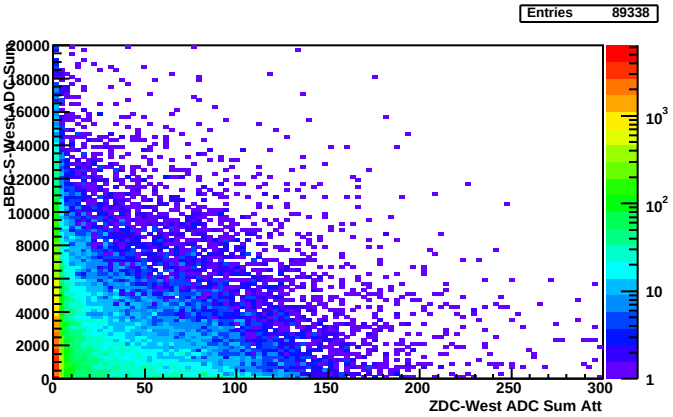

Entries 89338
BBC-L-East ADC Sum
BBC-S-East ADC Sum

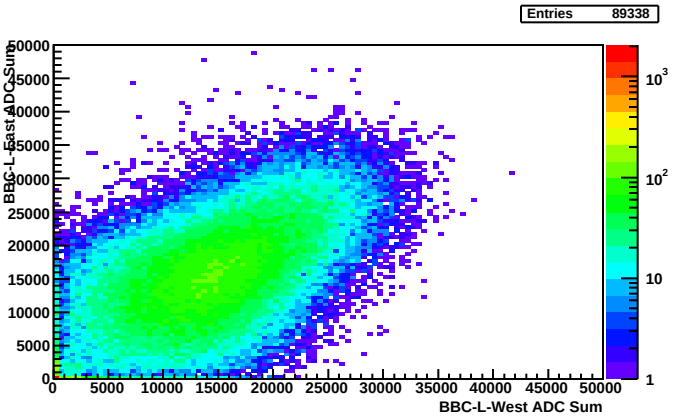

Entries 89338
BBC-L-West ADC Sum
BBC-S-West ADC Sum

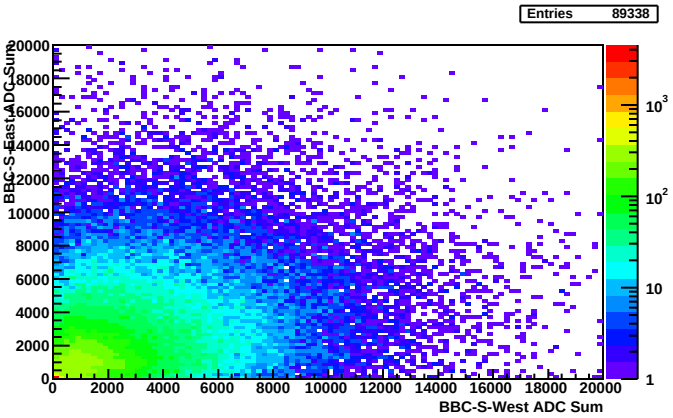

Entries 89338
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

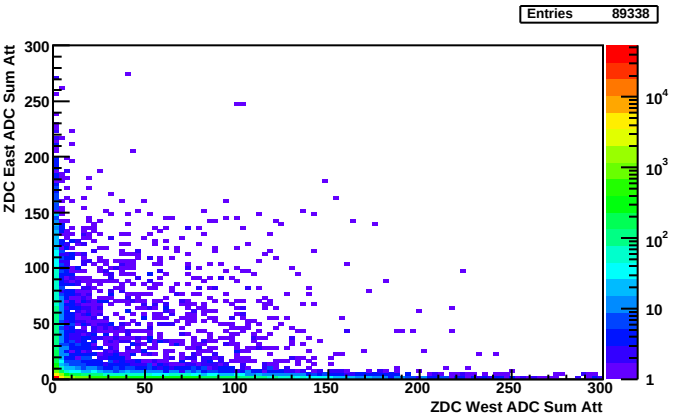

Entries 89338
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

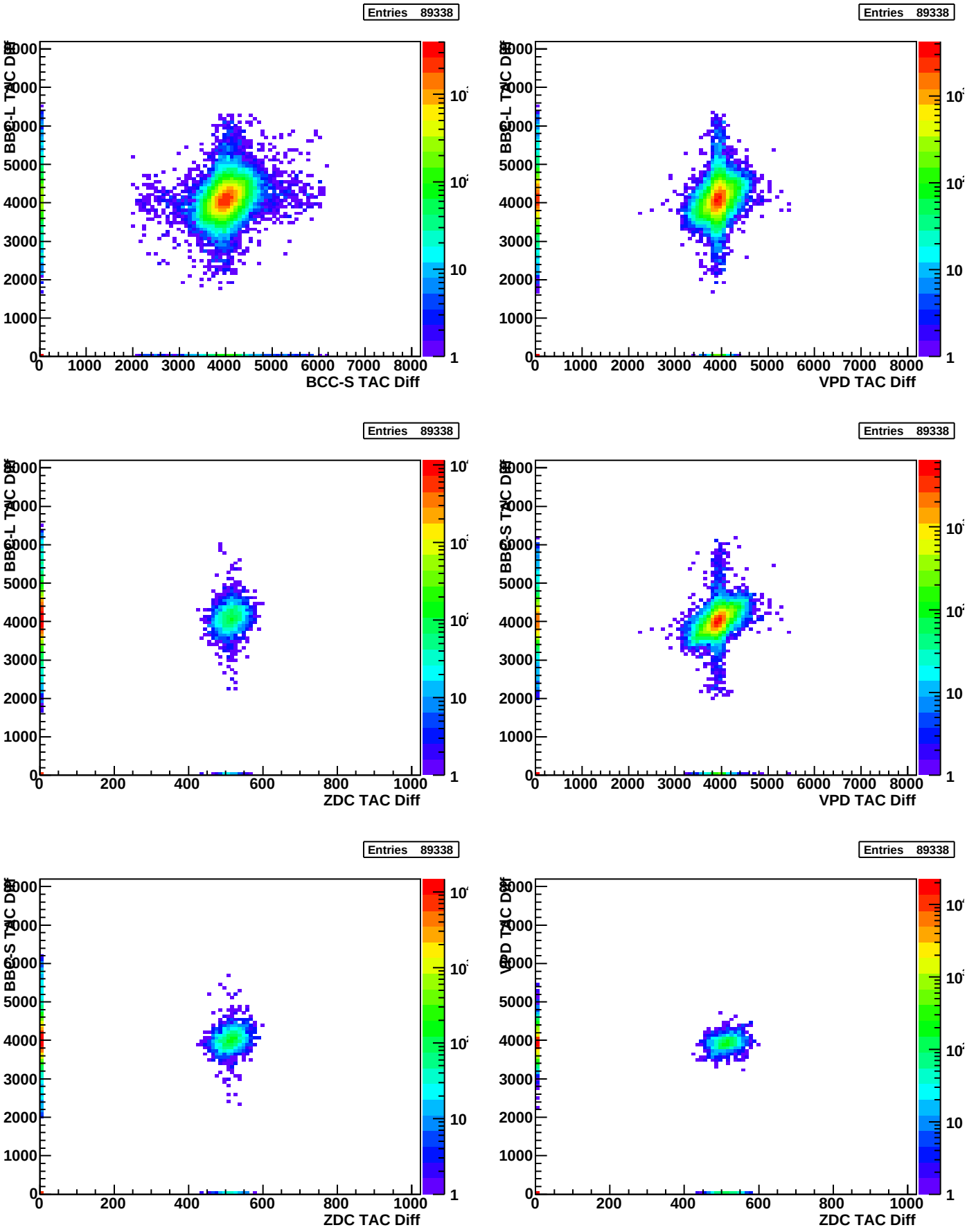

Entries 89338
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

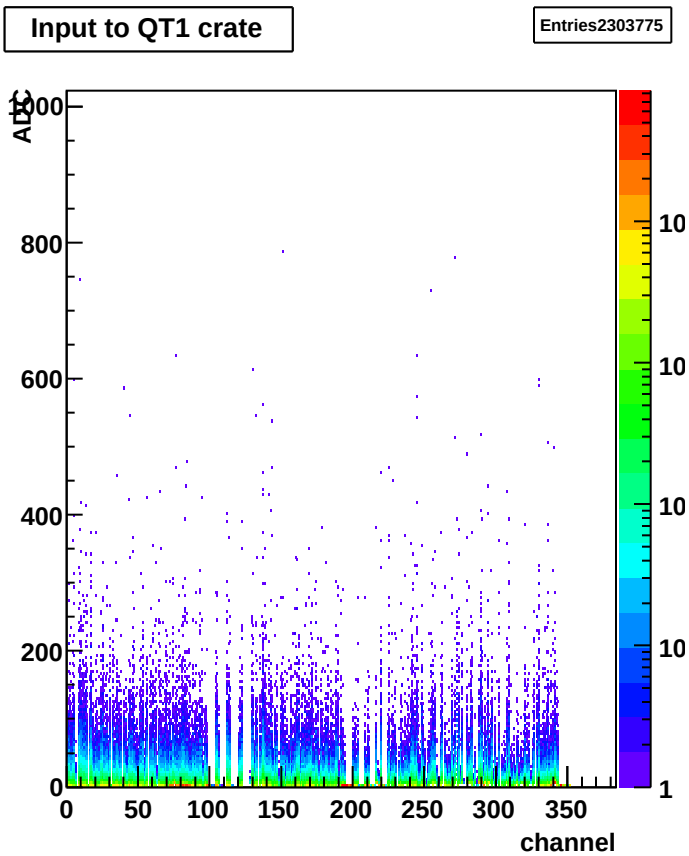

Entries 89338
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

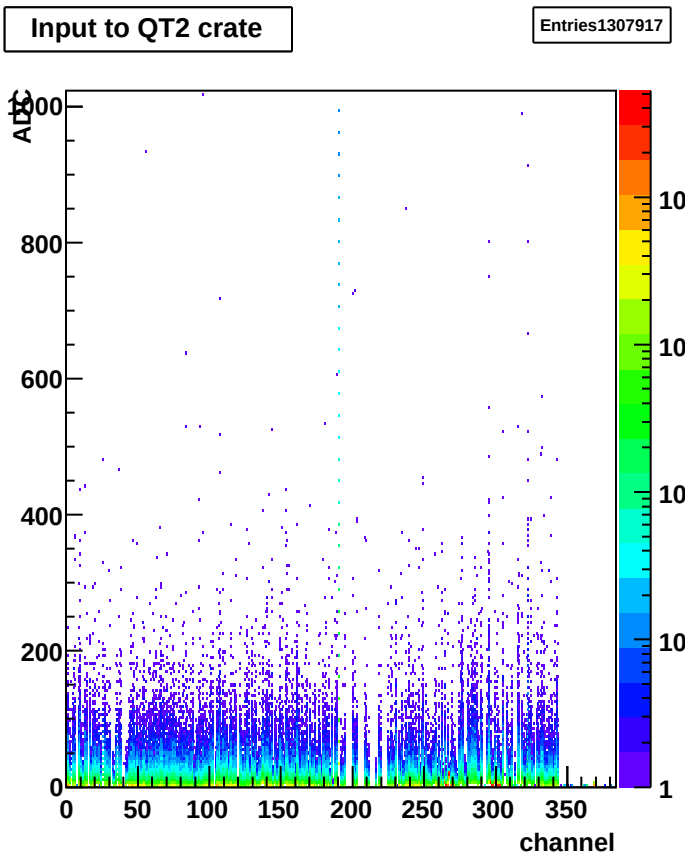

Entries 89338
BBC-L-East ADC Sum
BBC-L-West ADC Sum

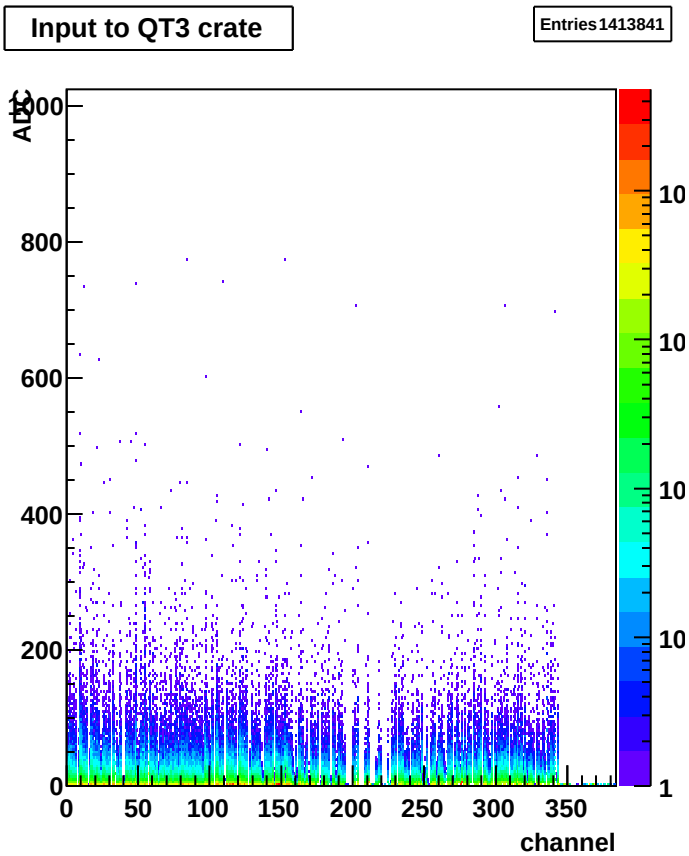

Entries 89338
BBC-S-East ADC Sum
BBC-S-West ADC Sum

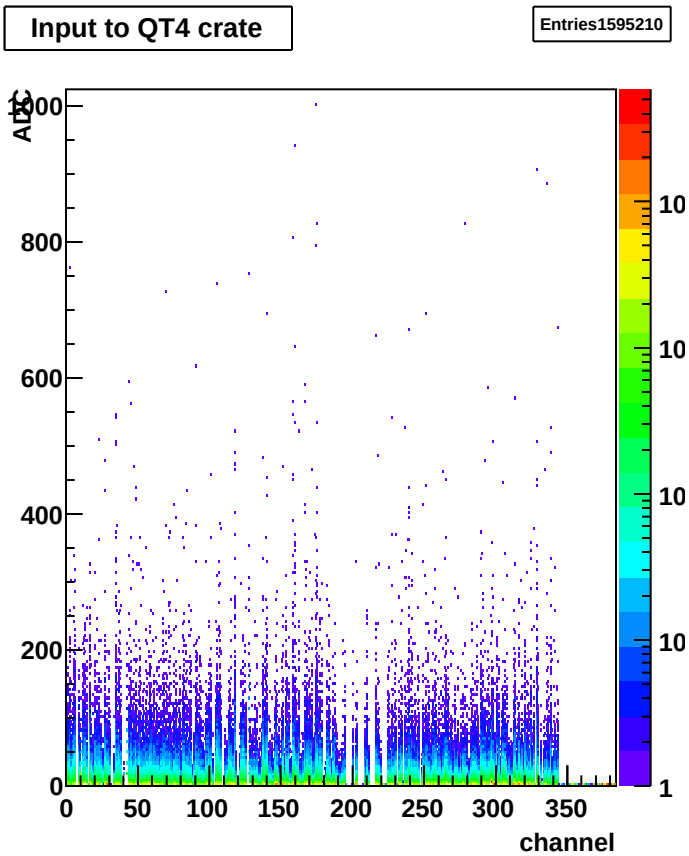

Entries 89338
ZDC East ADC Sum Att
ZDC West ADC Sum Att

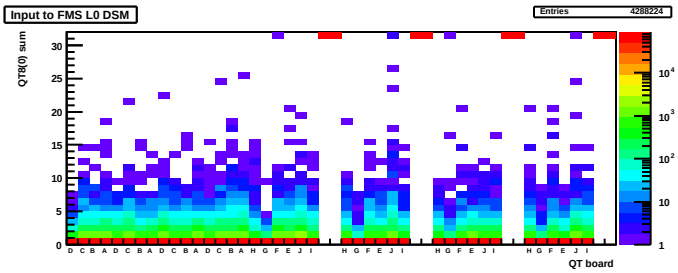

Entries 89338
BBC-L TAC Diff
BCC-S TAC Diff
Entries 89338
BBC-L TAC Diff
VPD TAC Diff
Entries 89338
BBC-L TAC Diff
ZDC TAC Diff
Entries 89338
BBC-S TAC Diff
VPD TAC Diff
Entries 89338
BBC-S TAC Diff
ZDC TAC Diff
Entries 89338
VPD TAC Diff
ZDC TAC Diff

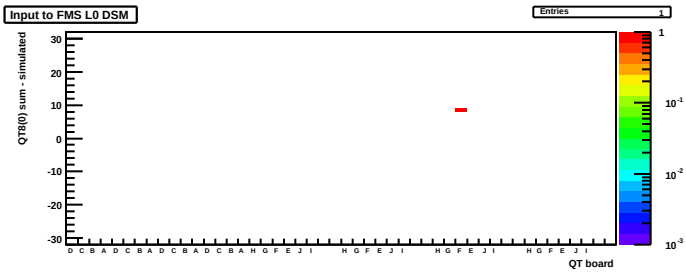

Input to QT1 crate
Entries2303775
ADC
channel

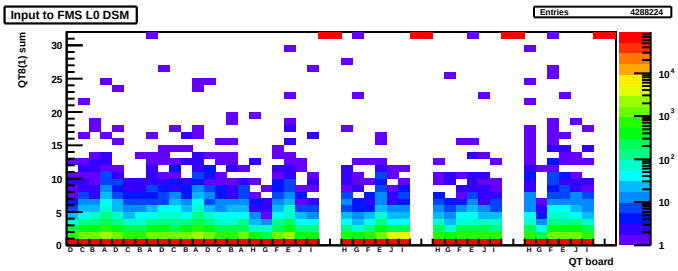

Input to QT2 crate
Entries1307917
ADC
channel

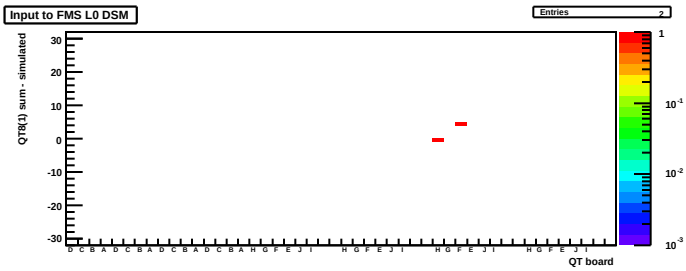

Input to QT3 crate
Entries1413841
ADC
channel

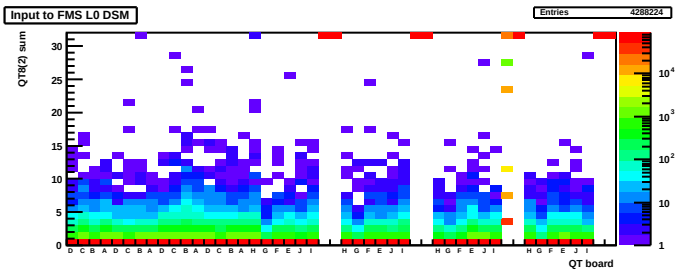

Input to QT4 crate
Entries1595210
ADC
channel

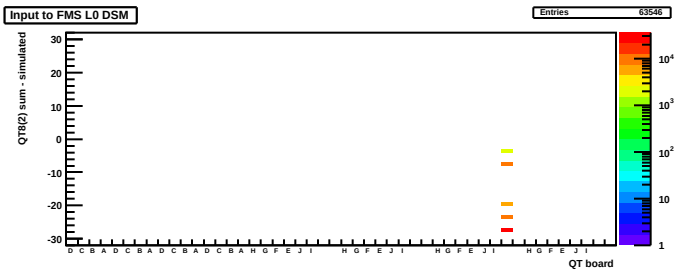

Input to FMS L0 DSM
Entries 4288224
QT8(0) sum
QT board

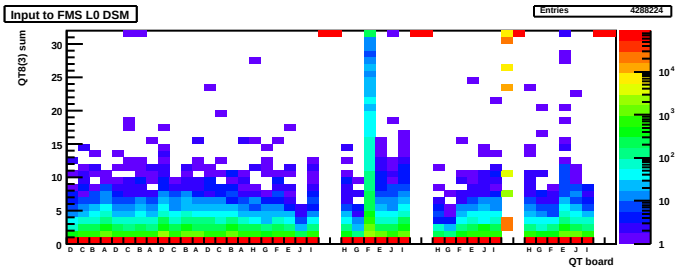

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

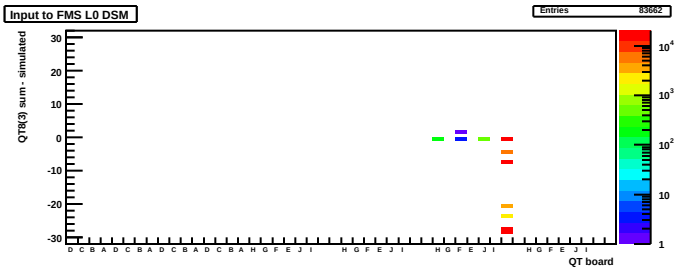

Input to FMS L0 DSM
Entries 4288224
QT8(1) sum
QT board

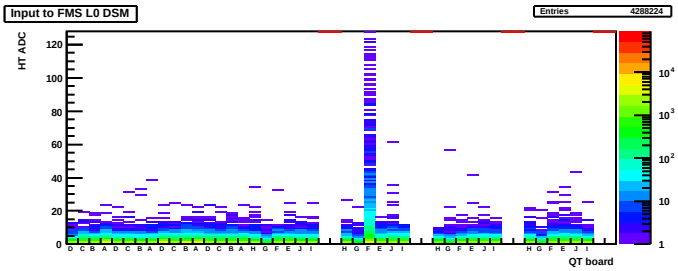

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

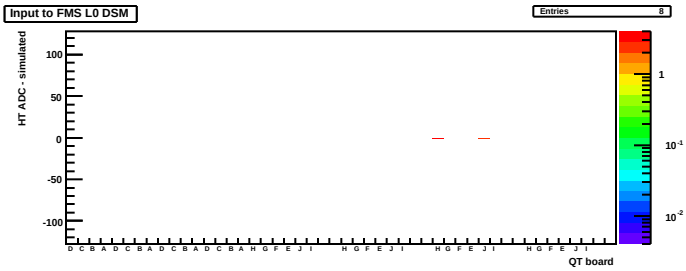

Input to FMS L0 DSM
Entries 4288224
QT8(2) sum
QT board

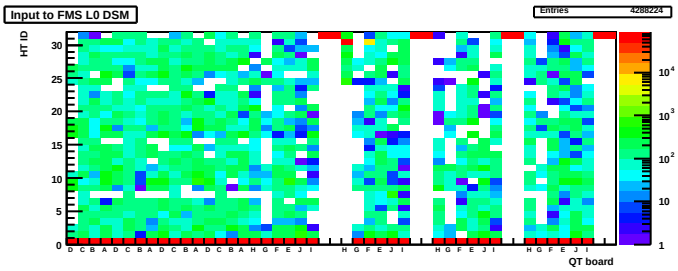

Input to FMS L0 DSM
Entries 63546
QT8(2) sum - simulated
QT board

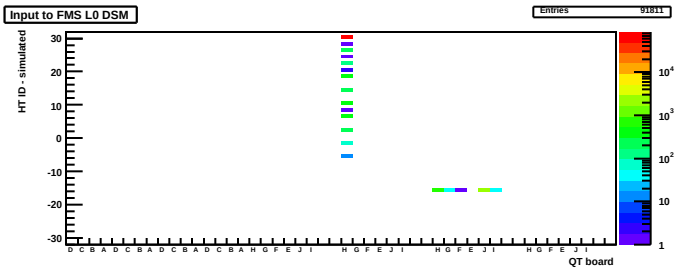

Input to FMS L0 DSM
Entries 4288224
QT8(3) sum
QT board

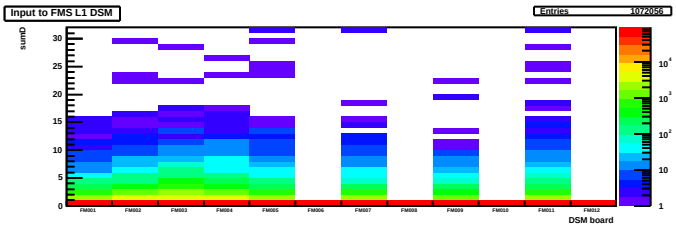

Input to FMS L0 DSM
Entries 83662
QT8(3) sum - simulated
QT board

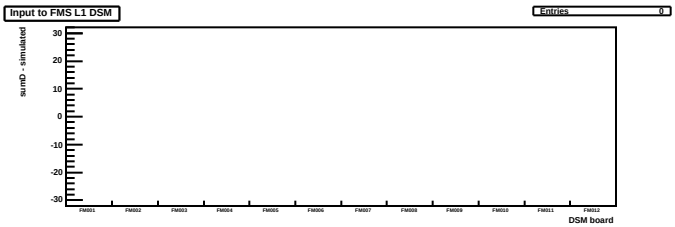

Input to FMS L0 DSM
Entries 4288224
HT ADC
QT board

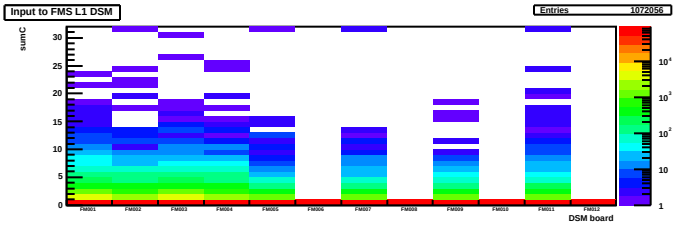

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

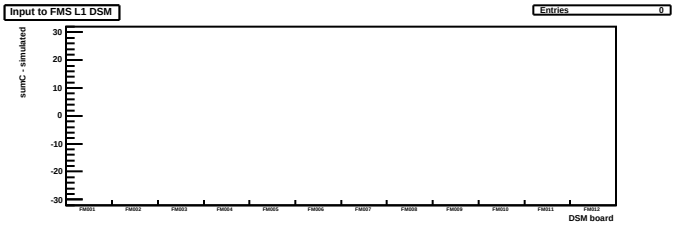

Input to FMS L0 DSM
Entries 4288224
HT ID
QT board

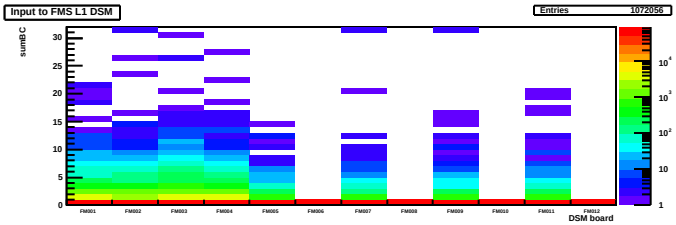

Input to FMS L0 DSM
Entries 91811
HT ID - simulated
QT board

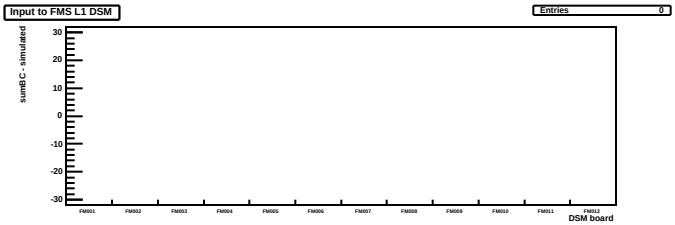

Input to FMS L1 DSM
Entries 1072056
sumD
DSM board

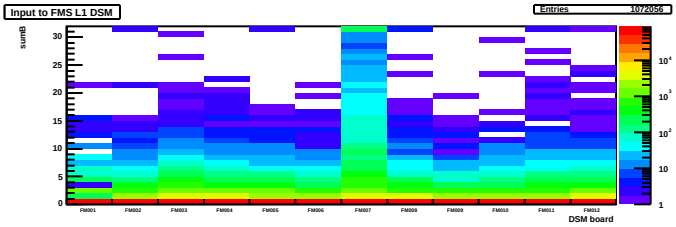

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

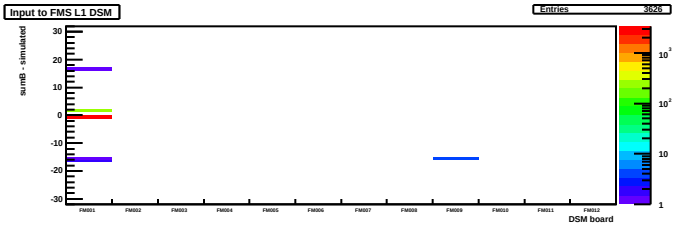

Input to FMS L1 DSM
Entries 1072056
sumC
DSM board

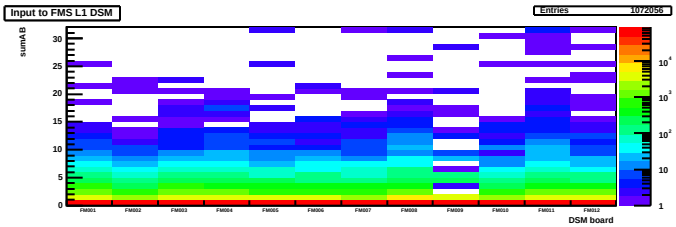

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

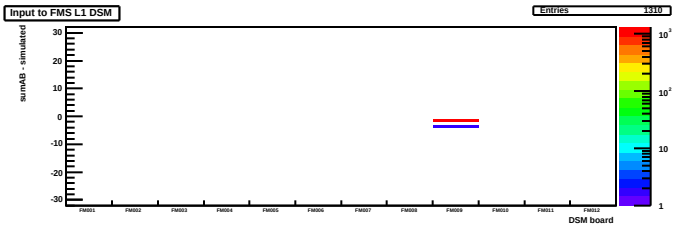

Input to FMS L1 DSM
Entries 1072056
sumBC
DSM board

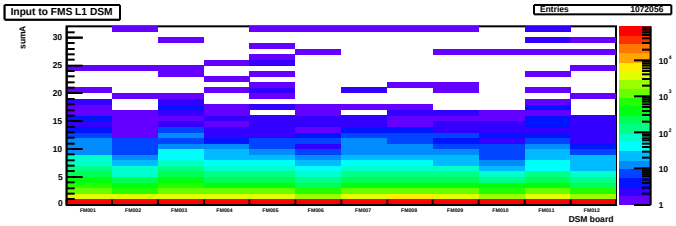

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

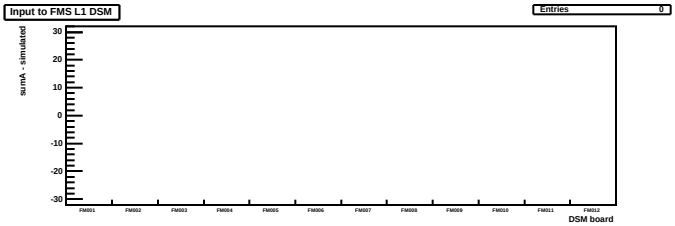

Input to FMS L1 DSM
Entries 1072056
sumB
DSM board

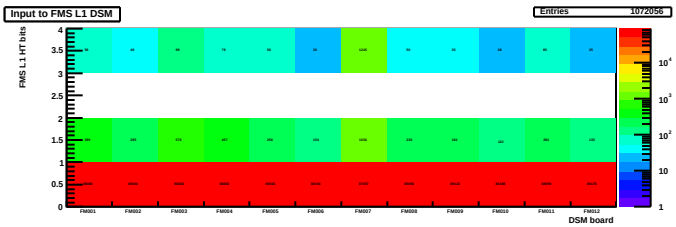

Input to FMS L1 DSM
Entries 3626
sumB - simulated
DSM board

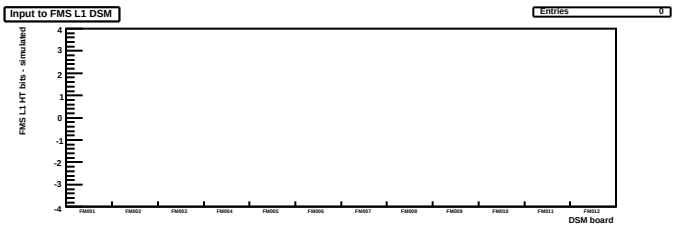

Input to FMS L1 DSM
Entries 1072056
sumAB
DSM board

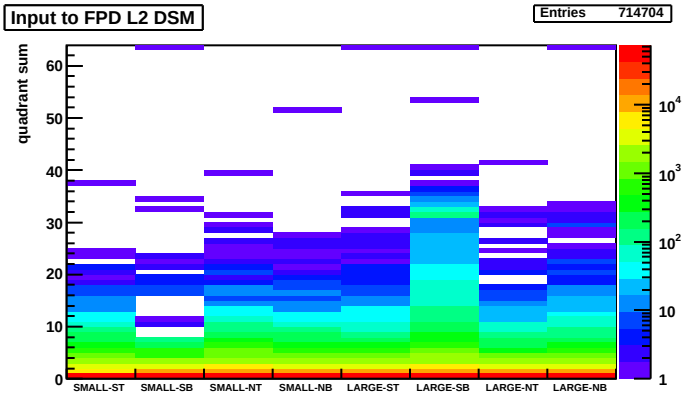

Input to FMS L1 DSM
Entries 1310
sumAB - simulated
DSM board

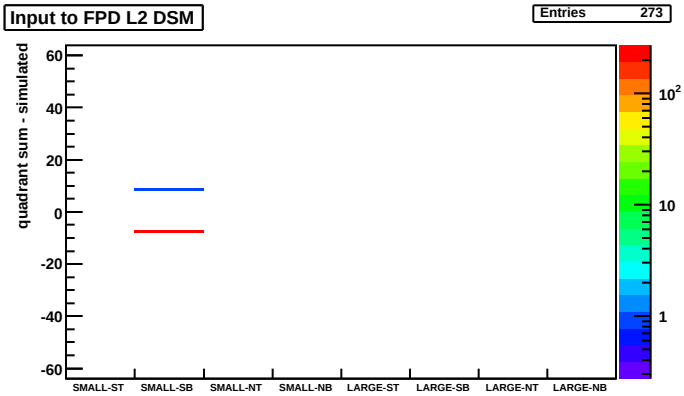

Input to FMS L1 DSM
Entries 1072056
sumA
DSM board

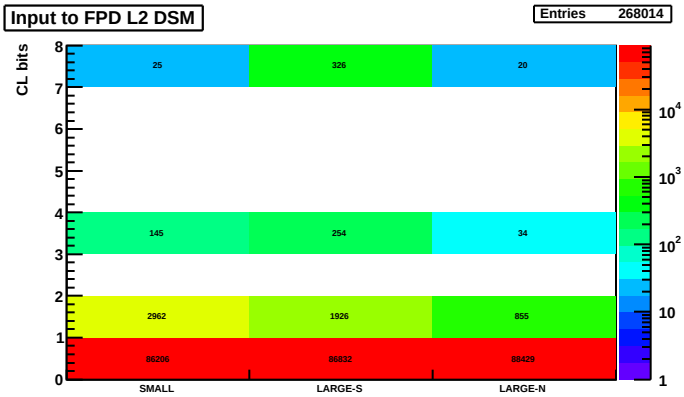

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

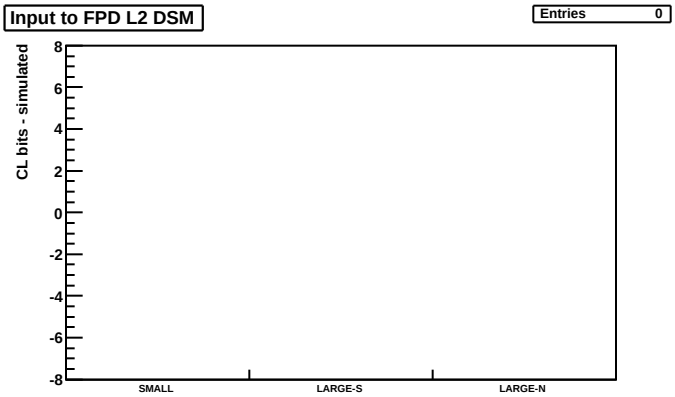

Input to FMS L1 DSM
Entries 1072056
FMS L1 HT bits
DSM board

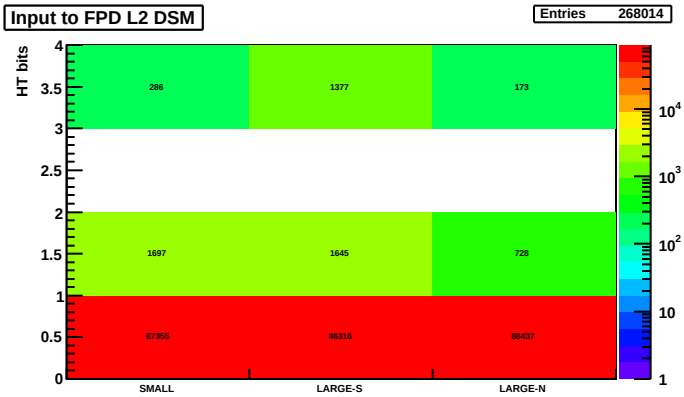

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

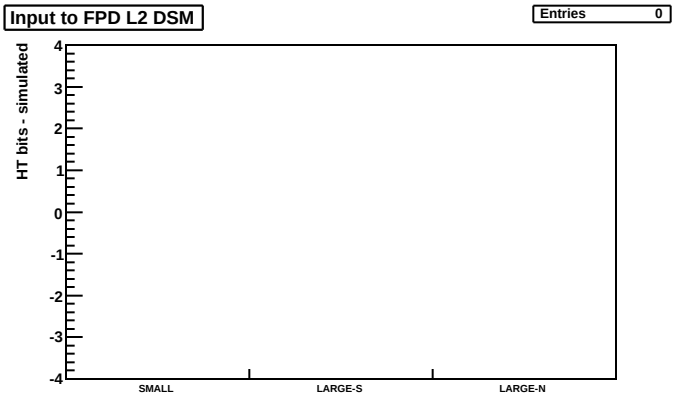

Input to FPD L2 DSM
Entries 714704
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

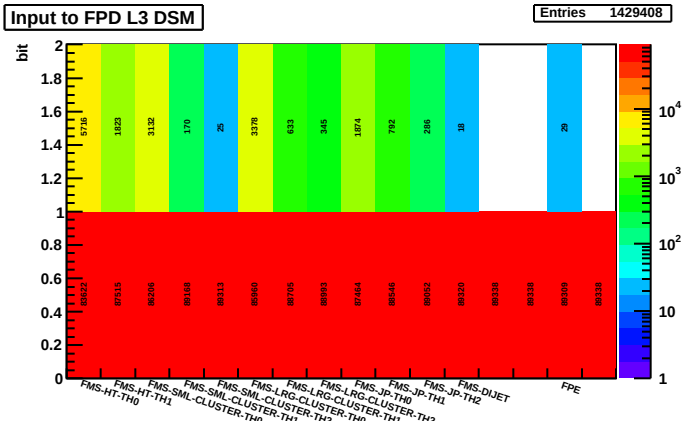

Input to FPD L2 DSM
Entries 273
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

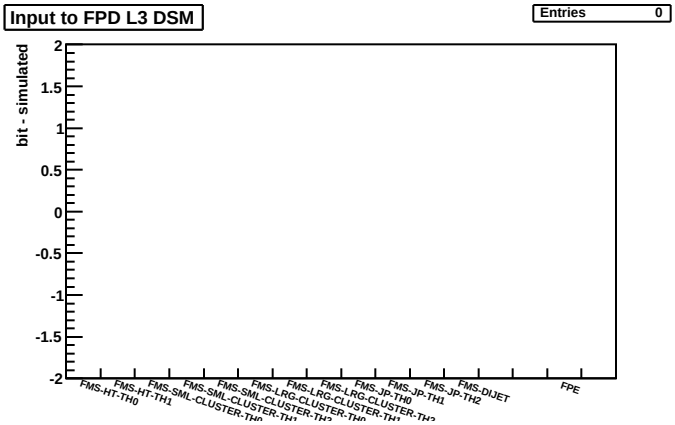

Input to FPD L2 DSM
Entries 268014
CL bits
25
326
20
145
254
34
2962
1926
855
86206
86832
88429
SMALL
LARGE-S
LARGE-N

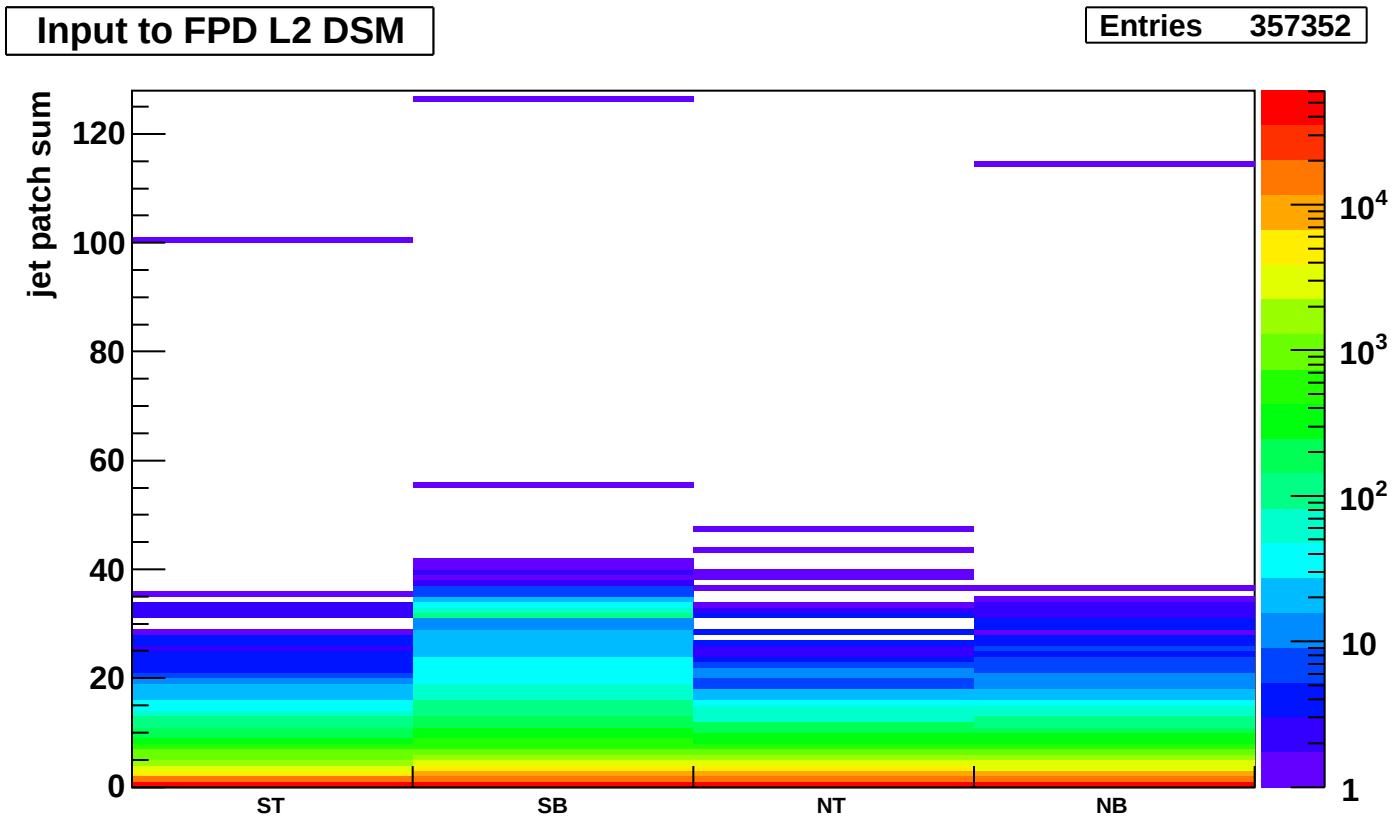

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

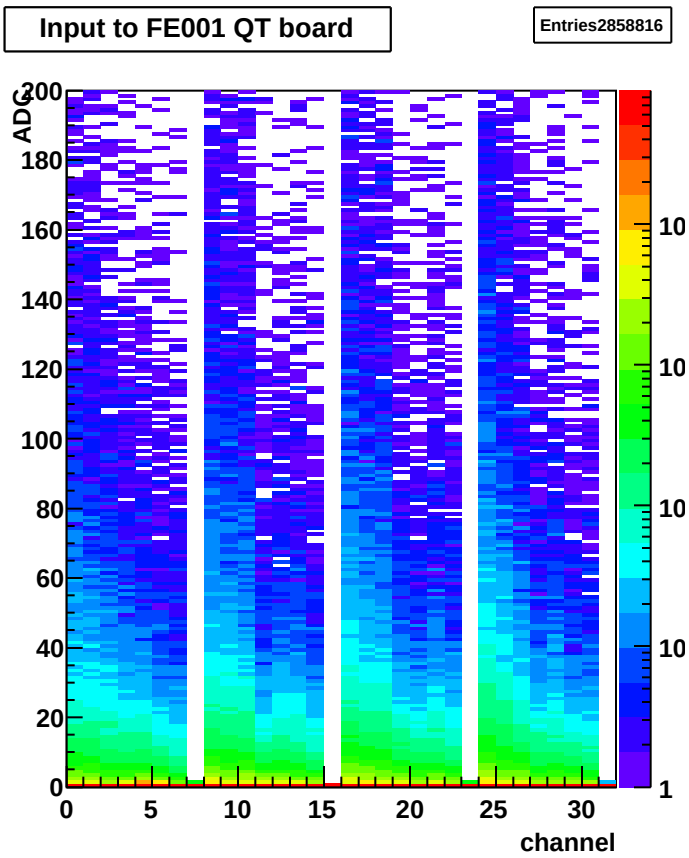

Input to FPD L2 DSM
Entries 268014
HT bits
286
1377
173
1697
1645
728
87355
86316
88437
SMALL
LARGE-S
LARGE-N

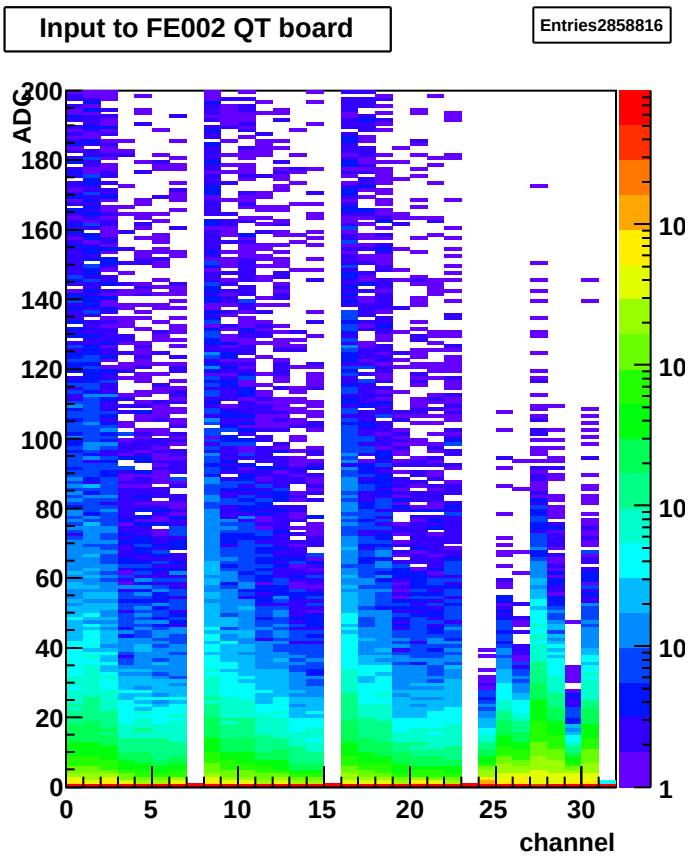

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

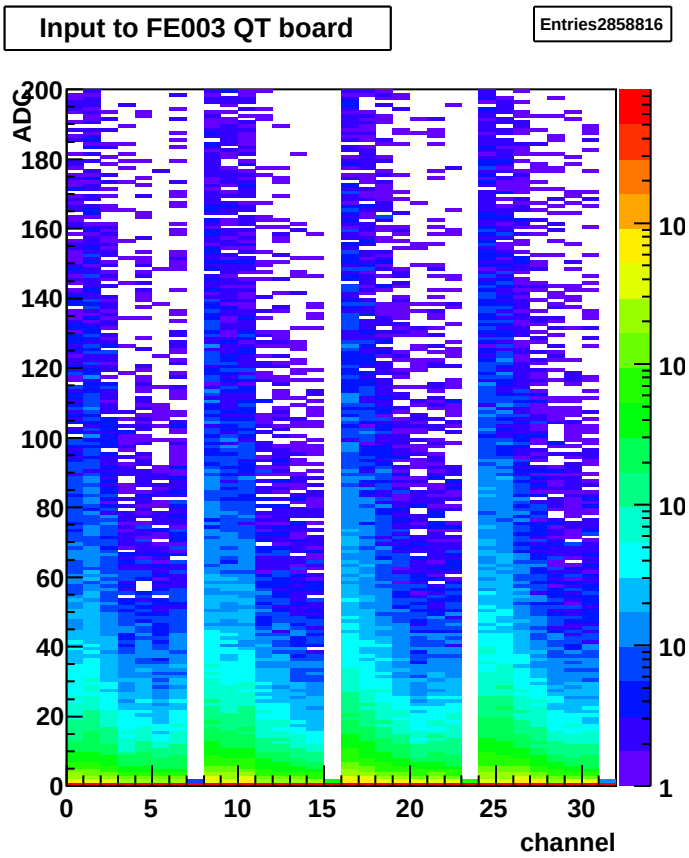

Input to FPD L3 DSM
Entries 1429408
bit
5736
1823
3132
170
25
3378
633
345
1874
792
286
18
29
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

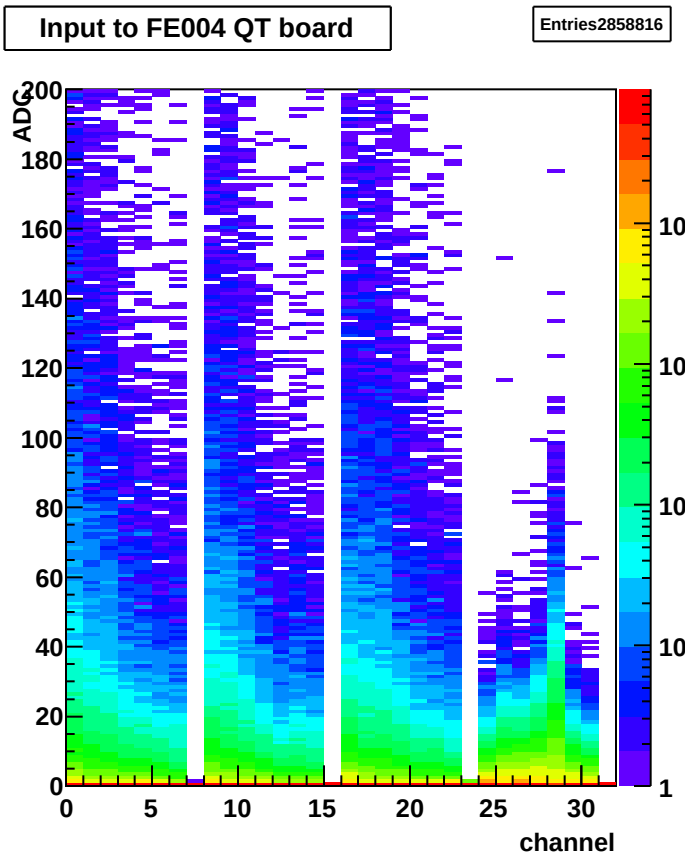

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

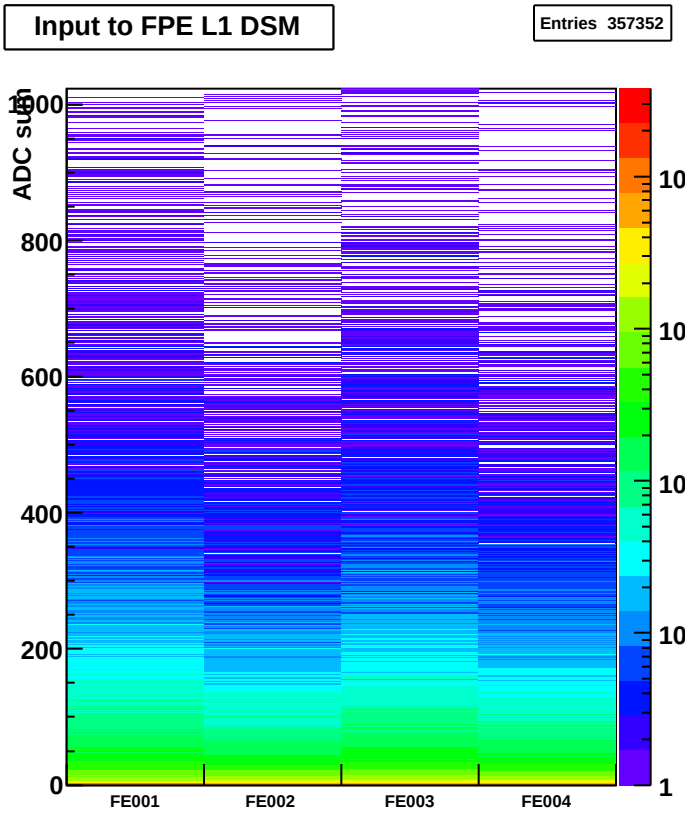

Input to FPD L2 DSM
Entries 357352
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

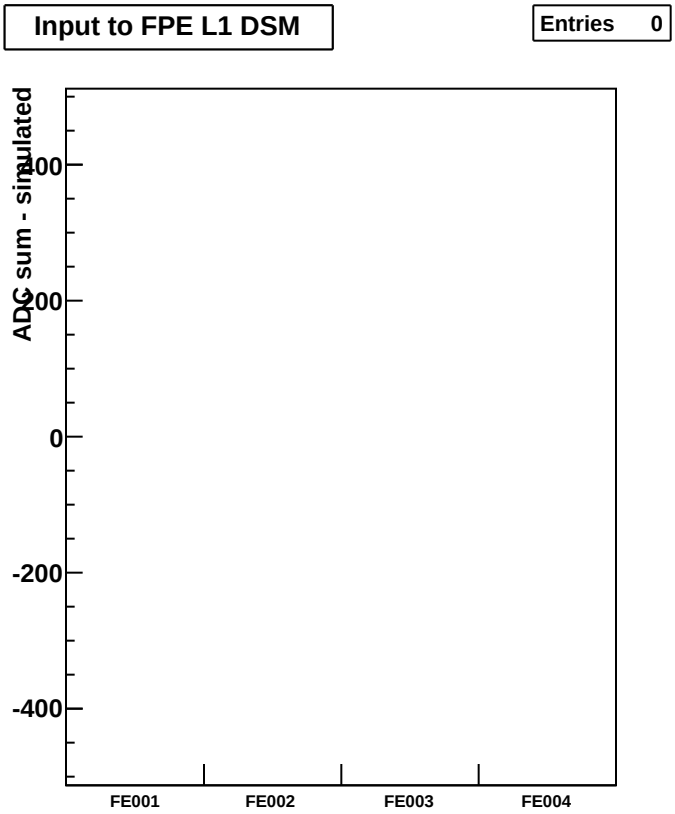

Input to FE001 QT board
Entries2858816
ADC
channel

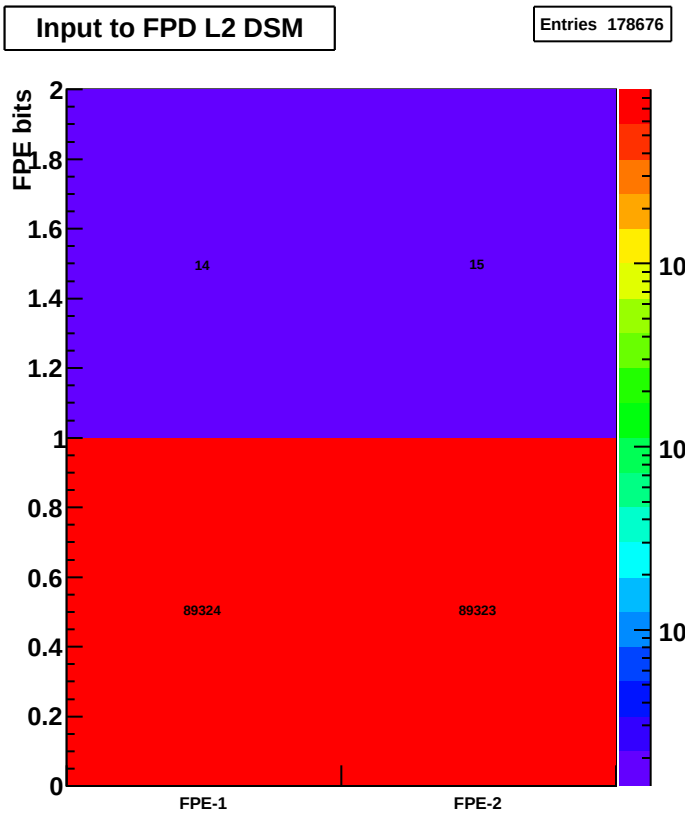

Input to FE002 QT board
Entries2858816
ADC
channel

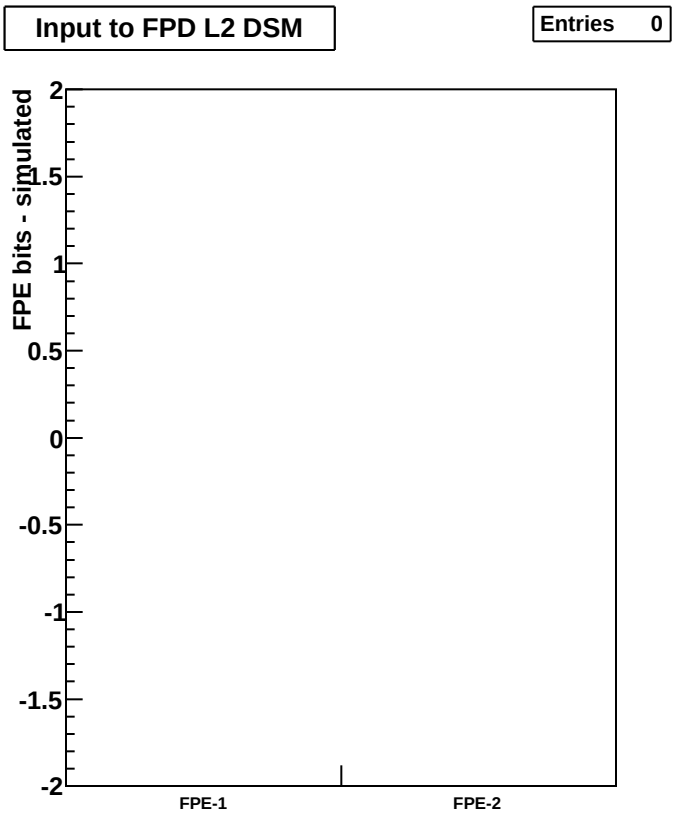

Input to FE003 QT board
Entries2858816
ADC
channel


Input to FE004 QT board
Entries2858816
ADC
channel


Input to FPE L1 DSM
Entries 357352
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 178676
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
14
15
89324
89323
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2