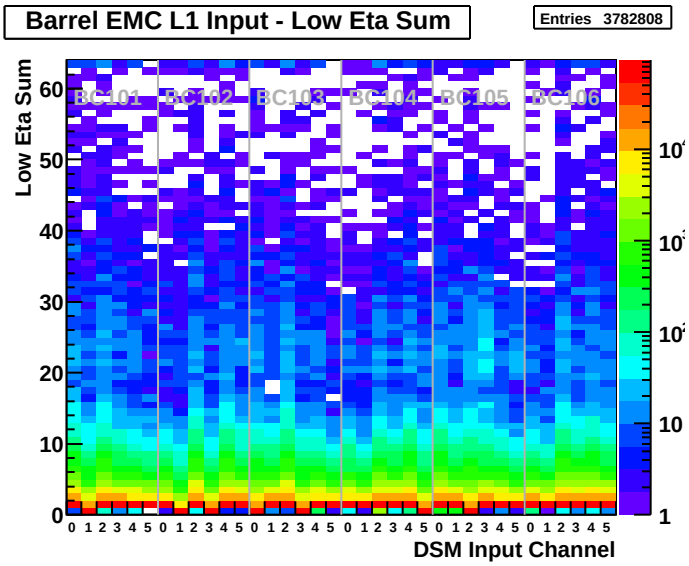
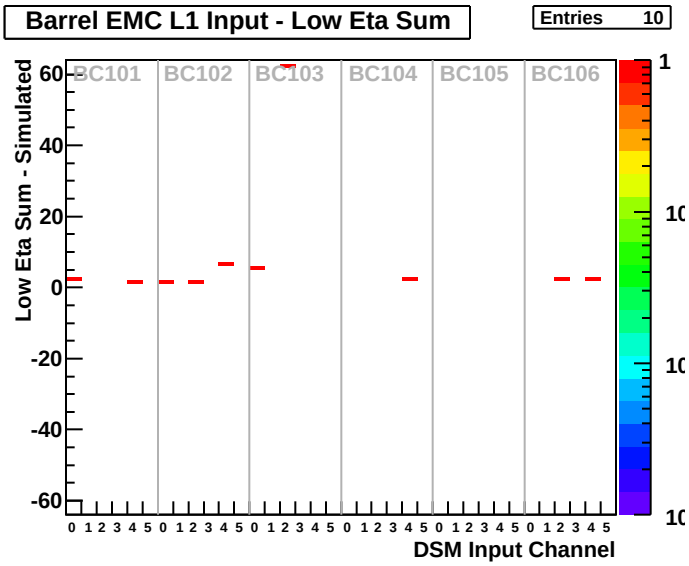
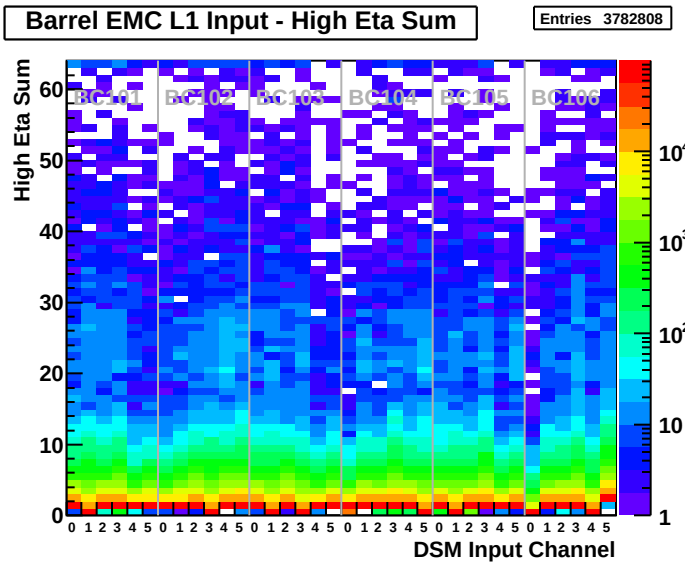
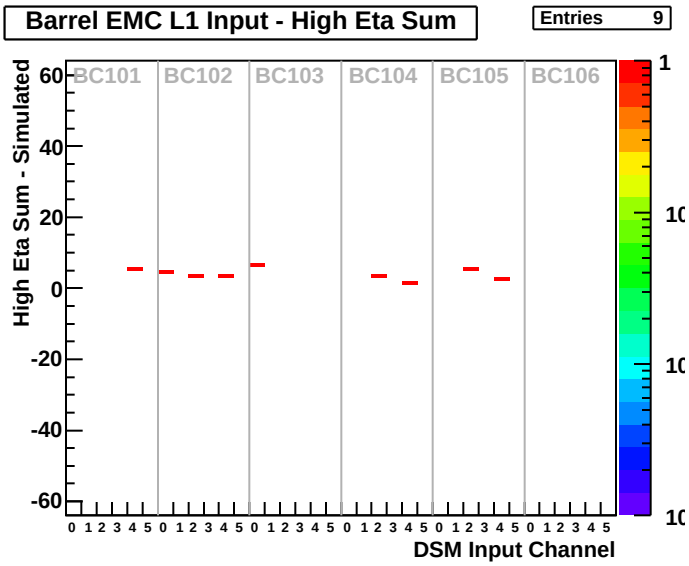
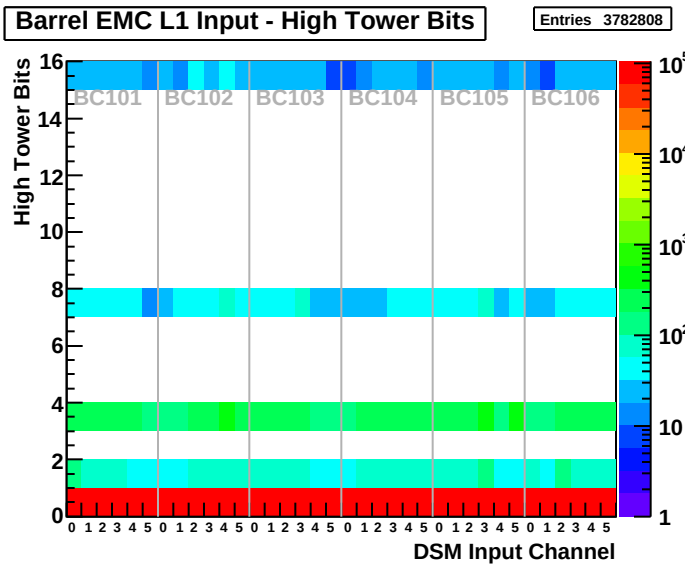
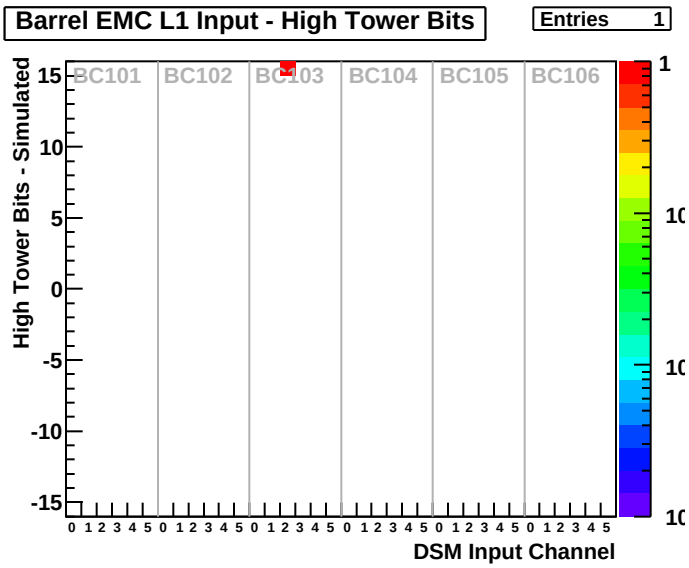

Barrel EMC L1 Input - Low Eta Sum
Entries 3782808
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

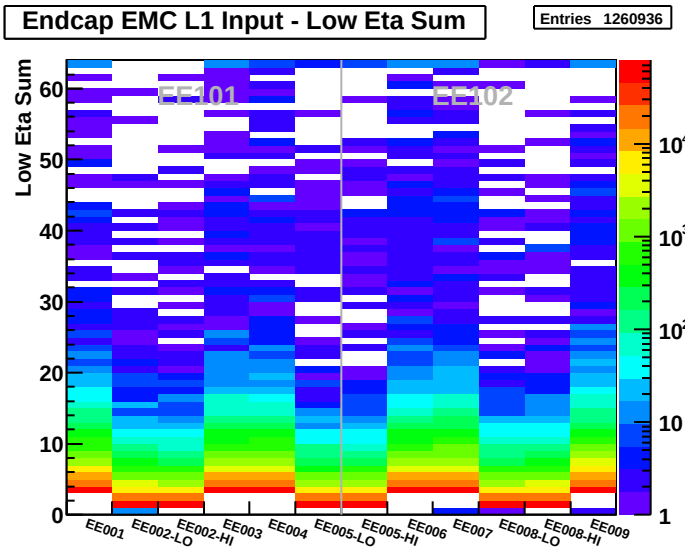

Barrel EMC L1 Input - Low Eta Sum
Entries 10
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

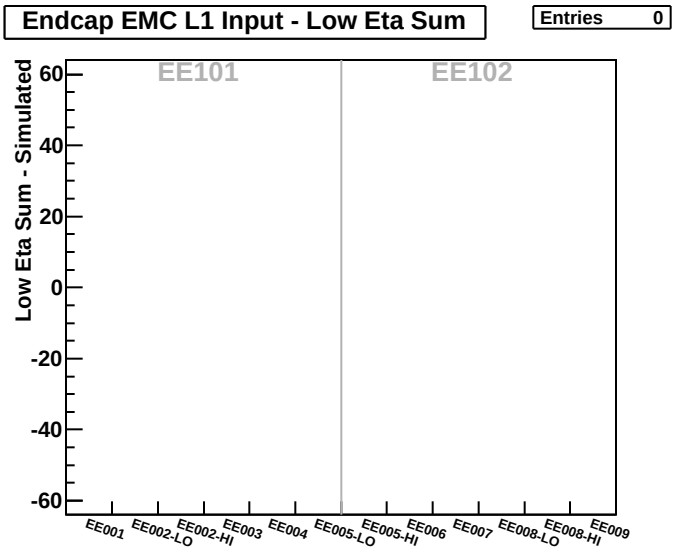

Barrel EMC L1 Input - High Eta Sum
Entries 3782808
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

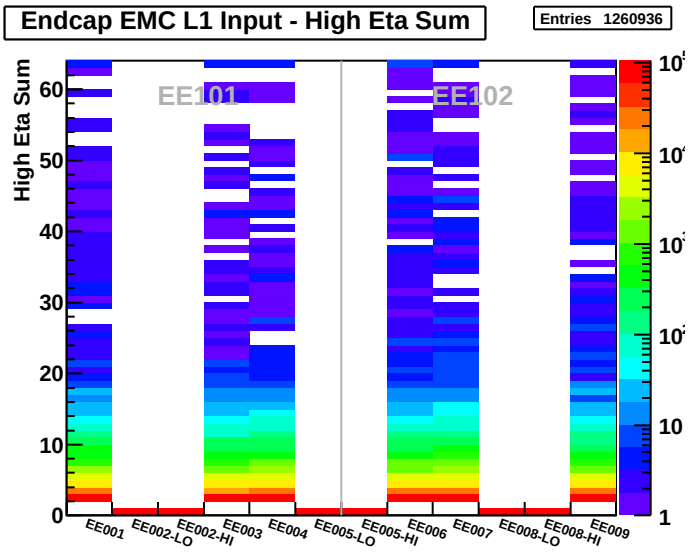

Barrel EMC L1 Input - High Eta Sum
Entries 9
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

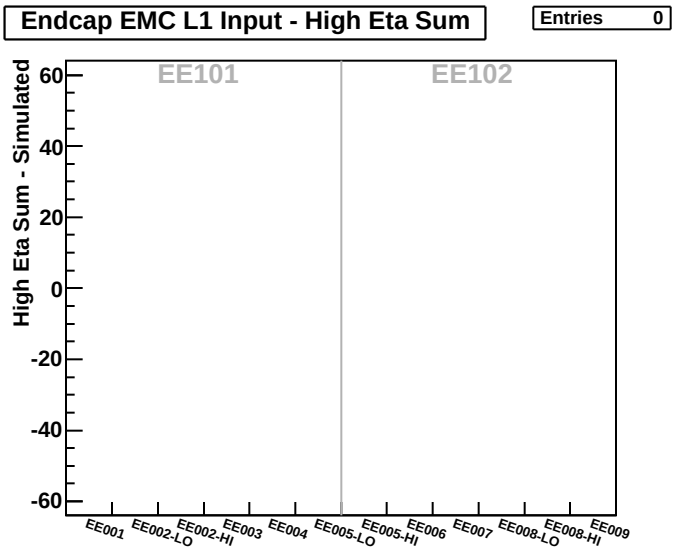

Barrel EMC L1 Input - High Tower Bits
Entries 3782808
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

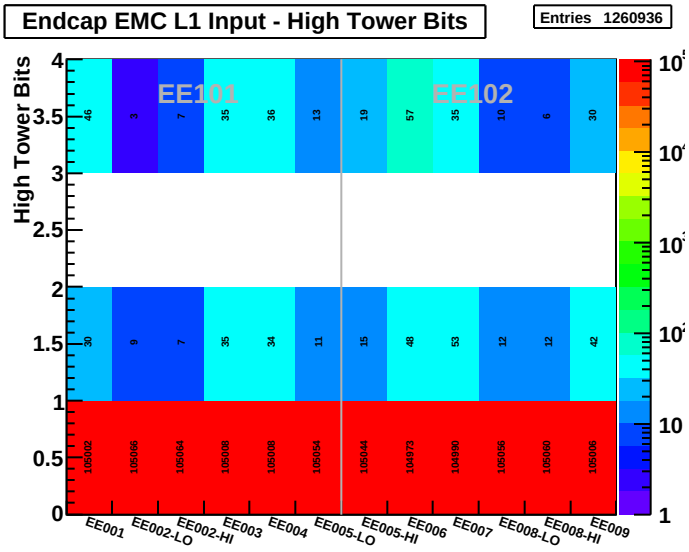

Barrel EMC L1 Input - High Tower Bits
Entries 1
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

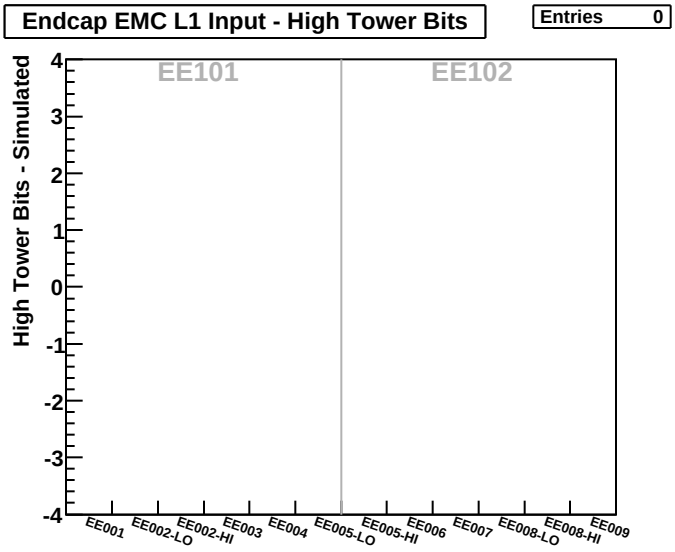

Endcap EMC L1 Input - Low Eta Sum
Entries 1260936
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

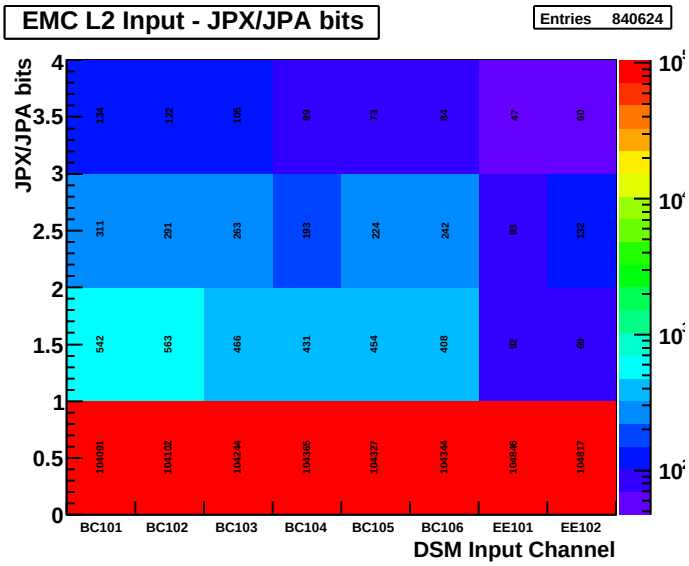

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

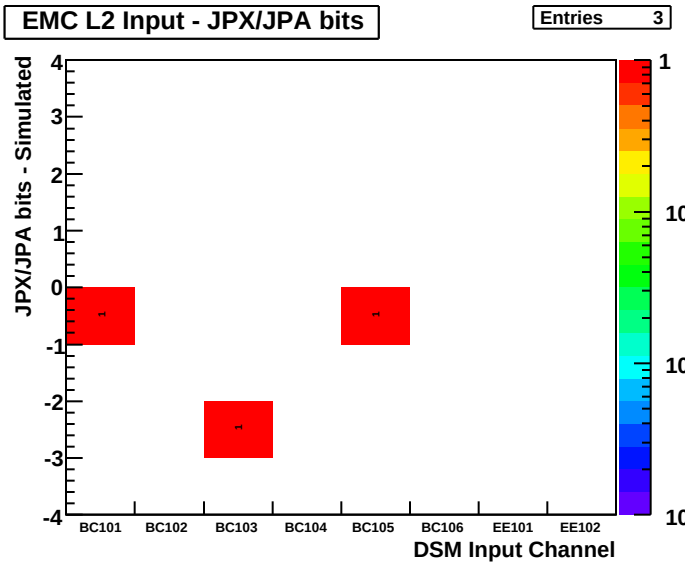

Endcap EMC L1 Input - High Eta Sum
Entries 1260936
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

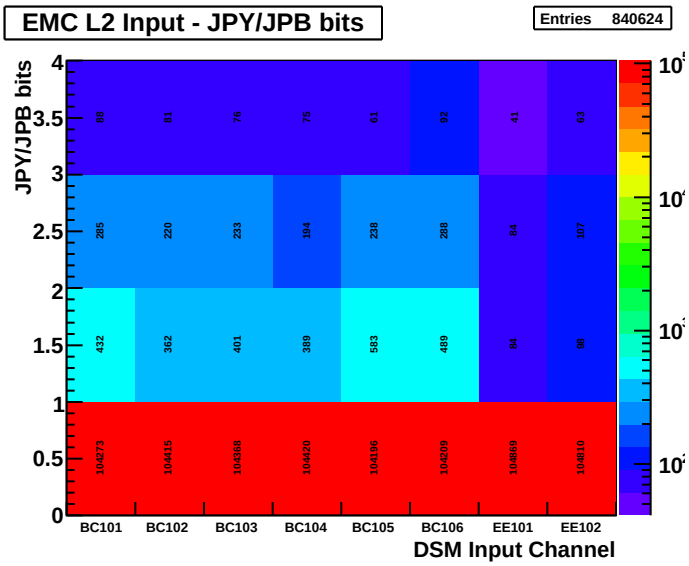

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

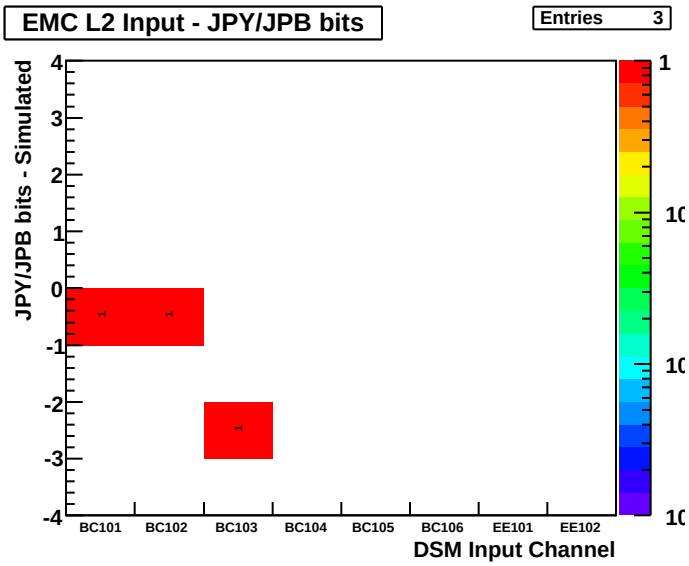

Endcap EMC L1 Input - High Tower Bits
Entries 1260936
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

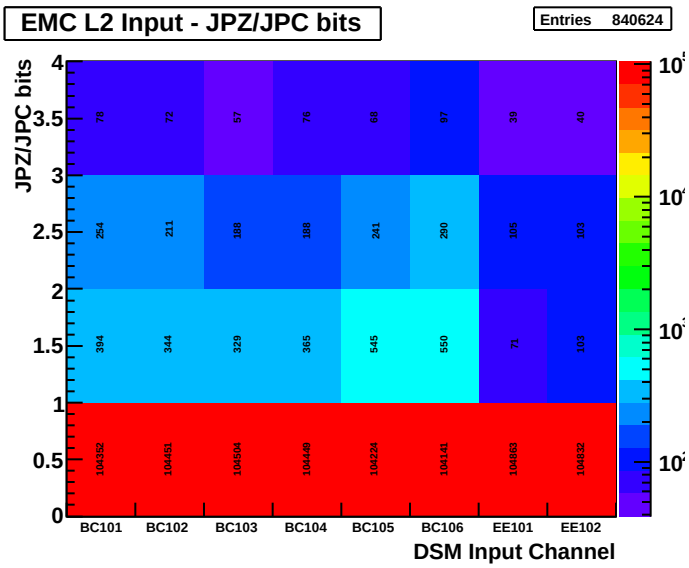

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

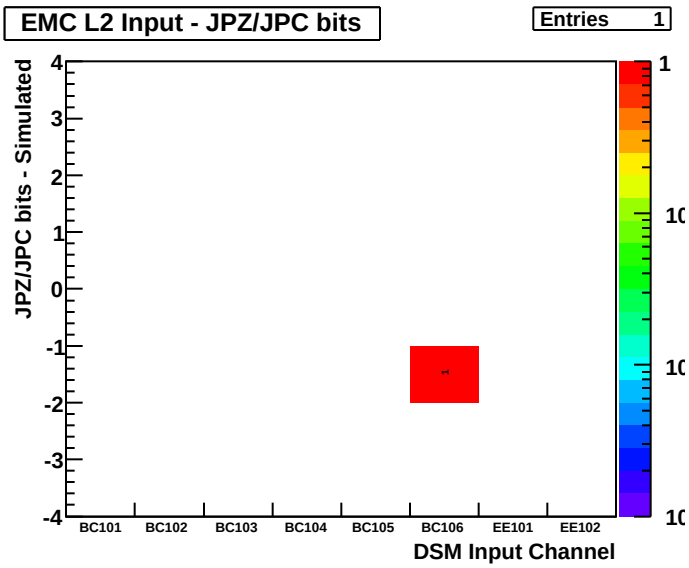

EMC L2 Input - JPX/JPA bits
Entries 840624
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
134 122 105 89 73 84 47 60
311 291 263 193 224 242 93 132
542 563 466 431 454 408 92 69
104091 104102 104244 104365 104327 104344 104846 104917

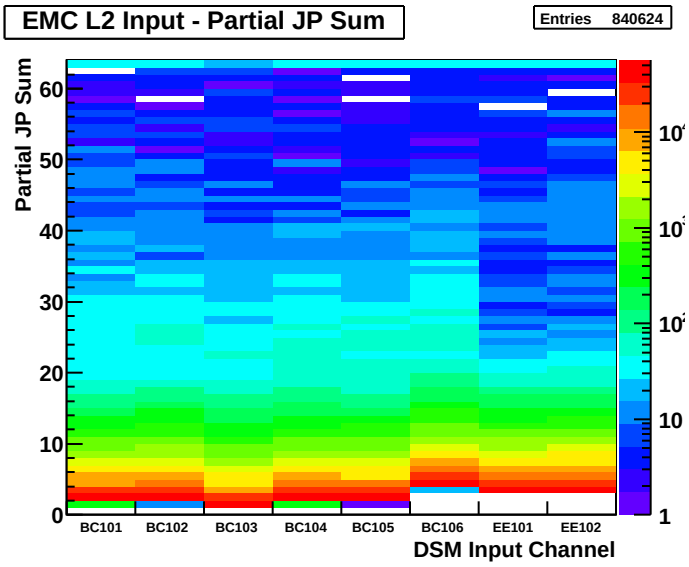

EMC L2 Input - JPX/JPA bits
Entries 3
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

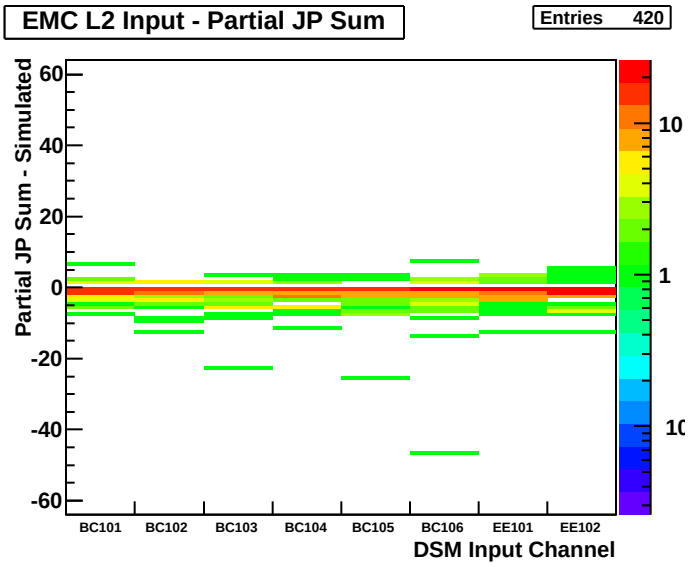

EMC L2 Input - JPY/JPB bits
Entries 840624
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
88 81 76 75 61 92 41 63
285 220 233 194 238 288 84 107
432 362 401 389 583 489 84 98
104273 104415 104368 104420 104196 104209 104869 104810

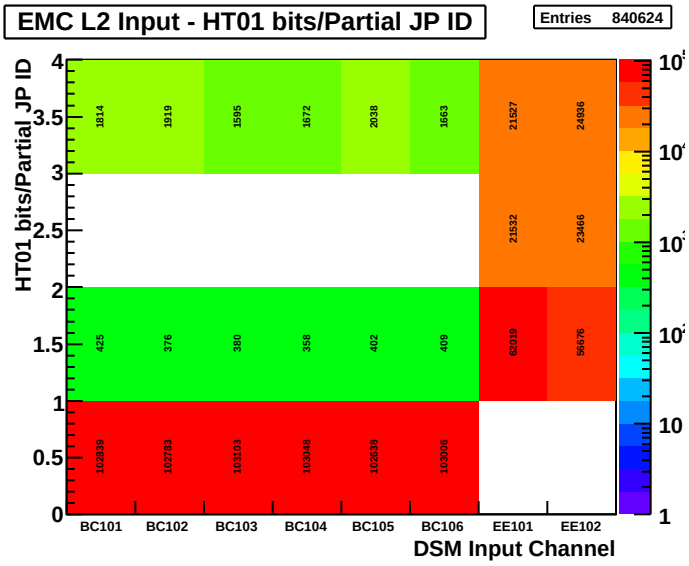

EMC L2 Input - JPY/JPB bits
Entries 3
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

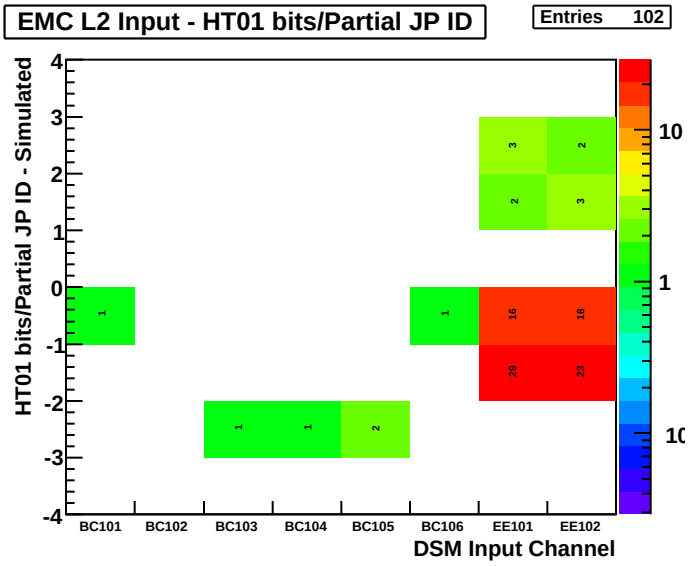

EMC L2 Input - JPZ/JPC bits
Entries 840624
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
78 72 57 76 68 97 39 40
254 211 188 188 241 290 105 103
394 344 329 365 545 550 71 103
104352 104451 104504 104449 104224 104141 104863 104832

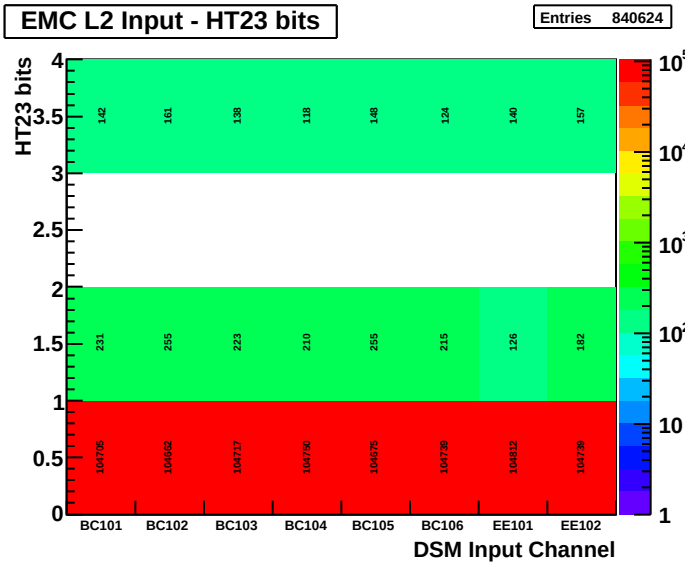

EMC L2 Input - JPZ/JPC bits
Entries 1
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

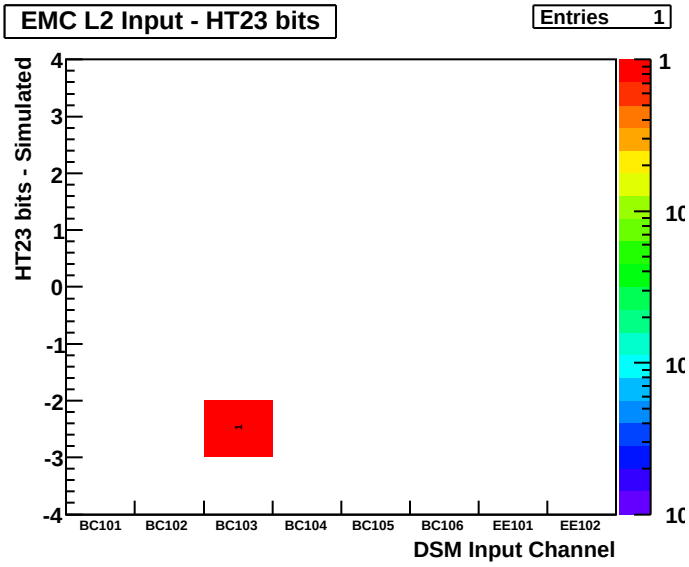

EMC L2 Input - Partial JP Sum
Entries 840624
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

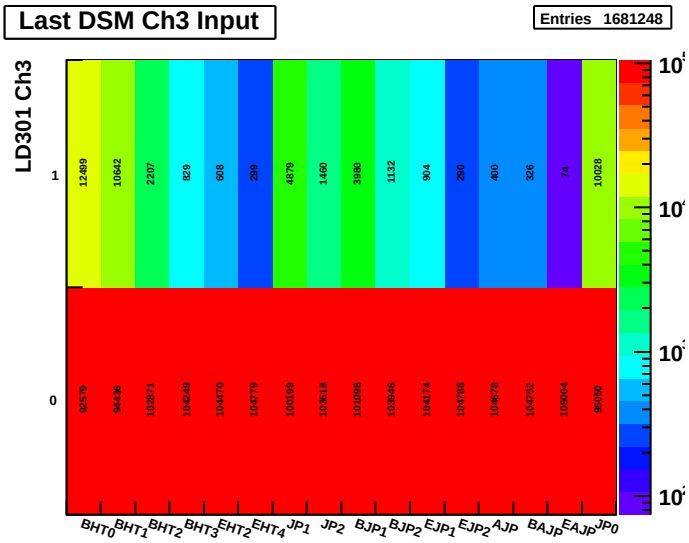

EMC L2 Input - Partial JP Sum
Entries 420
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

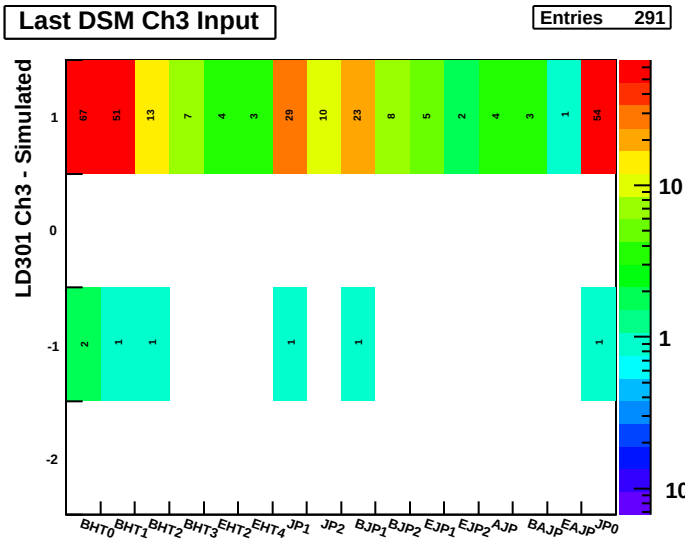

EMC L2 Input - HT01 bits/Partial JP ID
Entries 840624
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

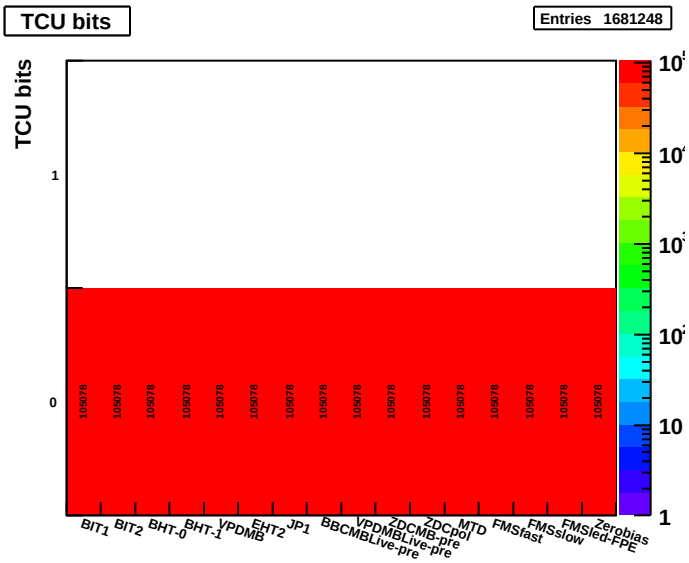

EMC L2 Input - HT01 bits/Partial JP ID
Entries 102
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

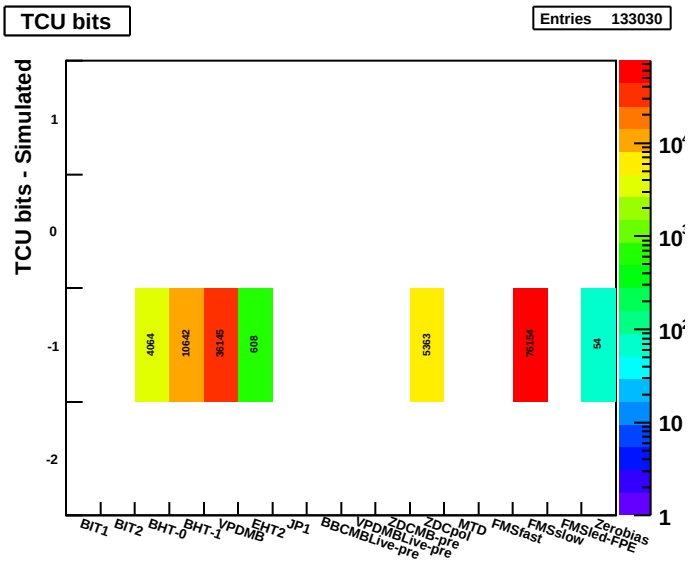

EMC L2 Input - HT23 bits
Entries 840624
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

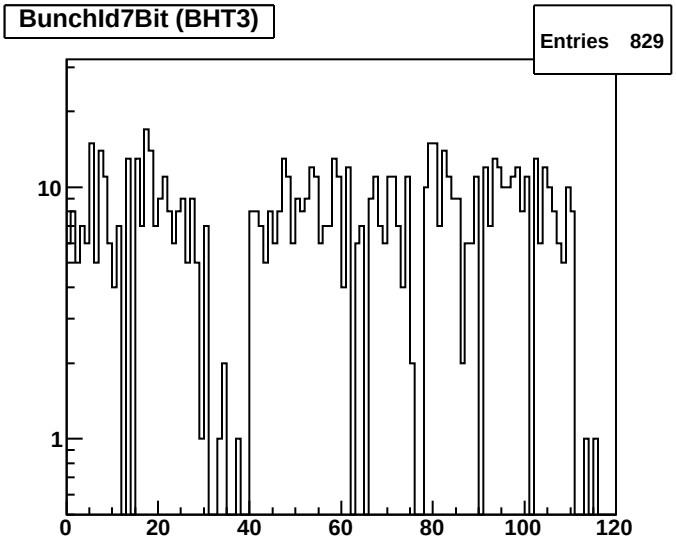

EMC L2 Input - HT23 bits
Entries 1
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

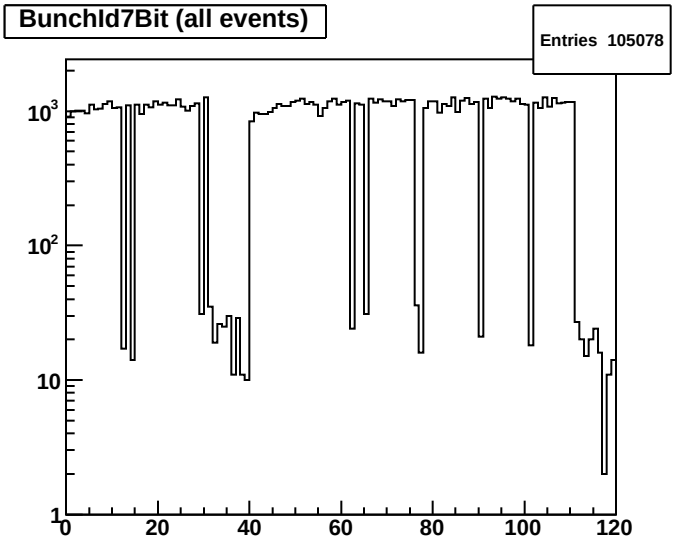

Last DSM Ch3 Input
Entries 1681248
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

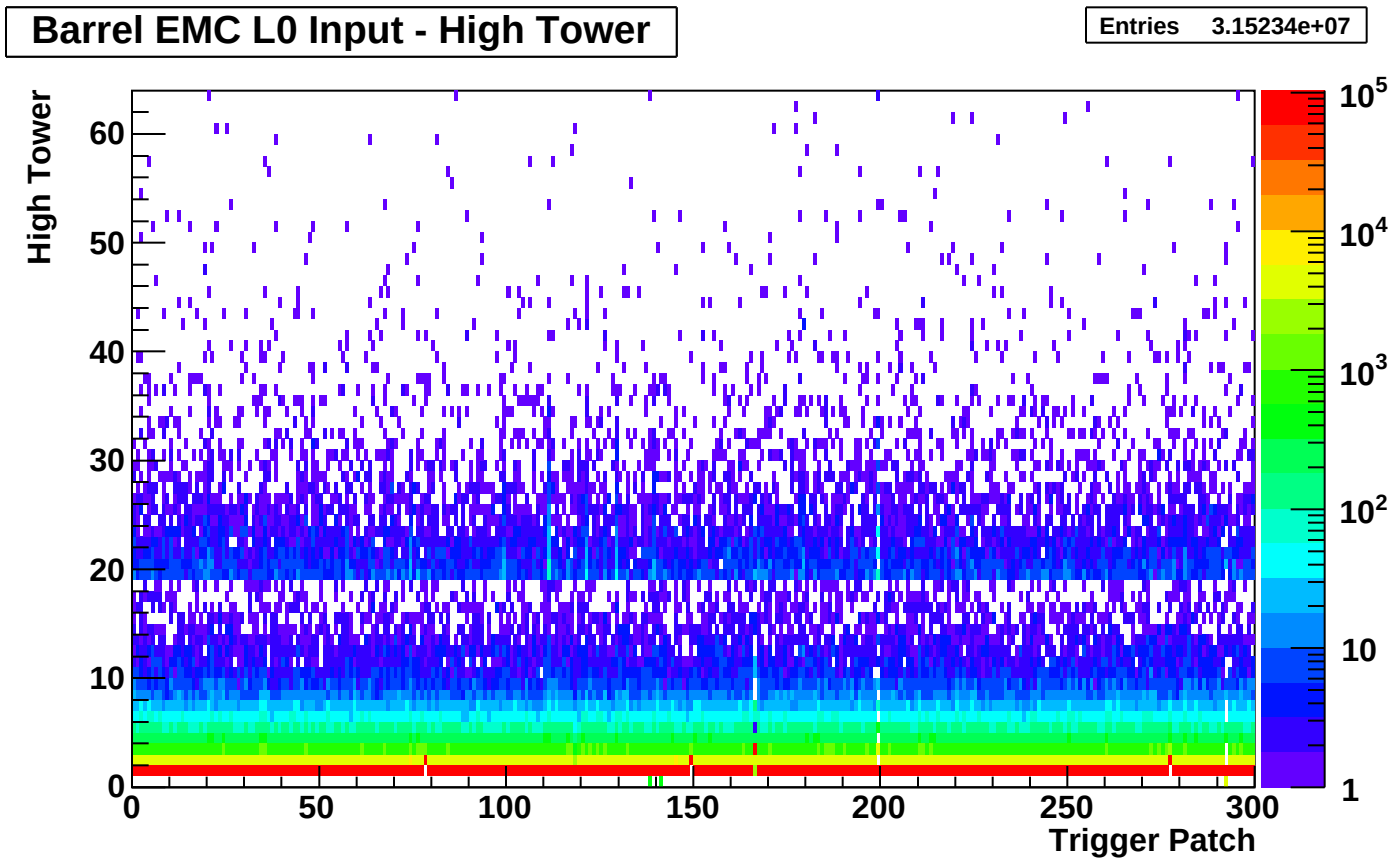

Last DSM Ch3 Input
Entries 291
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

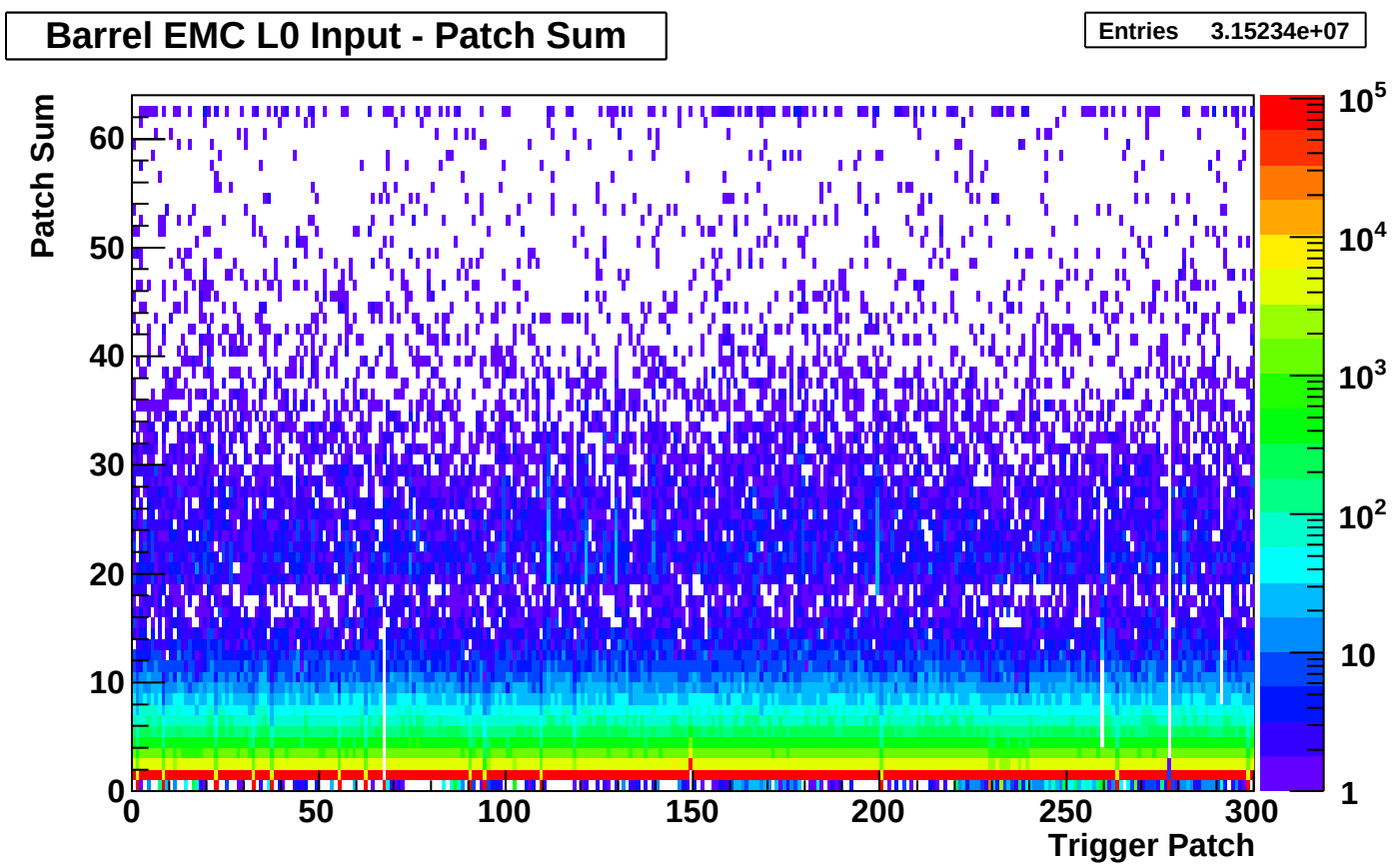

TCU bits
Entries 1681248
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

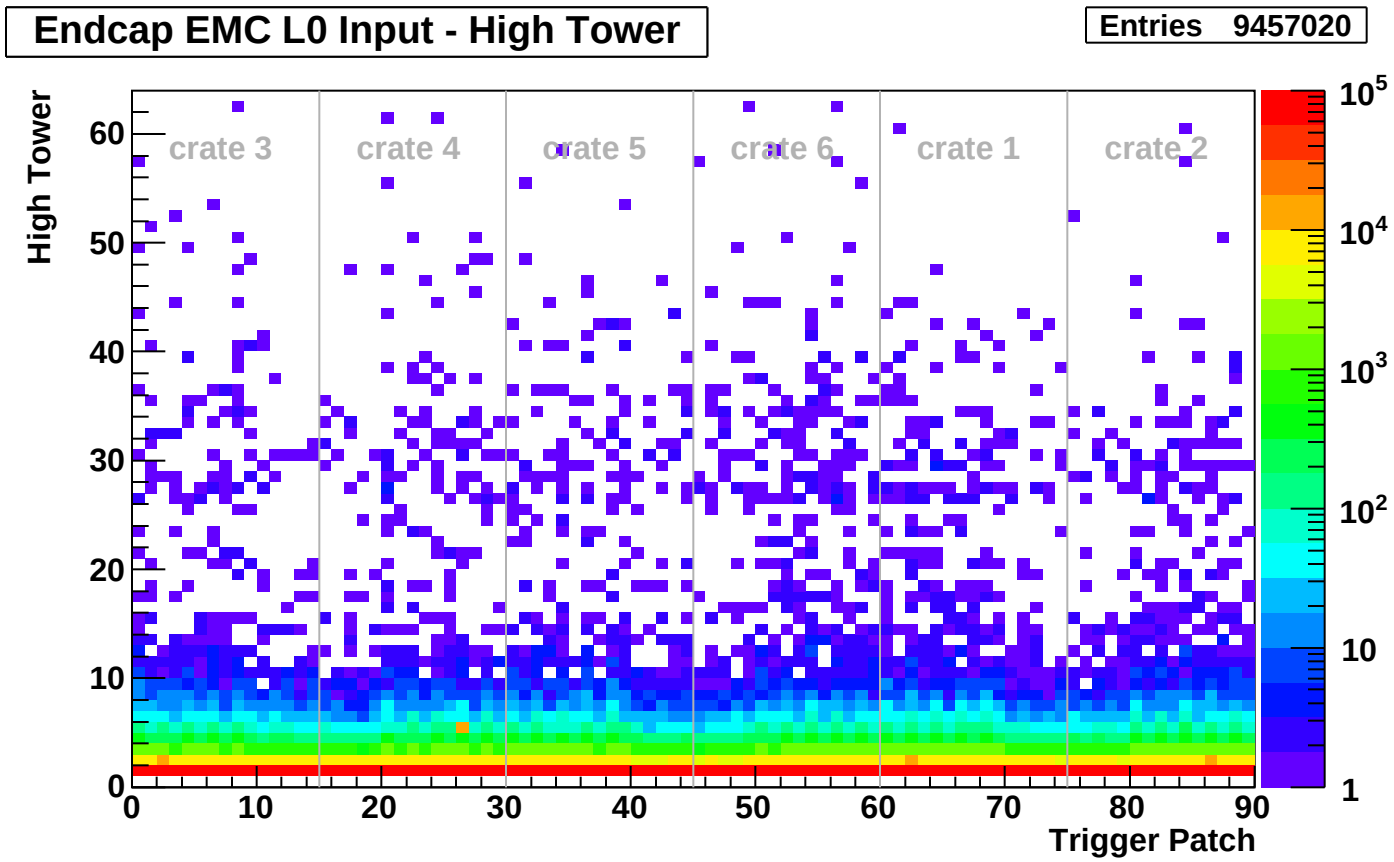

TCU bits
Entries 133030
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

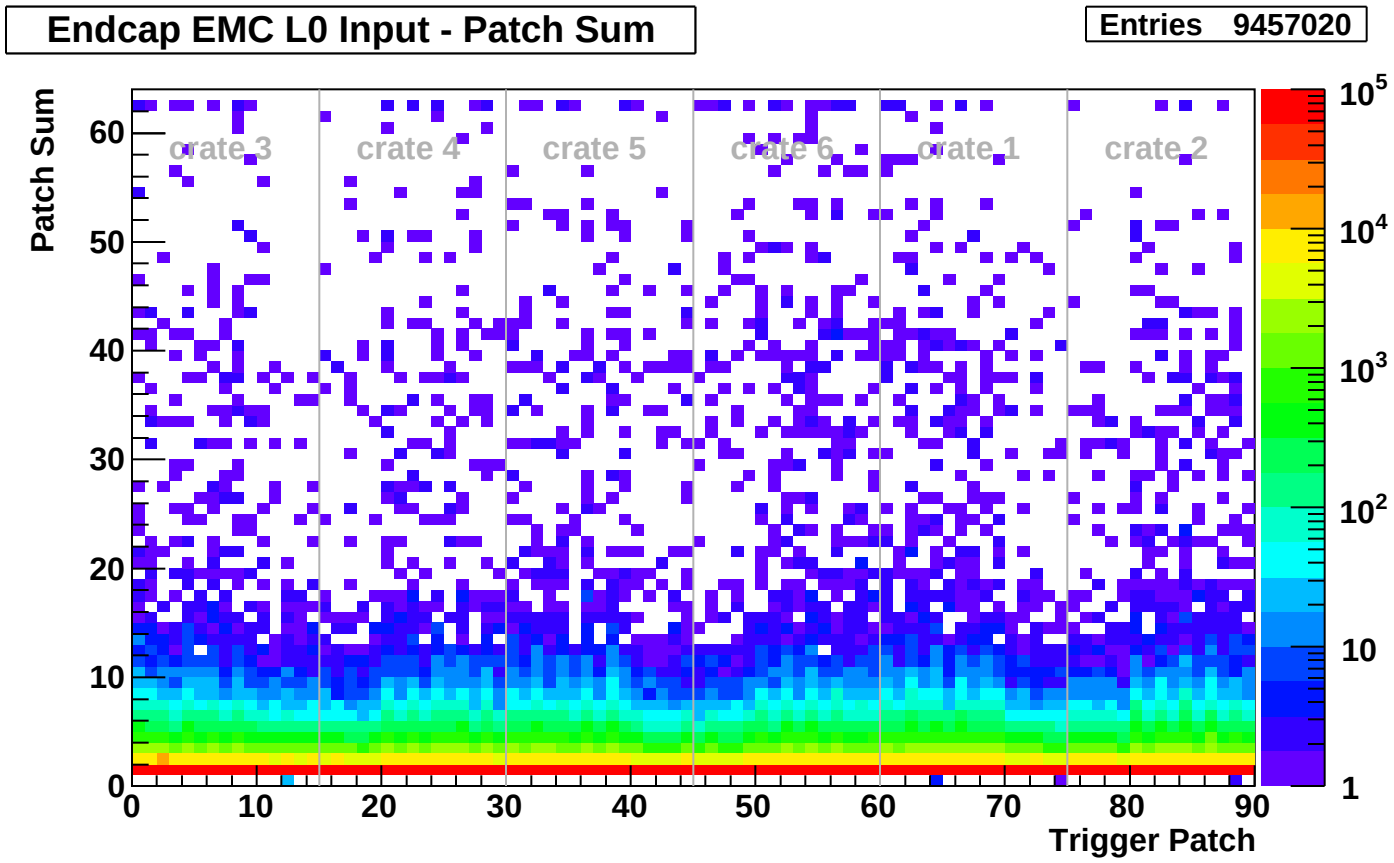

BunchId7Bit (BHT3)
Entries 829

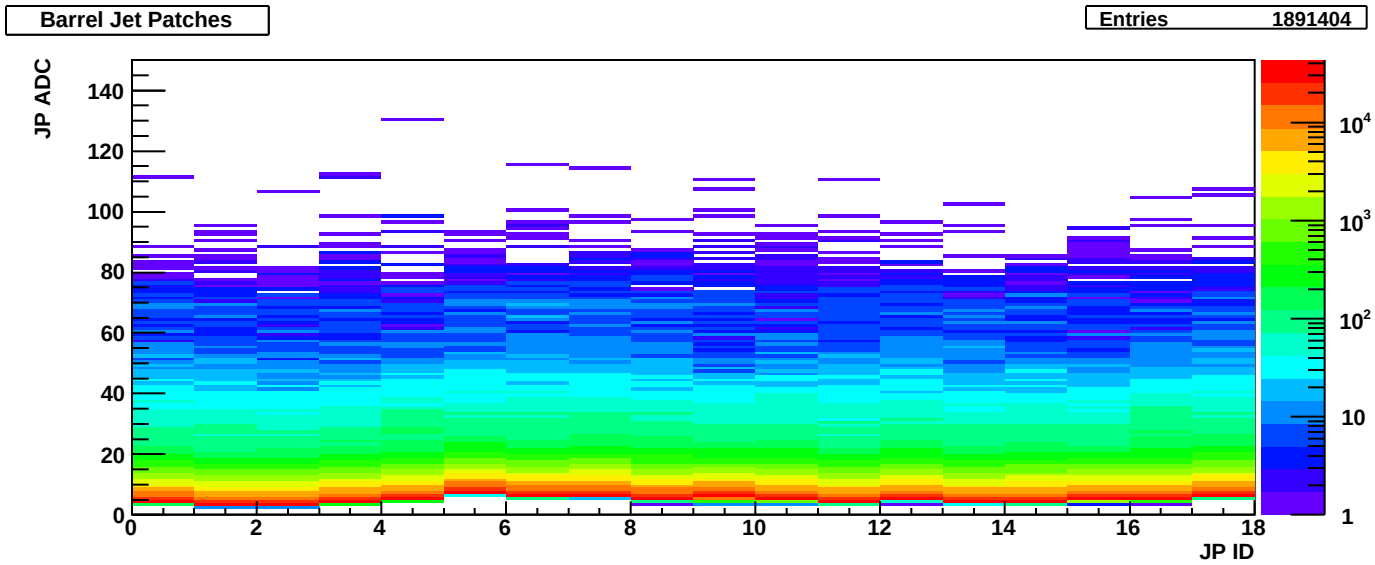

BunchId7Bit (all events)
Entries 105078

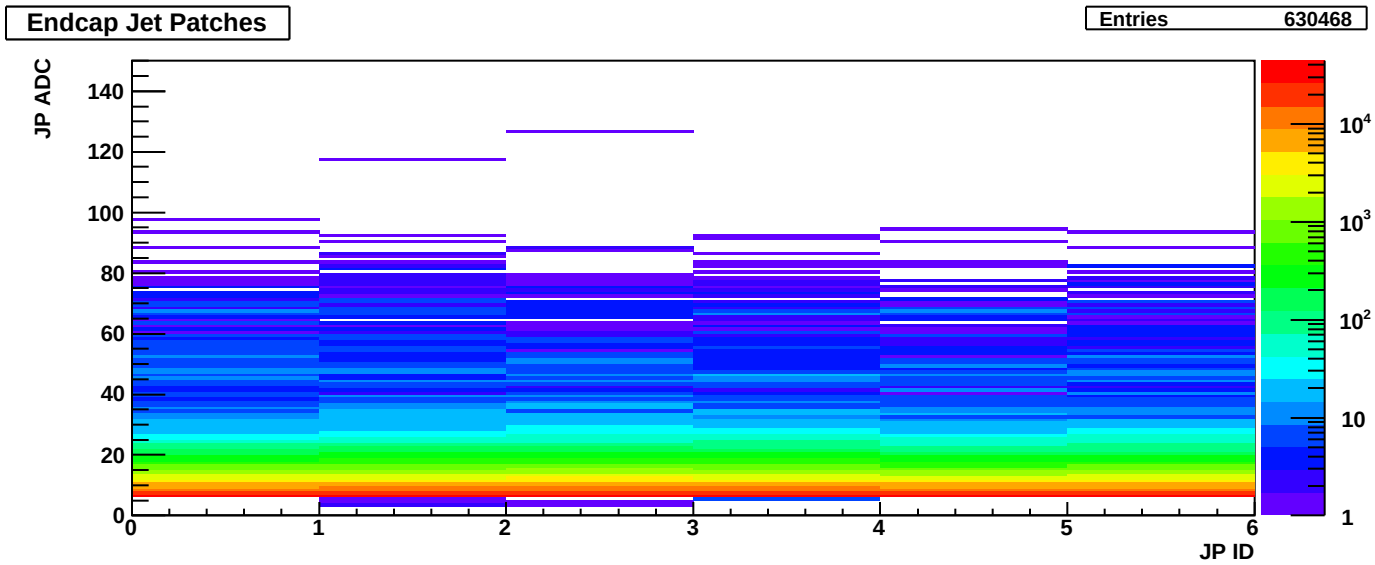

Barrel EMC L0 Input - High Tower
Entries 3.15234e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

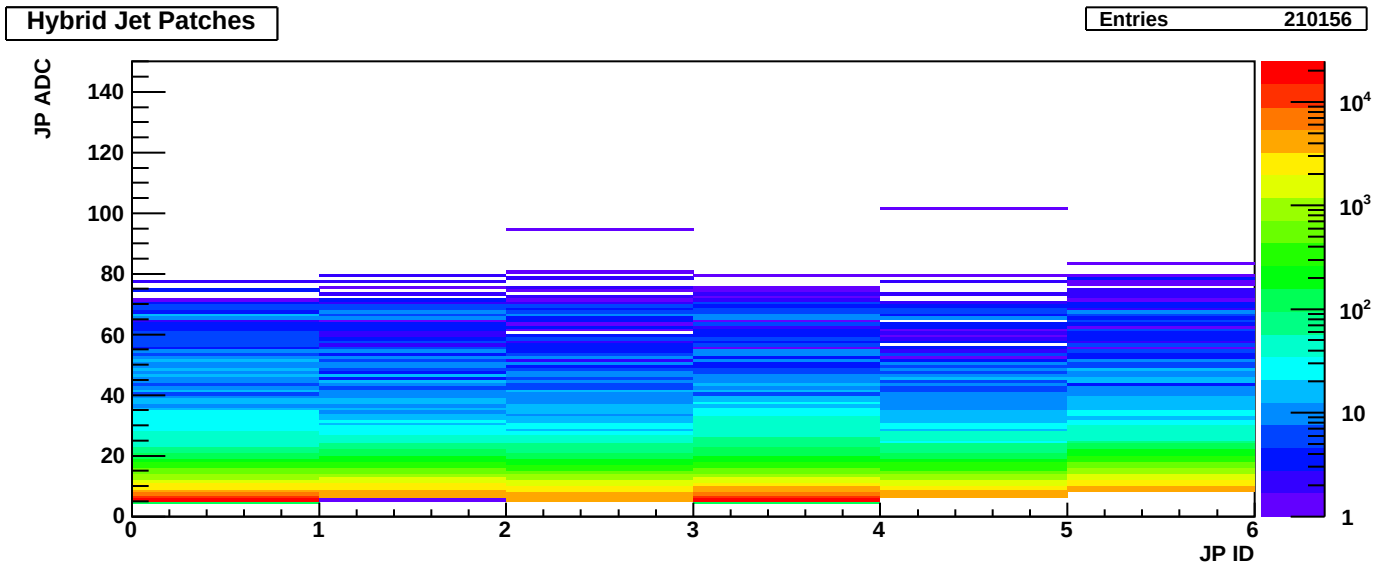

Barrel EMC L0 Input - Patch Sum
Entries 3.15234e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

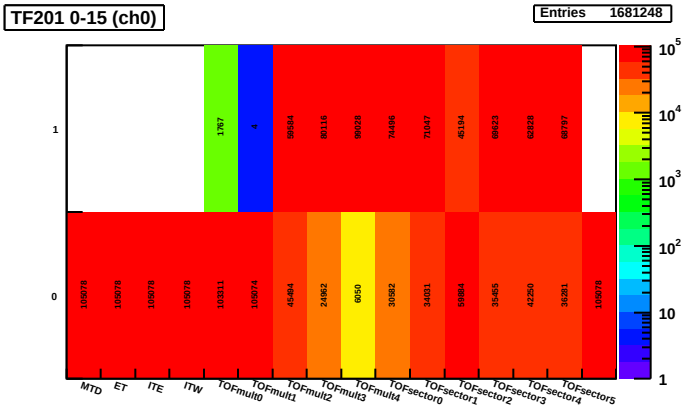

Endcap EMC L0 Input - High Tower
Entries 9457020
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

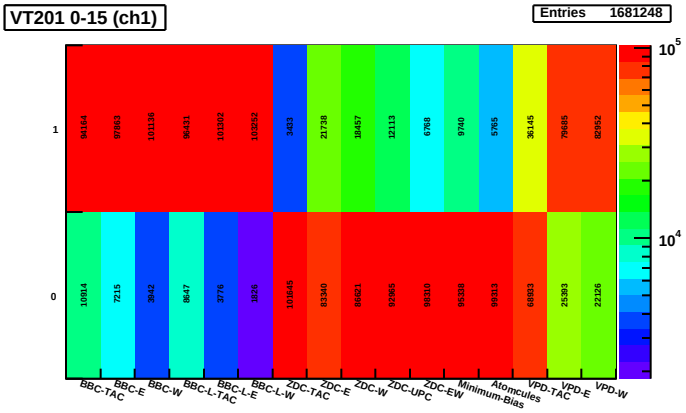

Endcap EMC L0 Input - Patch Sum
Entries 9457020
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

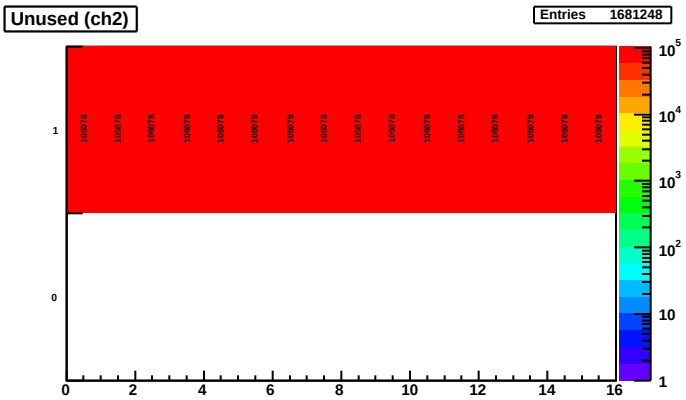

Barrel Jet Patches
Entries 1891404
JP ADC
JP ID

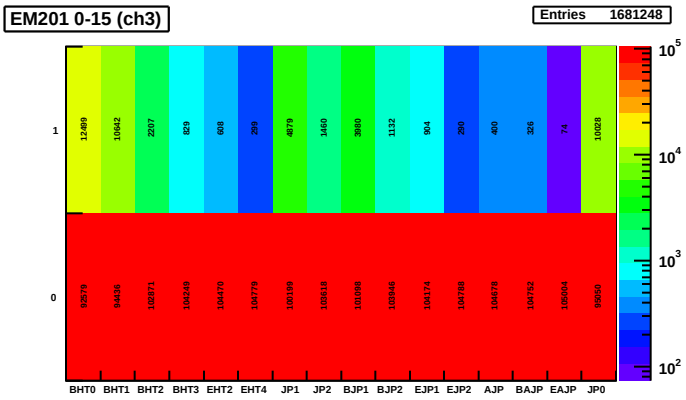

Endcap Jet Patches
Entries 630468
JP ADC
JP ID

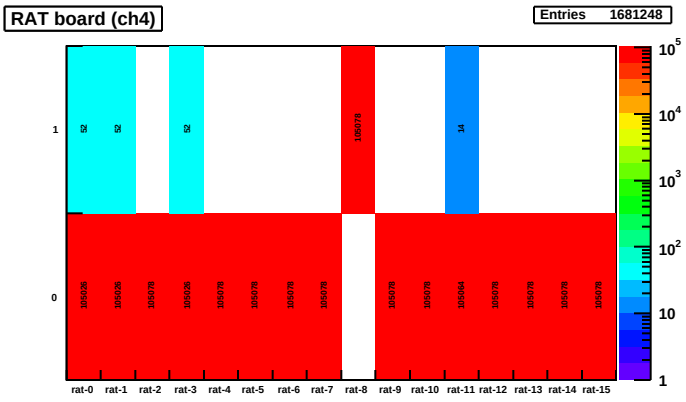

Hybrid Jet Patches
Entries 210156
JP ADC
JP ID

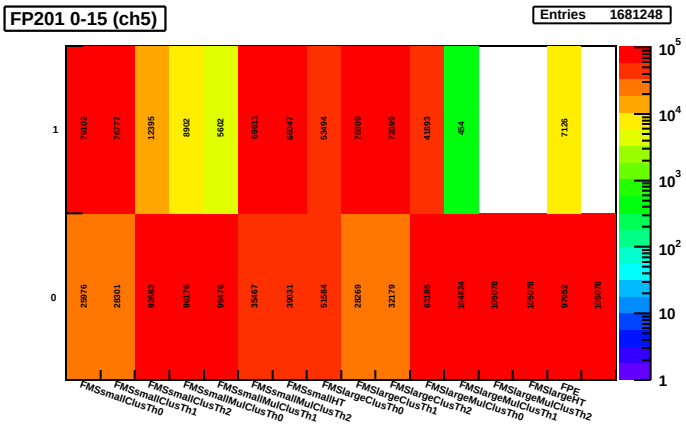

TF201 0-15 (ch0)
Entries 1681248
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

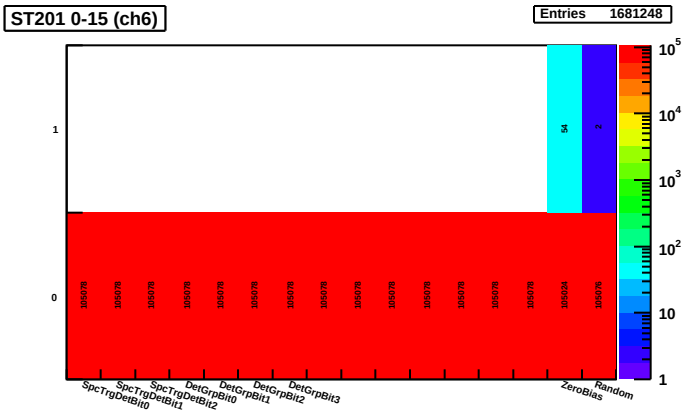

VT201 0-15 (ch1)
Entries 1681248
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

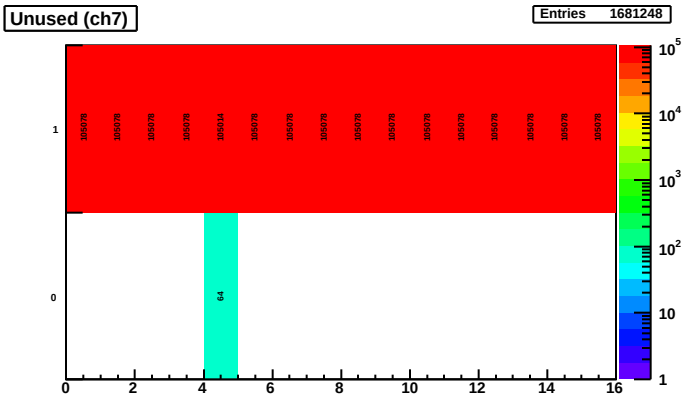

Unused (ch2)
Entries 1681248

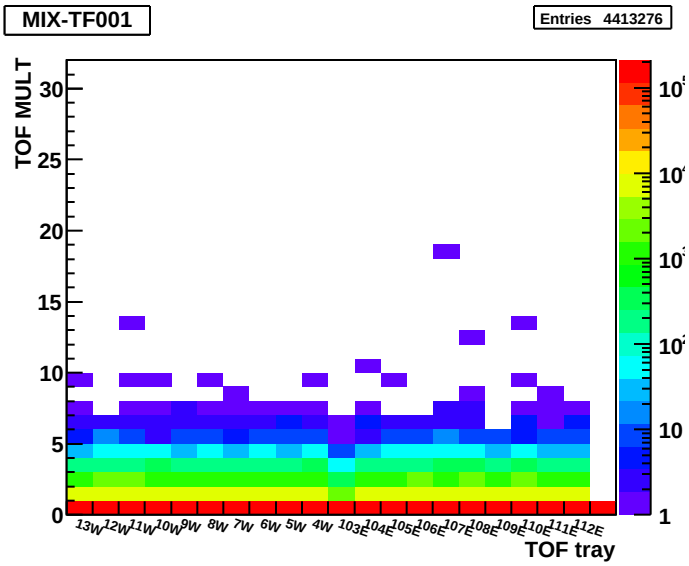

EM201 0-15 (ch3)
Entries 1681248
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

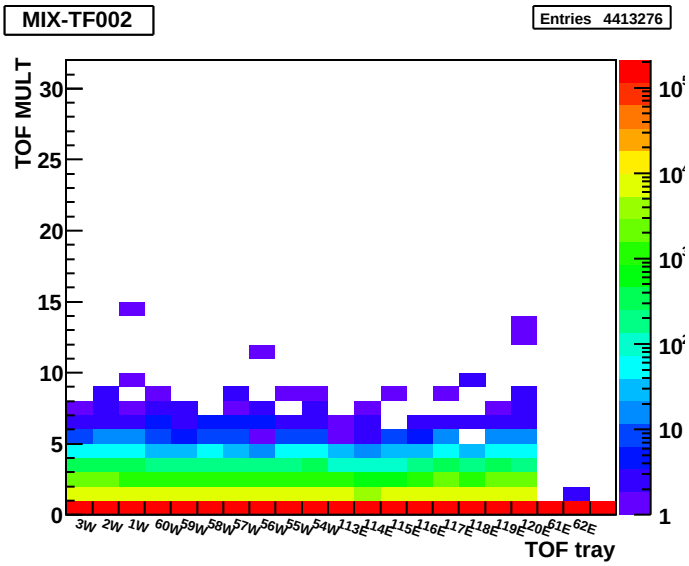

RAT board (ch4)
Entries 1681248
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

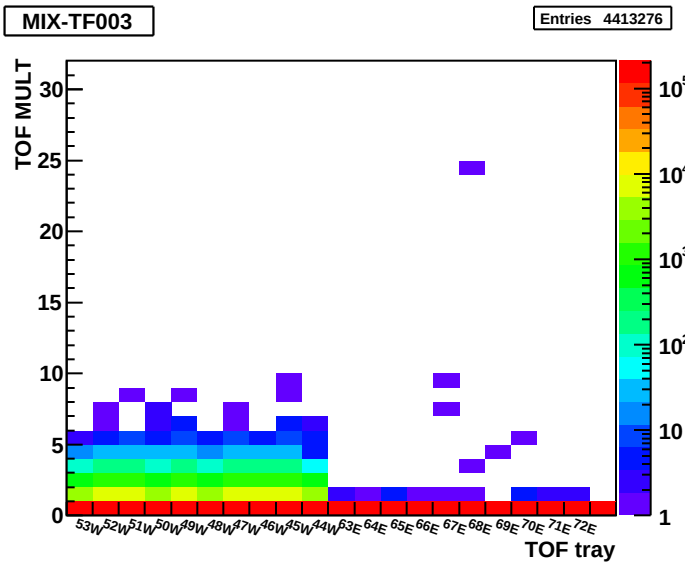

FP201 0-15 (ch5)
Entries 1681248
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

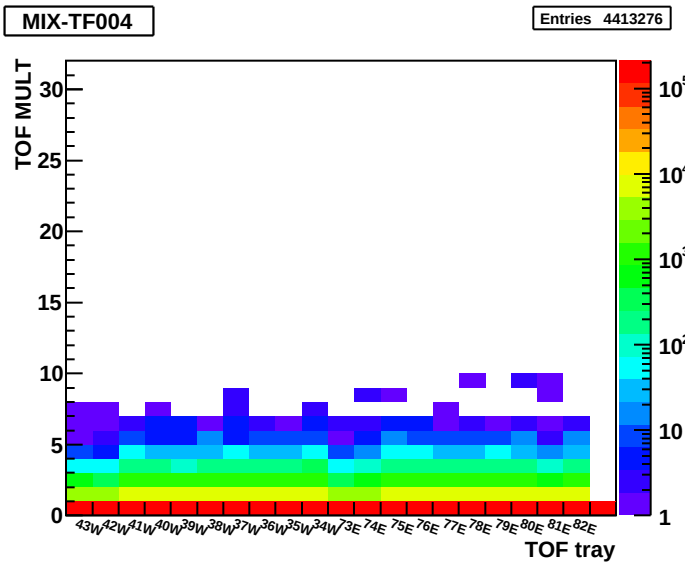

ST201 0-15 (ch6)
Entries 1681248
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

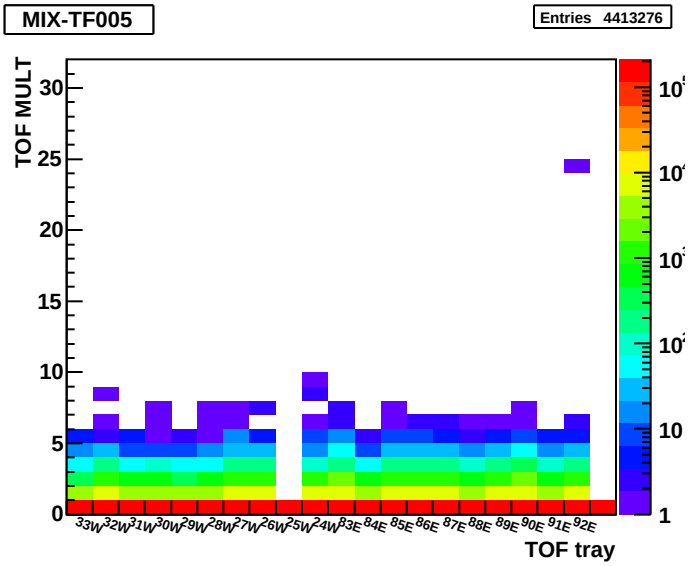

Unused (ch7)
Entries 1681248

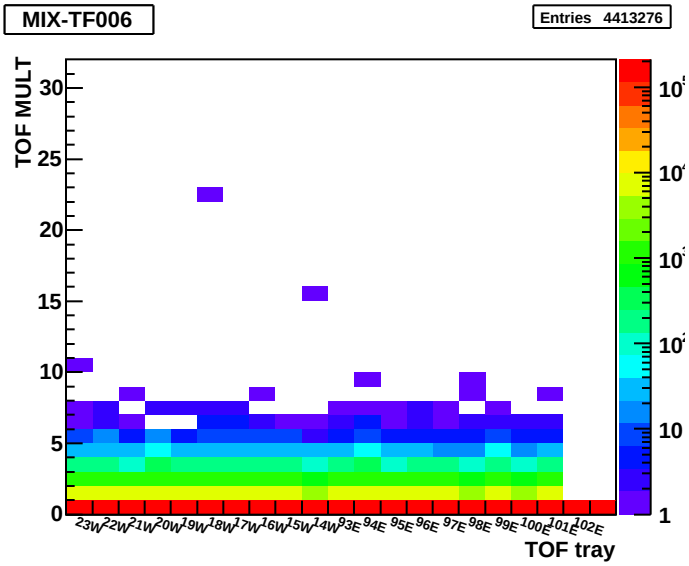

MIX-TF001
Entries 4413276
TOF MULT
TOF tray

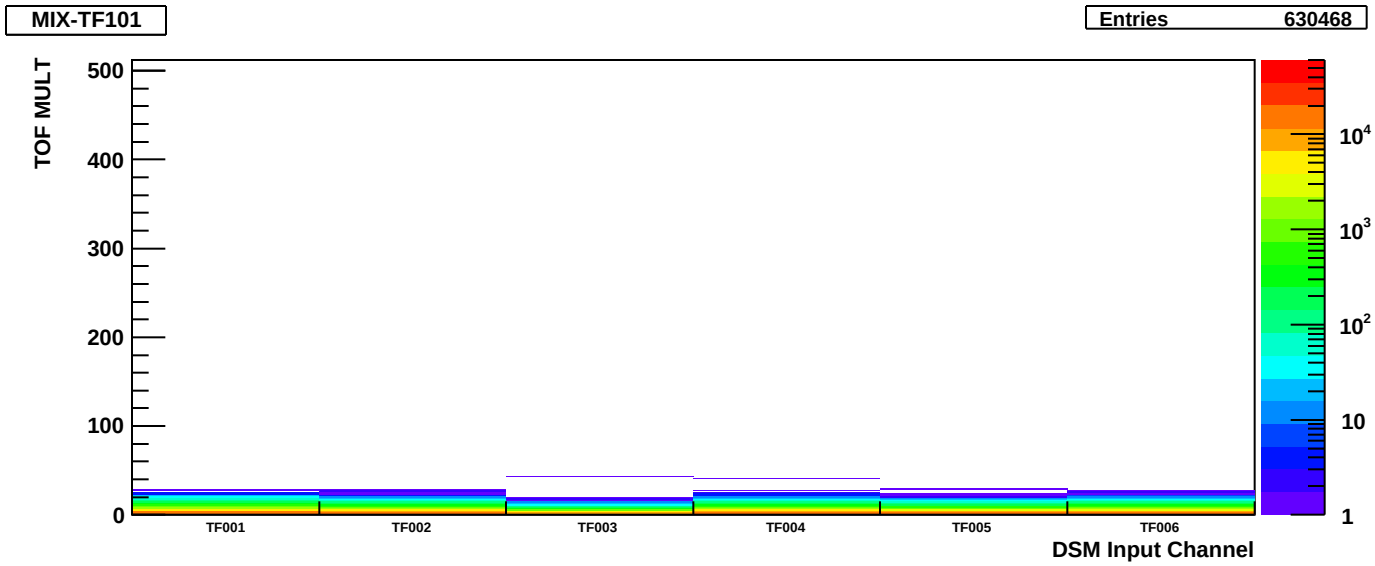

MIX-TF002
Entries 4413276
TOF MULT
TOF tray

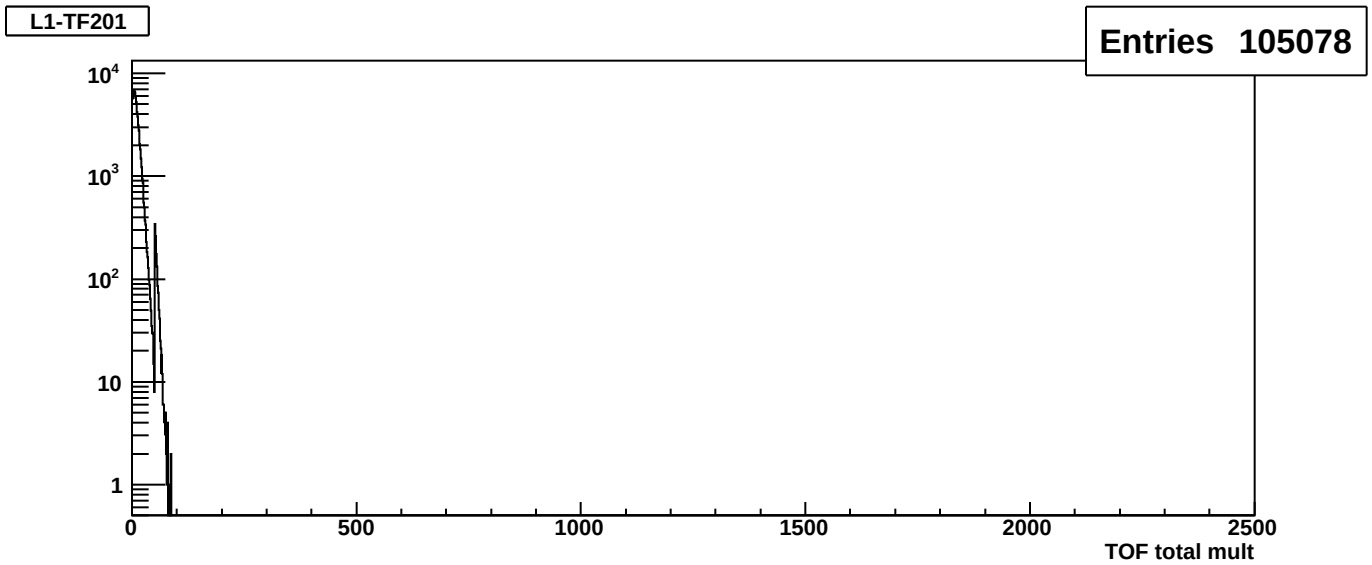

MIX-TF003
Entries 4413276
TOF MULT
TOF tray

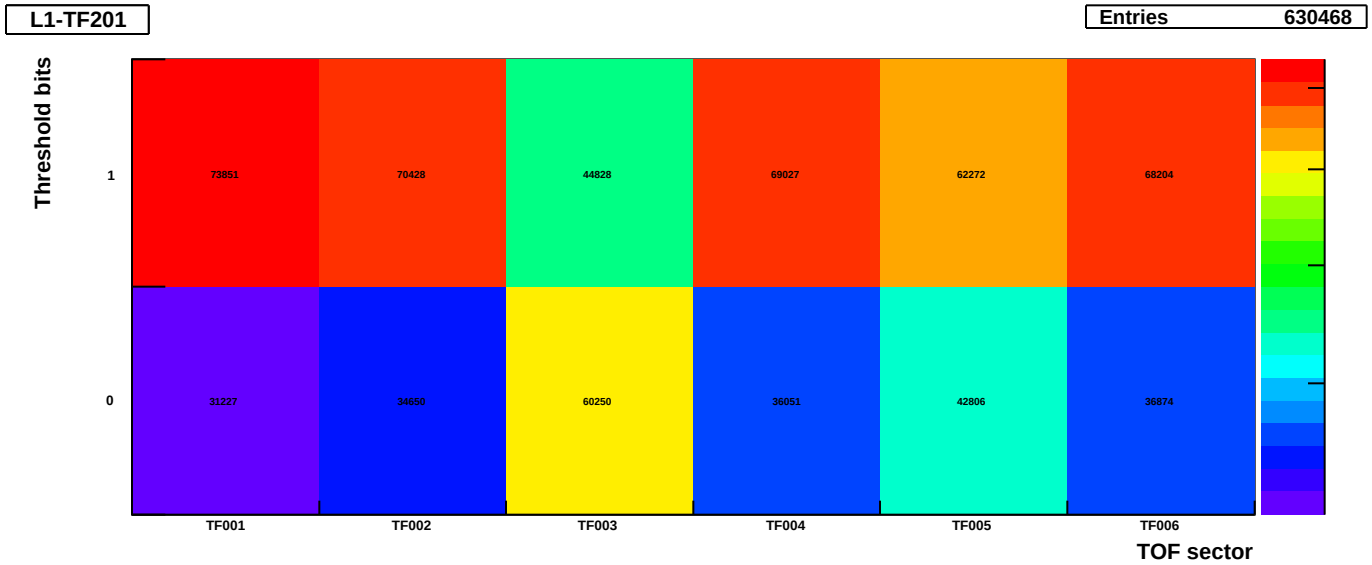

MIX-TF004
Entries 4413276
TOF MULT
TOF tray

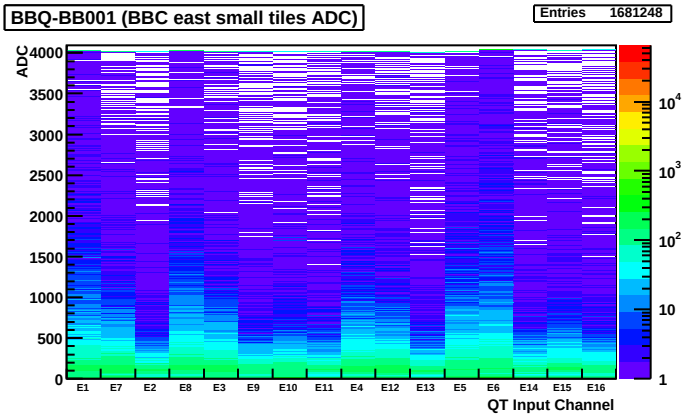

MIX-TF005
Entries 4413276
TOF MULT
TOF tray

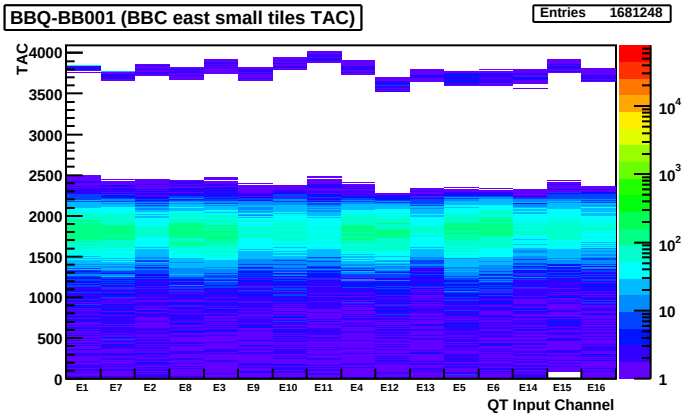

MIX-TF006
Entries 4413276
TOF MULT
TOF tray

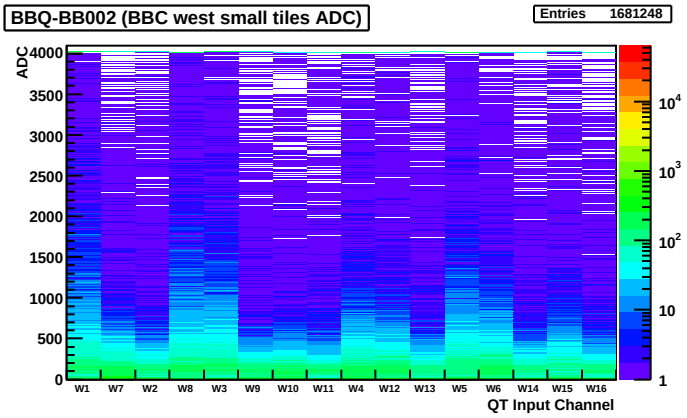

MIX-TF101
Entries 630468
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

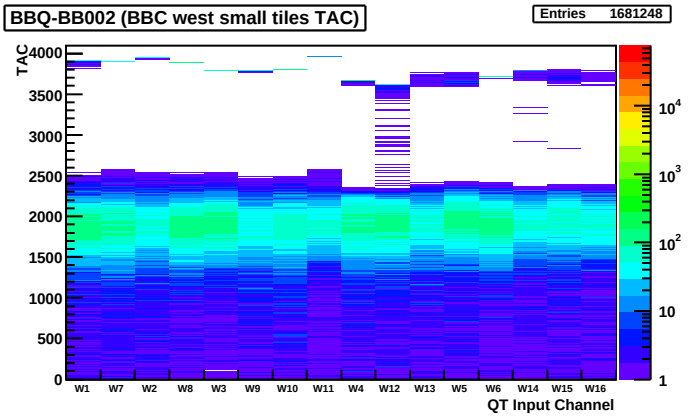

L1-TF201
Entries 105078
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

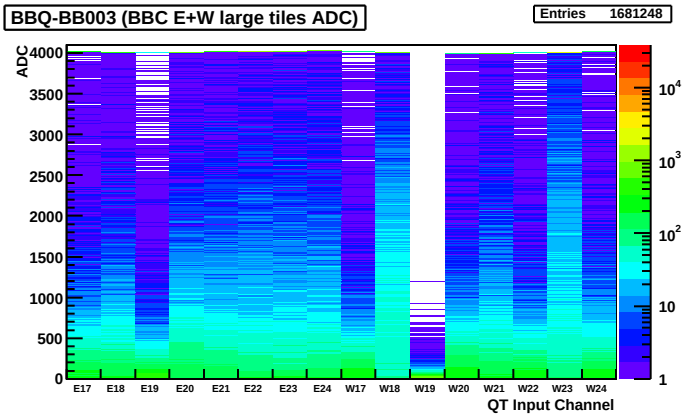

L1-TF201
Entries 630468
Threshold bits
1
0
73851
70428
44828
69027
62272
68204
31227
34650
60250
36051
42806
36874
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

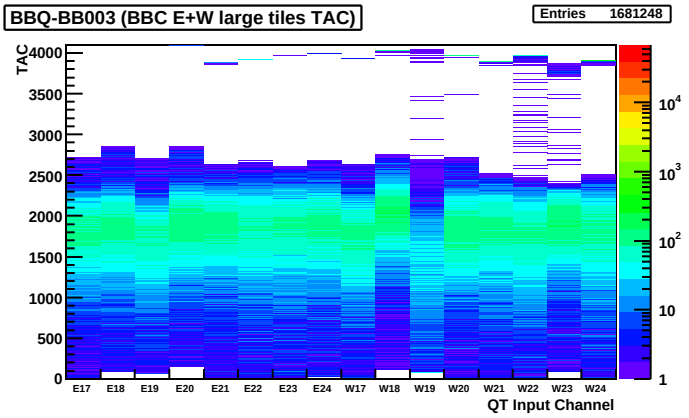

BBQ-BB001 (BBC east small tiles ADC)
Entries 1681248
ADC
QT Input Channel

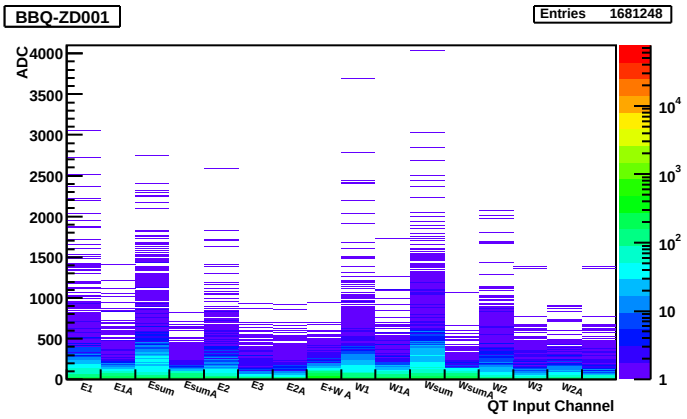

BBQ-BB001 (BBC east small tiles TAC)
Entries 1681248
TAC
QT Input Channel

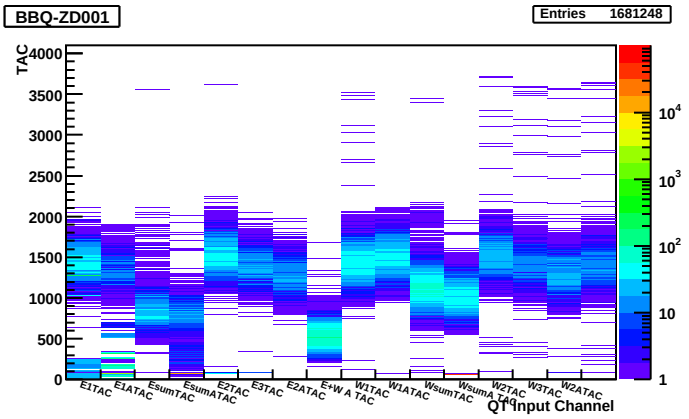

BBQ-BB002 (BBC west small tiles ADC)
Entries 1681248
ADC
QT Input Channel

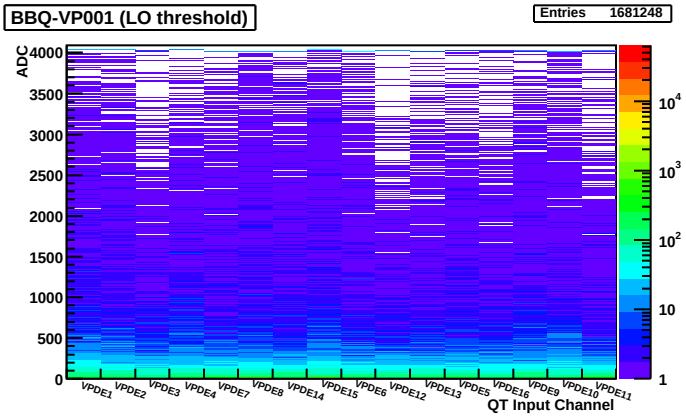

BBQ-BB002 (BBC west small tiles TAC)
Entries 1681248
TAC
QT Input Channel

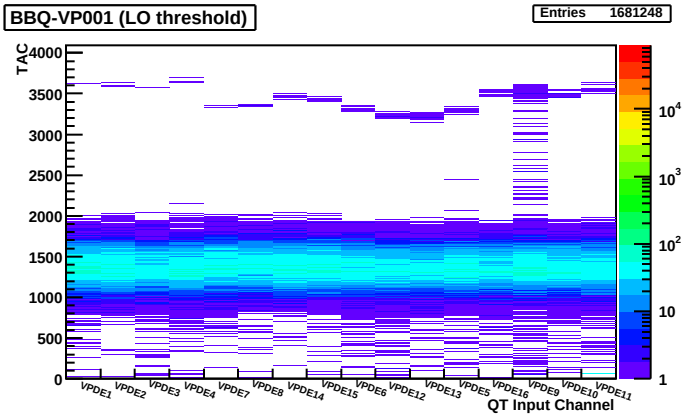

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1681248
ADC
QT Input Channel

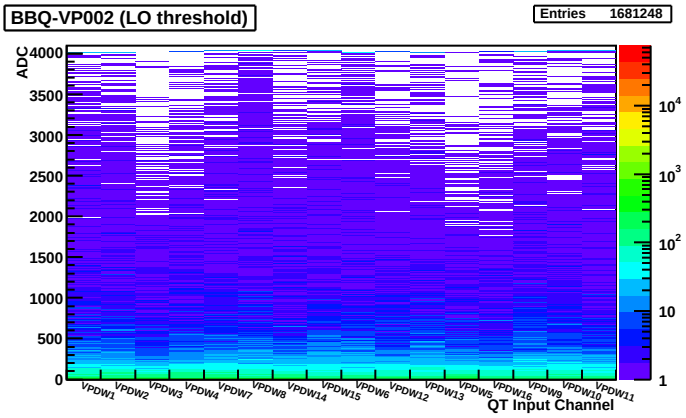

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1681248
TAC
QT Input Channel

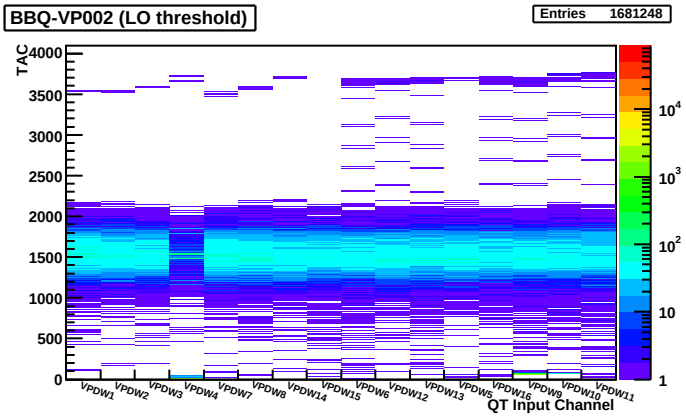

BBQ-ZD001
Entries 1681248
ADC
QT Input Channel

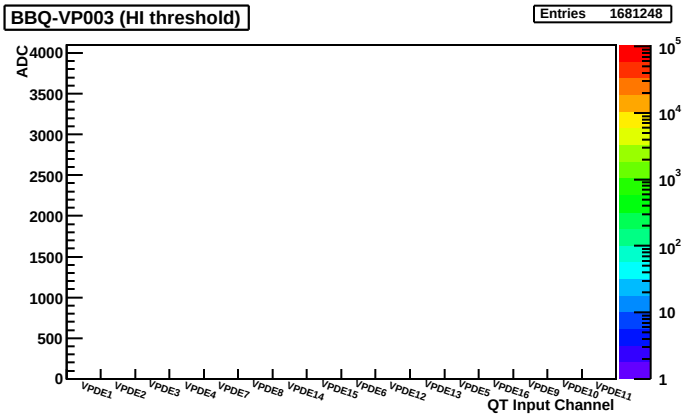

BBQ-ZD001
Entries 1681248
TAC
QT Input Channel

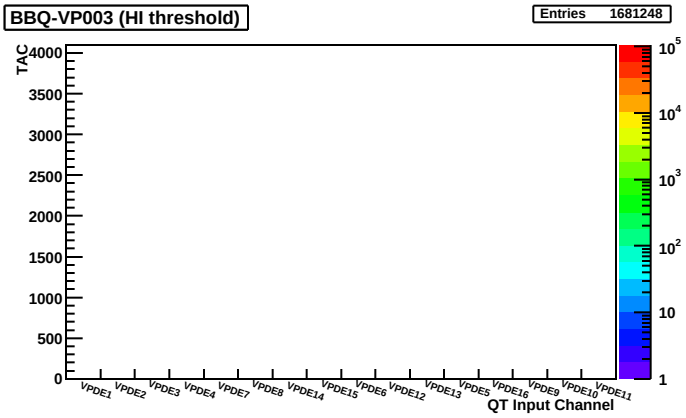

BBQ-VP001 (LO threshold)
Entries 1681248
ADC
QT Input Channel

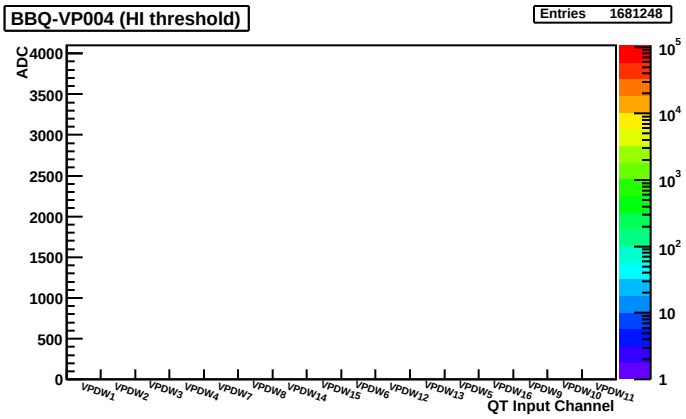

BBQ-VP001 (LO threshold)
Entries 1681248
TAC
QT Input Channel

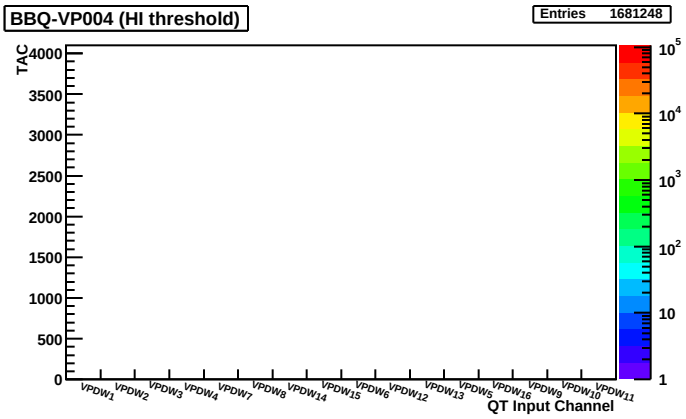

BBQ-VP002 (LO threshold)
Entries 1681248
ADC
QT Input Channel

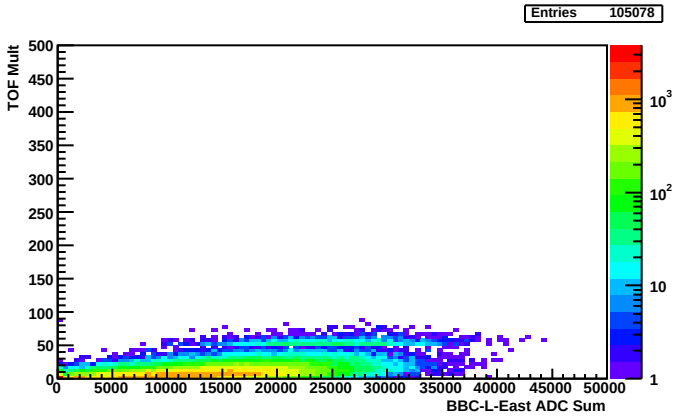

BBQ-VP002 (LO threshold)
Entries 1681248
TAC
QT Input Channel

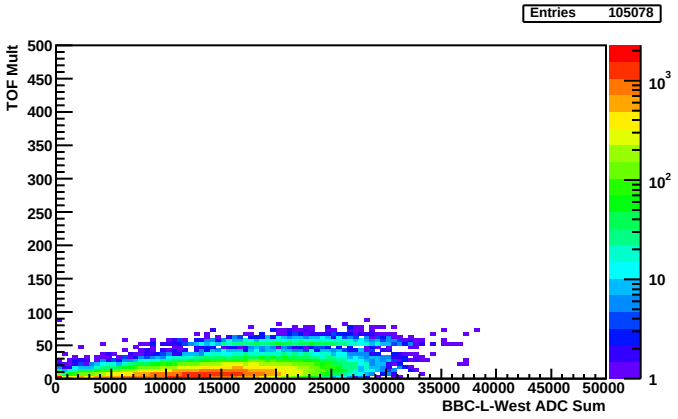

BBQ-VP003 (HI threshold)
Entries 1681248
ADC
QT Input Channel

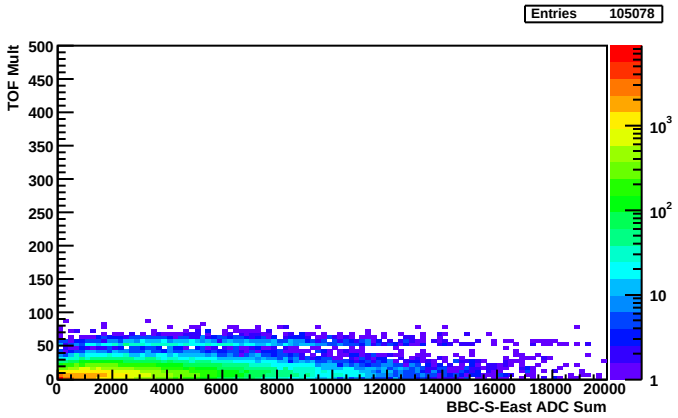

BBQ-VP003 (HI threshold)
Entries 1681248
TAC
QT Input Channel

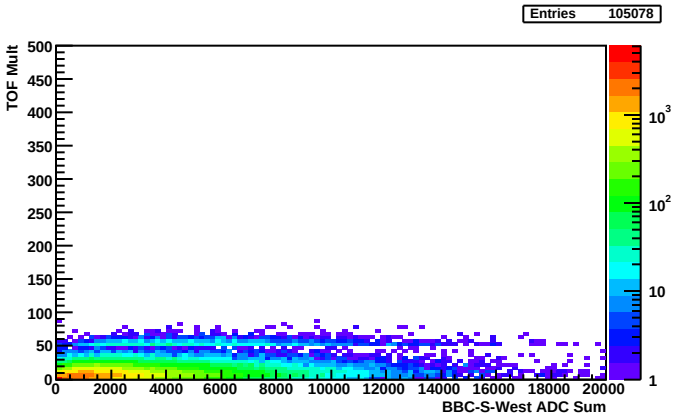

BBQ-VP004 (HI threshold)
Entries 1681248
ADC
QT Input Channel

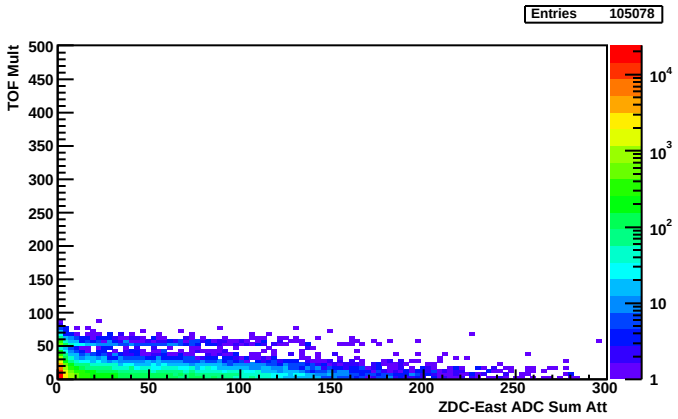

BBQ-VP004 (HI threshold)
Entries 1681248
TAC
QT Input Channel

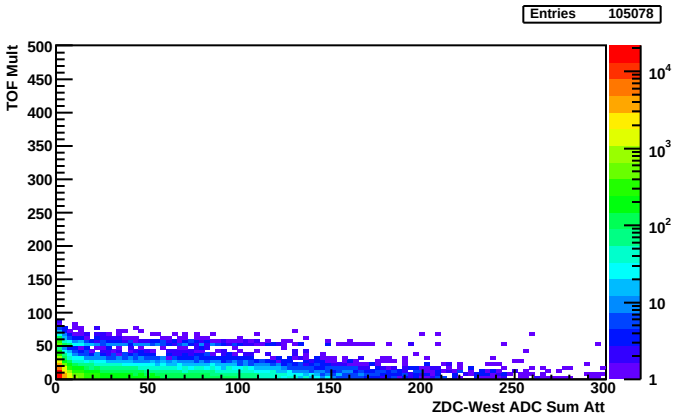

Entries 105078
TOF Mult
BBC-L-East ADC Sum

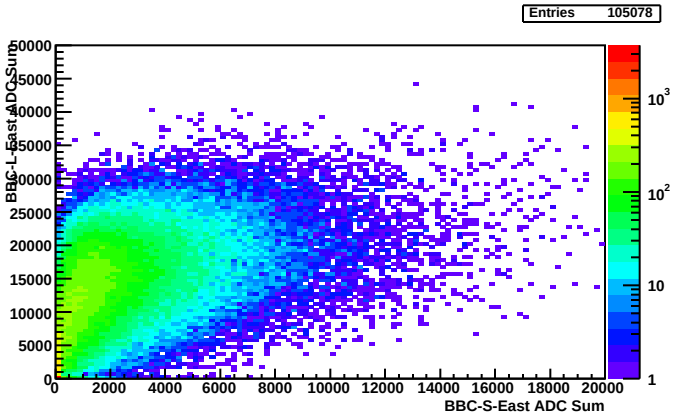

Entries 105078
TOF Mult
BBC-L-West ADC Sum

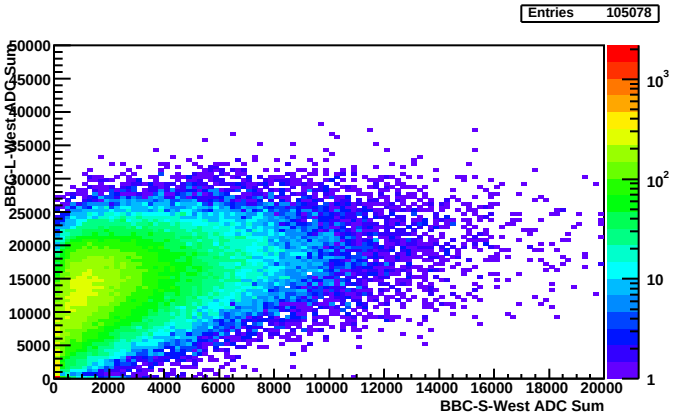

Entries 105078
TOF Mult
BBC-S-East ADC Sum

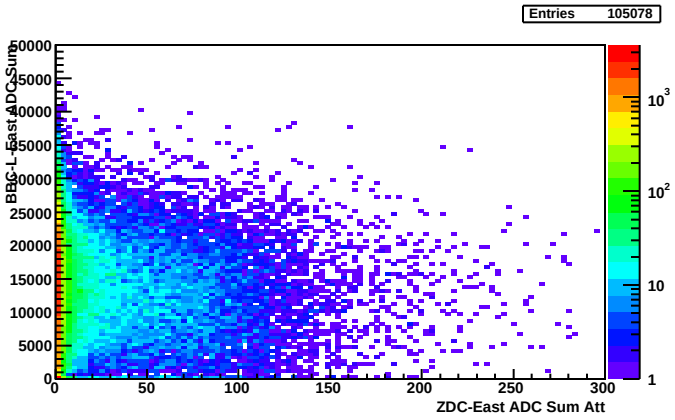

Entries 105078
TOF Mult
BBC-S-West ADC Sum

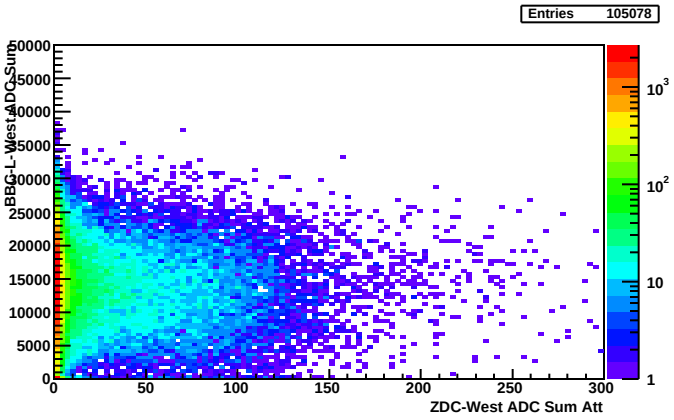

Entries 105078
TOF Mult
ZDC-East ADC Sum Att

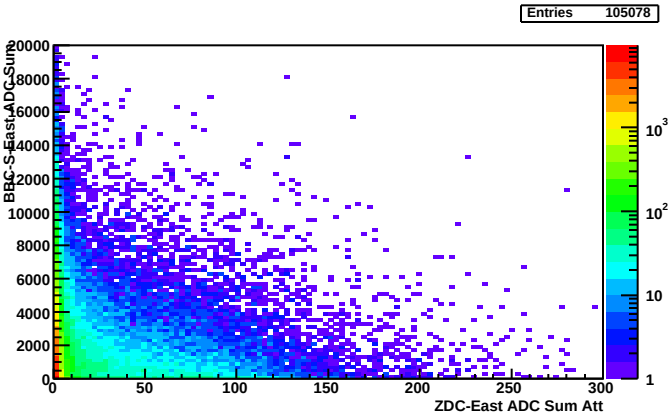

Entries 105078
TOF Mult
ZDC-West ADC Sum Att

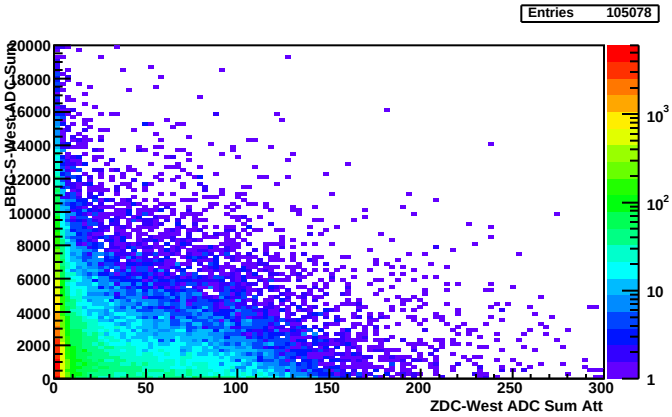

Entries 105078
BBC-L-East ADC Sum
BBC-S-East ADC Sum

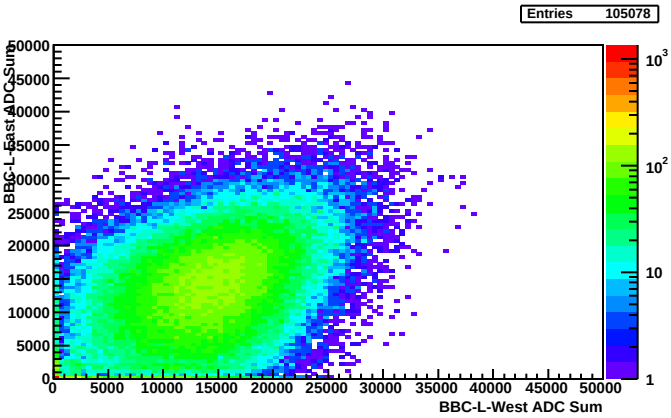

Entries 105078
BBC-L-West ADC Sum
BBC-S-West ADC Sum

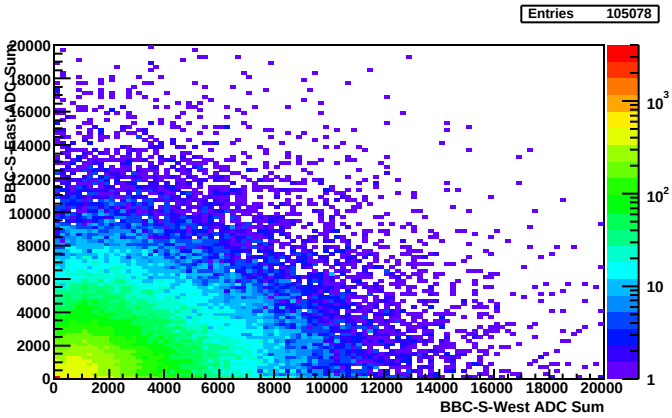

Entries 105078
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

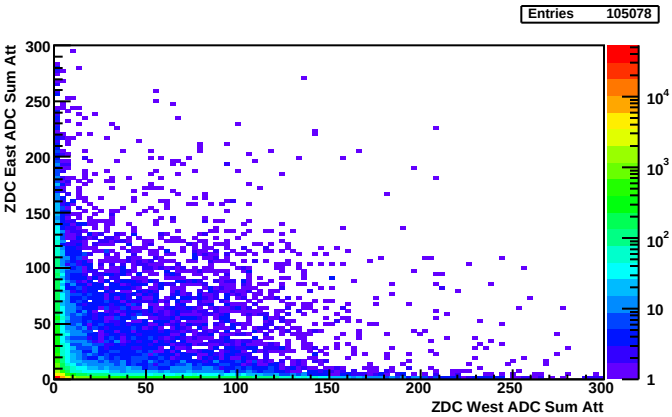

Entries 105078
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

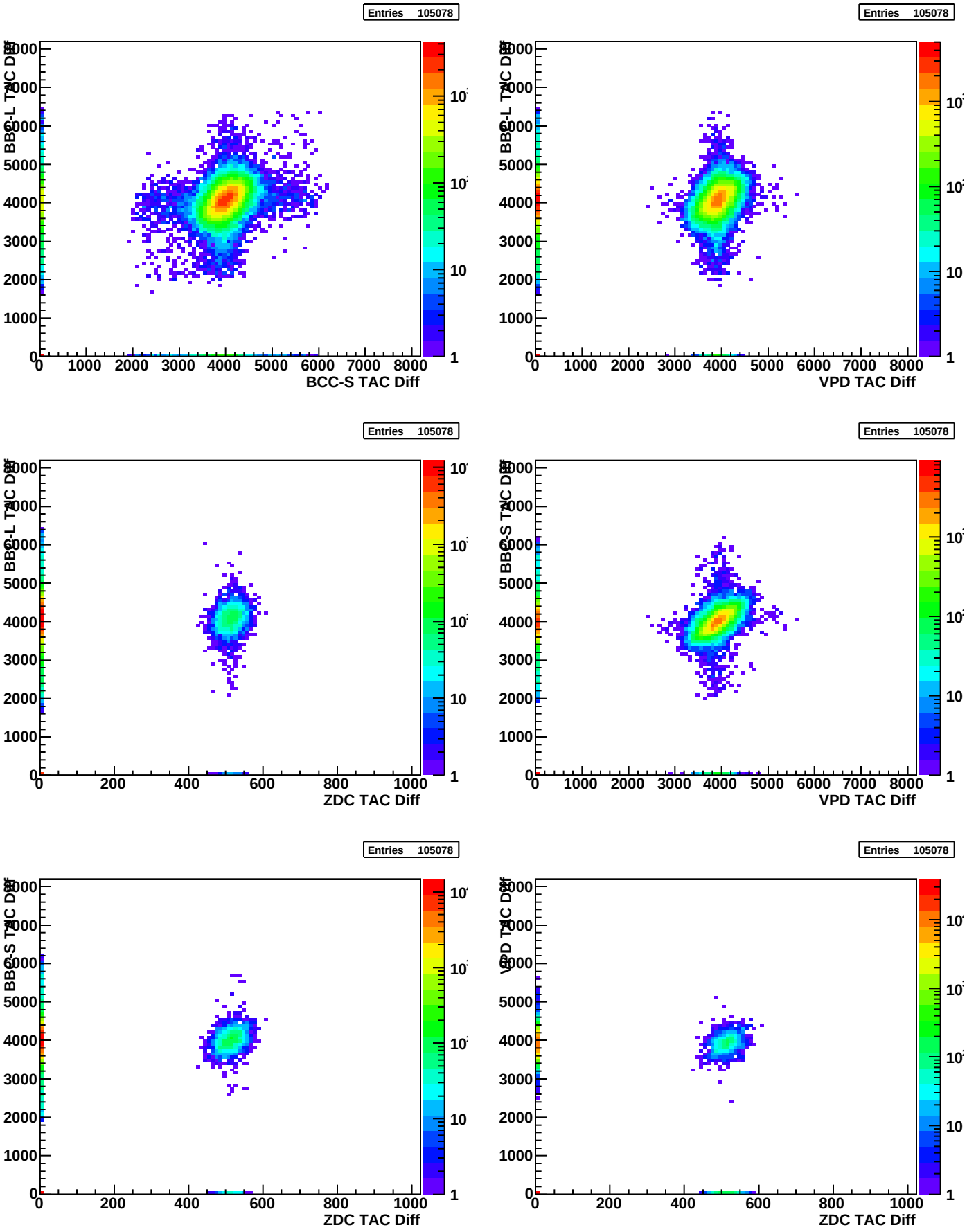

Entries 105078
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

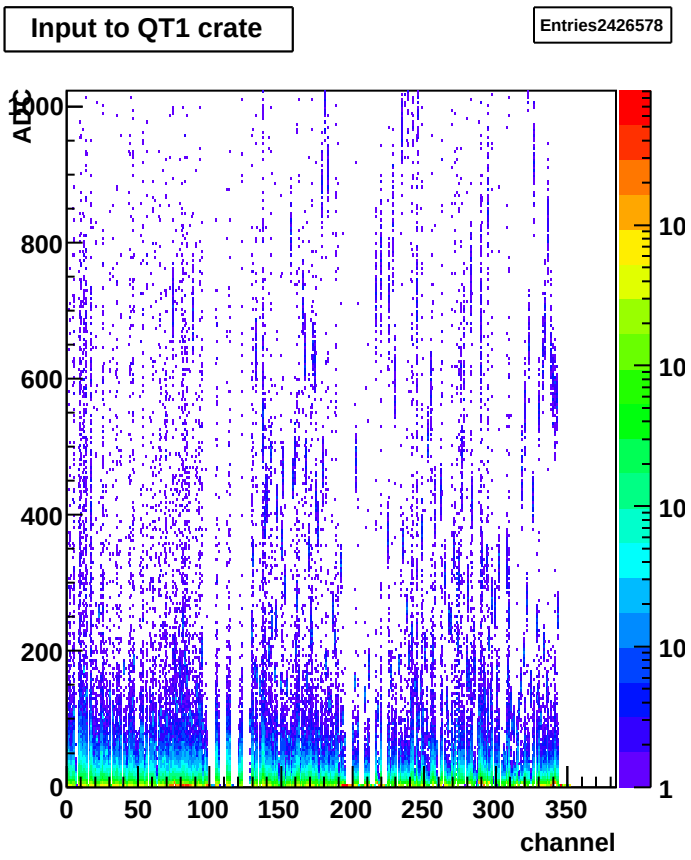

Entries 105078
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

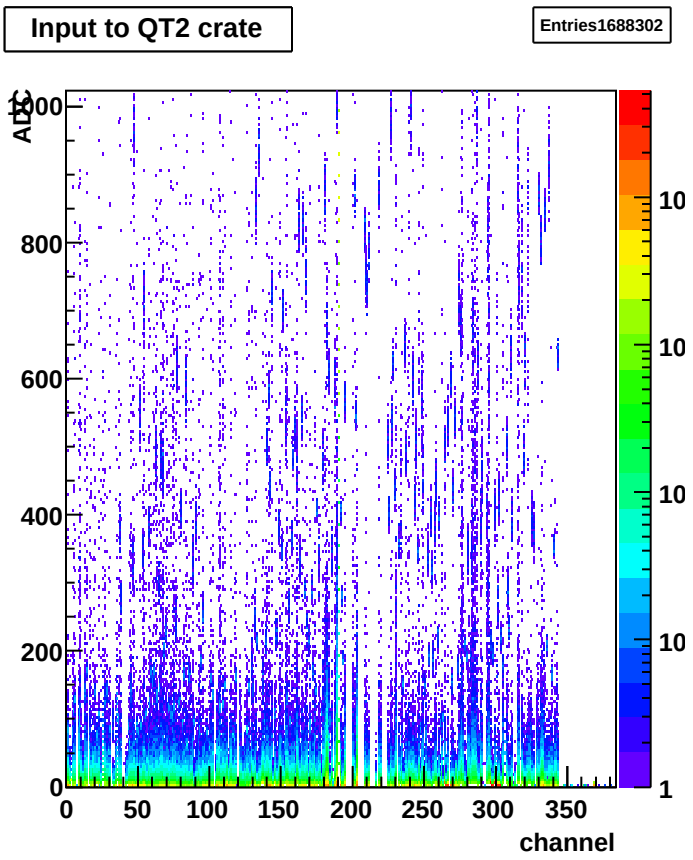

Entries 105078
BBC-L-East ADC Sum
BBC-L-West ADC Sum

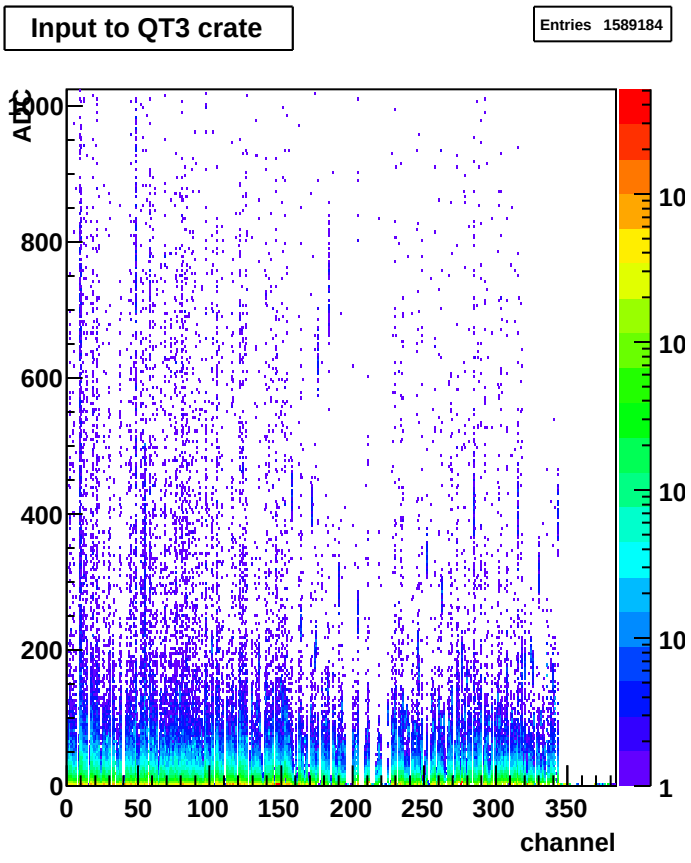

Entries 105078
BBC-S-East ADC Sum
BBC-S-West ADC Sum

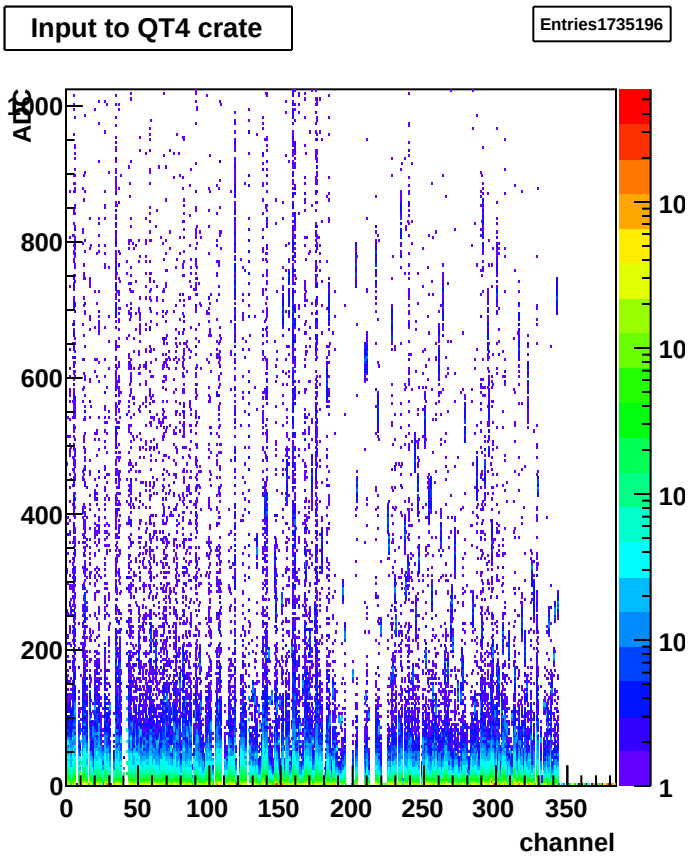

Entries 105078
ZDC East ADC Sum Att
ZDC West ADC Sum Att

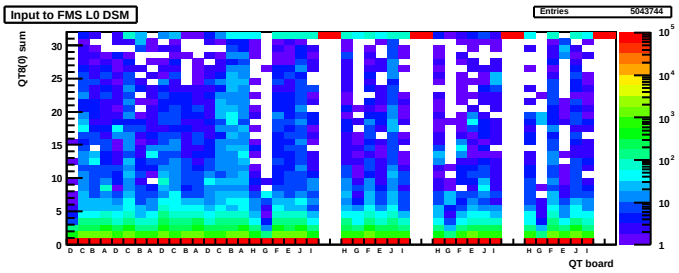

Entries 105078
BBC-L TAC Diff
BCC-S TAC Diff
Entries 105078
BBC-L TAC Diff
VPD TAC Diff
Entries 105078
BBC-L TAC Diff
ZDC TAC Diff
Entries 105078
BBC-S TAC Diff
VPD TAC Diff
Entries 105078
BBC-S TAC Diff
ZDC TAC Diff
Entries 105078
VPD TAC Diff
ZDC TAC Diff

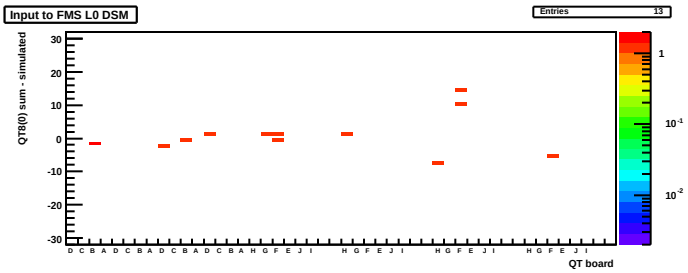

Input to QT1 crate
Entries2426578
ADC
channel

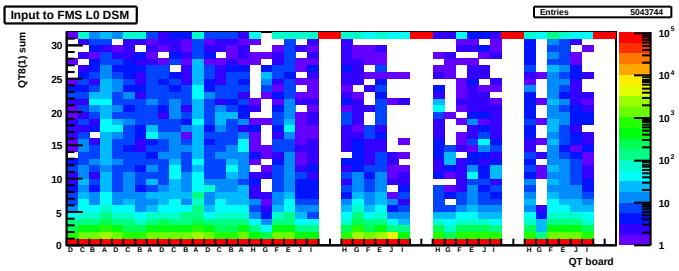

Input to QT2 crate
Entries1688302
ADC
channel

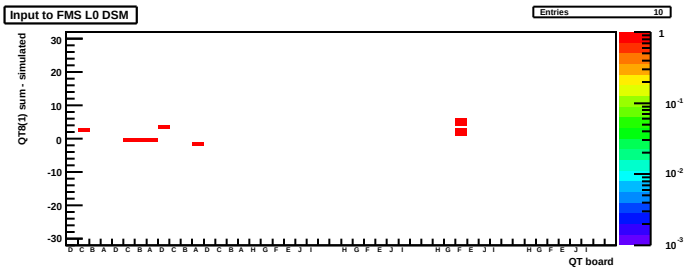

Input to QT3 crate
Entries 1589184
ADC
channel

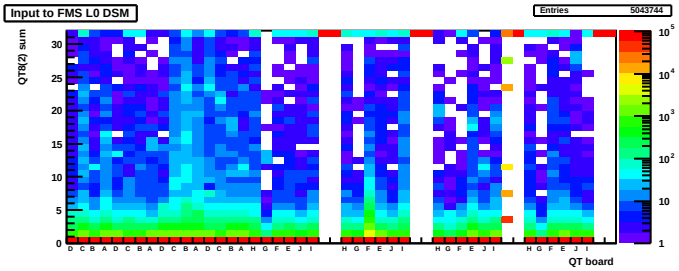

Input to QT4 crate
Entries1735196
ADC
channel

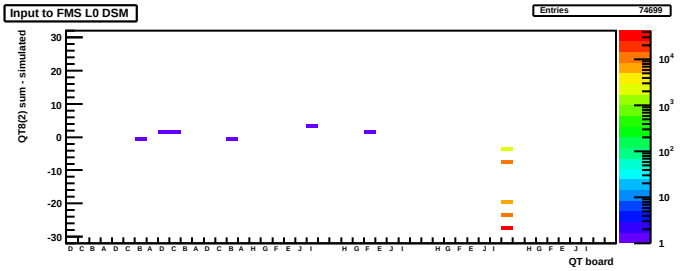

Input to FMS L0 DSM
Entries 5043744
QT8(0) sum
QT board

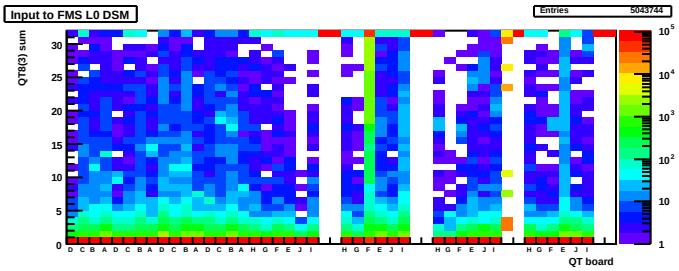

Input to FMS L0 DSM
Entries 13
QT8(0) sum - simulated
QT board

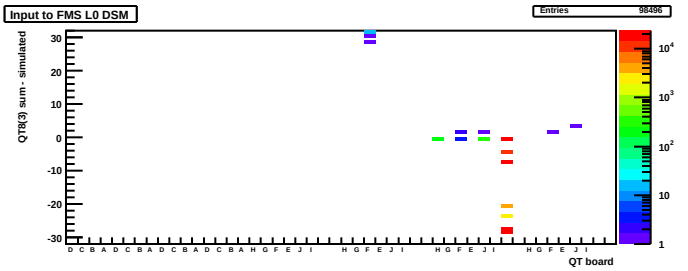

Input to FMS L0 DSM
Entries 5043744
QT8(1) sum
QT board

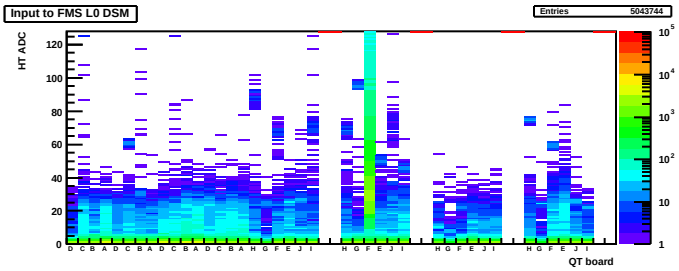

Input to FMS L0 DSM
Entries 10
QT8(1) sum - simulated
QT board

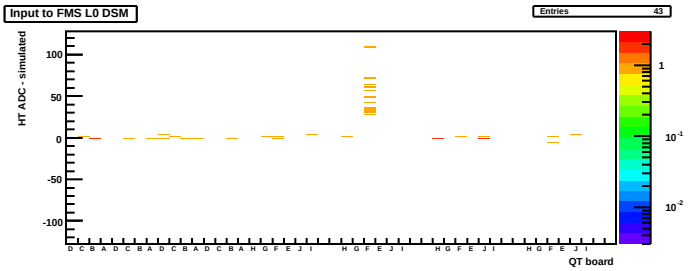

Input to FMS L0 DSM
Entries 5043744
QT8(2) sum
QT board

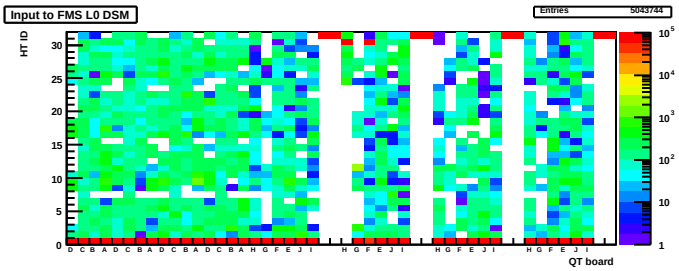

Input to FMS L0 DSM
Entries 74699
QT8(2) sum - simulated
QT board

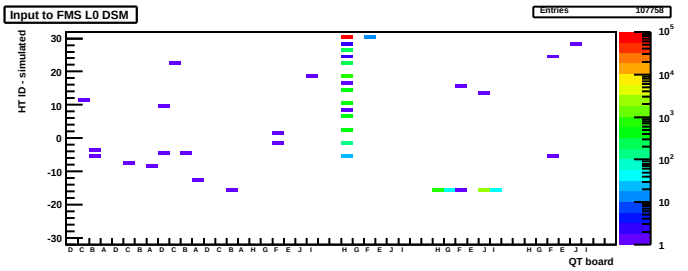

Input to FMS L0 DSM
Entries 5043744
QT8(3) sum
QT board

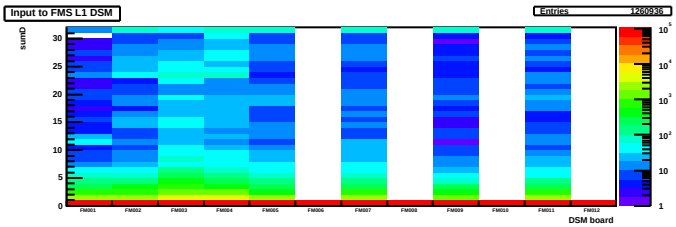

Input to FMS L0 DSM
Entries 98496
QT8(3) sum - simulated
QT board

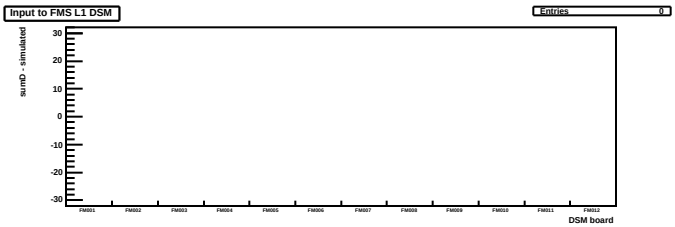

Input to FMS L0 DSM
Entries 5043744
HT ADC
QT board

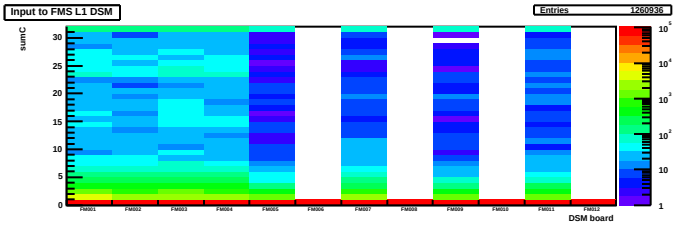

Input to FMS L0 DSM
Entries 43
HT ADC - simulated
QT board

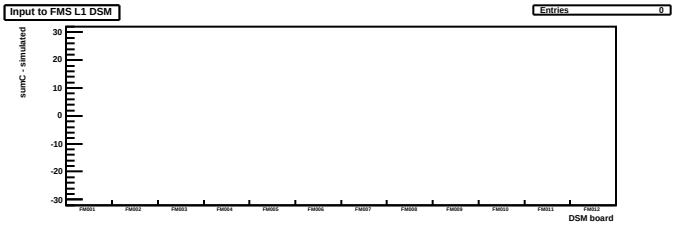

Input to FMS L0 DSM
Entries 5043744
HT ID
QT board

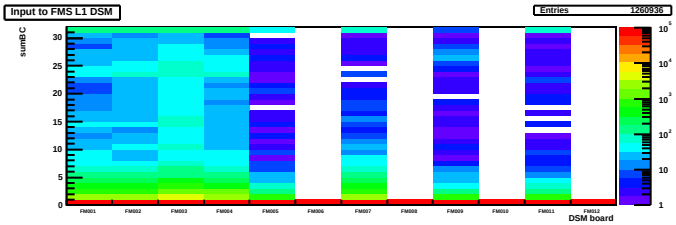

Input to FMS L0 DSM
Entries 107758
HT ID - simulated
QT board

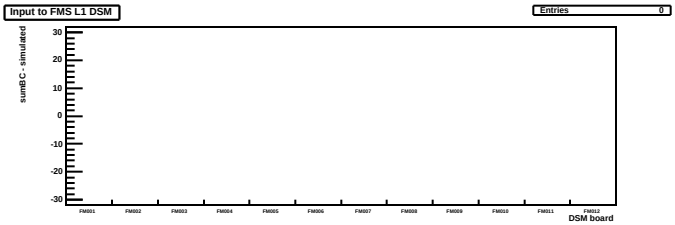

Input to FMS L1 DSM
Entries 1260936
sumD
DSM board

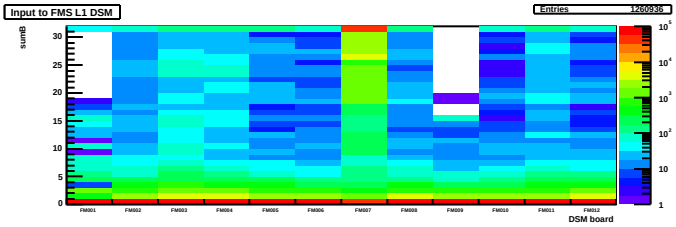

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

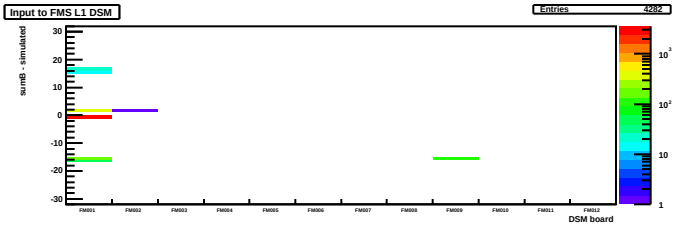

Input to FMS L1 DSM
Entries 1260936
sumC
DSM board

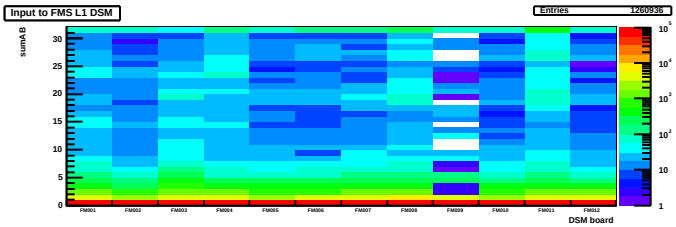

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

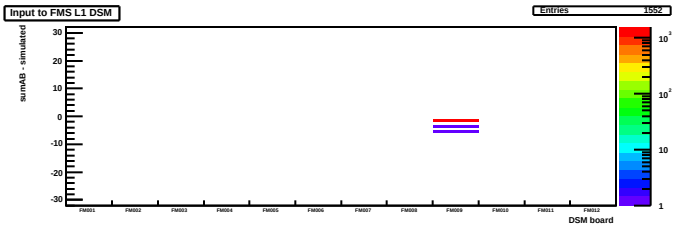

Input to FMS L1 DSM
Entries 1260936
sumBC
DSM board

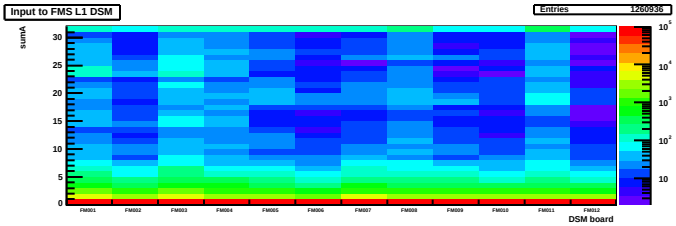

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

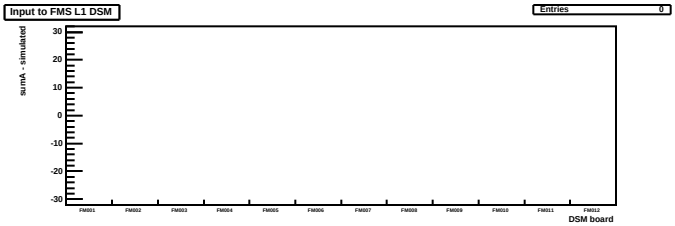

Input to FMS L1 DSM
Entries 1260936
sumB
DSM board

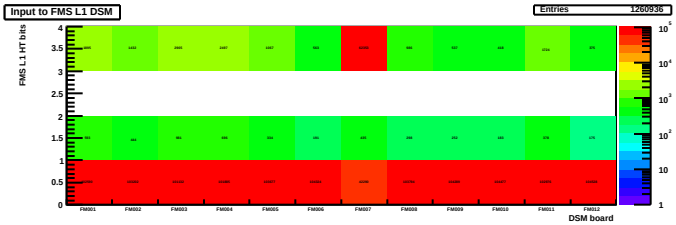

Input to FMS L1 DSM
Entries 4282
sumB - simulated
DSM board

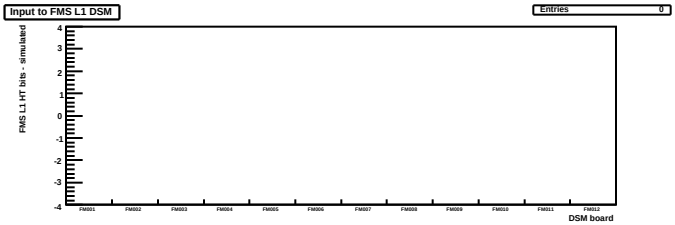

Input to FMS L1 DSM
Entries 1260936
sumAB
DSM board

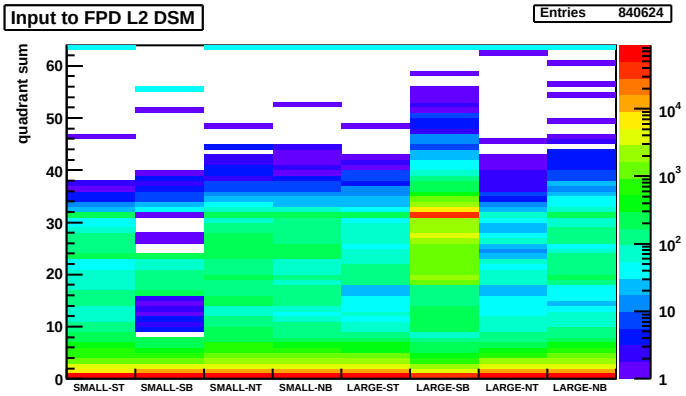

Input to FMS L1 DSM
Entries 1552
sumAB - simulated
DSM board

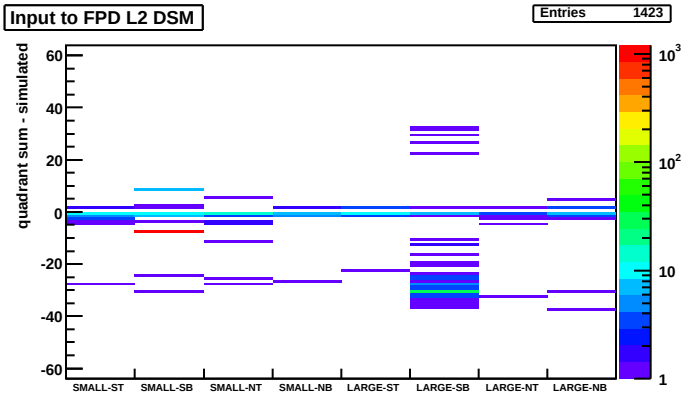

Input to FMS L1 DSM
Entries 1260936
sumA
DSM board

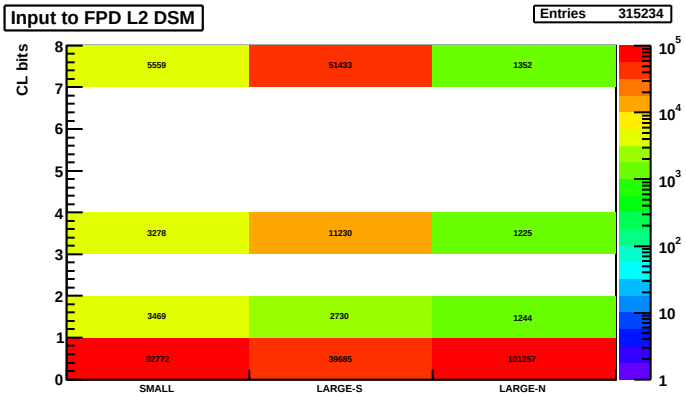

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

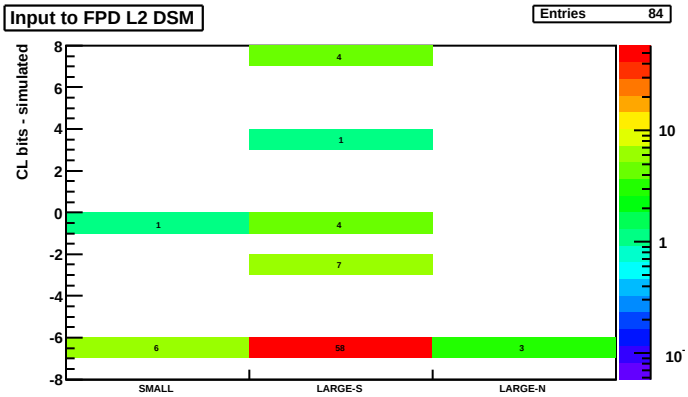

Input to FMS L1 DSM
Entries 1260936
FMS L1 HT bits
DSM board

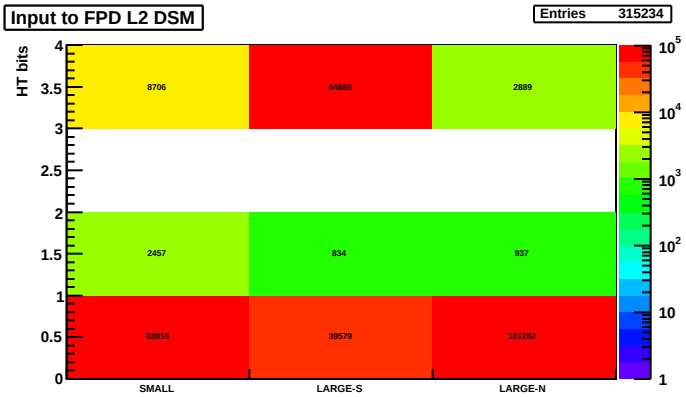

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

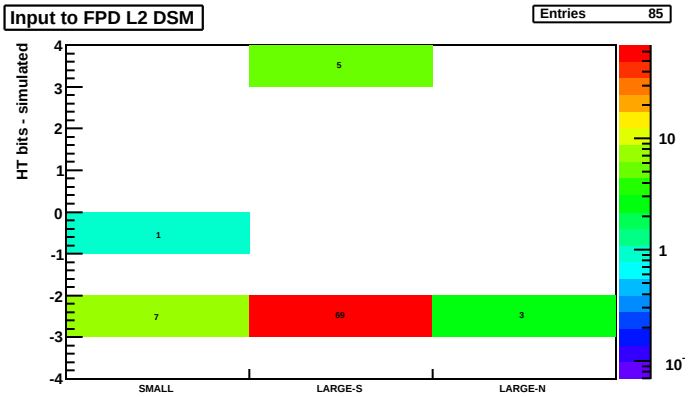

Input to FPD L2 DSM
Entries 840624
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

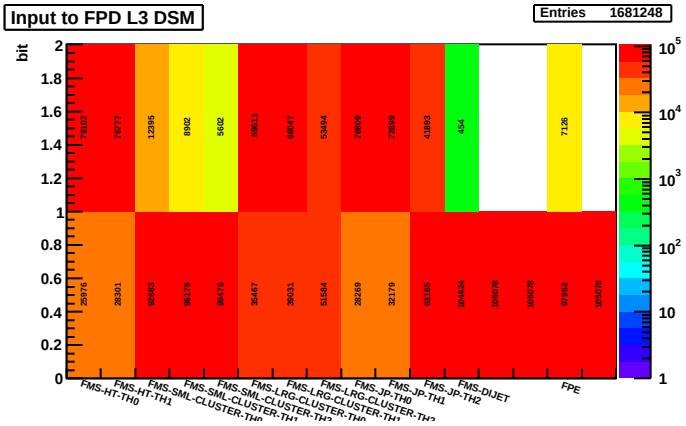

Input to FPD L2 DSM
Entries 1423
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

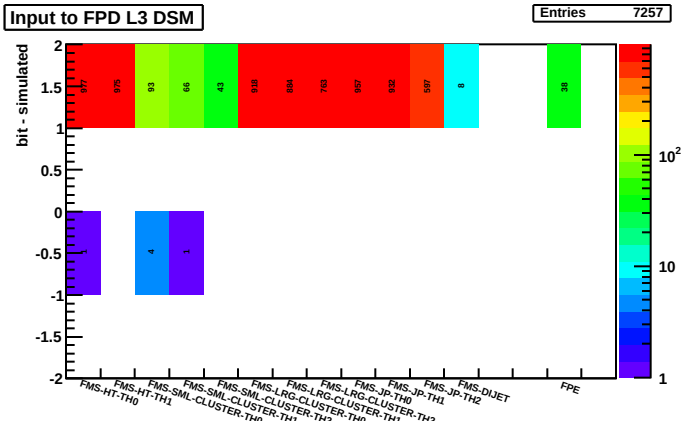

Input to FPD L2 DSM
Entries 315234
CL bits
5559
51433
1352
3278
11230
1225
3469
2730
1244
92772
39685
101257
SMALL
LARGE-S
LARGE-N

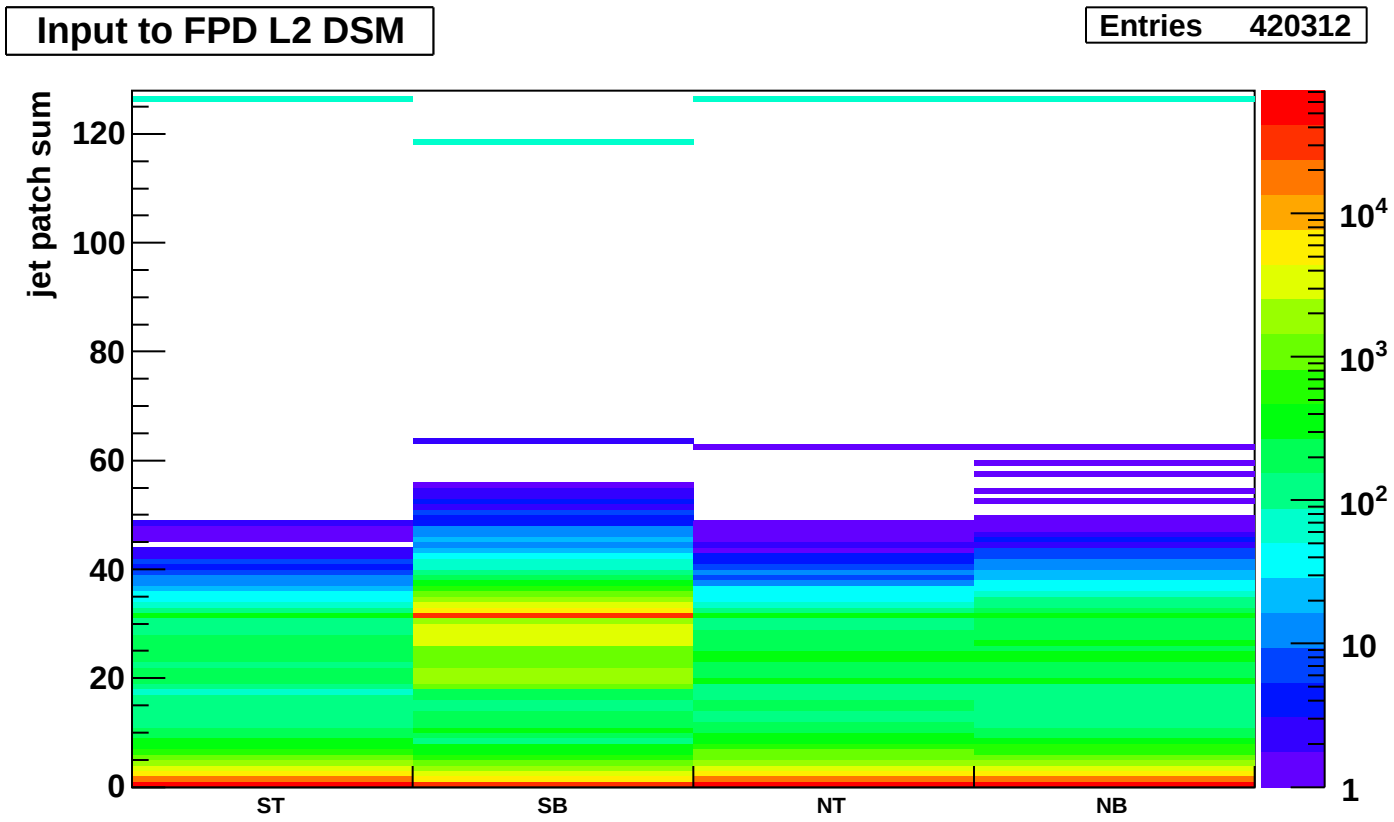

Input to FPD L2 DSM
Entries 84
CL bits - simulated
4
1
1
4
7
6
58
3
SMALL
LARGE-S
LARGE-N

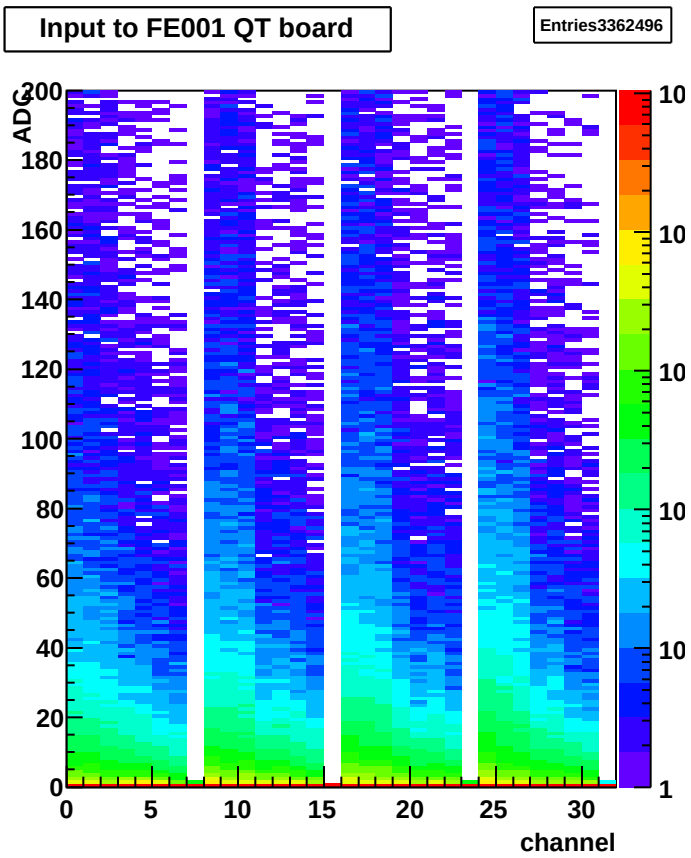

Input to FPD L2 DSM
Entries 315234
HT bits
8706
64665
2889
2457
834
937
93915
39579
101252
SMALL
LARGE-S
LARGE-N

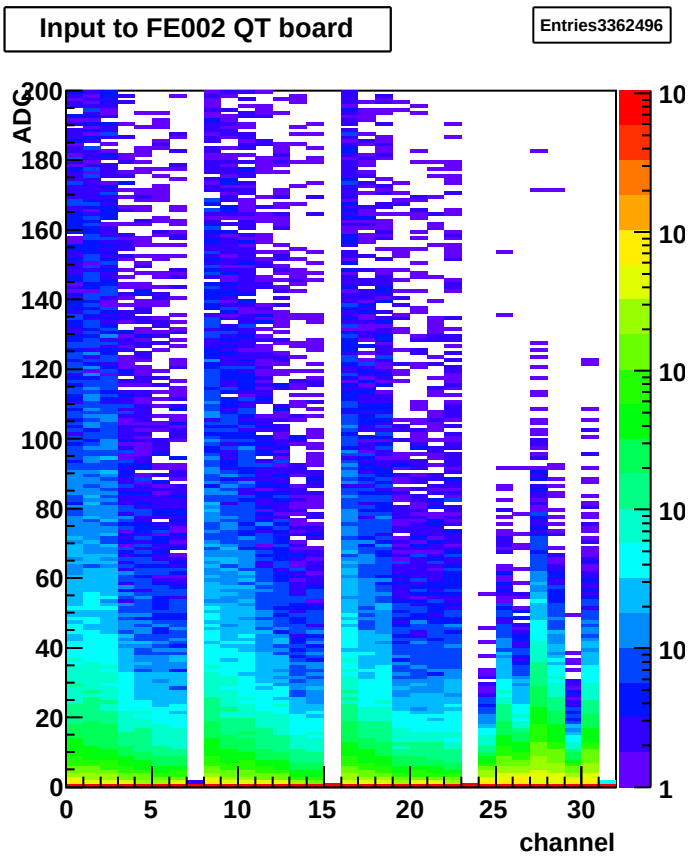

Input to FPD L2 DSM
Entries 85
HT bits - simulated
5
1
7
69
3
SMALL
LARGE-S
LARGE-N

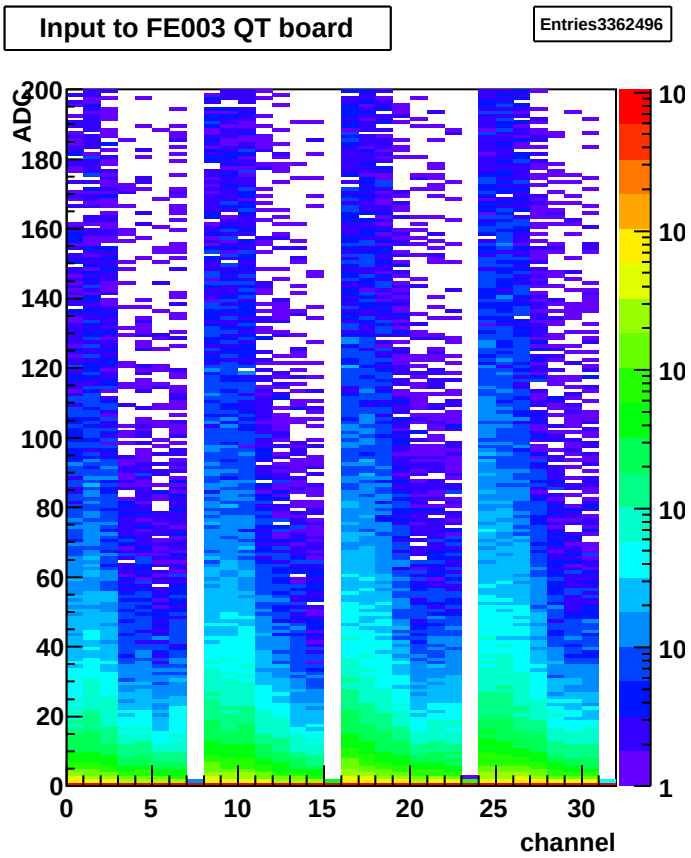

Input to FPD L3 DSM
Entries 1681248
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

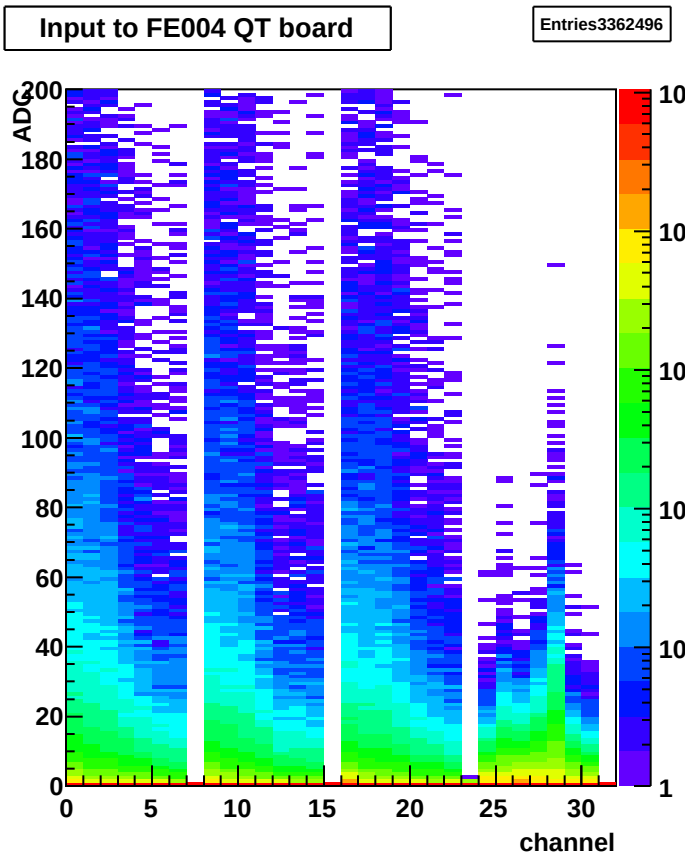

Input to FPD L3 DSM
Entries 7257
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

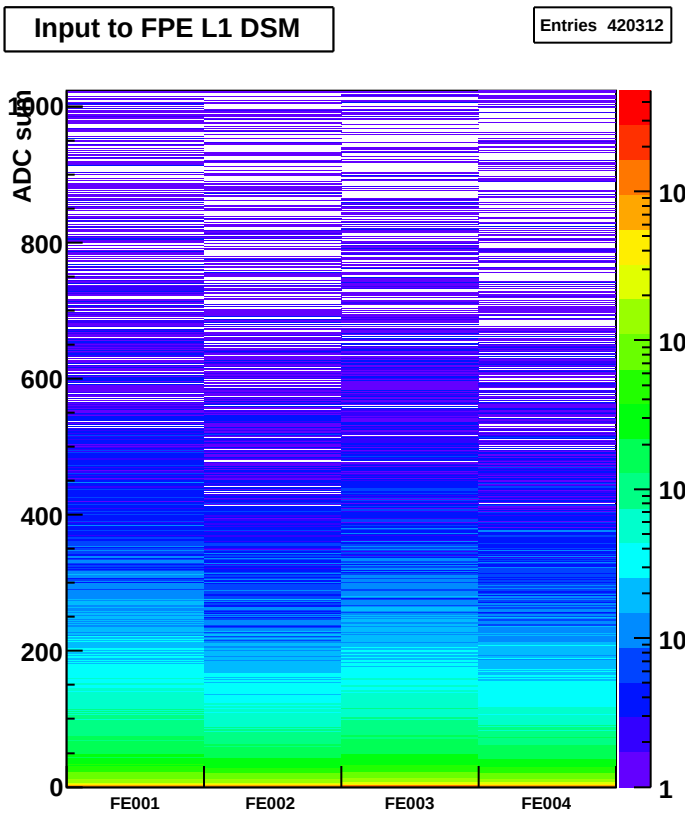

Input to FPD L2 DSM
Entries 420312
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

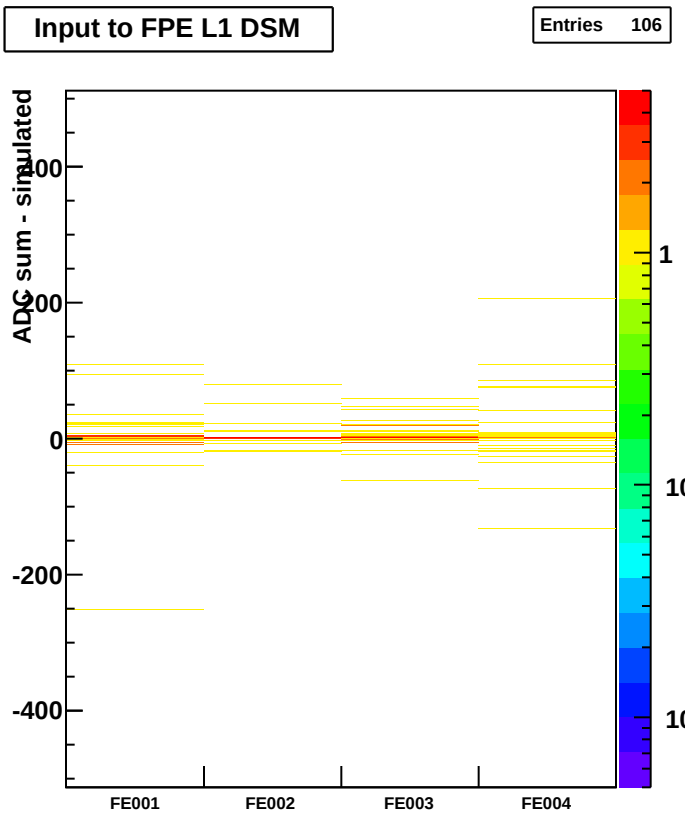

Input to FE001 QT board
Entries3362496
ADC
channel

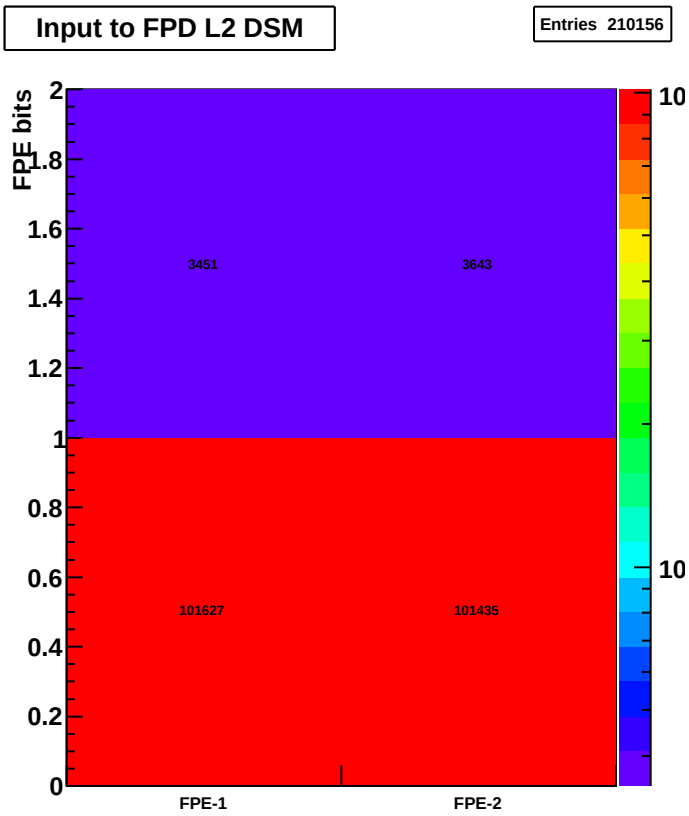

Input to FE002 QT board
Entries3362496
ADC
channel

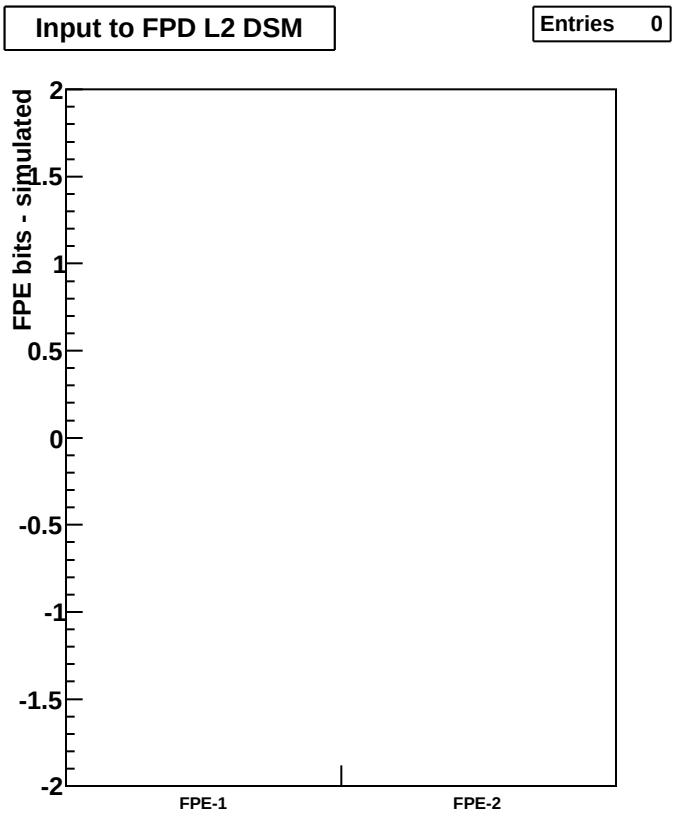

Input to FE003 QT board
Entries3362496
ADC
channel


Input to FE004 QT board
Entries3362496
ADC
channel


Input to FPE L1 DSM
Entries 420312
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 106
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
1


Input to FPD L2 DSM
Entries 210156
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3451
3643
101627
101435
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2