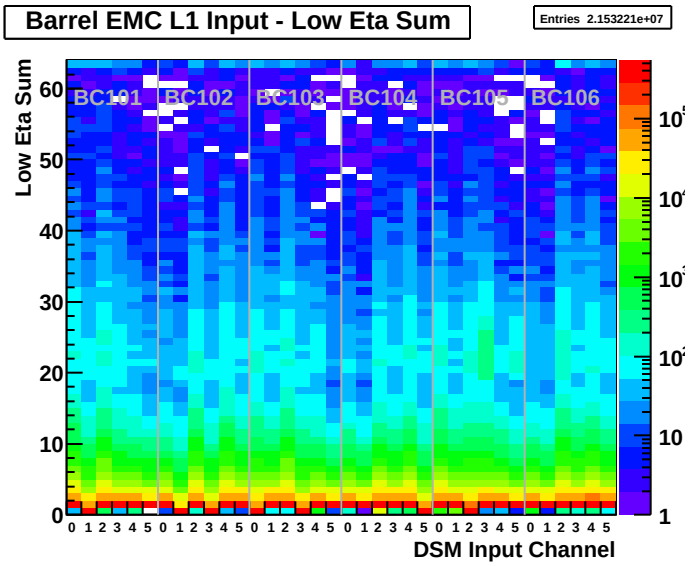
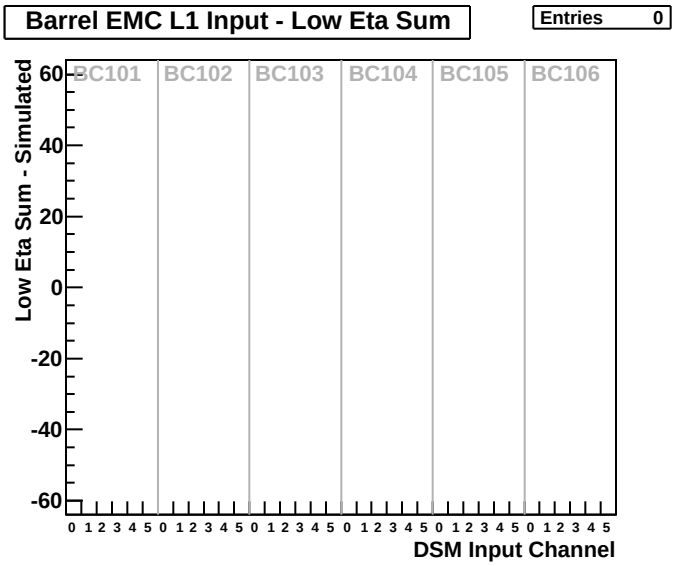
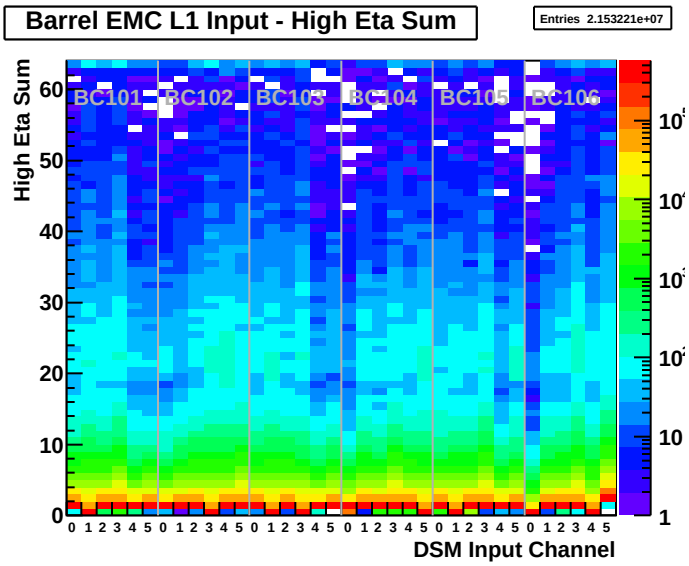
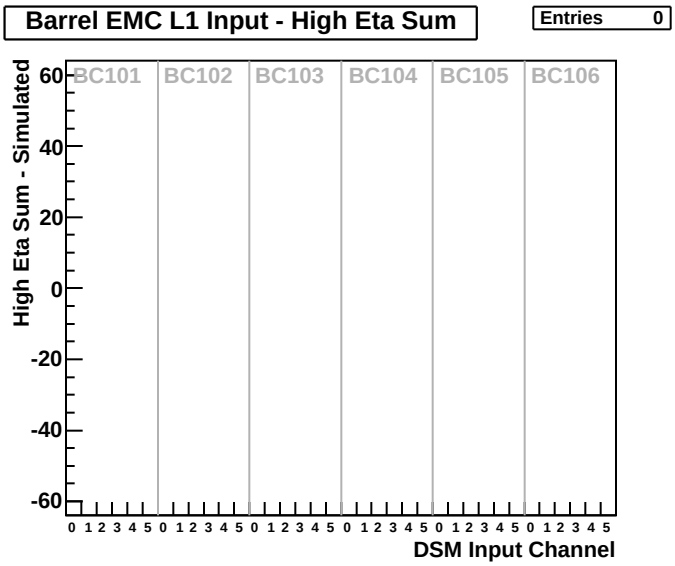
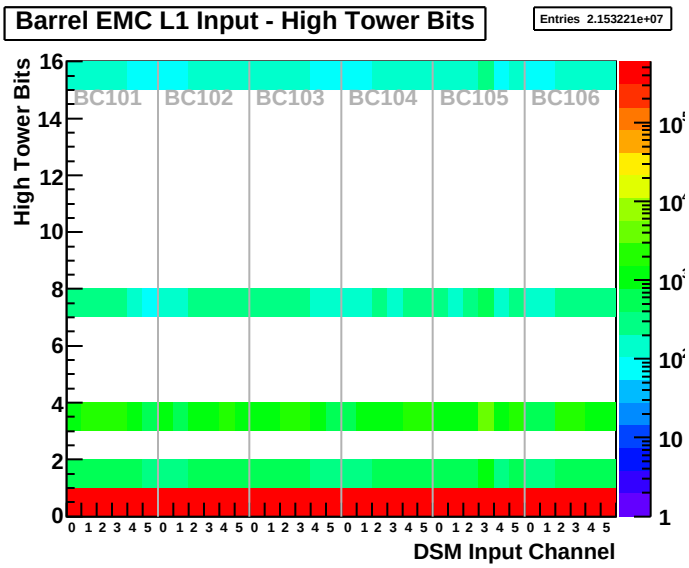
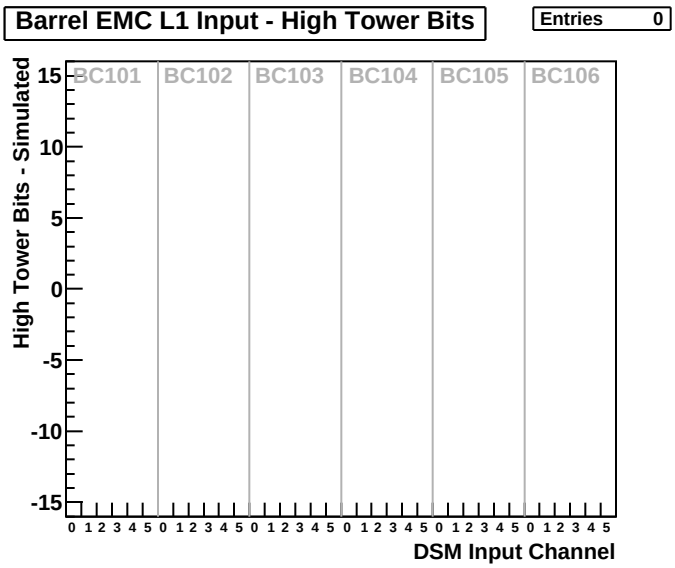

Barrel EMC L1 Input - Low Eta Sum
Entries 2.153221e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

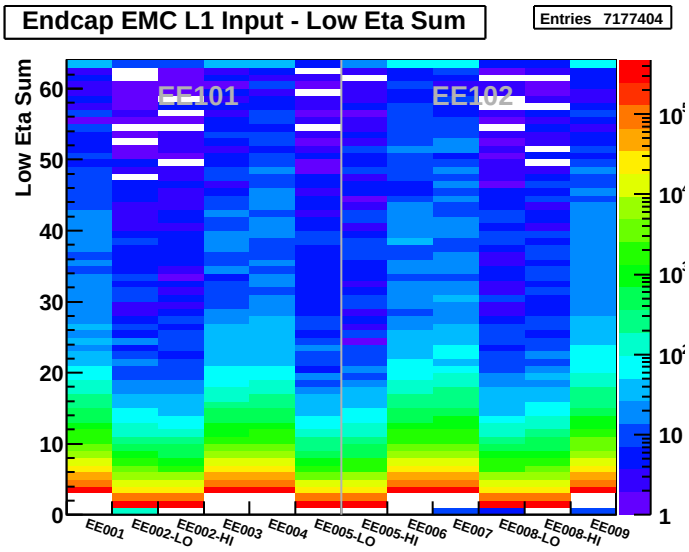

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

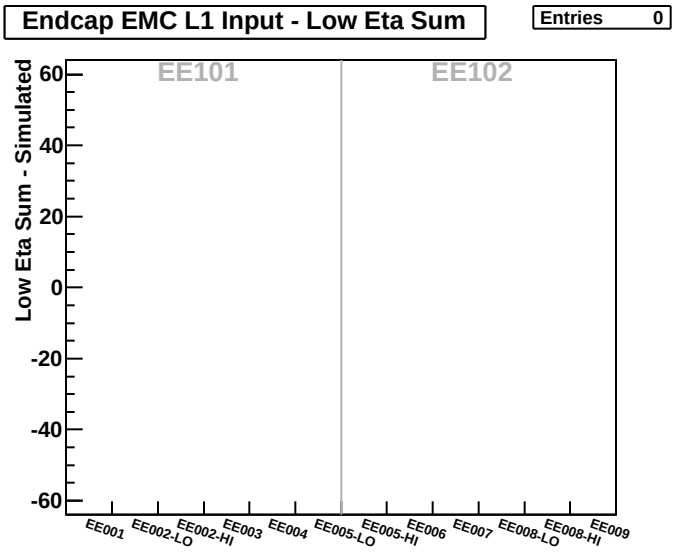

Barrel EMC L1 Input - High Eta Sum
Entries 2.153221e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

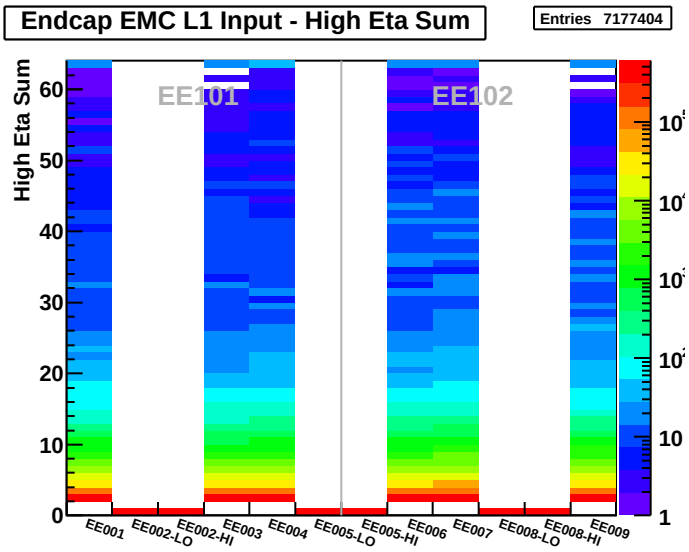

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

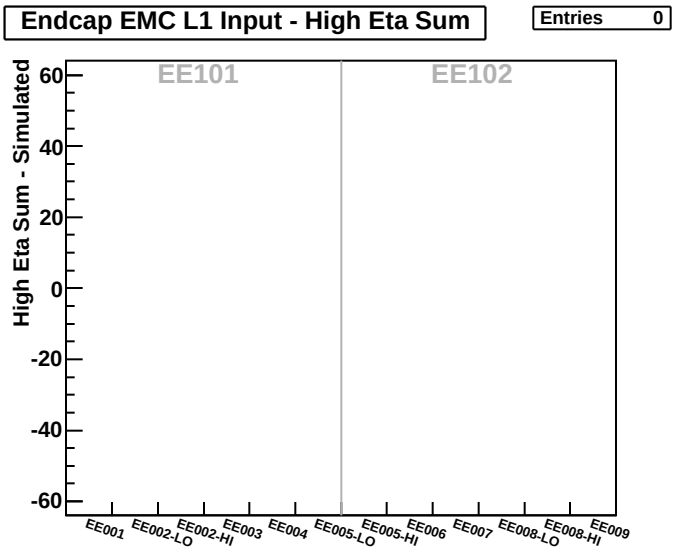

Barrel EMC L1 Input - High Tower Bits
Entries 2.153221e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

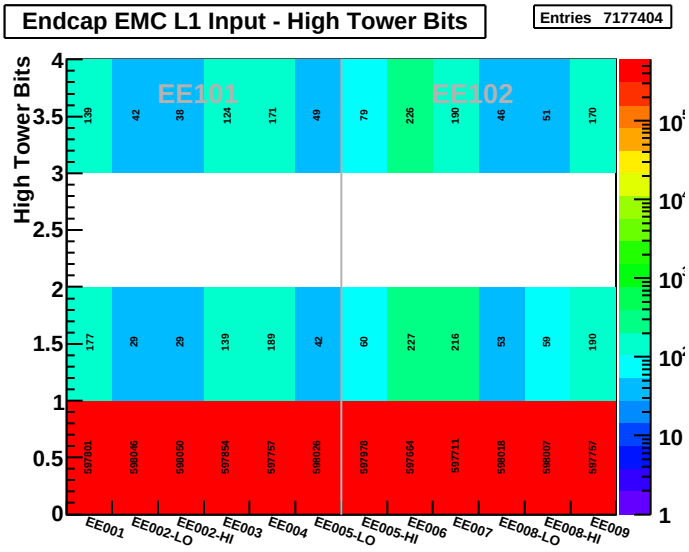

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

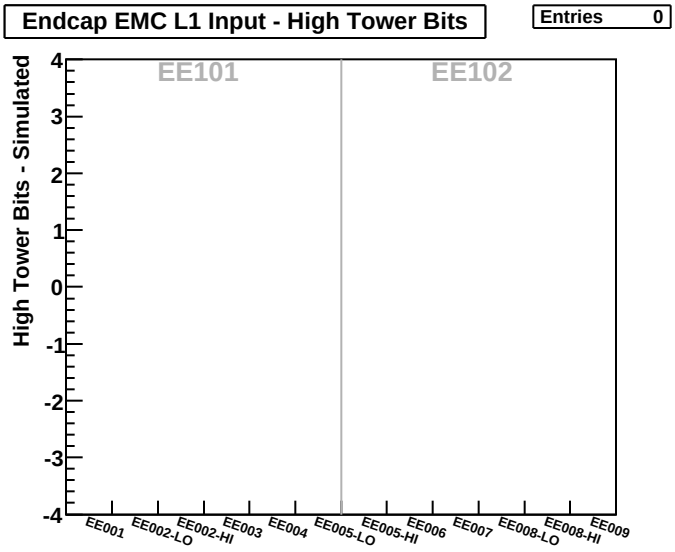

Endcap EMC L1 Input - Low Eta Sum
Entries 7177404
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

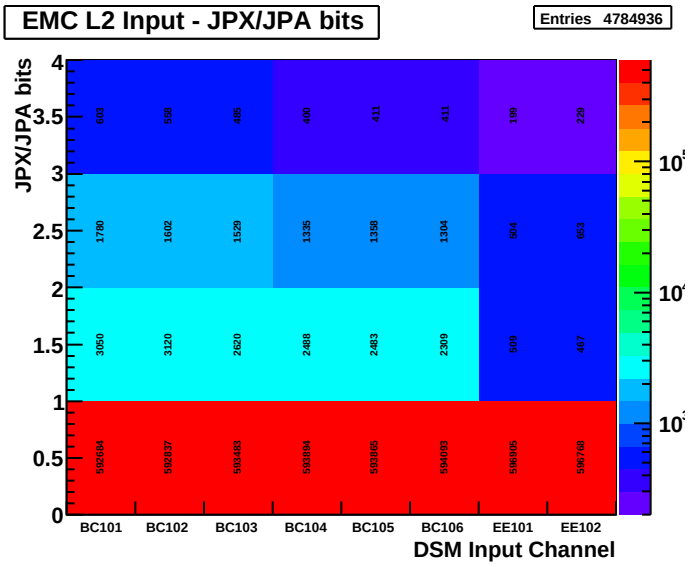

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

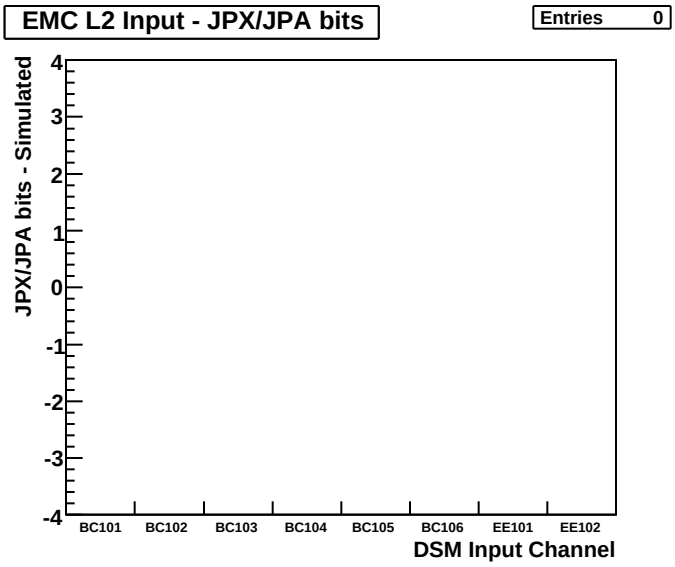

Endcap EMC L1 Input - High Eta Sum
Entries 7177404
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

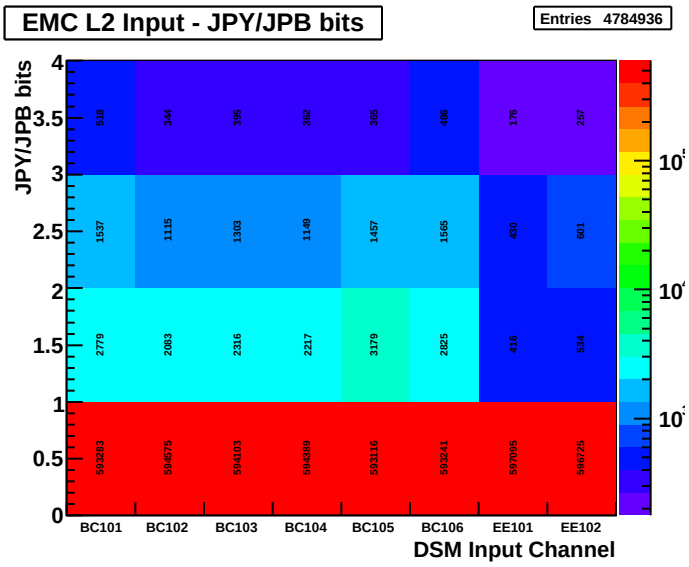

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

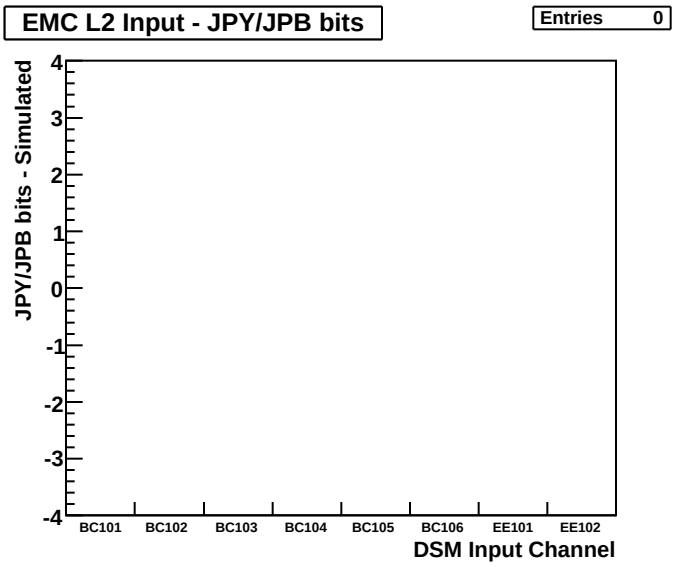

Endcap EMC L1 Input - High Tower Bits
Entries 7177404
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

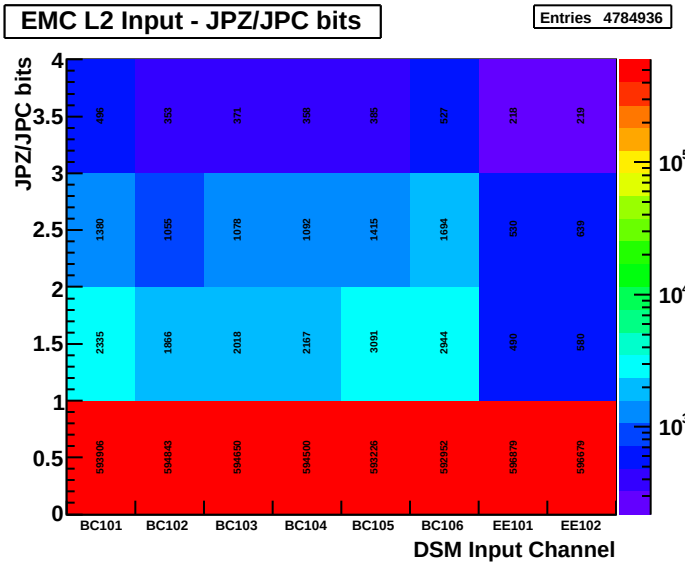

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

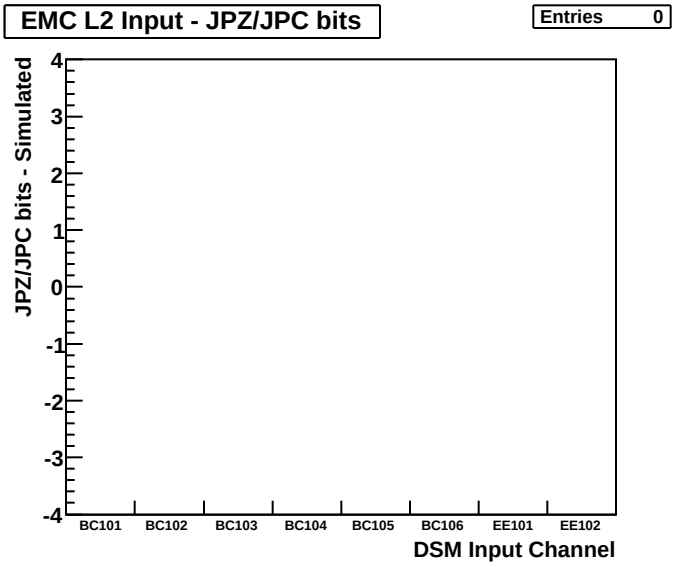

EMC L2 Input - JPX/JPA bits
Entries 4784936
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
603 598 485 400 411 411 199 229
1780 1602 1529 1335 1358 1304 504 653
3050 3120 2620 2488 2483 2309 509 467
592684 592837 593403 593894 593865 594093 595905 596768

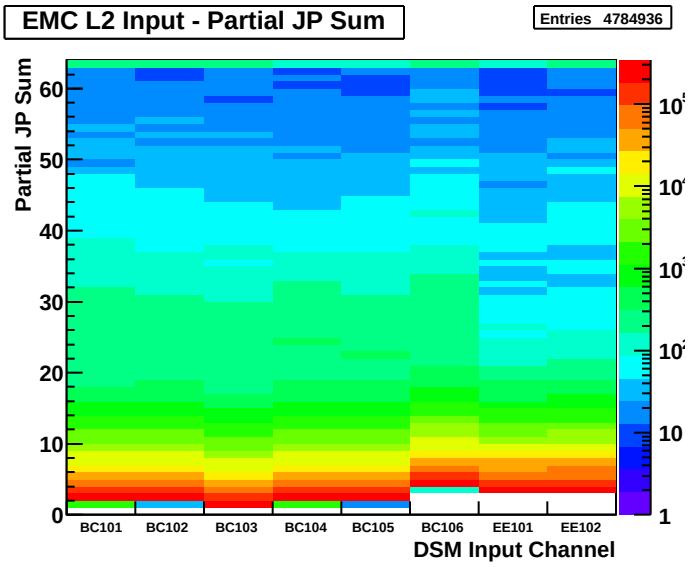

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

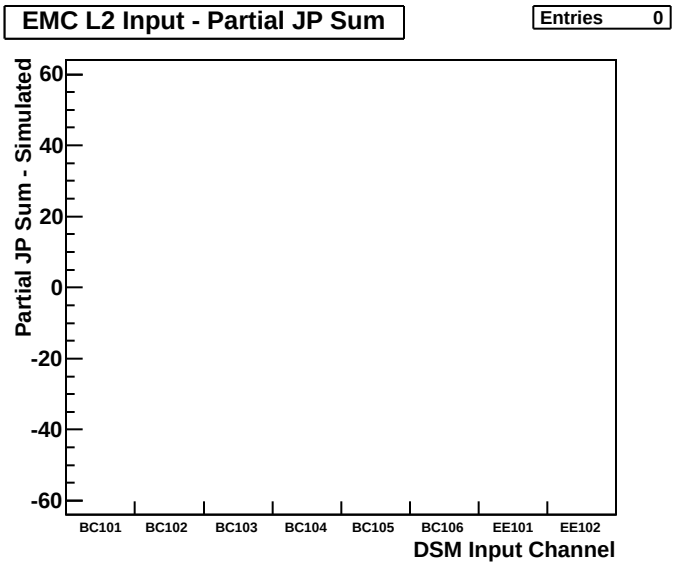

EMC L2 Input - JPY/JPB bits
Entries 4784936
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
516 344 395 362 365 486 176 257
1537 1115 1303 1140 1457 1565 430 601
2779 2083 2316 2217 3179 2825 416 534
593283 594575 594103 594389 593116 593241 597095 596725

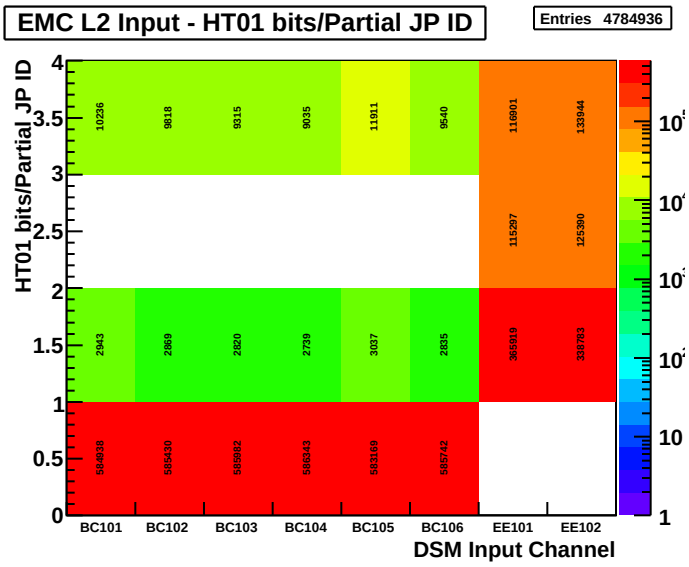

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

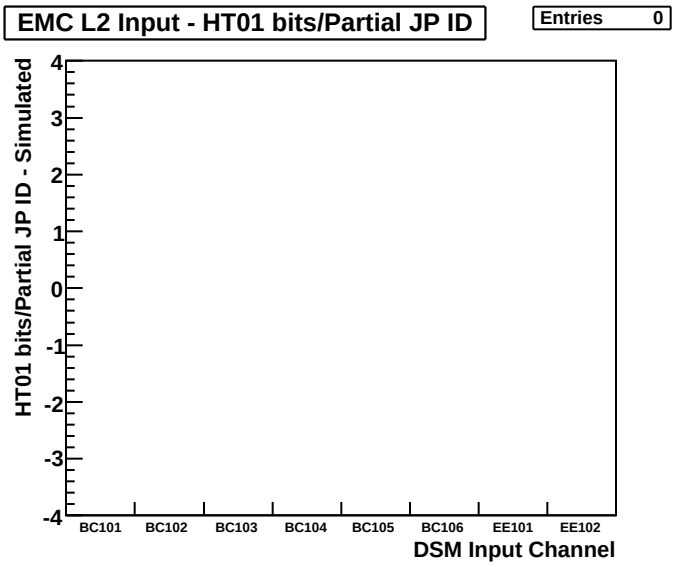

EMC L2 Input - JPZ/JPC bits
Entries 4784936
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
486 353 371 358 385 527 218 219
1380 1055 1078 1092 1415 1694 530 639
2335 1866 2018 2167 3091 2944 490 530
593906 594843 594650 594500 593226 592952 596879 596679

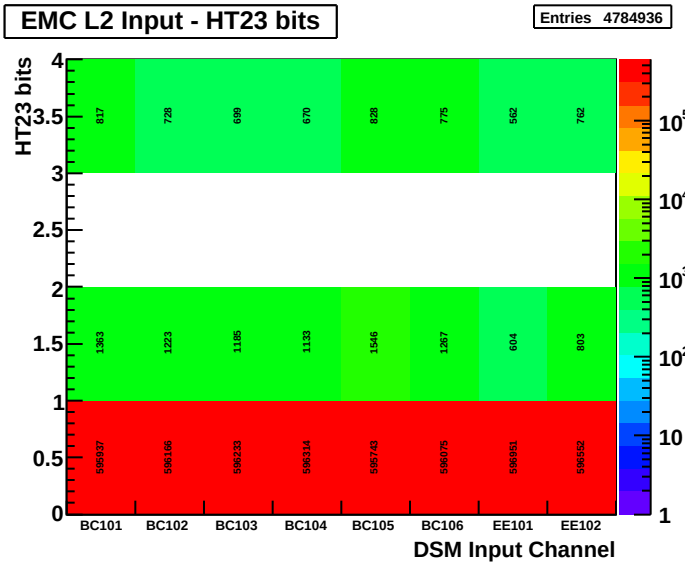

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

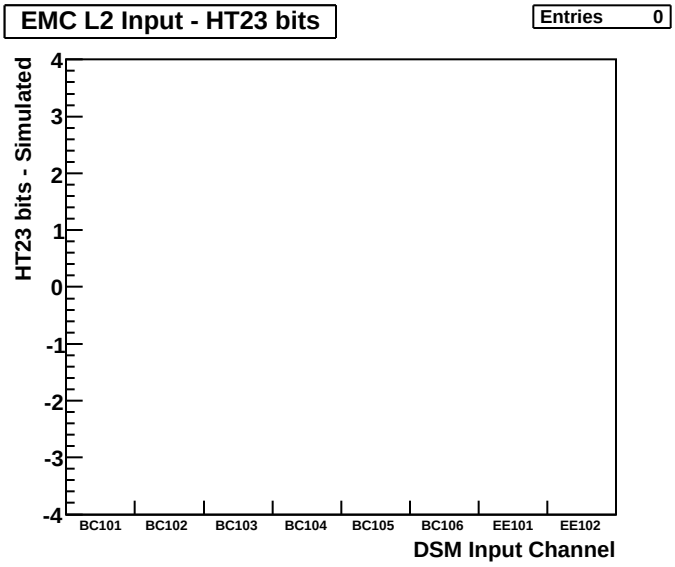

EMC L2 Input - Partial JP Sum
Entries 4784936
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

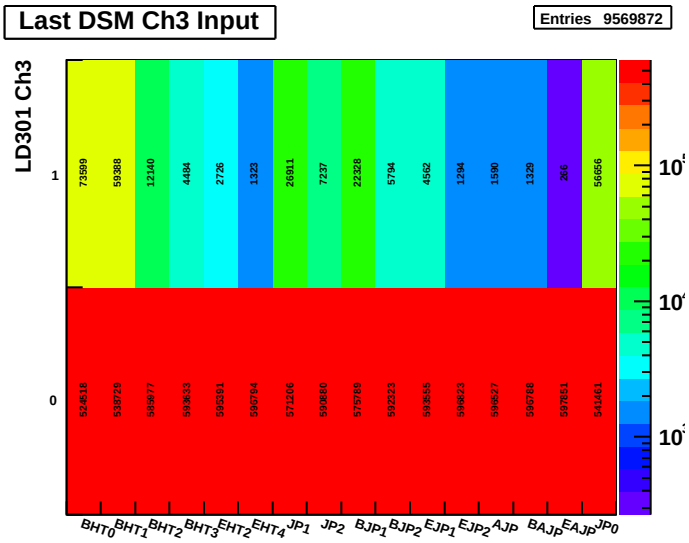

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

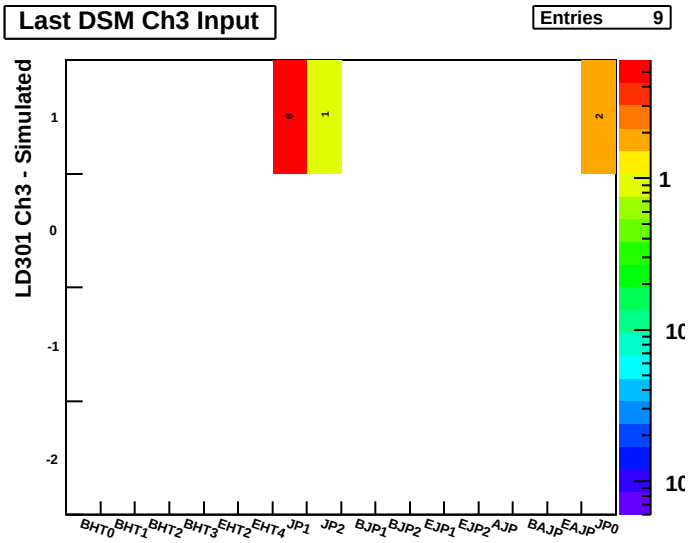

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4784936
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

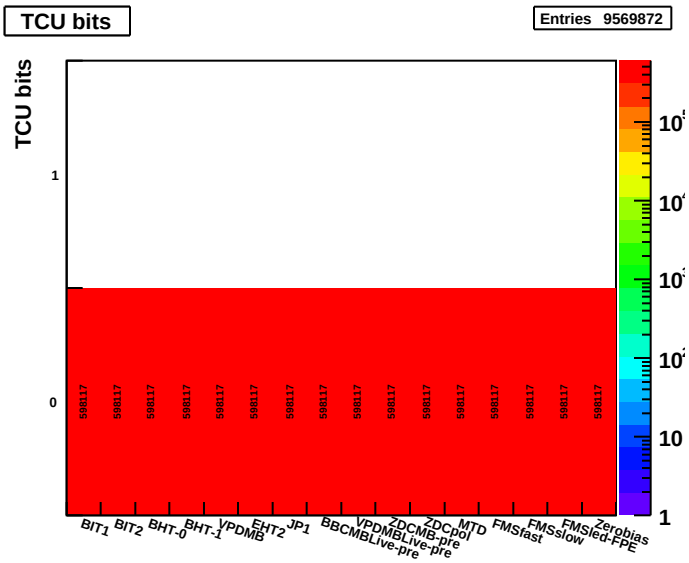

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

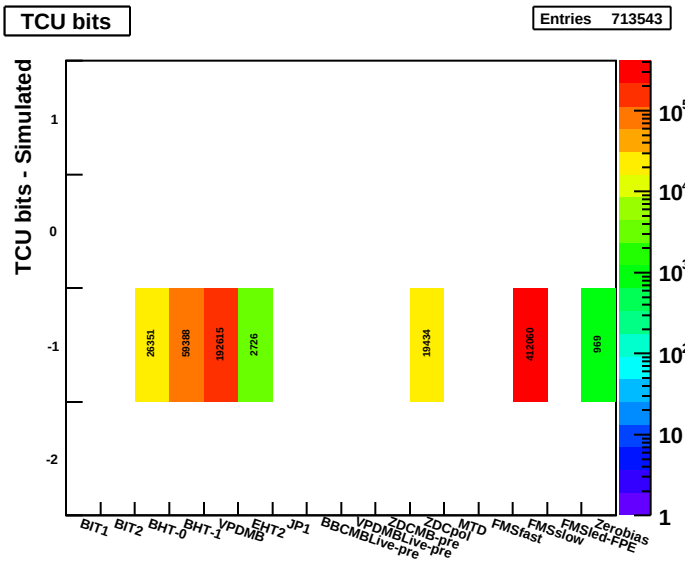

EMC L2 Input - HT23 bits
Entries 4784936
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

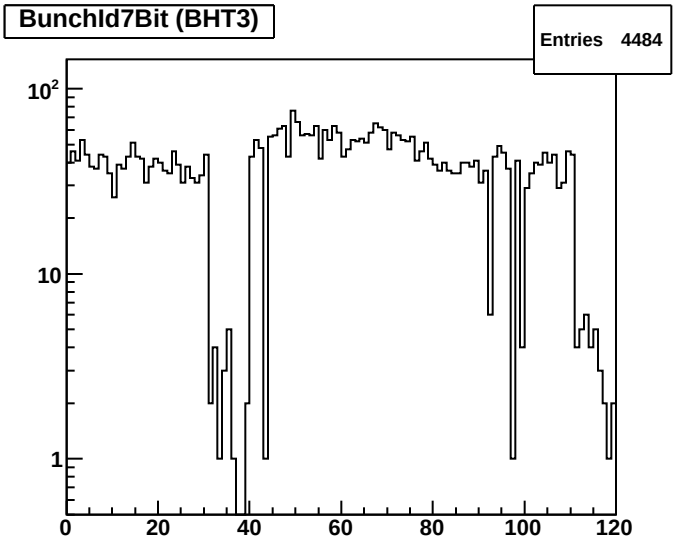

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

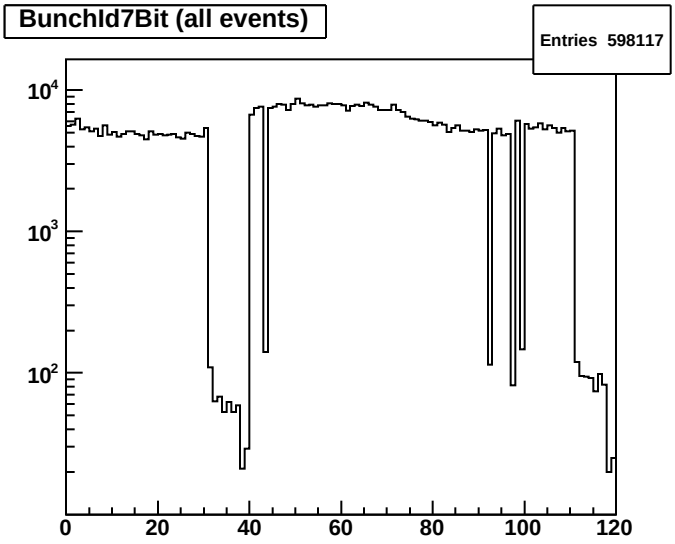

Last DSM Ch3 Input
Entries 9569872
LD301 Ch3
73599 59388 12140 4484 2726 1323 26911 7237 22328 5794 4562 1294 1590 1329 266 56656
524518 538729 585977 593633 595391 596794 571206 590880 575789 592323 593555 596823 596527 596788 597851 541461
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

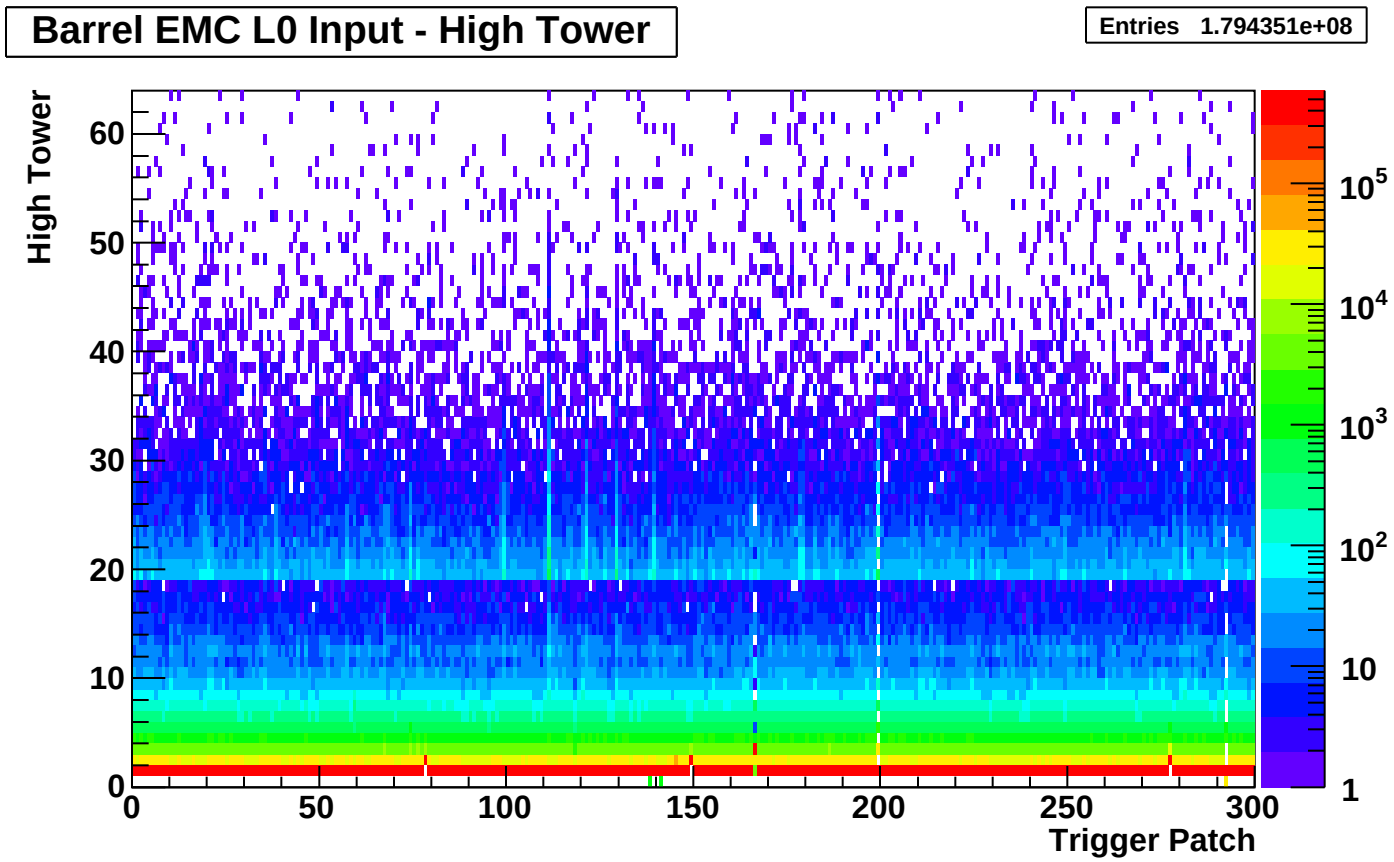

Last DSM Ch3 Input
Entries 9
LD301 Ch3 - Simulated
6 1 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

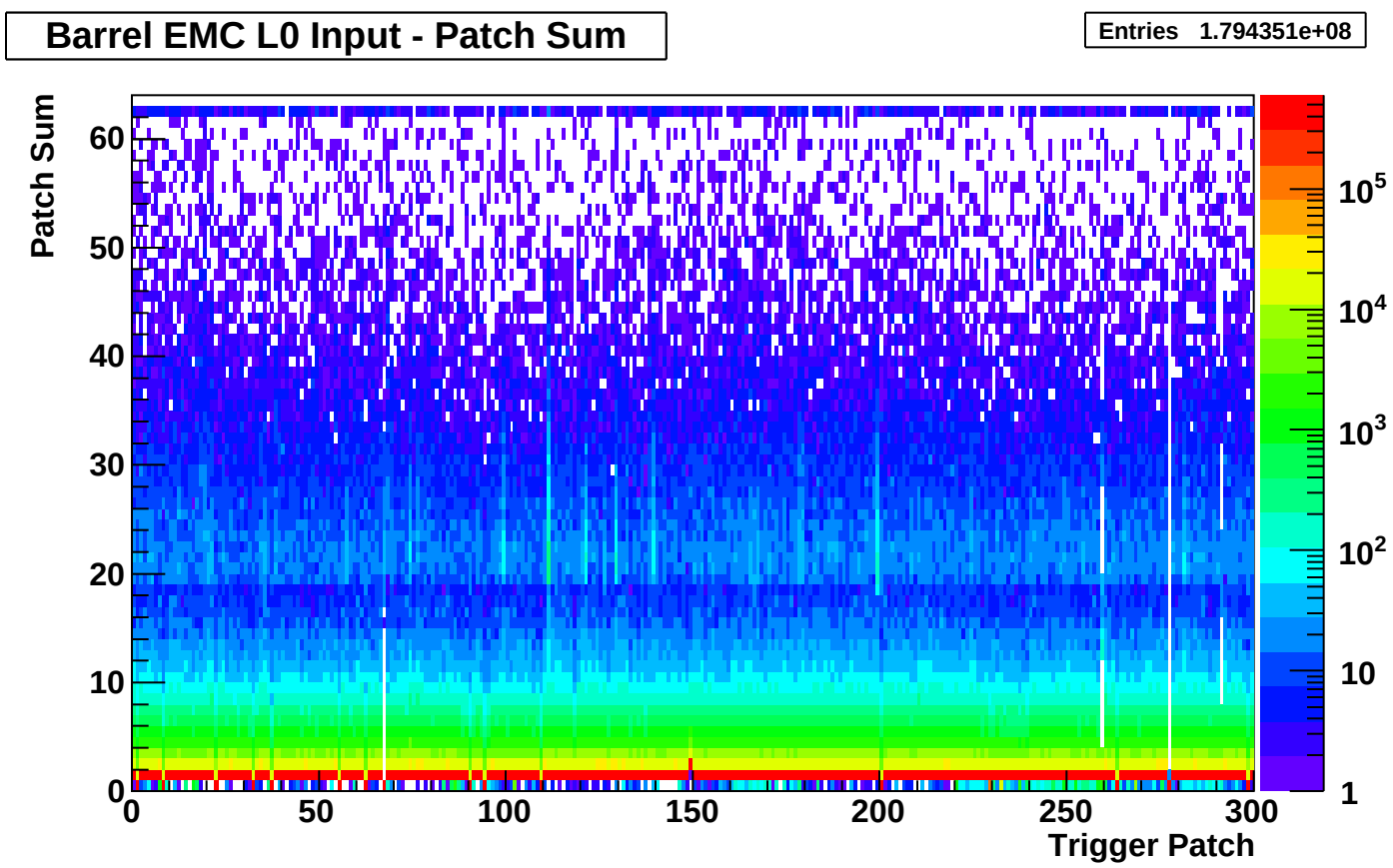

TCU bits
Entries 9569872
TCU bits
598117
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

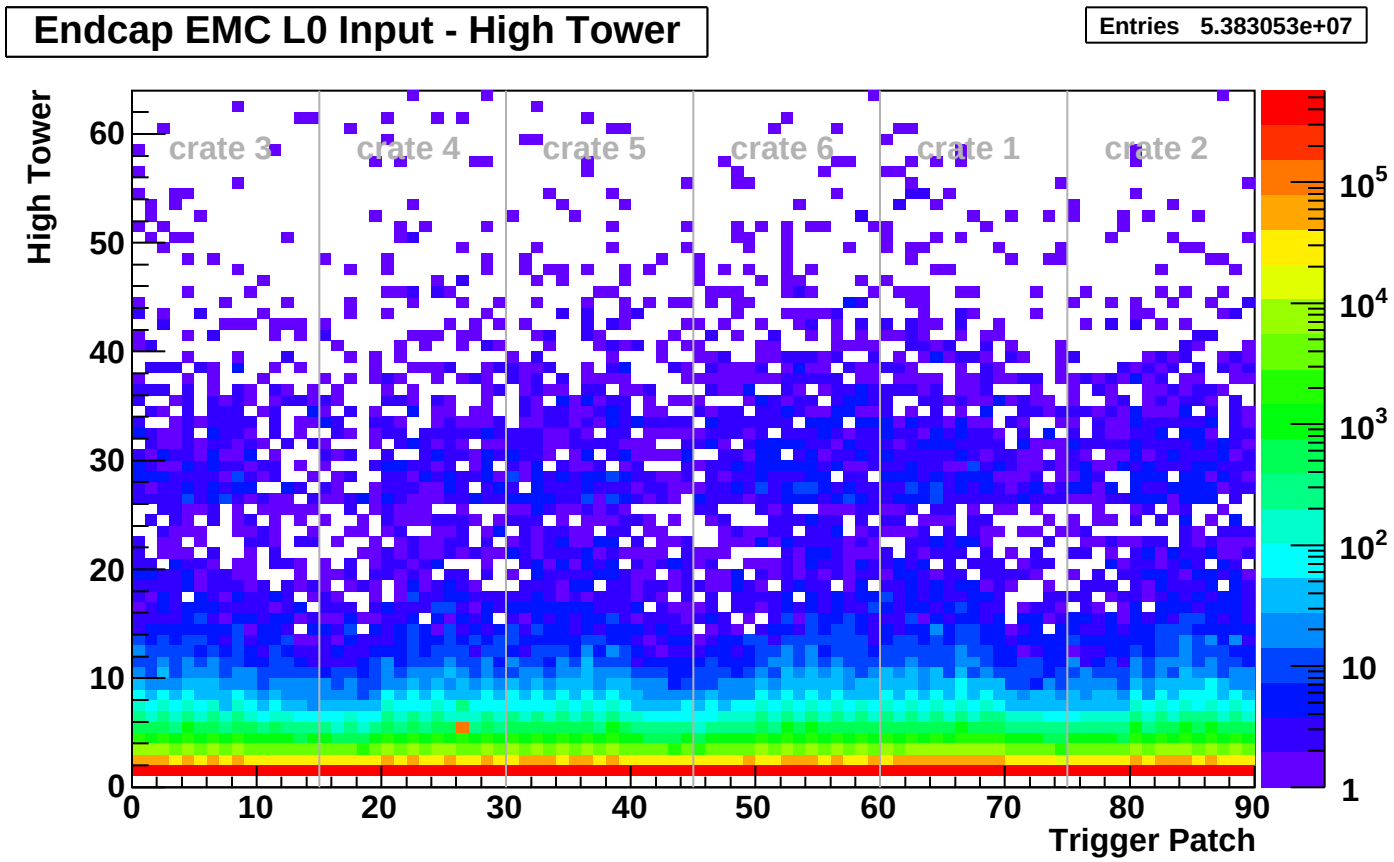

TCU bits
Entries 713543
TCU bits - Simulated
26351 59388 192615 2726 19434 412060 969
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

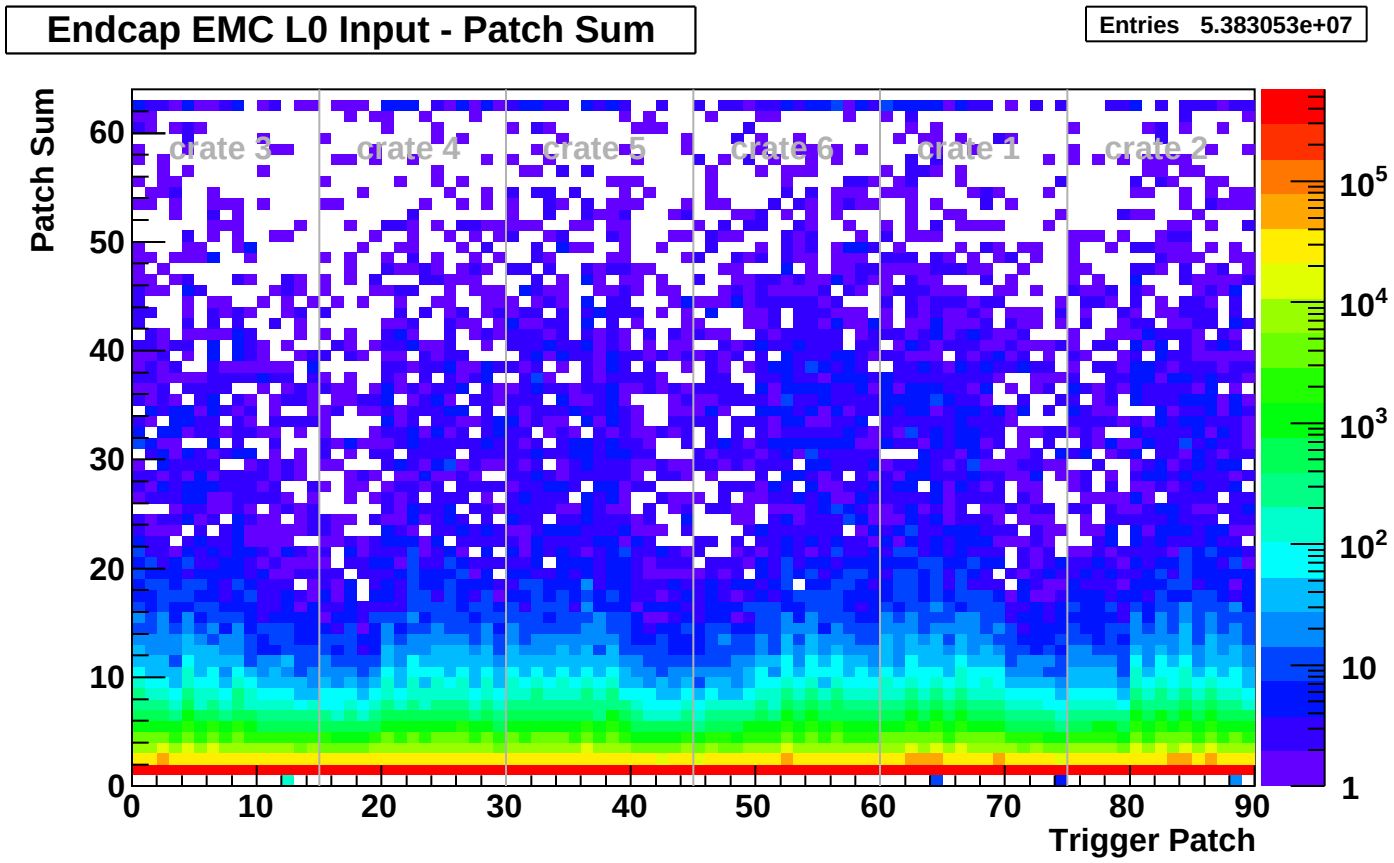

BunchId7Bit (BHT3)
Entries 4484

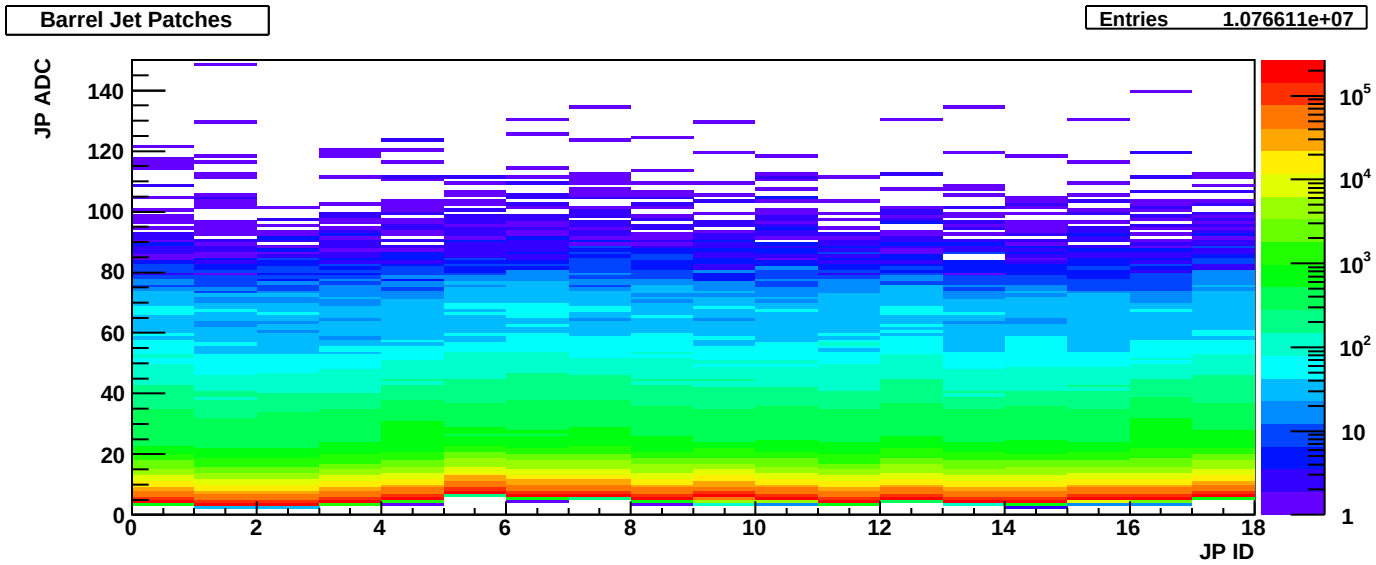

BunchId7Bit (all events)
Entries 598117

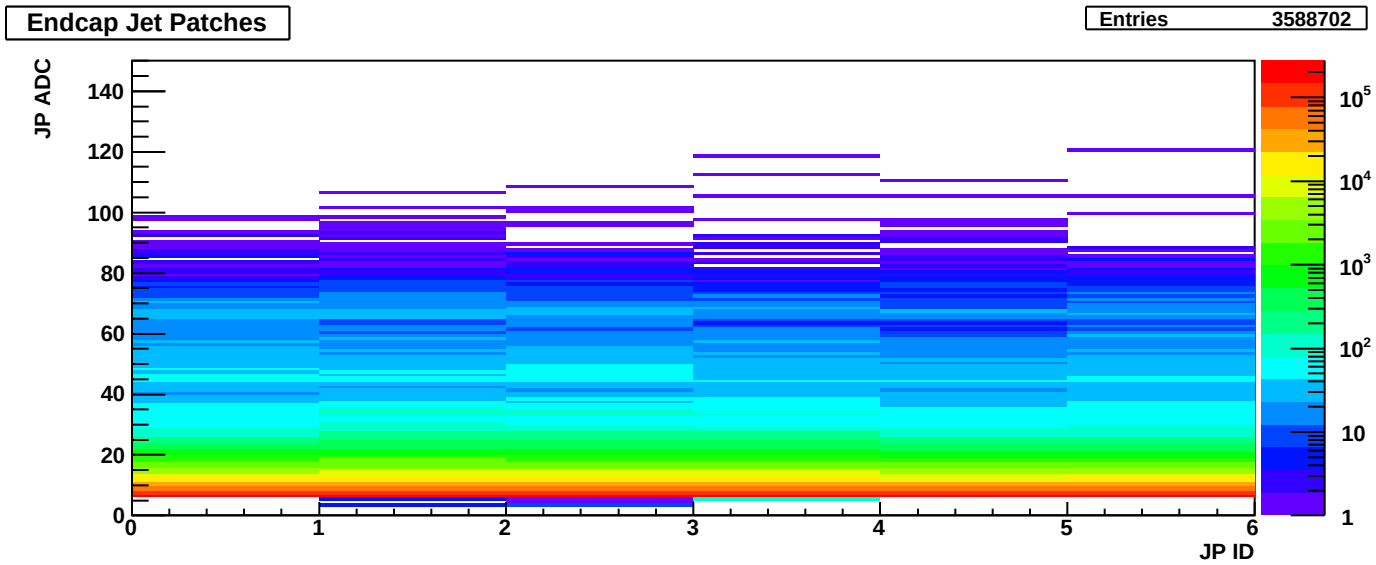

Barrel EMC L0 Input - High Tower
Entries 1.794351e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

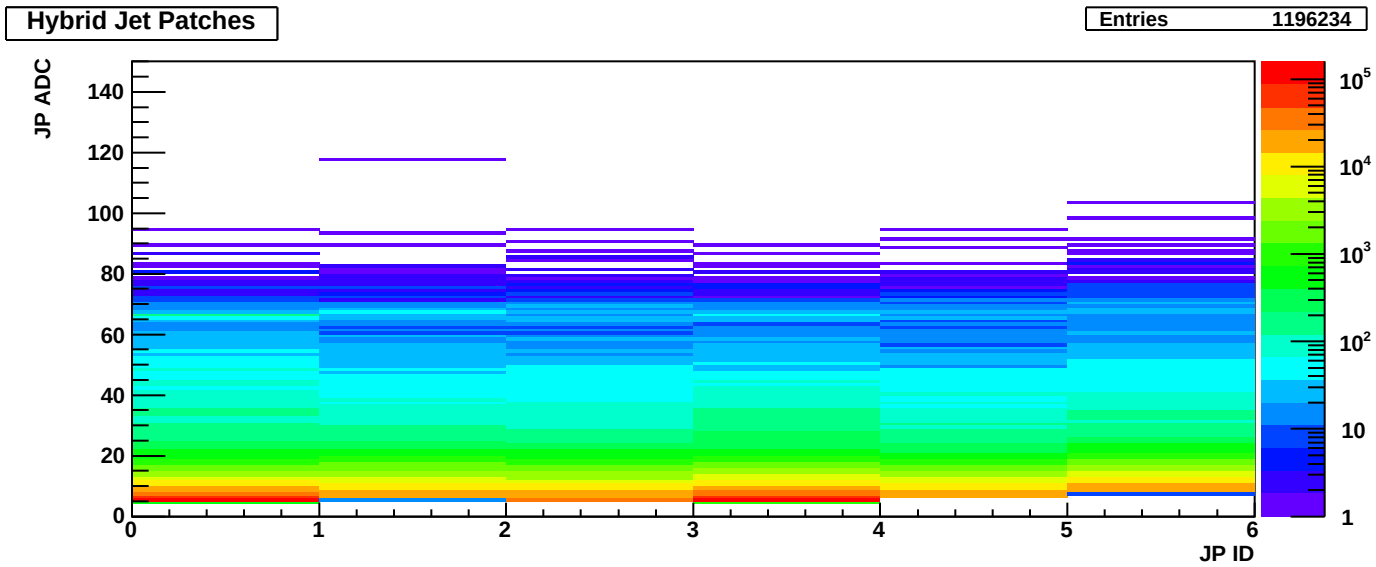

Barrel EMC L0 Input - Patch Sum
Entries 1.794351e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

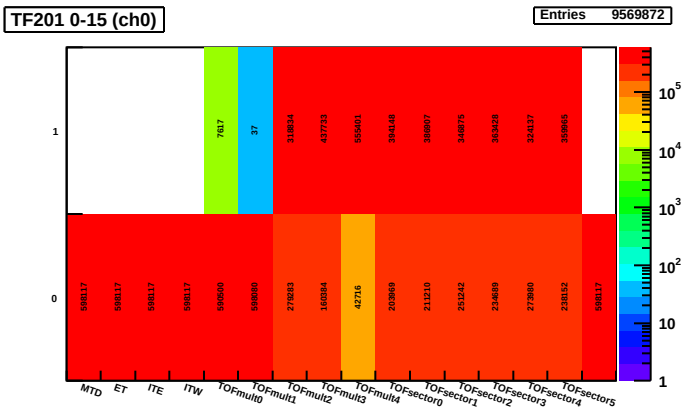

Endcap EMC L0 Input - High Tower
Entries 5.383053e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

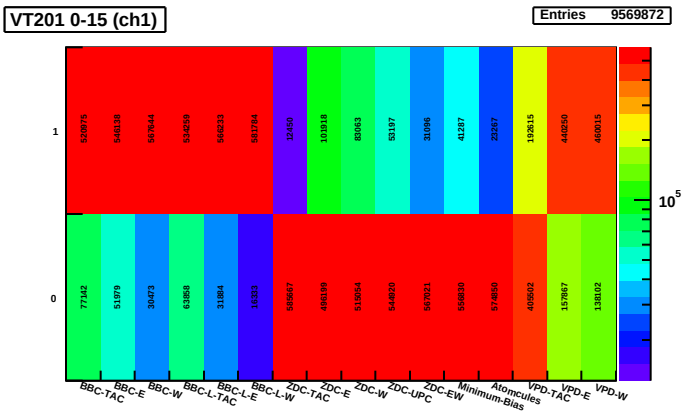

Endcap EMC L0 Input - Patch Sum
Entries 5.383053e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

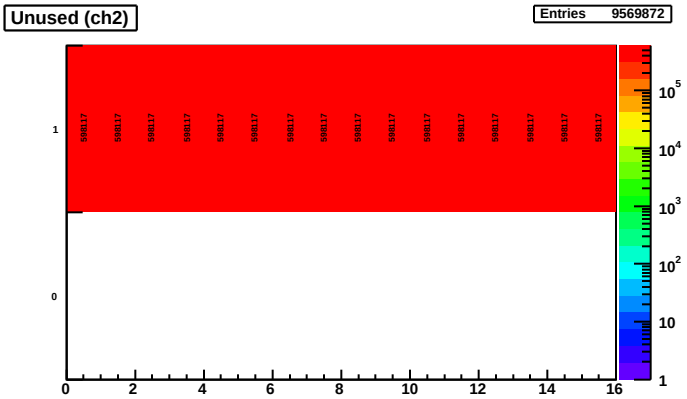

Barrel Jet Patches
Entries 1.076611e+07
JP ADC
JP ID

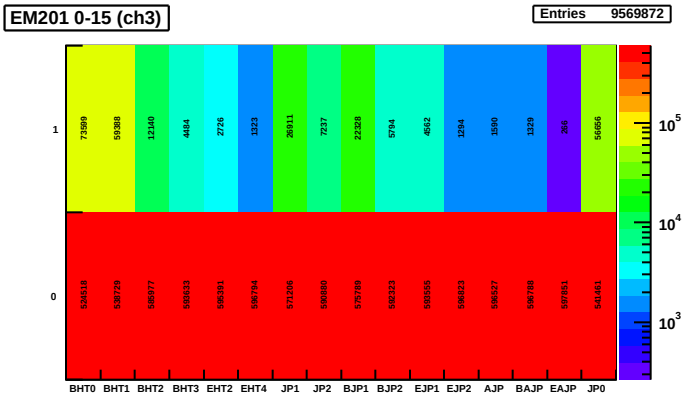

Endcap Jet Patches
Entries 3588702
JP ADC
JP ID

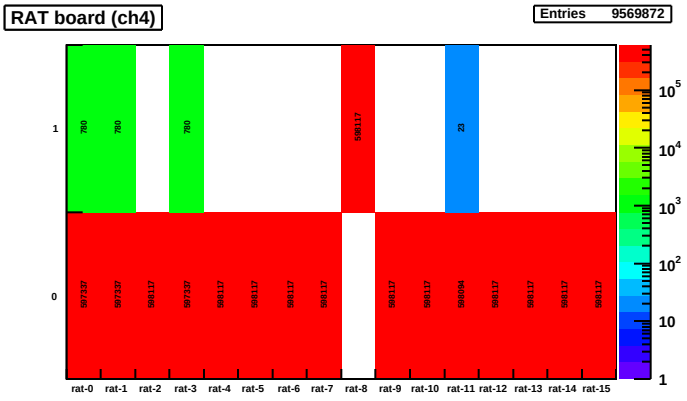

Hybrid Jet Patches
Entries 1196234
JP ADC
JP ID

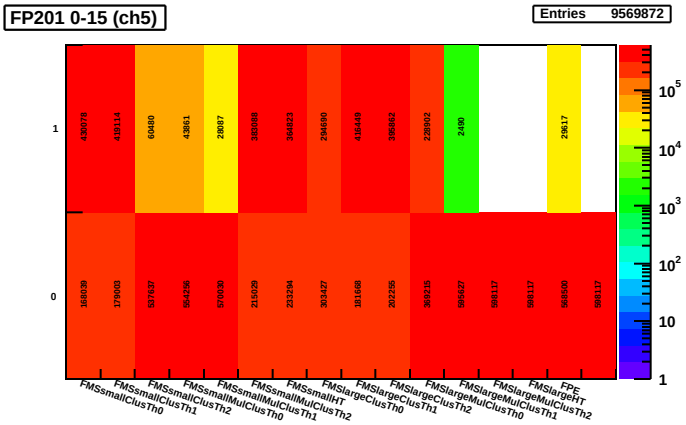

TF201 0-15 (ch0)
Entries 9569872
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

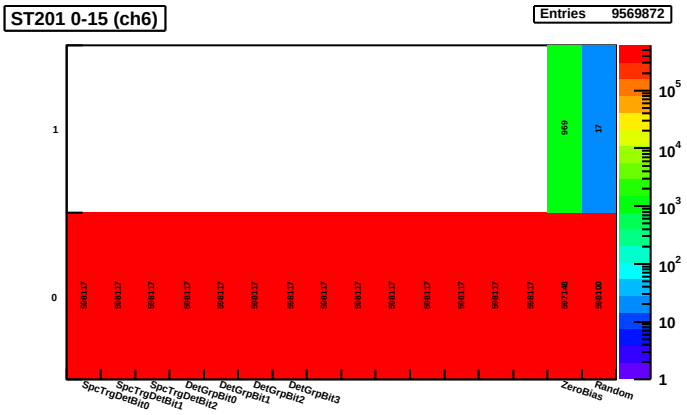

VT201 0-15 (ch1)
Entries 9569872
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

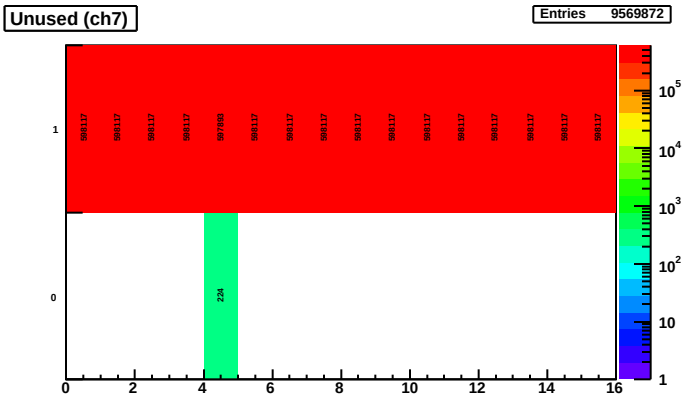

Unused (ch2)
Entries 9569872

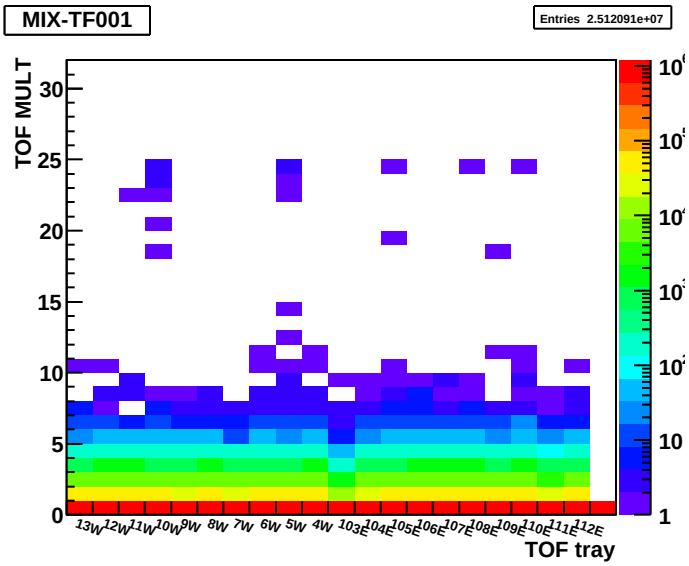

EM201 0-15 (ch3)
Entries 9569872
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

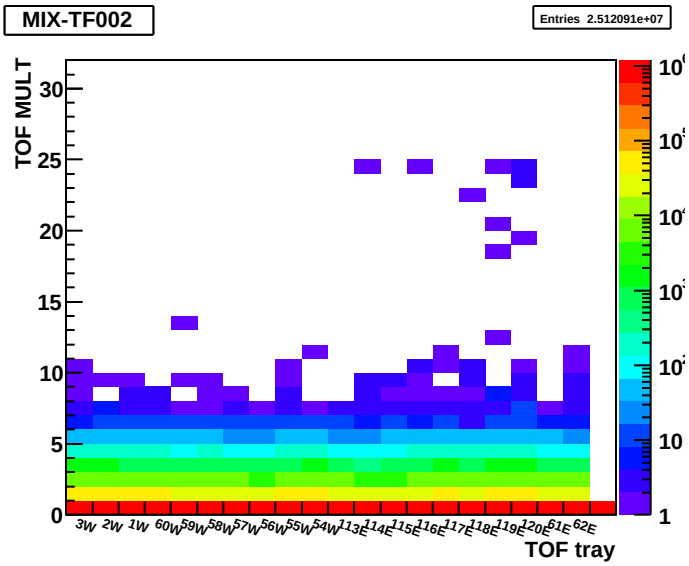

RAT board (ch4)
Entries 9569872
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

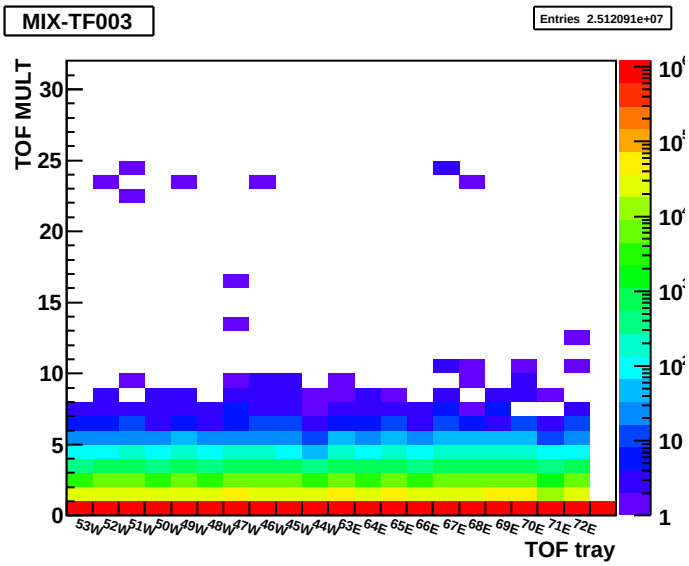

FP201 0-15 (ch5)
Entries 9569872
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

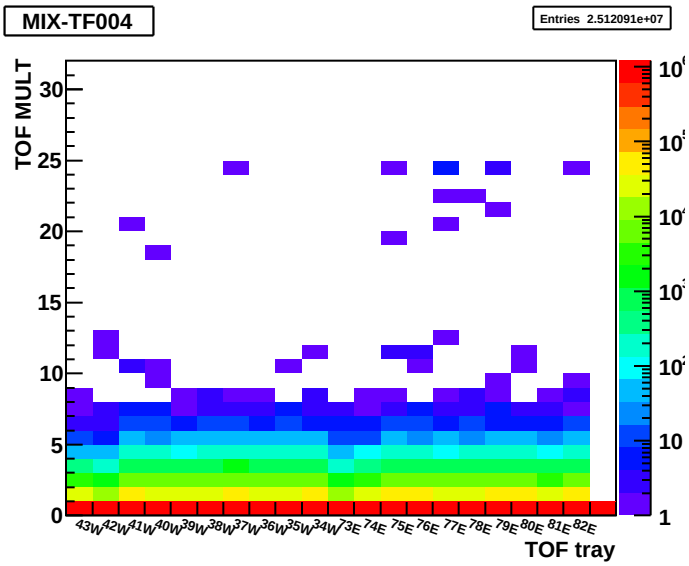

ST201 0-15 (ch6)
Entries 9569872
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

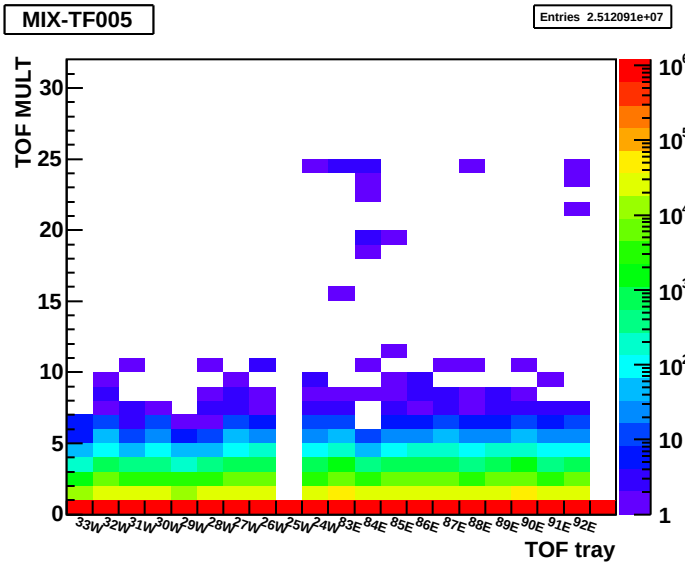

Unused (ch7)
Entries 9569872

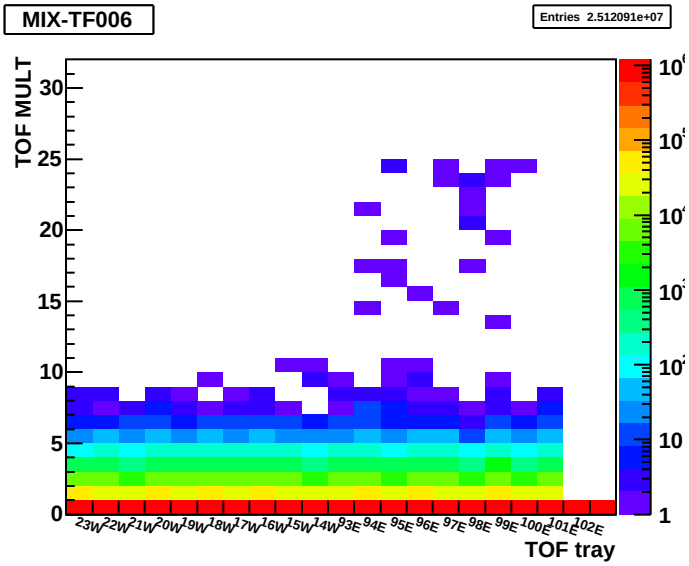

MIX-TF001
Entries 2.512091e+07
TOF MULT
TOF tray

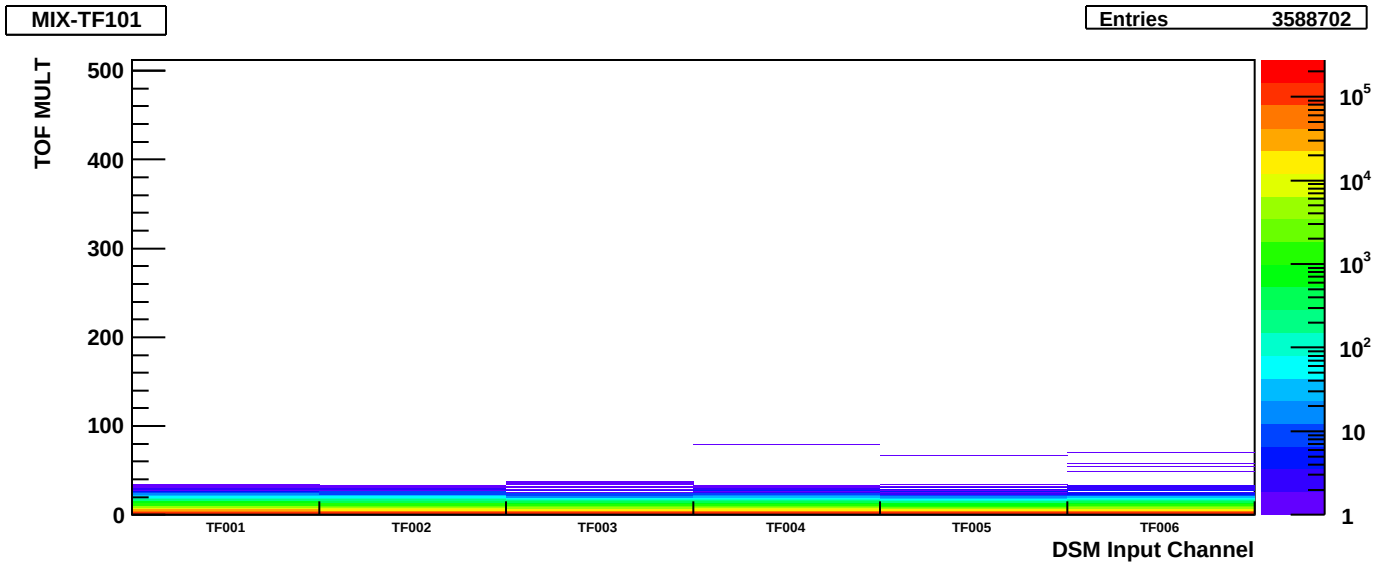

MIX-TF002
Entries 2.512091e+07
TOF MULT
TOF tray

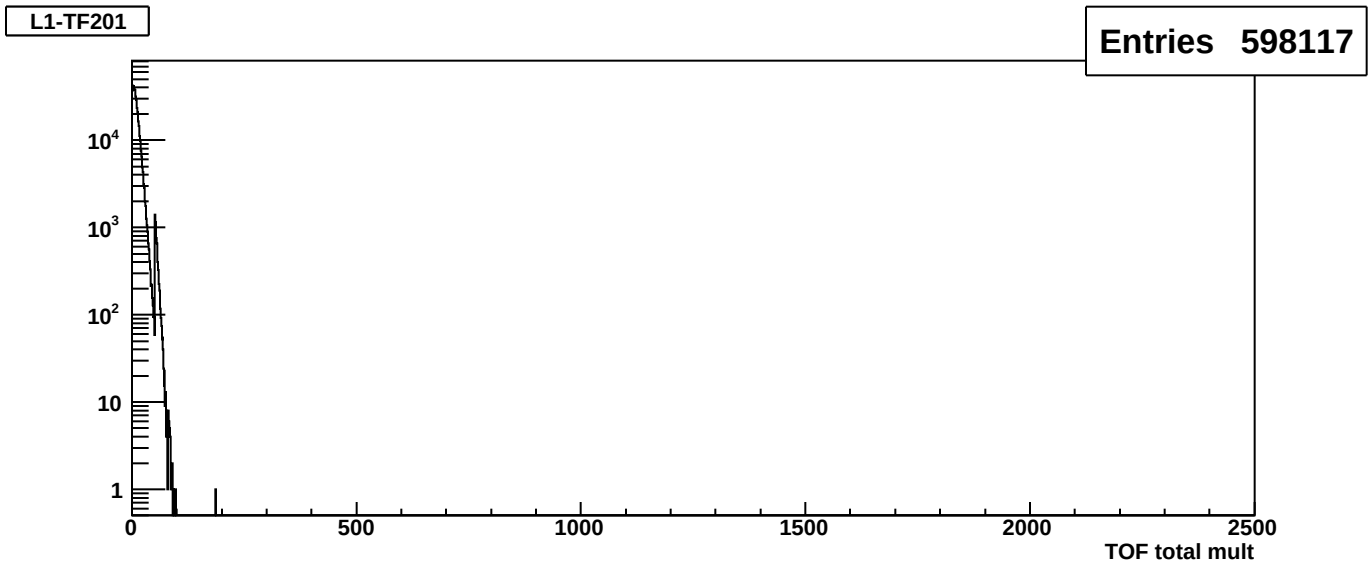

MIX-TF003
Entries 2.512091e+07
TOF MULT
TOF tray

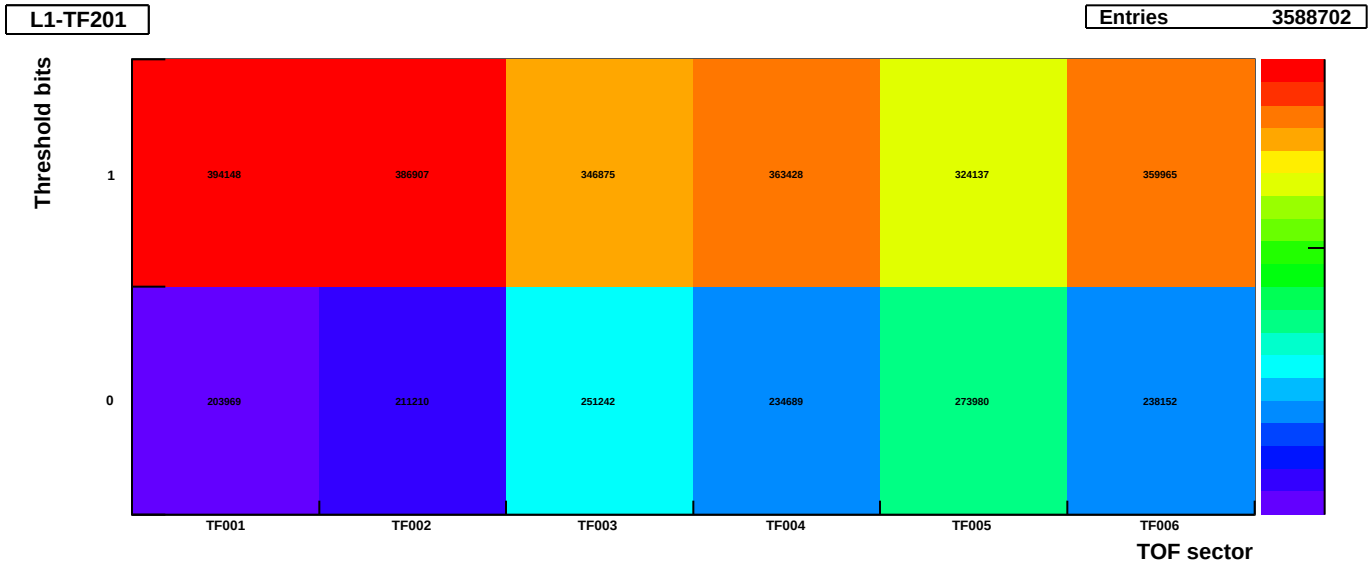

MIX-TF004
Entries 2.512091e+07
TOF MULT
TOF tray

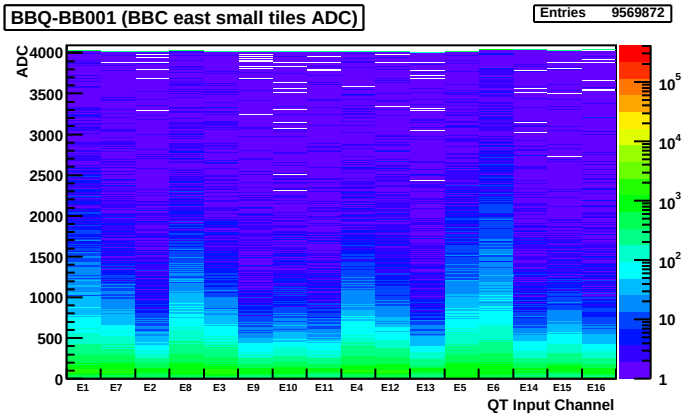

MIX-TF005
Entries 2.512091e+07
TOF MULT
TOF tray

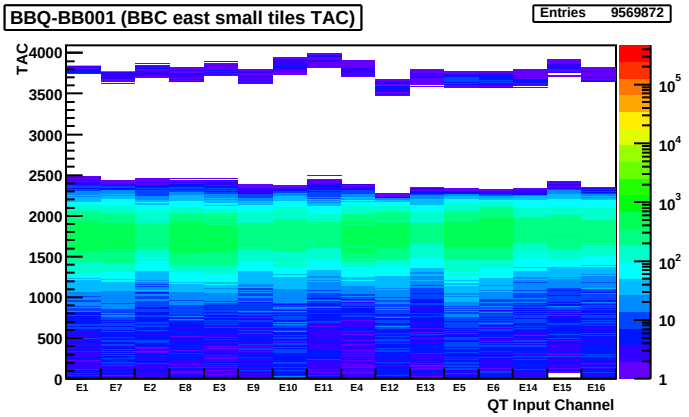

MIX-TF006
Entries 2.512091e+07
TOF MULT
TOF tray

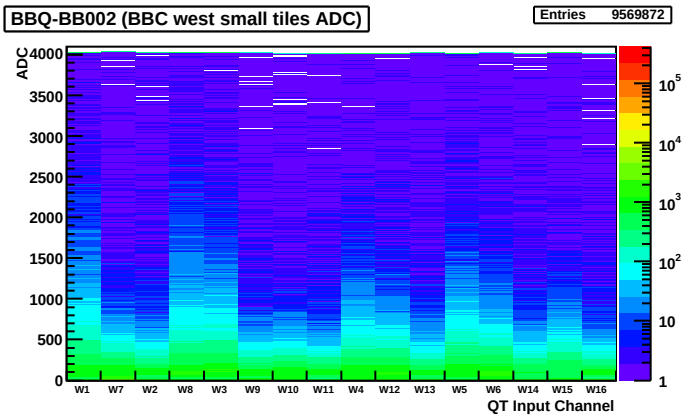

MIX-TF101
Entries 3588702
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

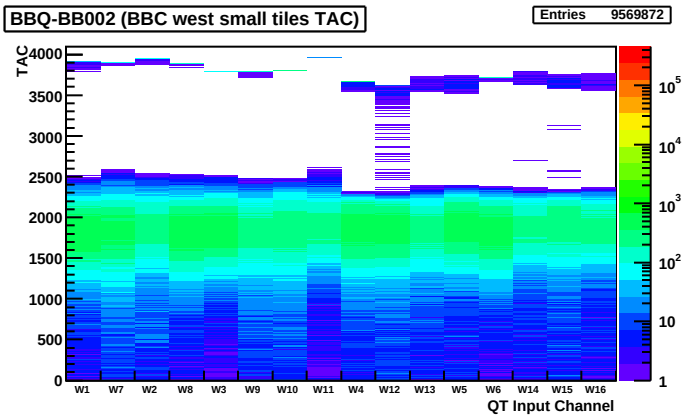

L1-TF201
Entries 598117
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

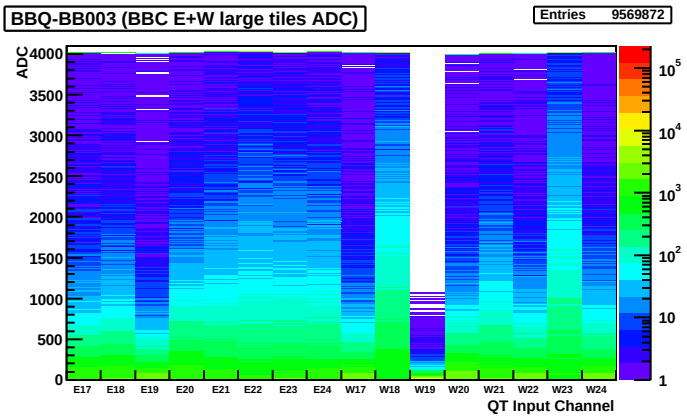

L1-TF201
Entries 3588702
Threshold bits
1
0
394148
386907
346875
363428
324137
359965
203969
211210
251242
234689
273980
238152
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

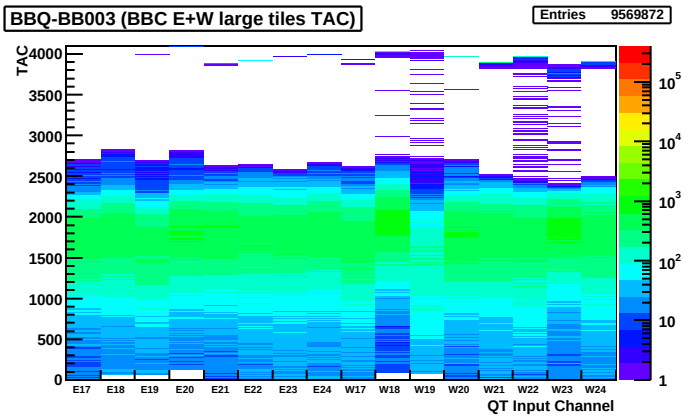

BBQ-BB001 (BBC east small tiles ADC)
Entries 9569872
ADC
QT Input Channel

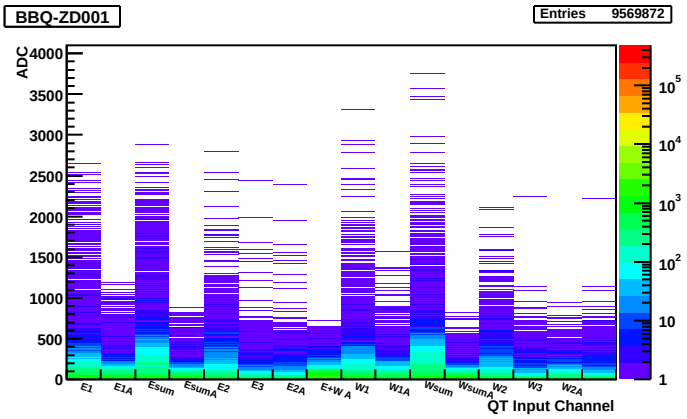

BBQ-BB001 (BBC east small tiles TAC)
Entries 9569872
TAC
QT Input Channel

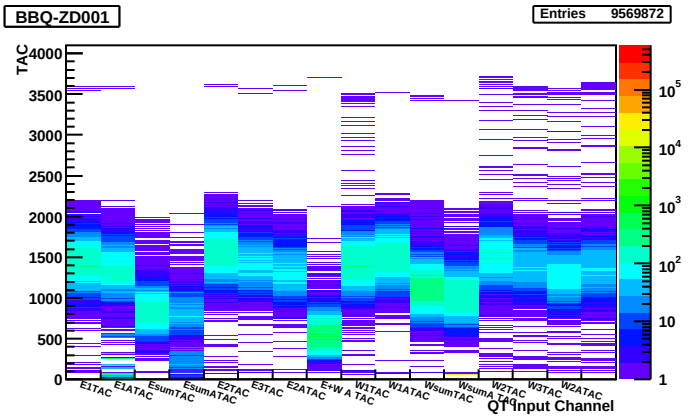

BBQ-BB002 (BBC west small tiles ADC)
Entries 9569872
ADC
QT Input Channel

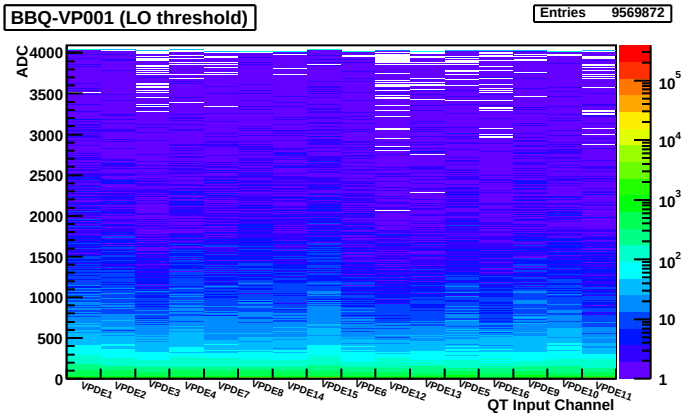

BBQ-BB002 (BBC west small tiles TAC)
Entries 9569872
TAC
QT Input Channel

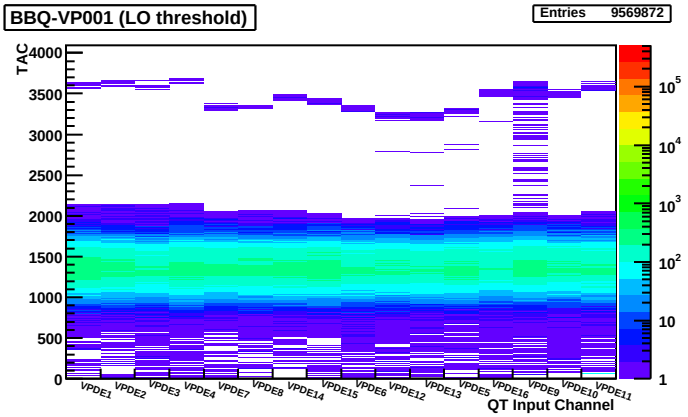

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 9569872
ADC
QT Input Channel

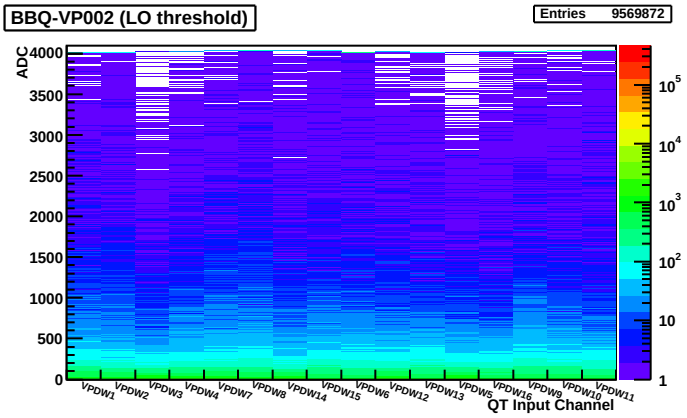

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 9569872
TAC
QT Input Channel

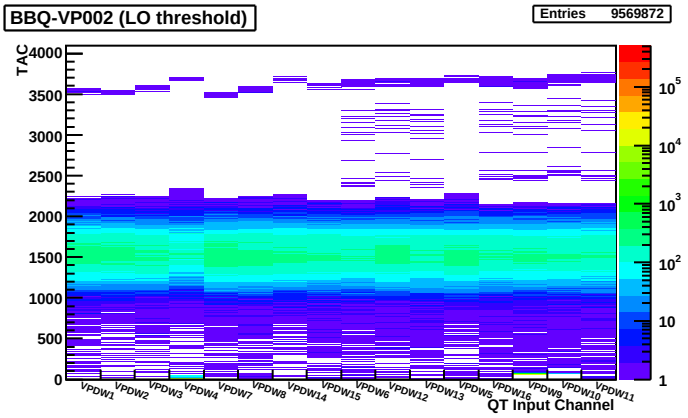

BBQ-ZD001
Entries 9569872
ADC
QT Input Channel

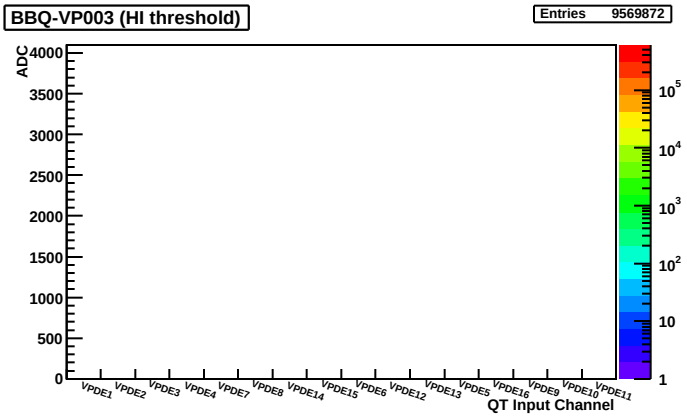

BBQ-ZD001
Entries 9569872
TAC
QT Input Channel

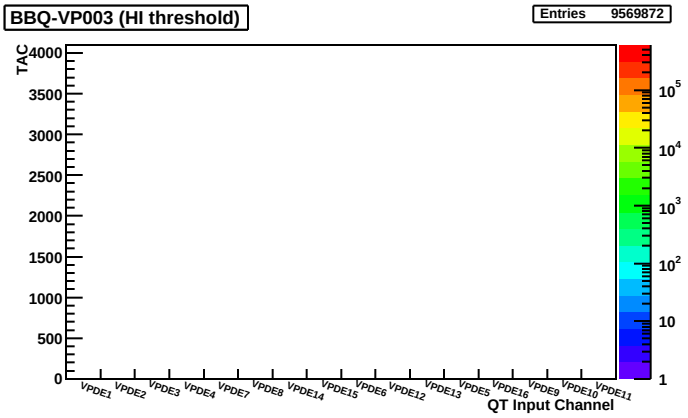

BBQ-VP001 (LO threshold)
Entries 9569872
ADC
QT Input Channel

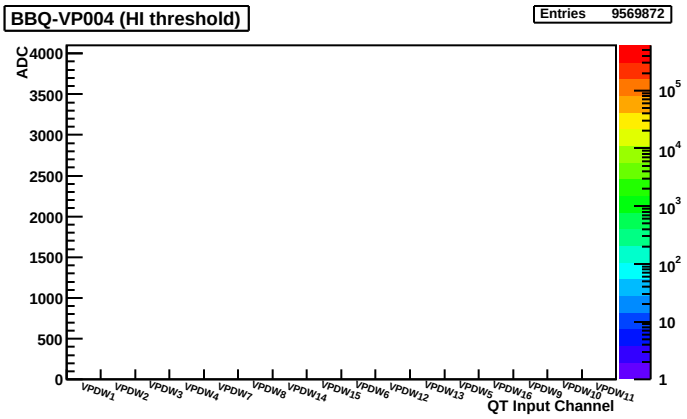

BBQ-VP001 (LO threshold)
Entries 9569872
TAC
QT Input Channel

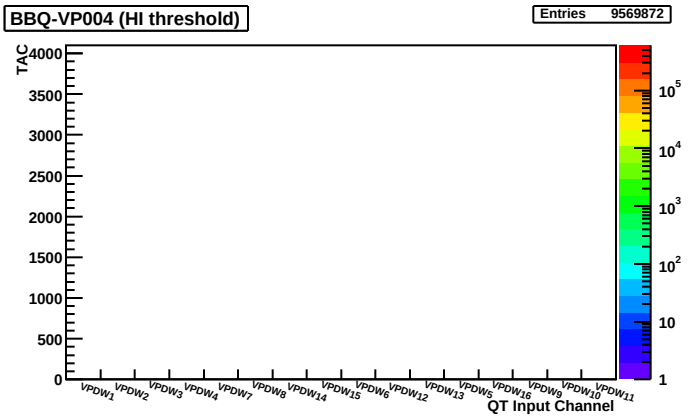

BBQ-VP002 (LO threshold)
Entries 9569872
ADC
QT Input Channel

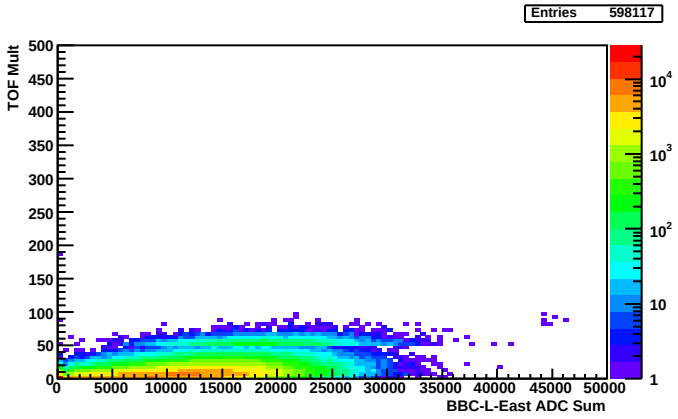

BBQ-VP002 (LO threshold)
Entries 9569872
TAC
QT Input Channel

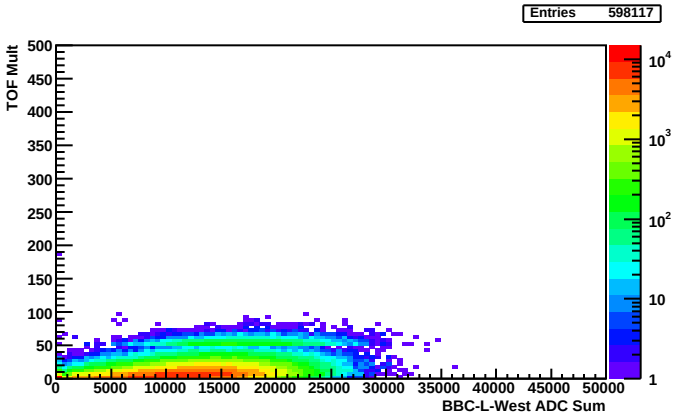

BBQ-VP003 (HI threshold)
Entries 9569872
ADC
QT Input Channel

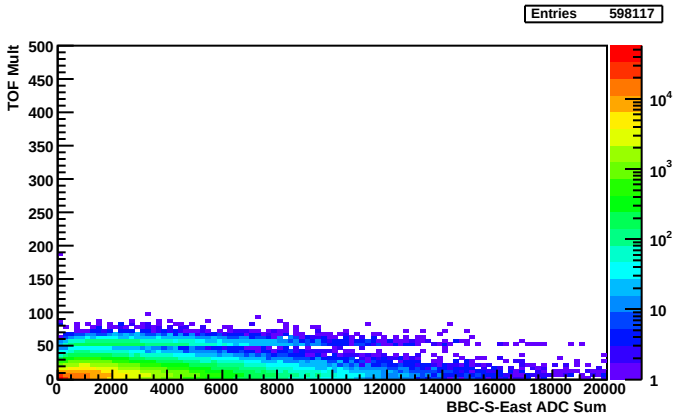

BBQ-VP003 (HI threshold)
Entries 9569872
TAC
QT Input Channel

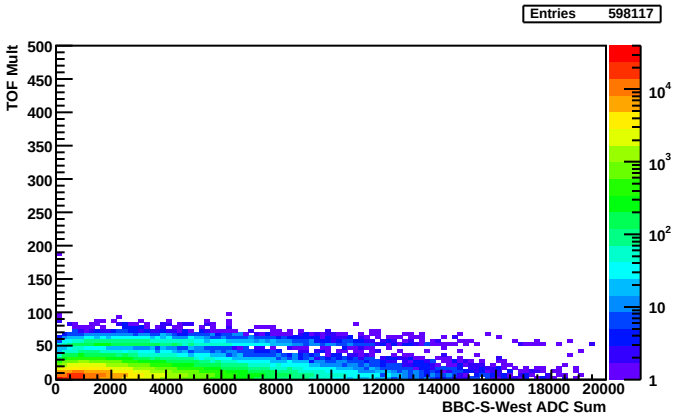

BBQ-VP004 (HI threshold)
Entries 9569872
ADC
QT Input Channel

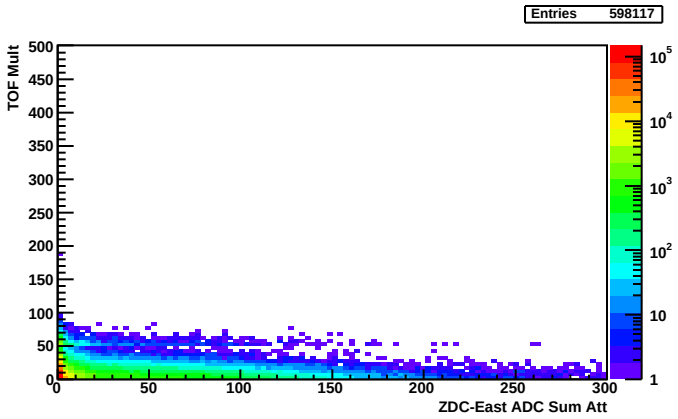

BBQ-VP004 (HI threshold)
Entries 9569872
TAC
QT Input Channel

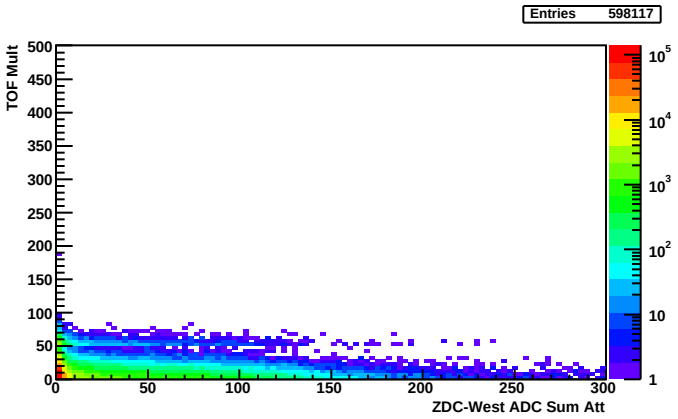

Entries 598117
TOF Mult
BBC-L-East ADC Sum

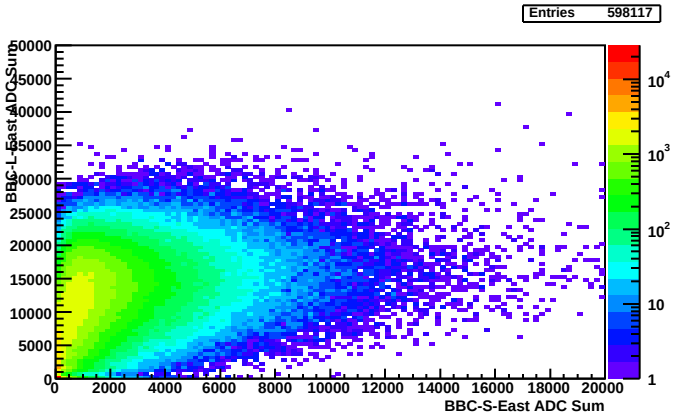

Entries 598117
TOF Mult
BBC-L-West ADC Sum

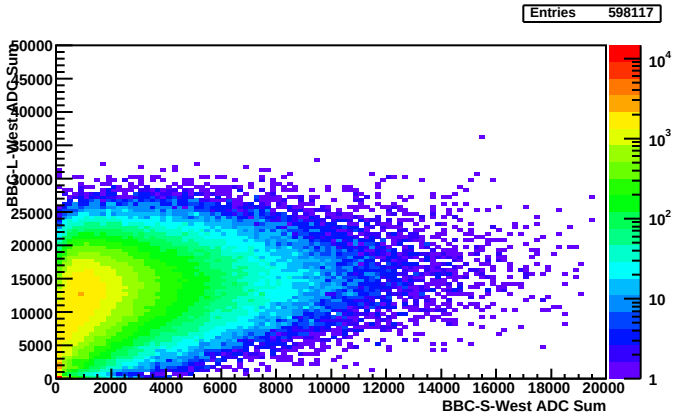

Entries 598117
TOF Mult
BBC-S-East ADC Sum

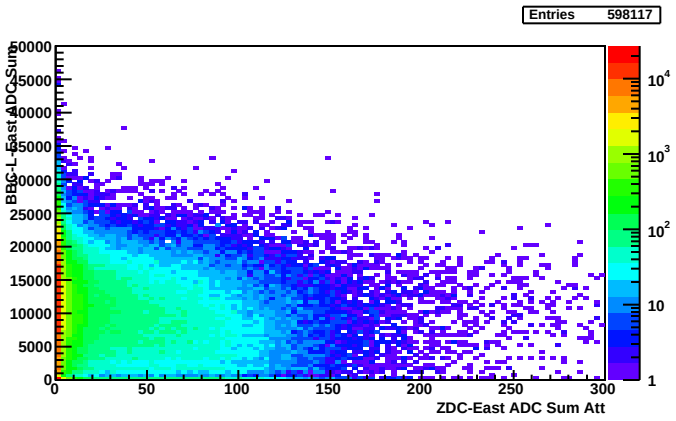

Entries 598117
TOF Mult
BBC-S-West ADC Sum

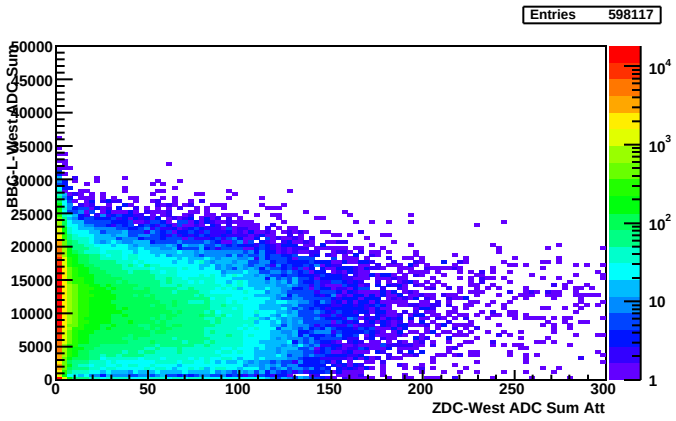

Entries 598117
TOF Mult
ZDC-East ADC Sum Att

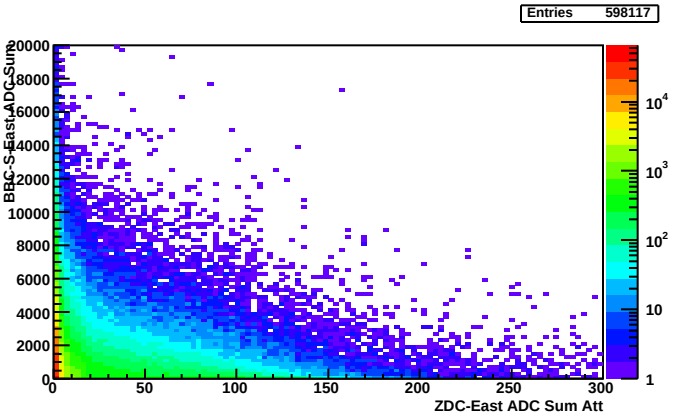

Entries 598117
TOF Mult
ZDC-West ADC Sum Att

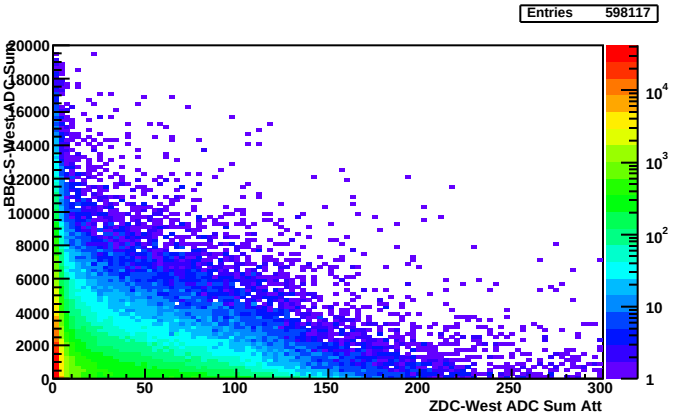

Entries 598117
BBC-L-East ADC Sum
BBC-S-East ADC Sum

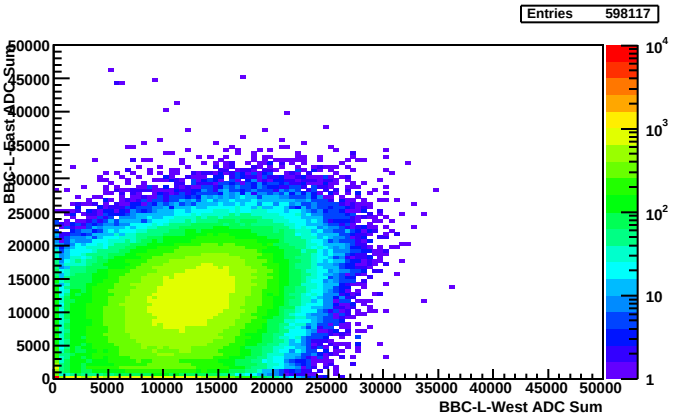

Entries 598117
BBC-L-West ADC Sum
BBC-S-West ADC Sum

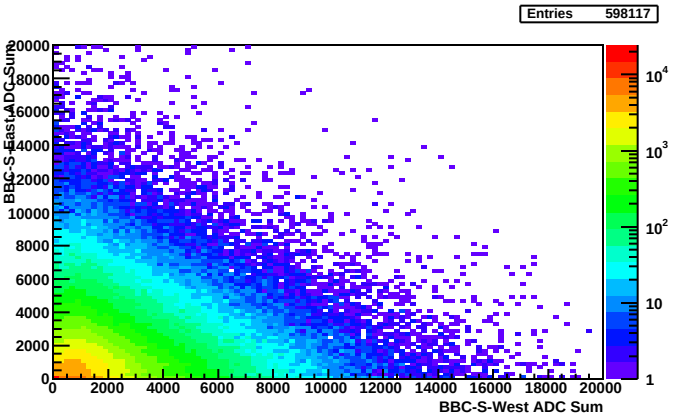

Entries 598117
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

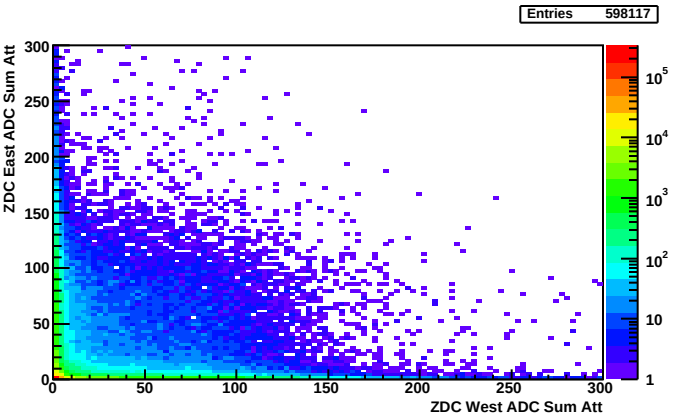

Entries 598117
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

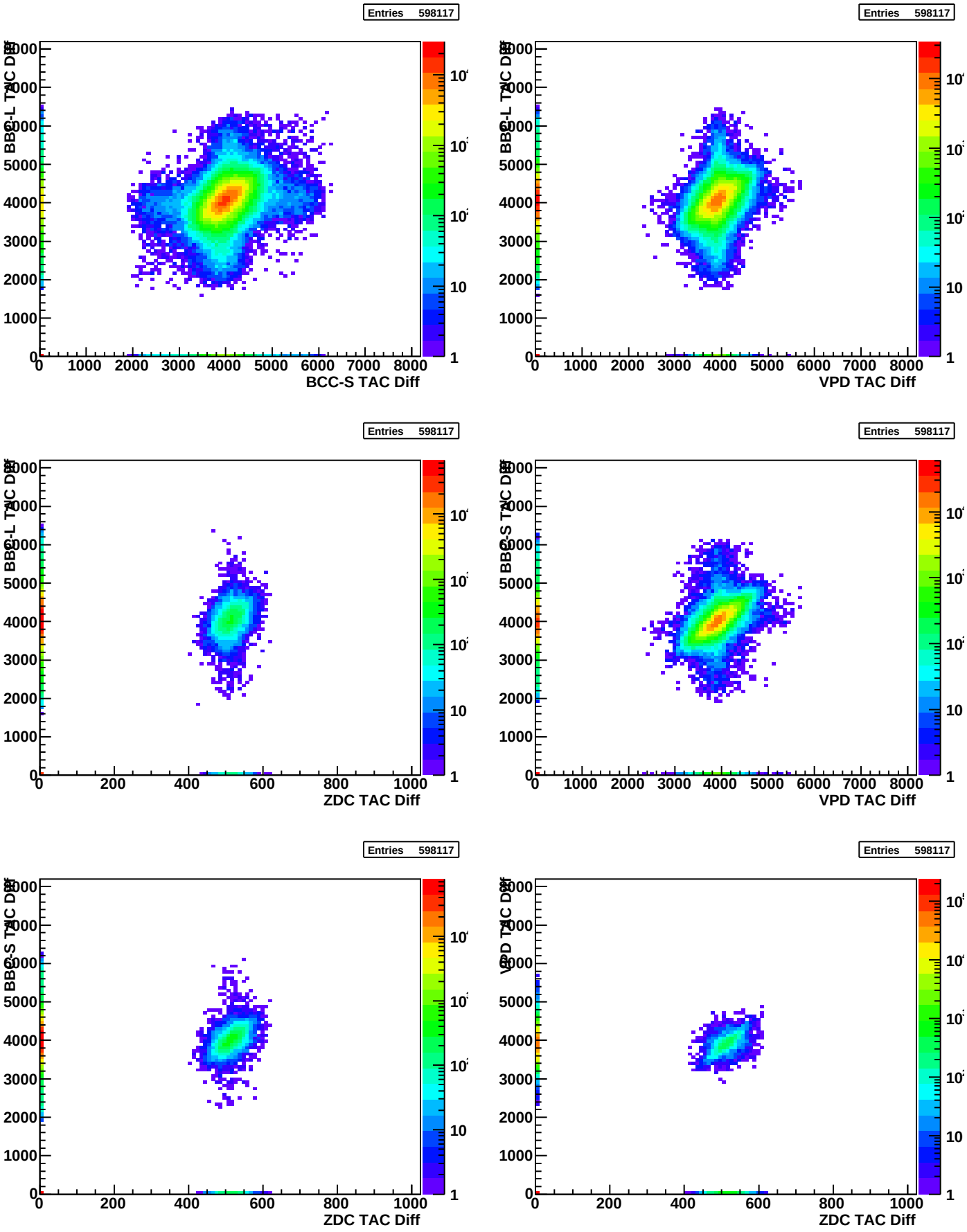

Entries 598117
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

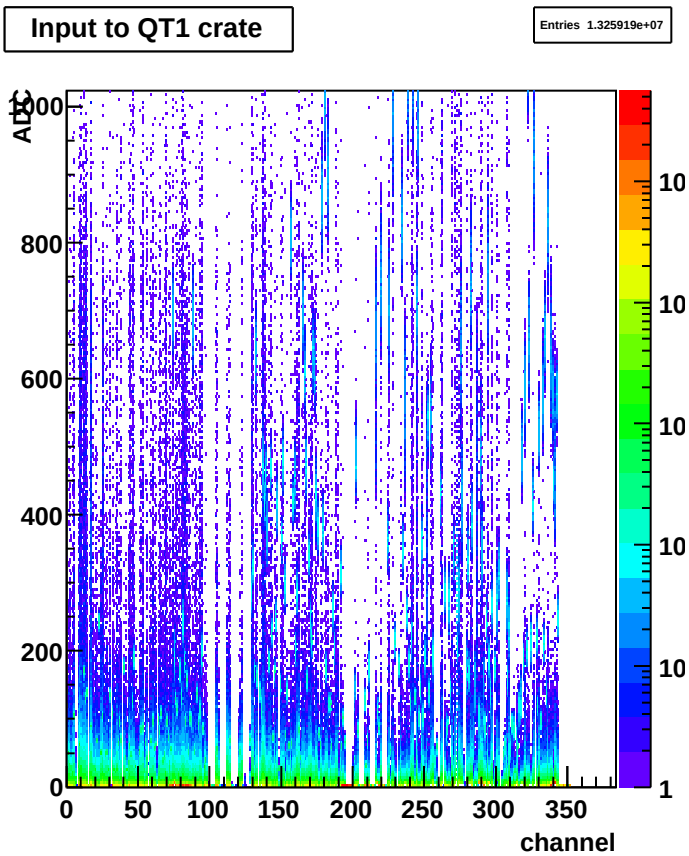

Entries 598117
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

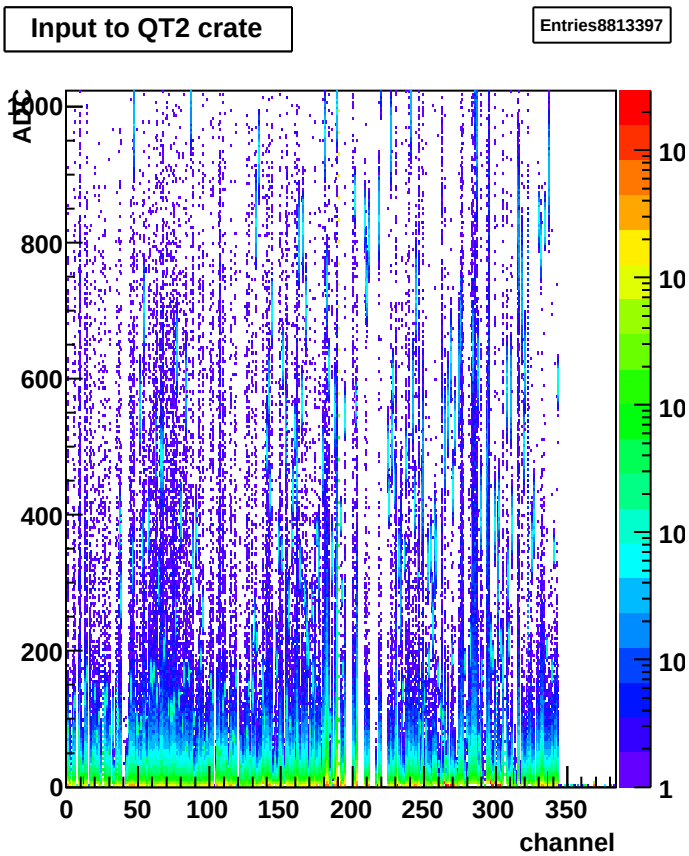

Entries 598117
BBC-L-East ADC Sum
BBC-L-West ADC Sum

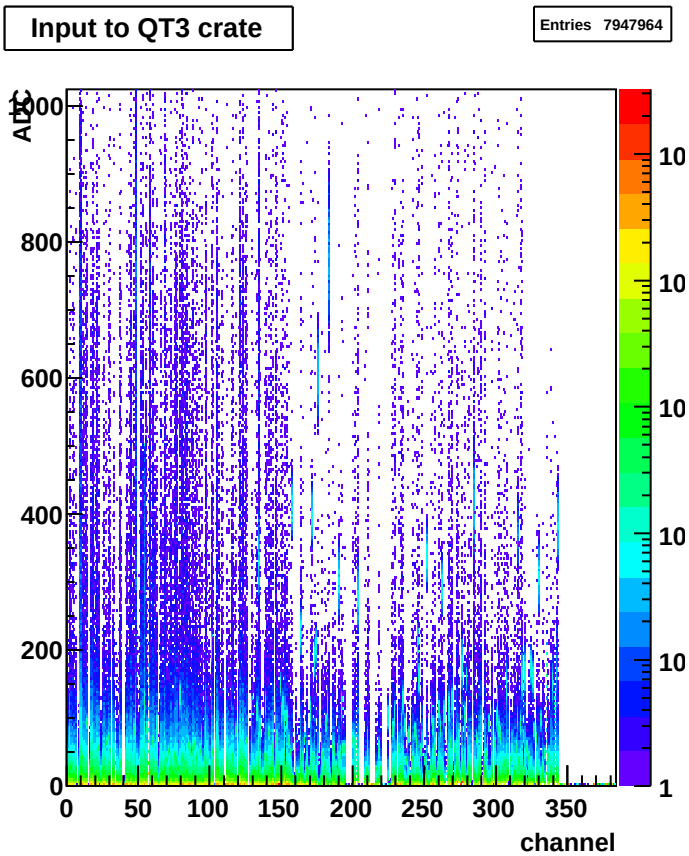

Entries 598117
BBC-S-East ADC Sum
BBC-S-West ADC Sum

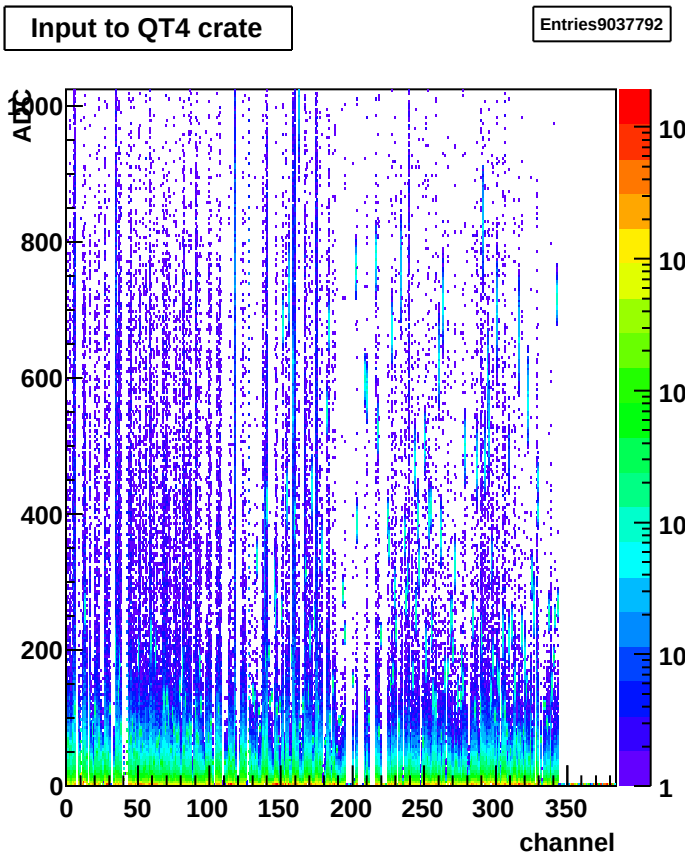

Entries 598117
ZDC East ADC Sum Att
ZDC West ADC Sum Att

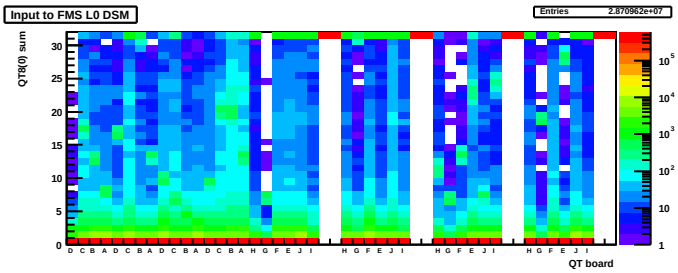

Entries 598117
BBC-L TAC Diff
BCC-S TAC Diff
Entries 598117
BBC-L TAC Diff
VPD TAC Diff
Entries 598117
BBC-L TAC Diff
ZDC TAC Diff
Entries 598117
BBC-S TAC Diff
VPD TAC Diff
Entries 598117
BBC-S TAC Diff
ZDC TAC Diff
Entries 598117
VPD TAC Diff
ZDC TAC Diff

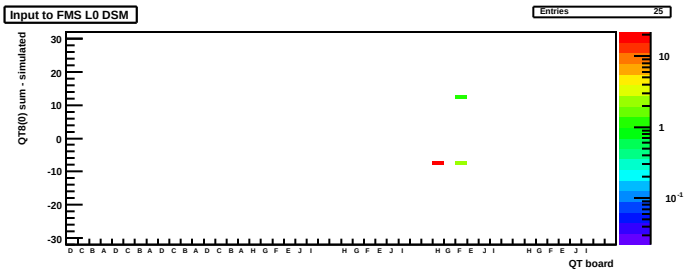

Input to QT1 crate
Entries 1.325919e+07
ADC
channel

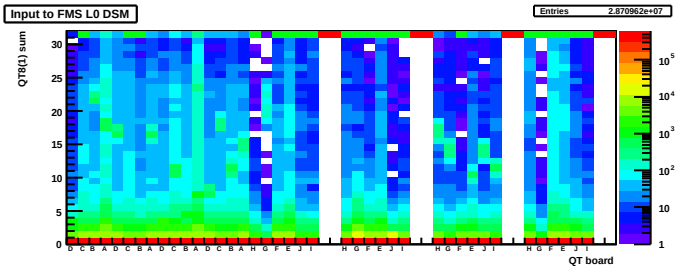

Input to QT2 crate
Entries8813397
ADC
channel

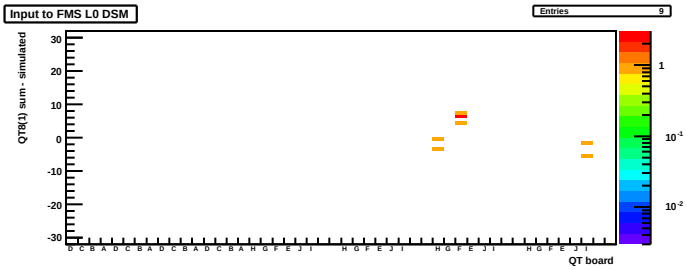

Input to QT3 crate
Entries 7947964
ADC
channel

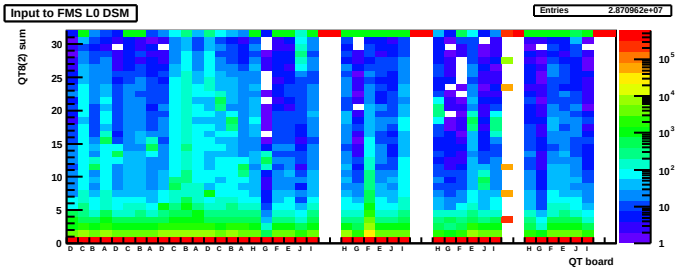

Input to QT4 crate
Entries9037792
ADC
channel

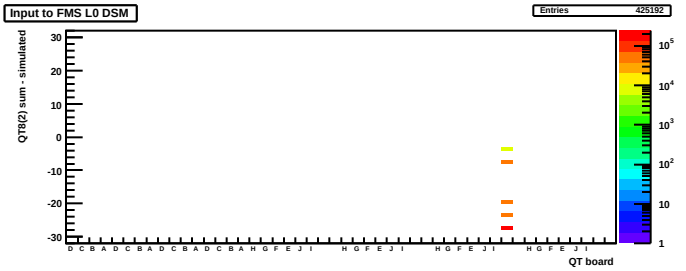

Input to FMS L0 DSM
Entries 2.870962e+07
QT8(0) sum
QT board

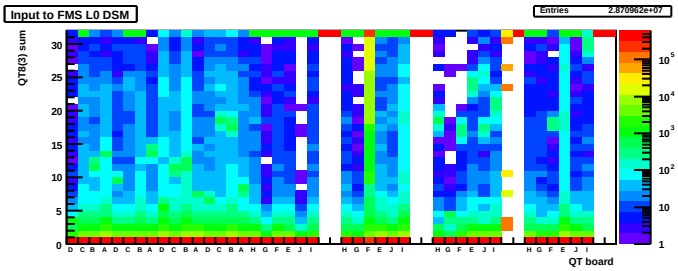

Input to FMS L0 DSM
Entries 25
QT8(0) sum - simulated
QT board

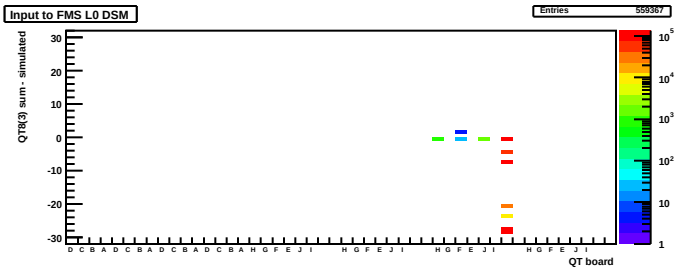

Input to FMS L0 DSM
Entries 2.870962e+07
QT8(1) sum
QT board

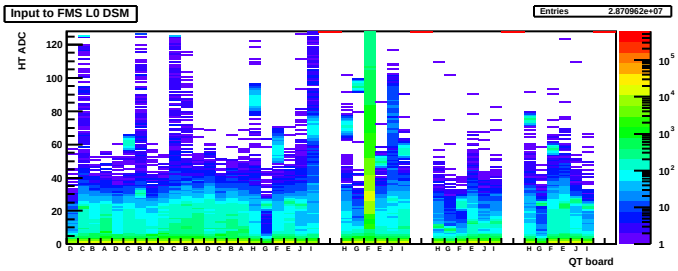

Input to FMS L0 DSM
Entries 9
QT8(1) sum - simulated
QT board

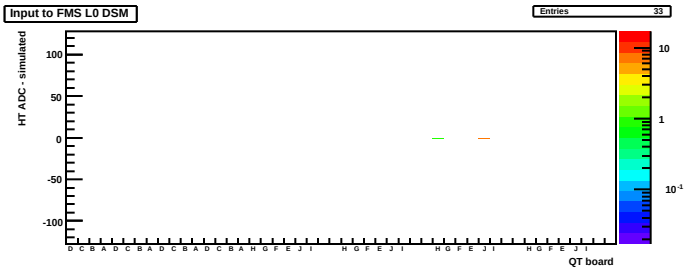

Input to FMS L0 DSM
Entries 2.870962e+07
QT8(2) sum
QT board

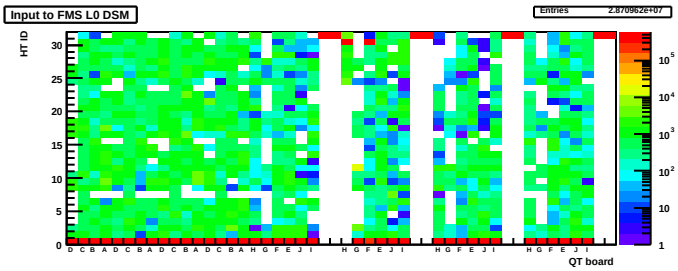

Input to FMS L0 DSM
Entries 425192
QT8(2) sum - simulated
QT board

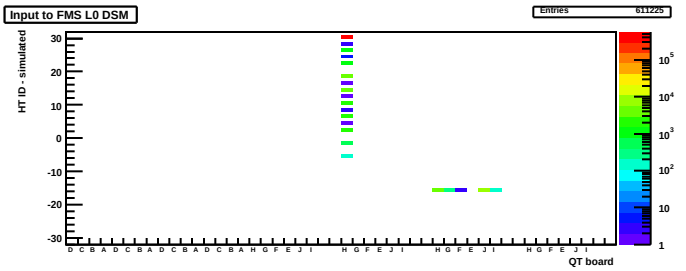

Input to FMS L0 DSM
Entries 2.870962e+07
QT8(3) sum
QT board

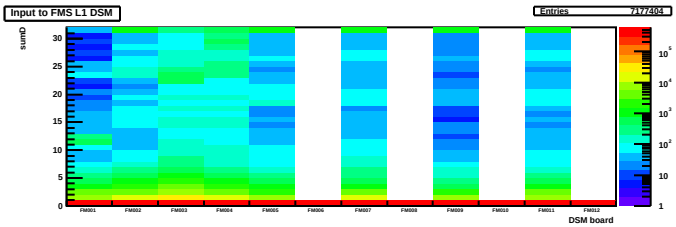

Input to FMS L0 DSM
Entries 559367
QT8(3) sum - simulated
QT board

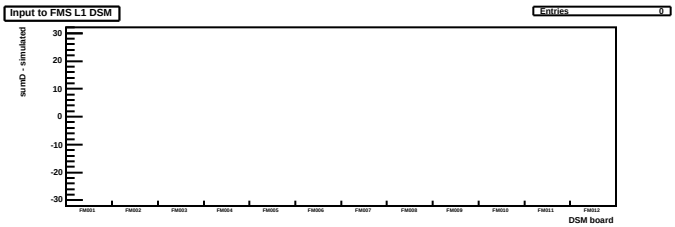

Input to FMS L0 DSM
Entries 2.870962e+07
HT ADC
QT board

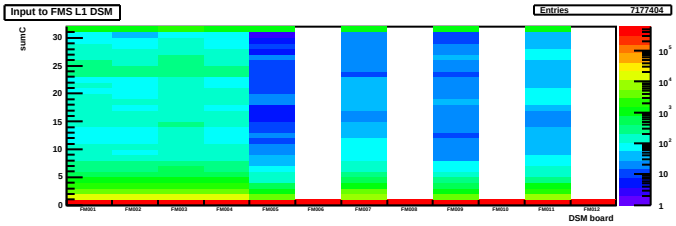

Input to FMS L0 DSM
Entries 33
HT ADC - simulated
QT board

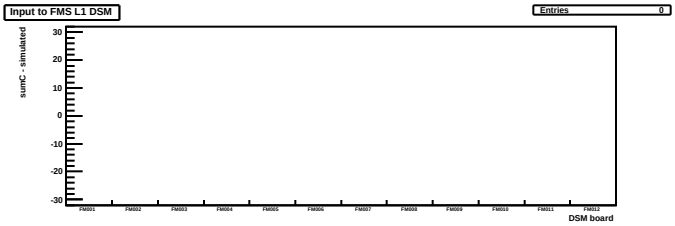

Input to FMS L0 DSM
Entries 2.870962e+07
HT ID
QT board

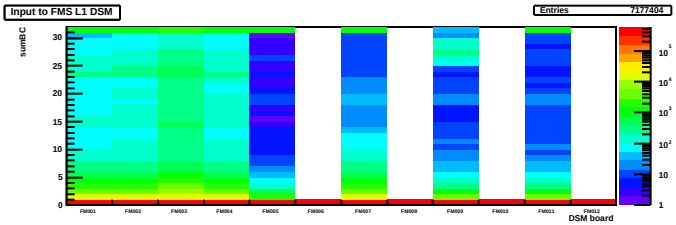

Input to FMS L0 DSM
Entries 611225
HT ID - simulated
QT board

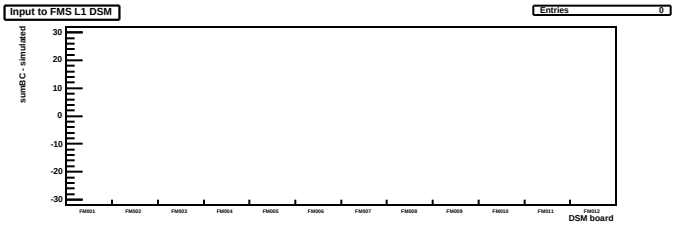

Input to FMS L1 DSM
Entries 7177404
sumD
DSM board

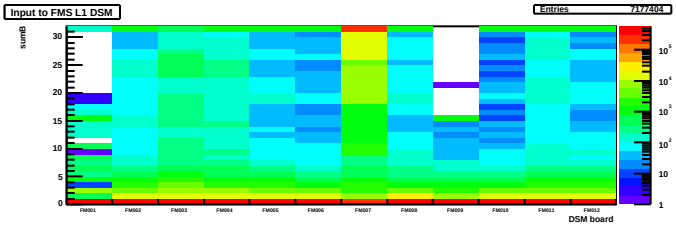

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

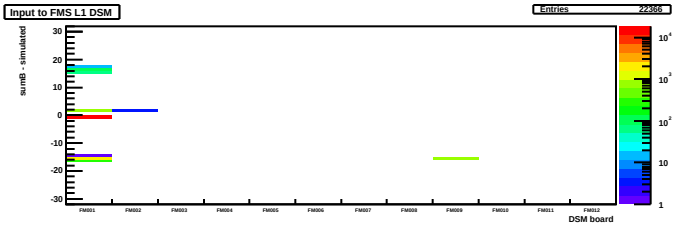

Input to FMS L1 DSM
Entries 7177404
sumC
DSM board

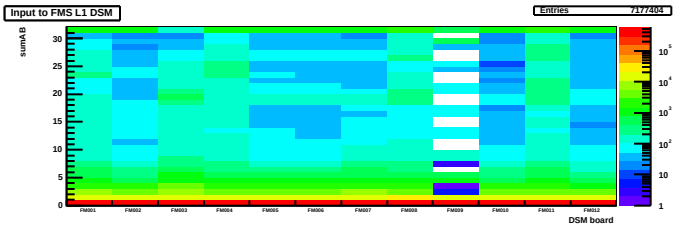

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

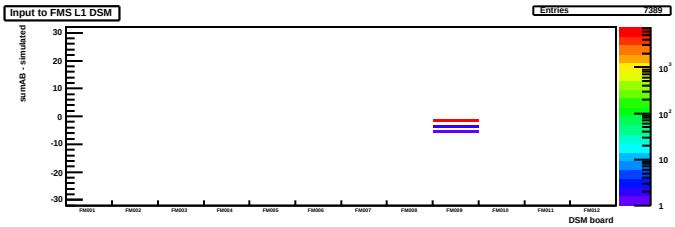

Input to FMS L1 DSM
Entries 7177404
sumBC
DSM board

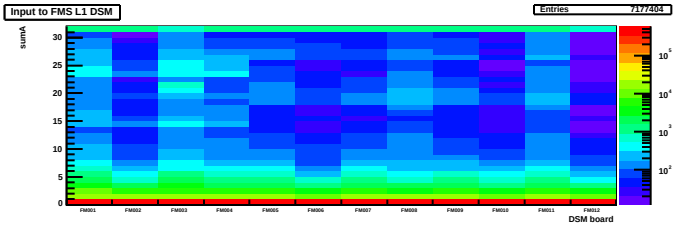

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

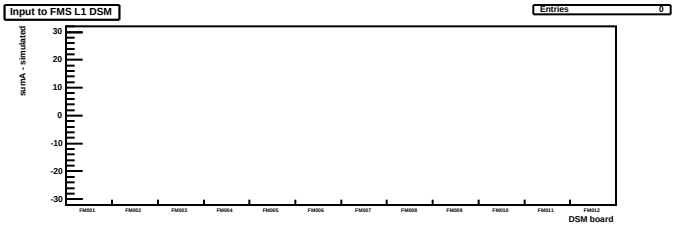

Input to FMS L1 DSM
Entries 7177404
sumB
DSM board

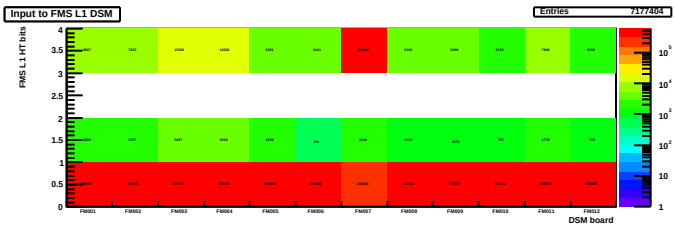

Input to FMS L1 DSM
Entries 22366
sumB - simulated
DSM board

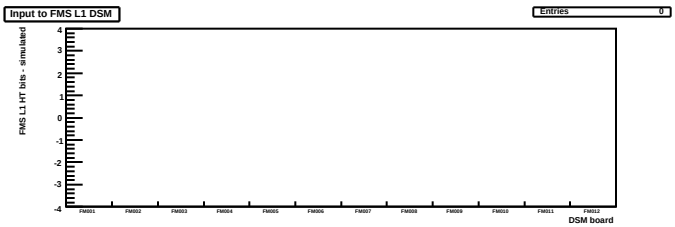

Input to FMS L1 DSM
Entries 7177404
sumAB
DSM board

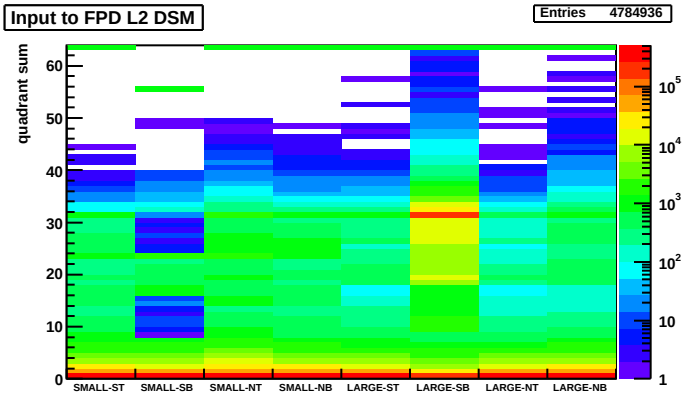

Input to FMS L1 DSM
Entries 7389
sumAB - simulated
DSM board

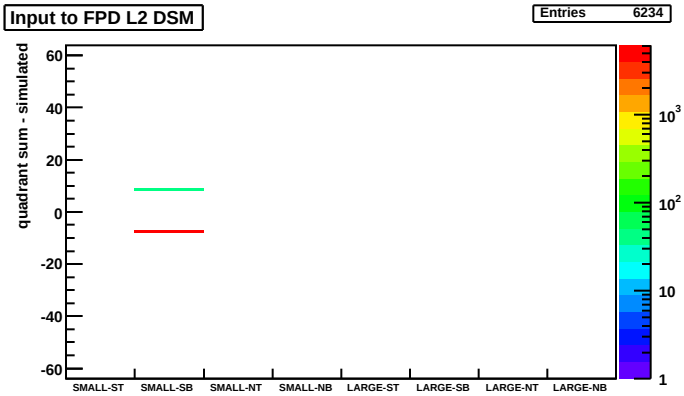

Input to FMS L1 DSM
Entries 7177404
sumA
DSM board

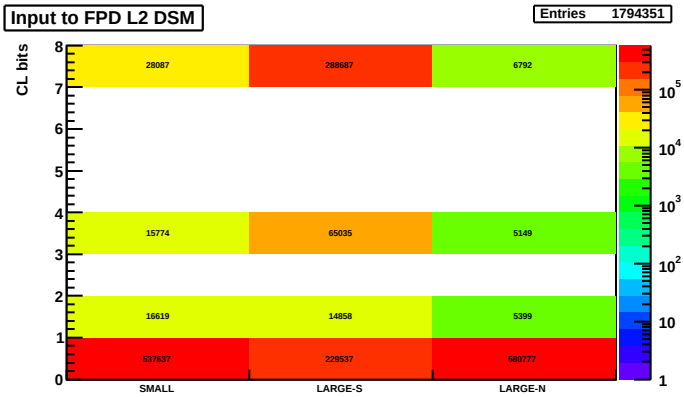

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

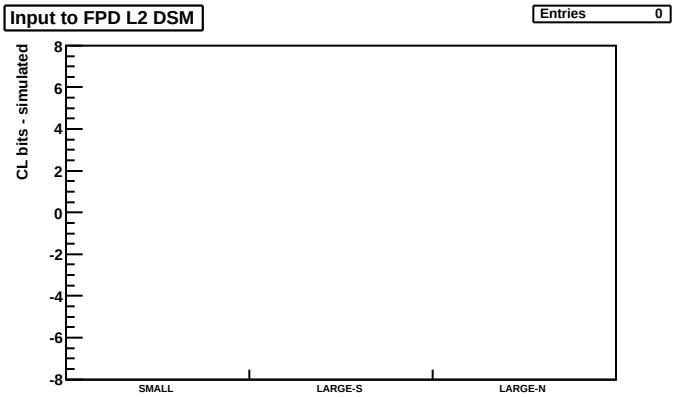

Input to FMS L1 DSM
Entries 7177404
FMS L1 HT bits
DSM board

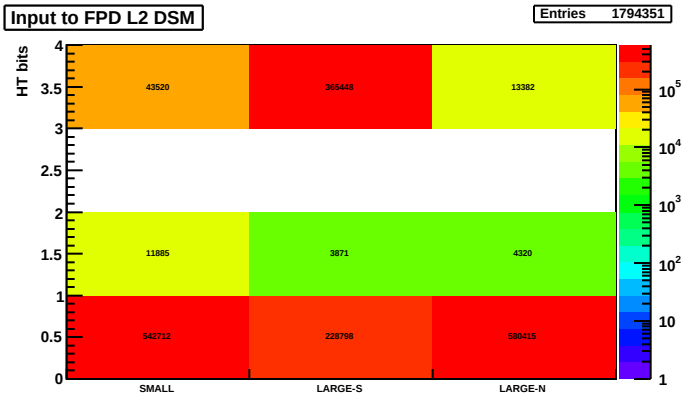

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

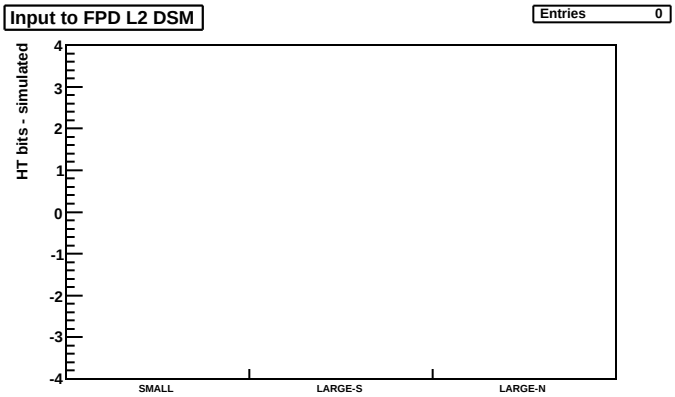

Input to FPD L2 DSM
Entries 4784936
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

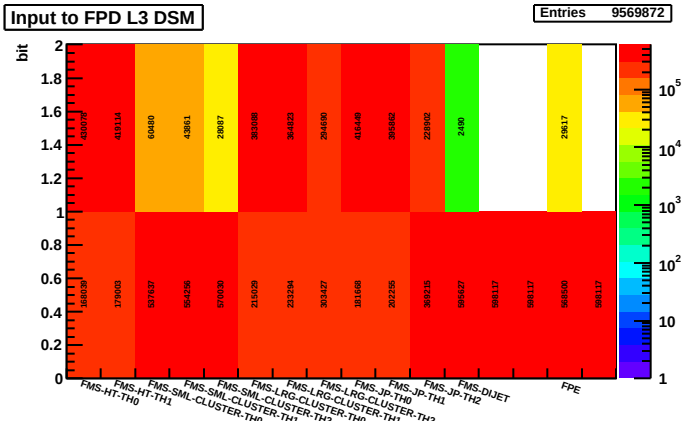

Input to FPD L2 DSM
Entries 6234
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

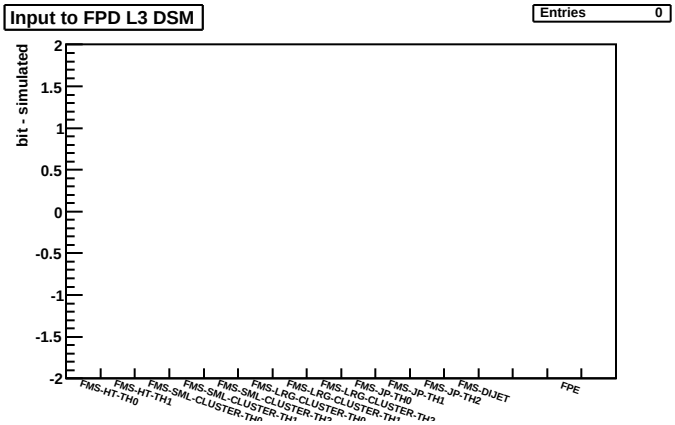

Input to FPD L2 DSM
Entries 1794351
CL bits
28087
288687
6792
15774
65035
5149
16619
14858
5399
537637
229537
580777
SMALL
LARGE-S
LARGE-N

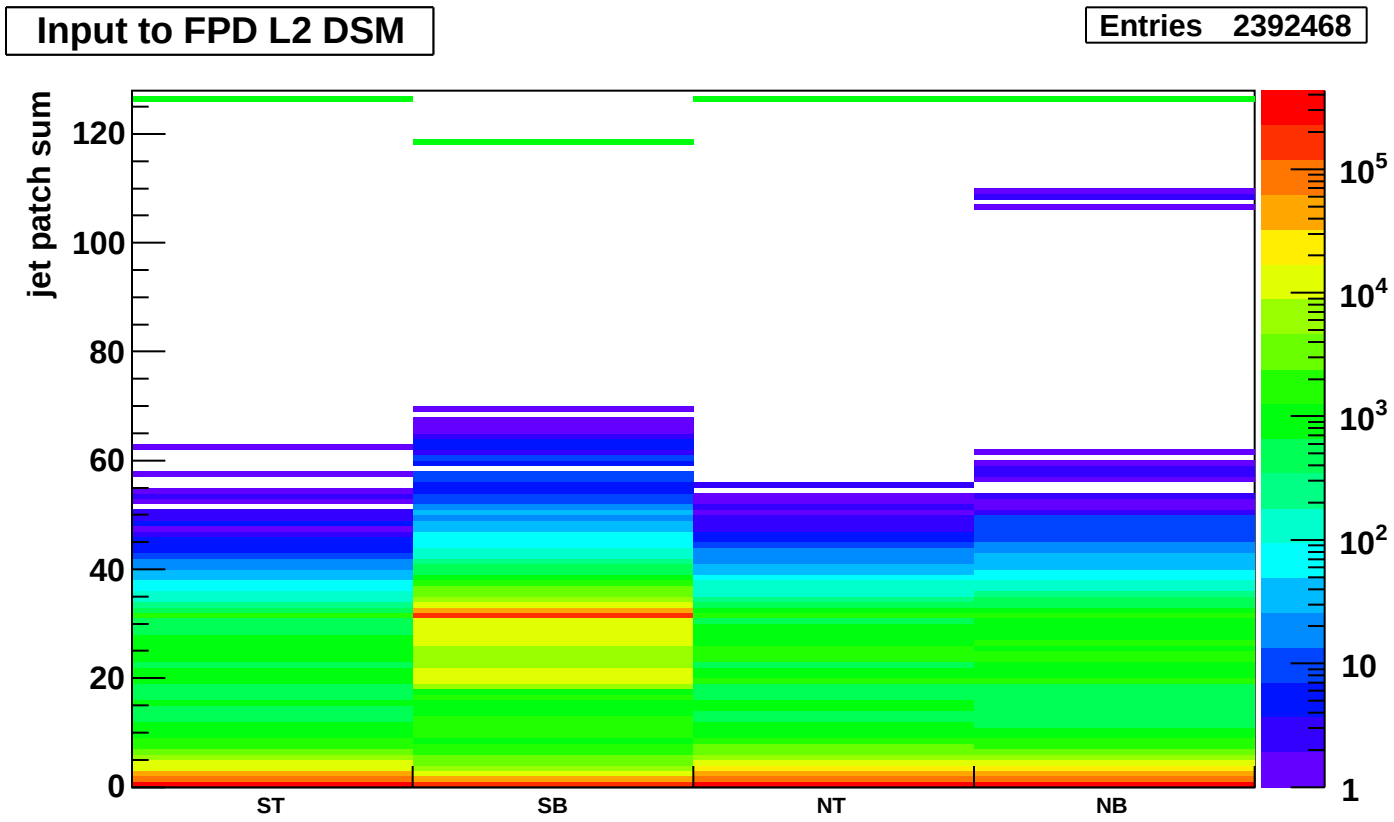

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

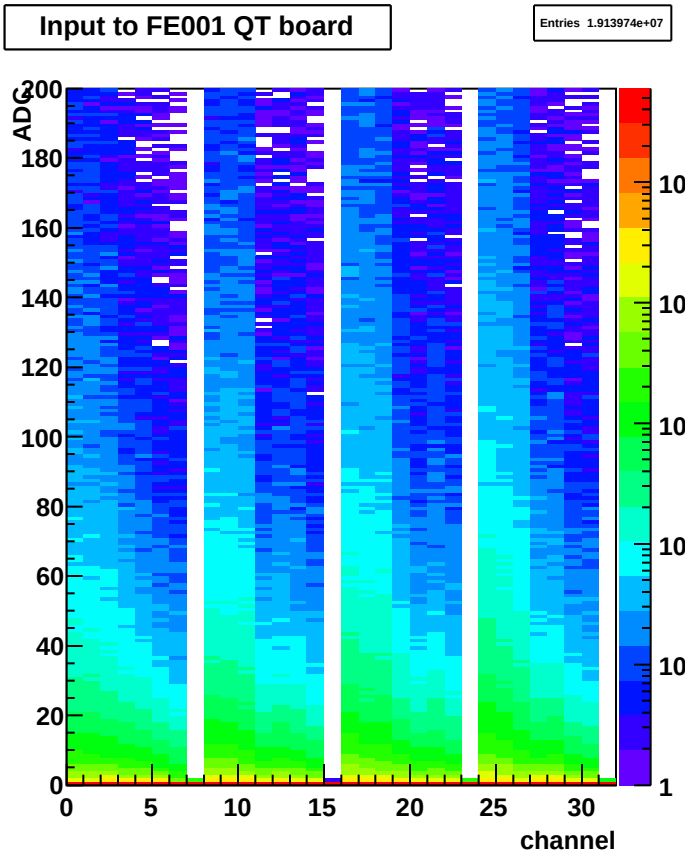

Input to FPD L2 DSM
Entries 1794351
HT bits
43520
365448
13382
11885
3871
4320
542712
228798
580415
SMALL
LARGE-S
LARGE-N

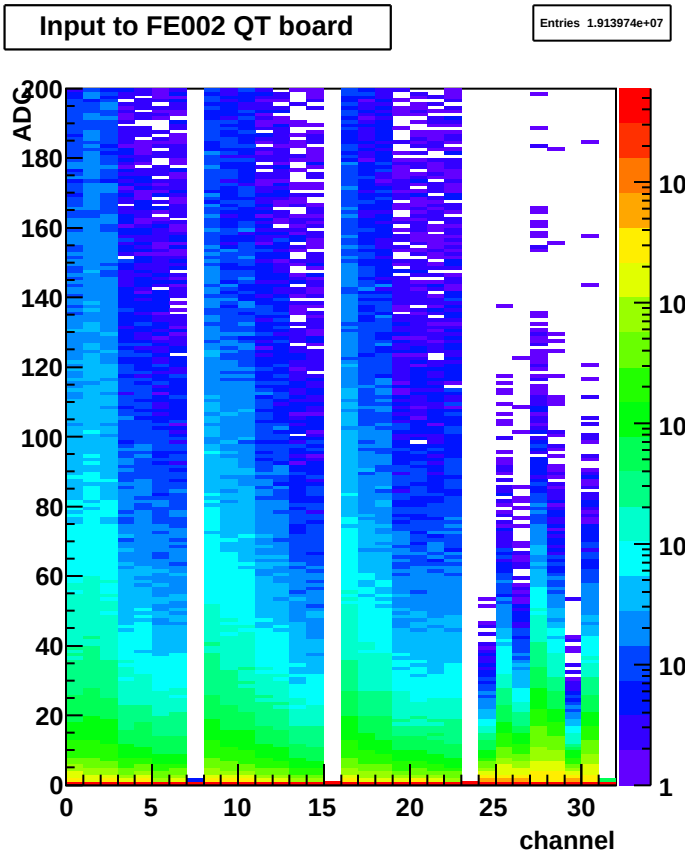

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

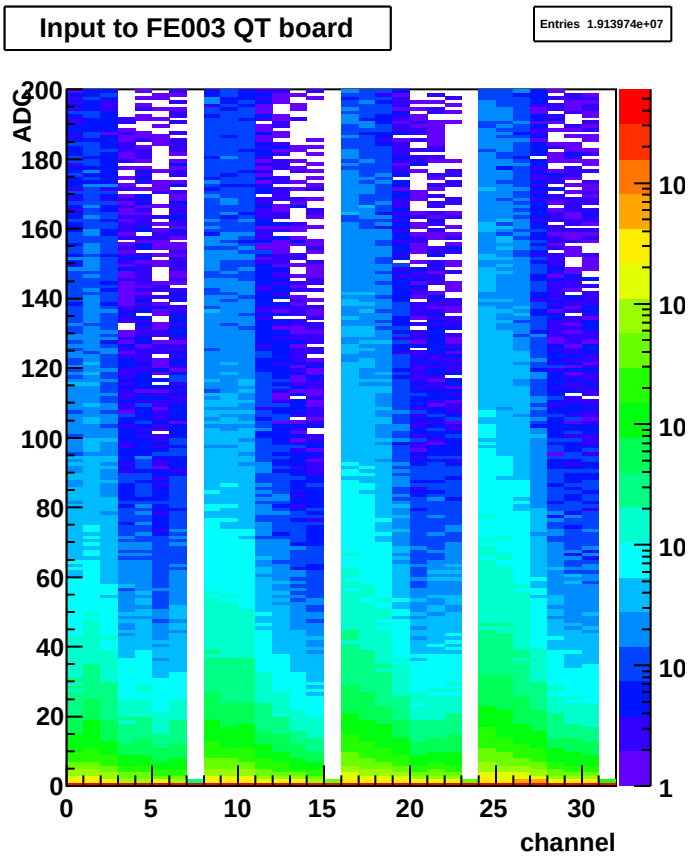

Input to FPD L3 DSM
Entries 9569872
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

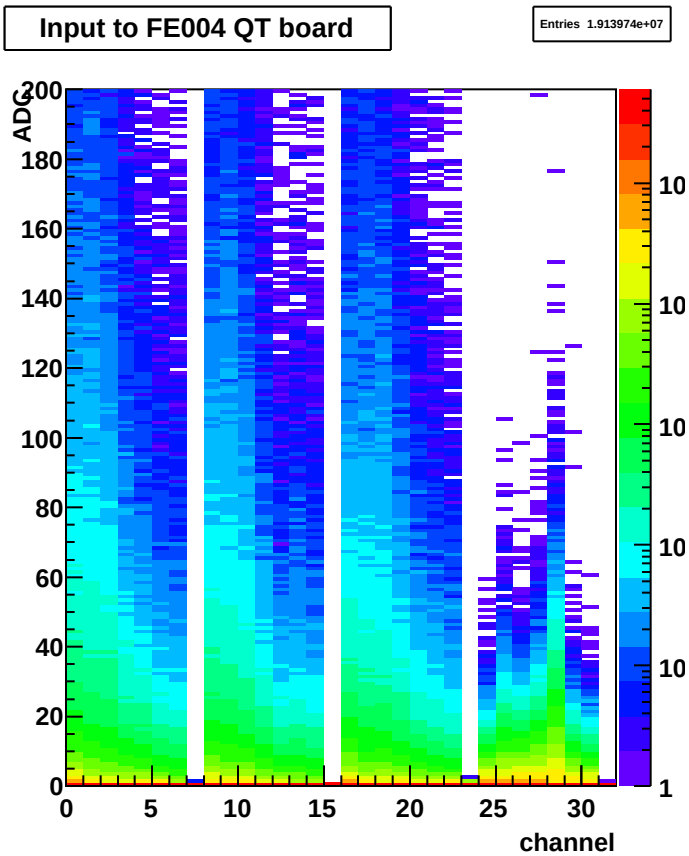

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

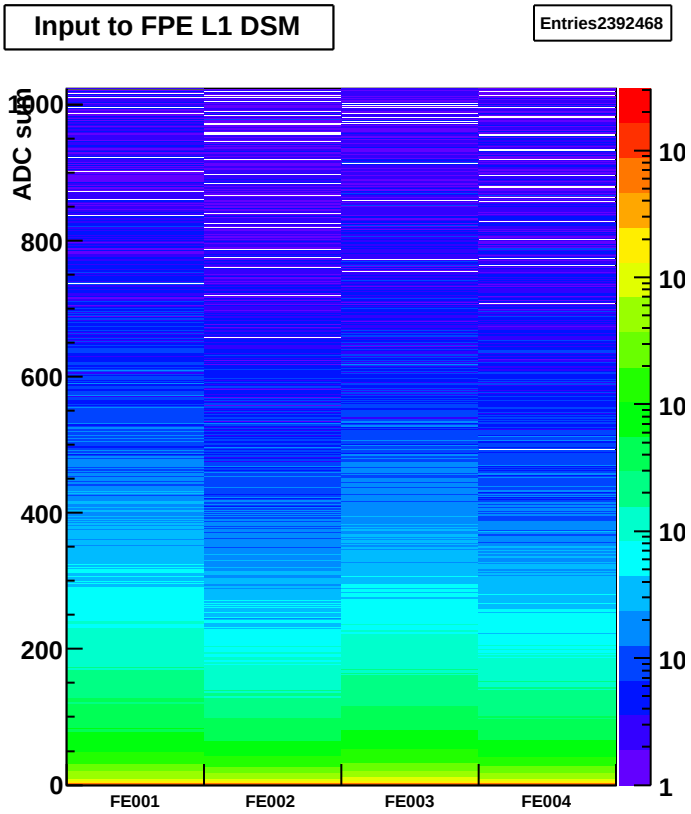

Input to FPD L2 DSM
Entries 2392468
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

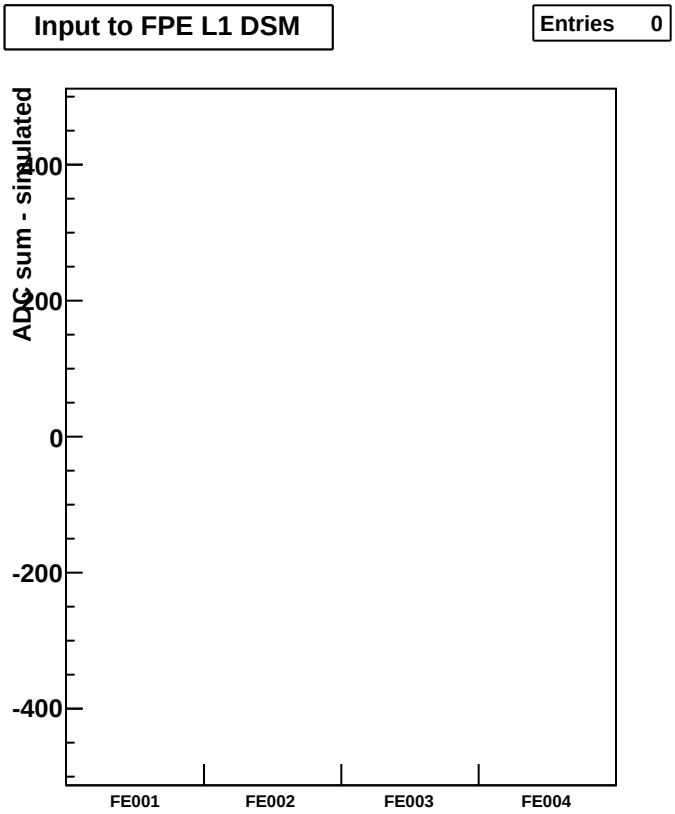

Input to FE001 QT board
Entries 1.913974e+07
ADC
channel

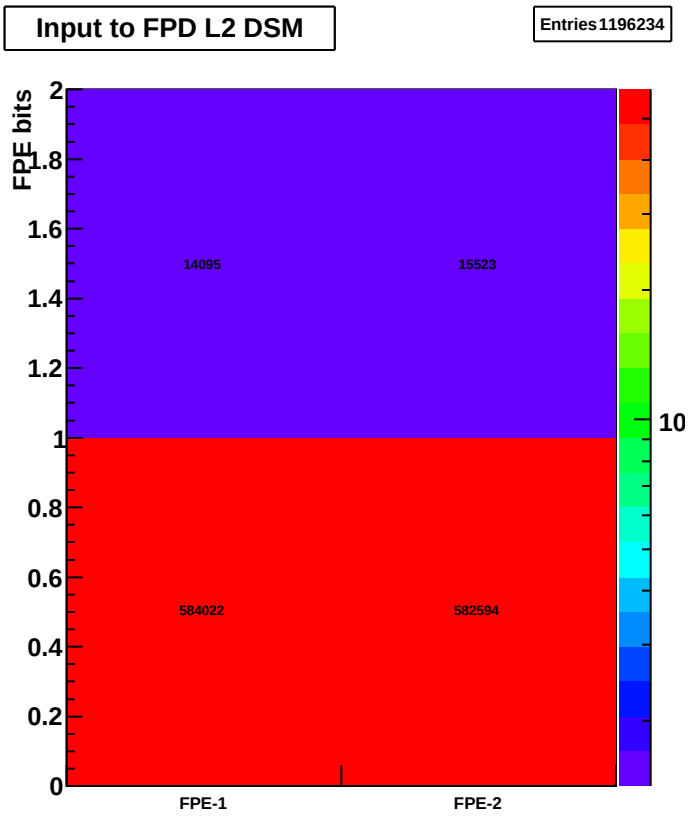

Input to FE002 QT board
Entries 1.913974e+07
ADC
channel

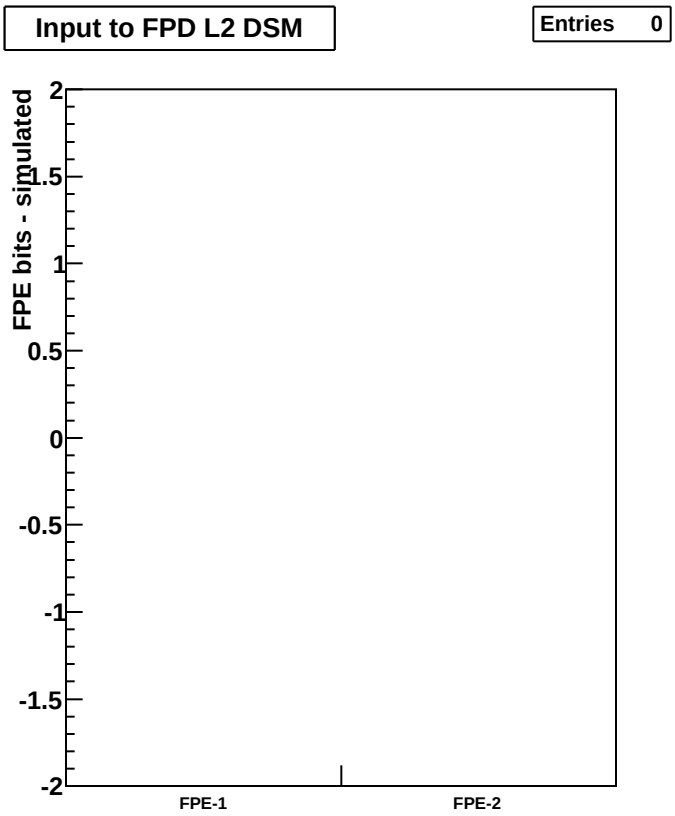

Input to FE003 QT board
Entries 1.913974e+07
ADC
channel


Input to FE004 QT board
Entries 1.913974e+07
ADC
channel


Input to FPE L1 DSM
Entries2392468
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1196234
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
14095
15523
584022
582594
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2