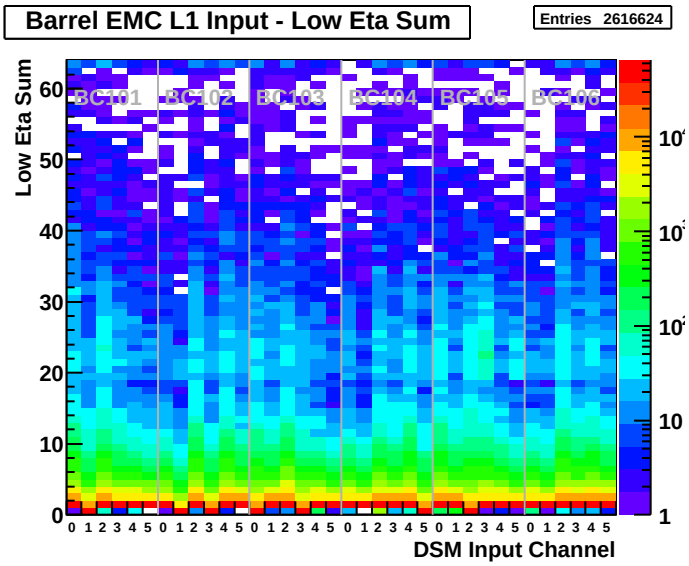
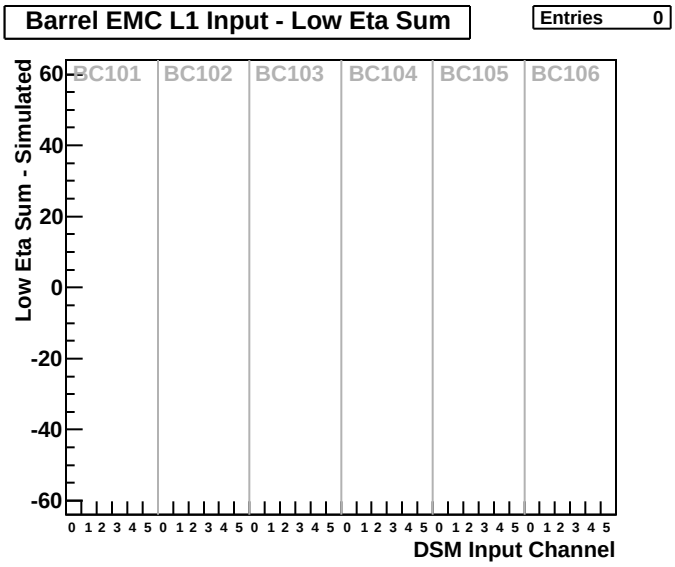
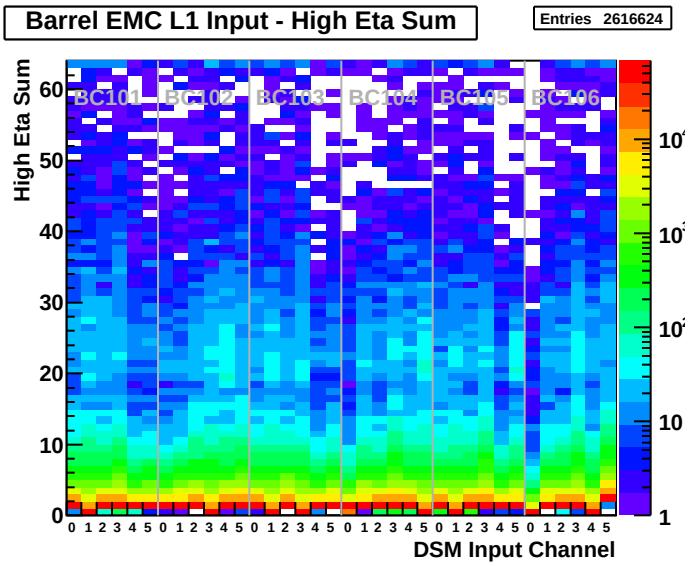
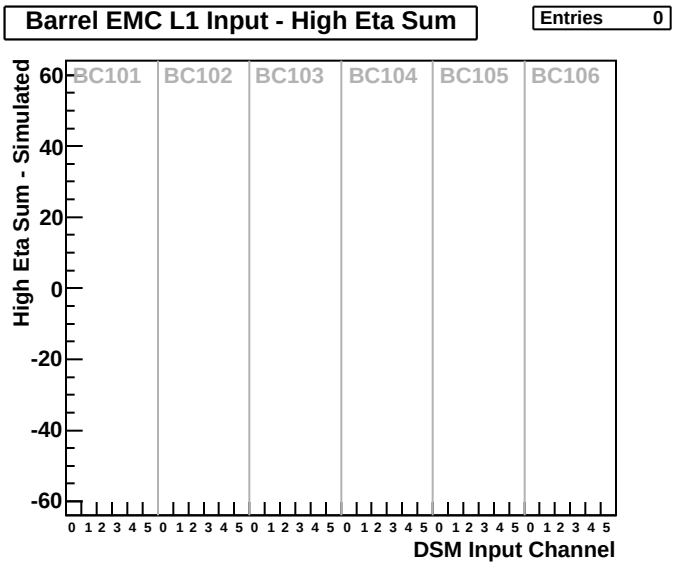
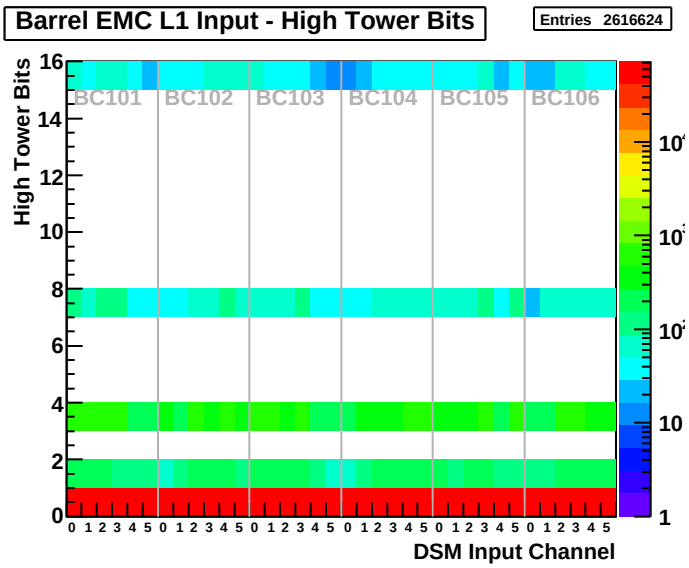
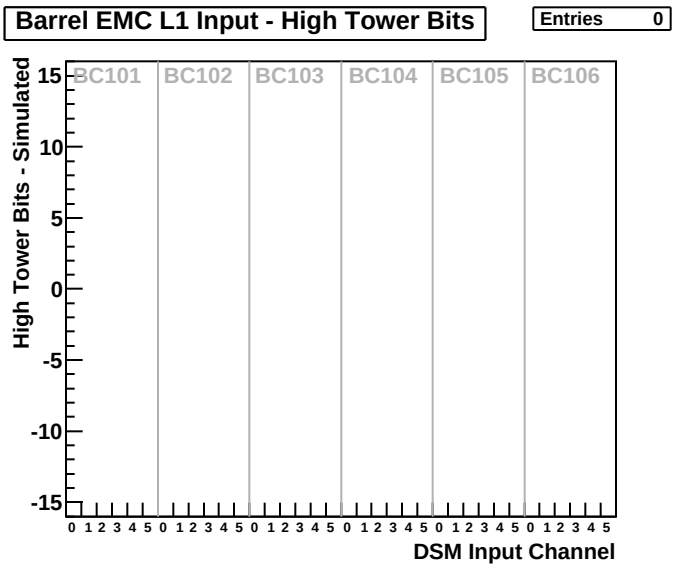

Barrel EMC L1 Input - Low Eta Sum
Entries 2616624
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

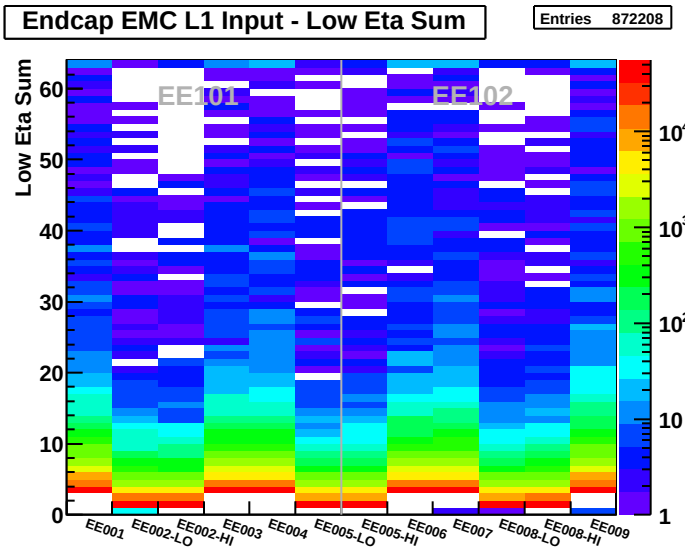

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

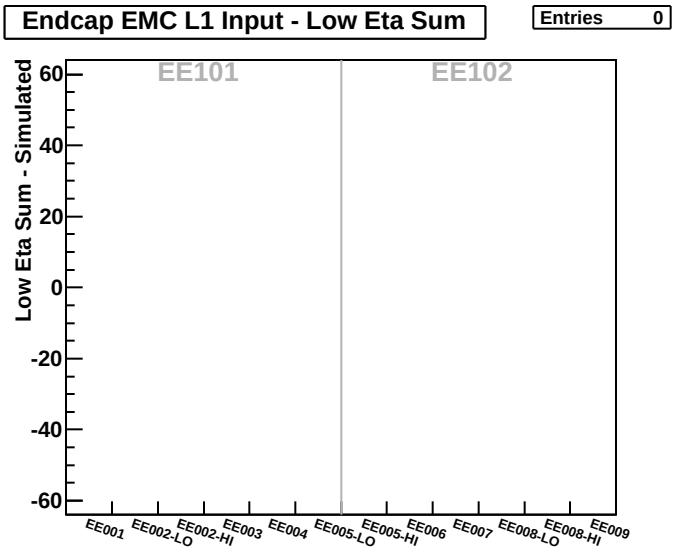

Barrel EMC L1 Input - High Eta Sum
Entries 2616624
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

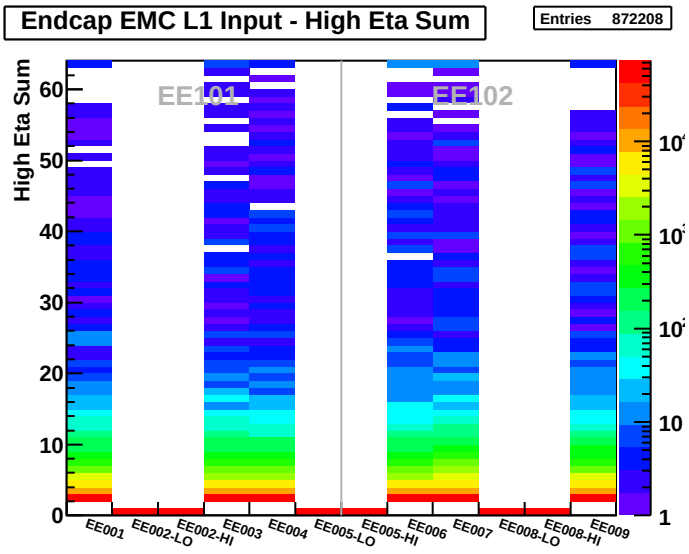

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

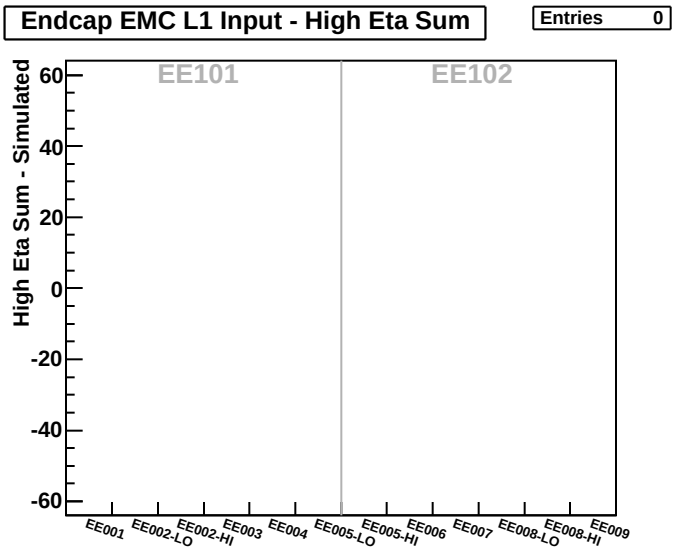

Barrel EMC L1 Input - High Tower Bits
Entries 2616624
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

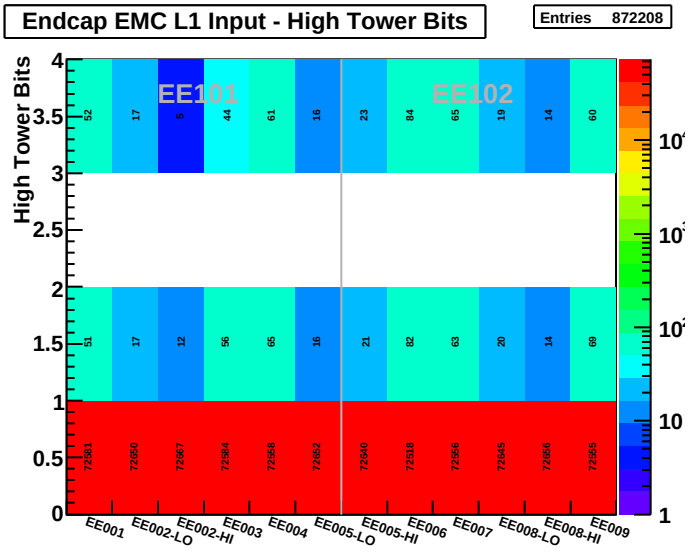

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

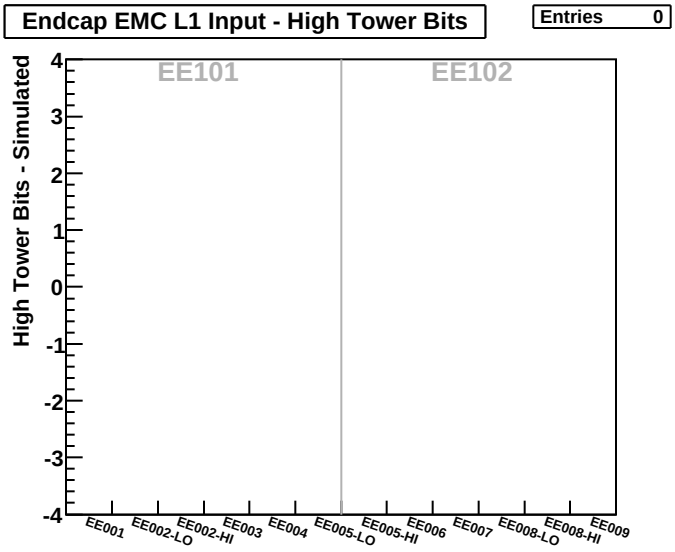

Endcap EMC L1 Input - Low Eta Sum
Entries 872208
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

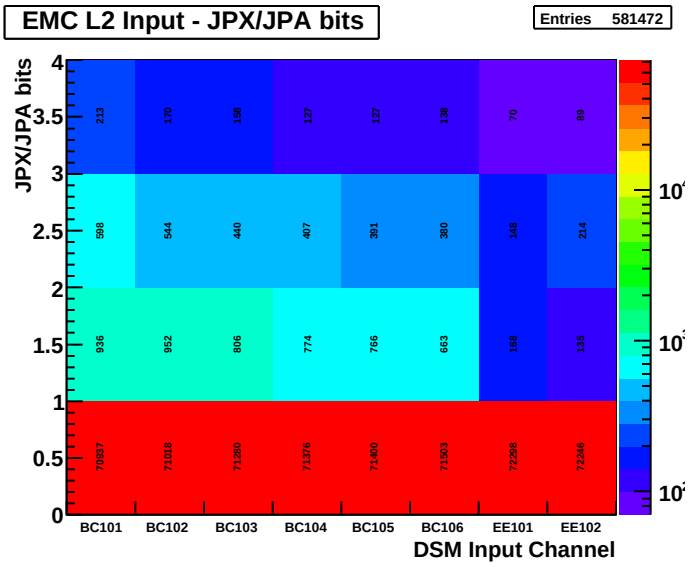

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

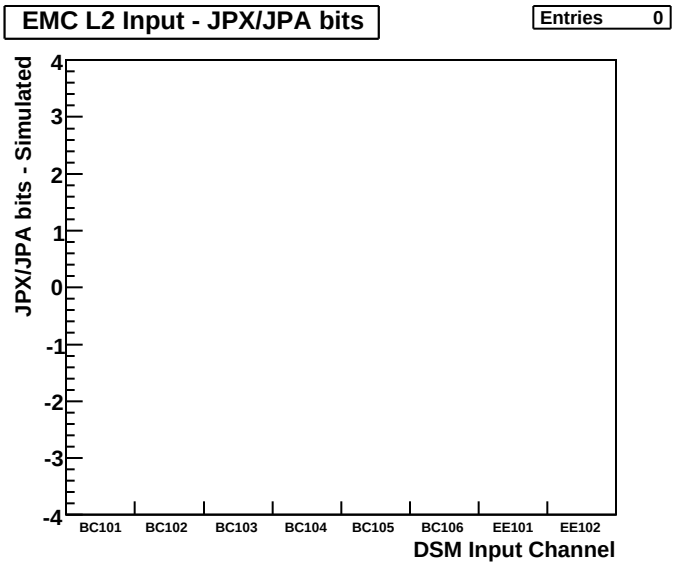

Endcap EMC L1 Input - High Eta Sum
Entries 872208
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

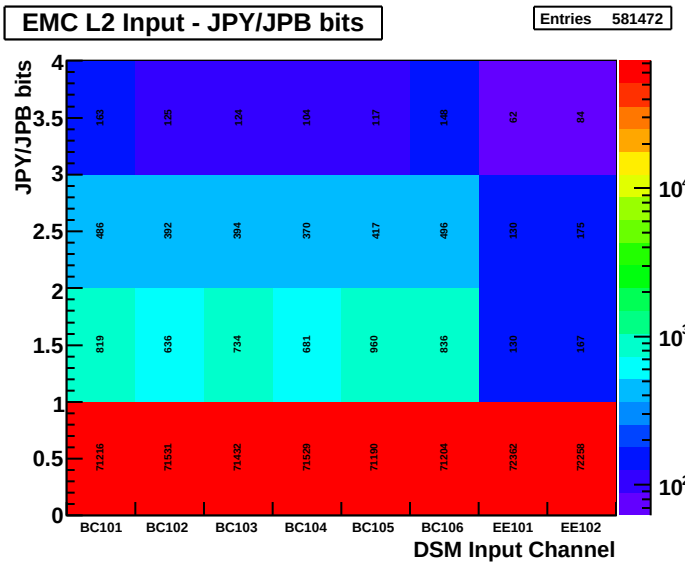

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

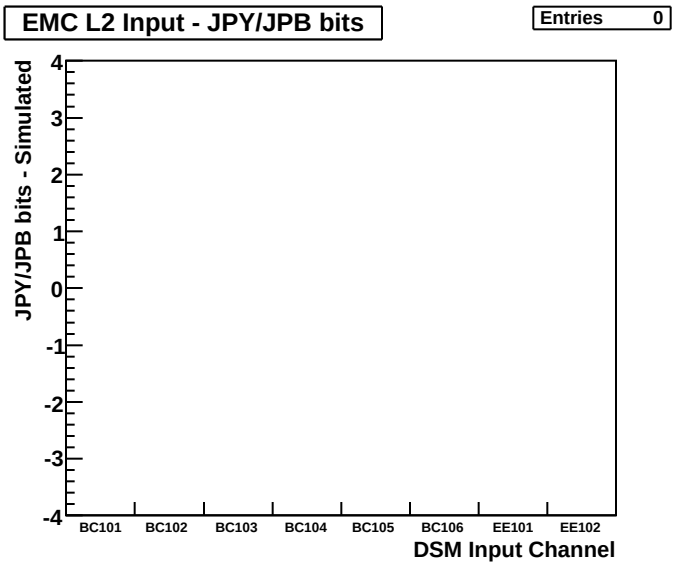

Endcap EMC L1 Input - High Tower Bits
Entries 872208
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

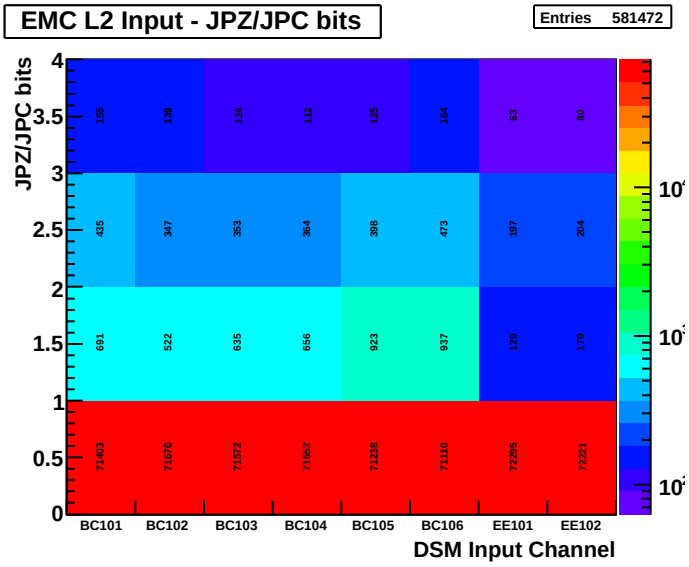

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

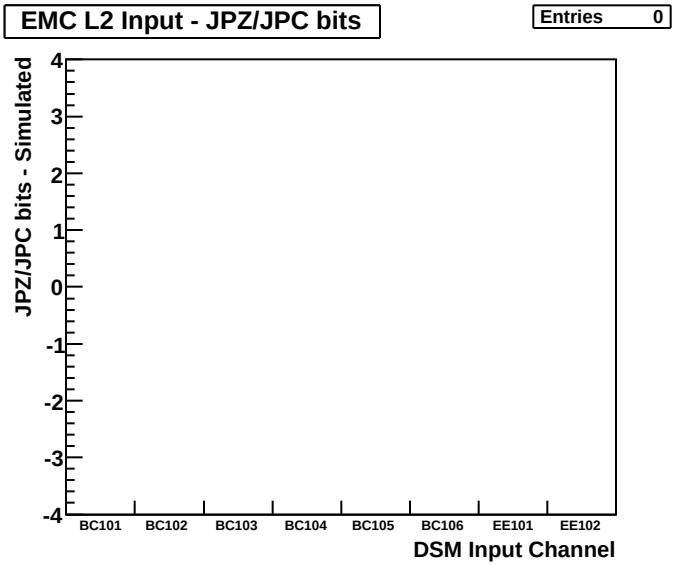

EMC L2 Input - JPX/JPA bits
Entries 581472
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
213 170 158 127 127 138 70 89
598 544 440 407 391 380 148 214
936 952 806 774 766 663 168 135
70937 71018 71280 71376 71400 71593 72298 72246

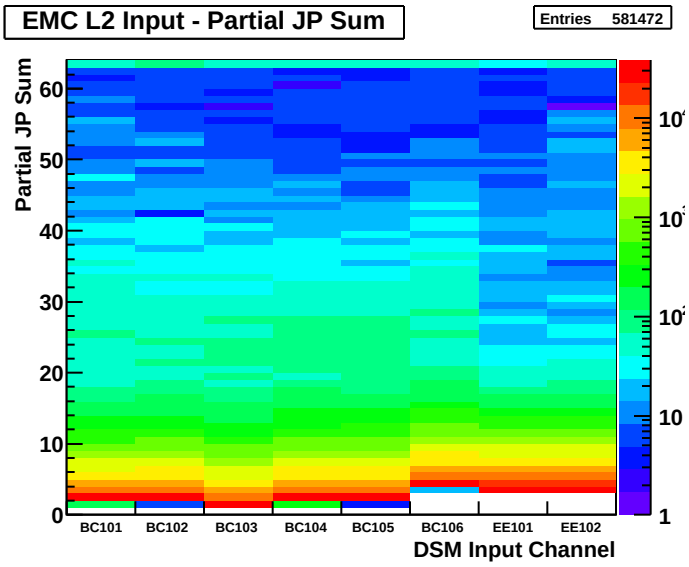

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

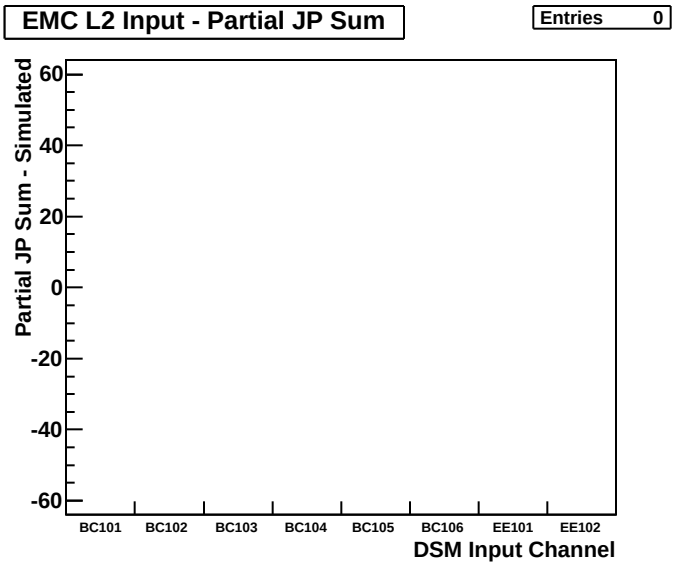

EMC L2 Input - JPY/JPB bits
Entries 581472
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
163 125 124 104 117 148 62 84
486 392 394 370 417 496 130 175
819 636 734 681 960 836 130 167
71216 71531 71432 71529 71190 71204 72362 72298

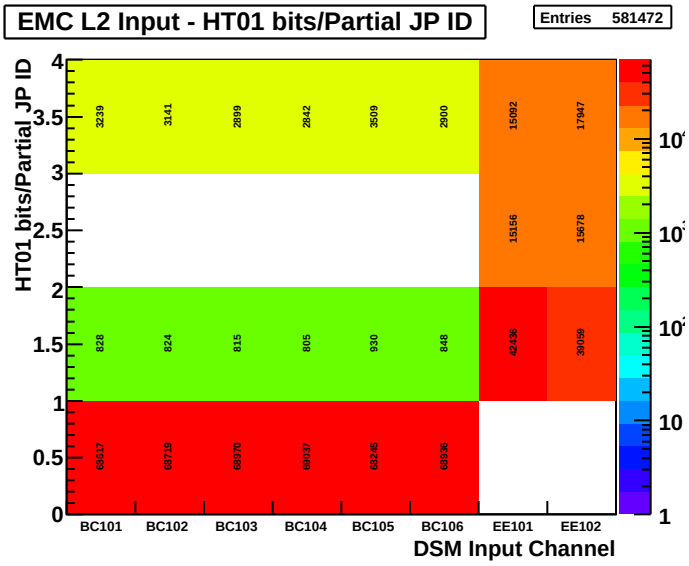

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

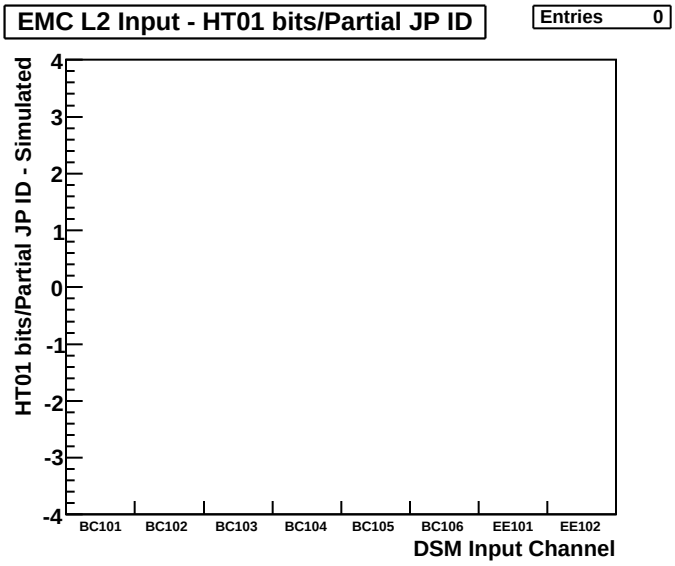

EMC L2 Input - JPZ/JPC bits
Entries 581472
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
155 139 124 112 125 164 63 80
435 347 353 364 398 473 197 204
691 522 635 656 923 937 129 179
71403 71676 71572 71552 71238 71110 72295 72221

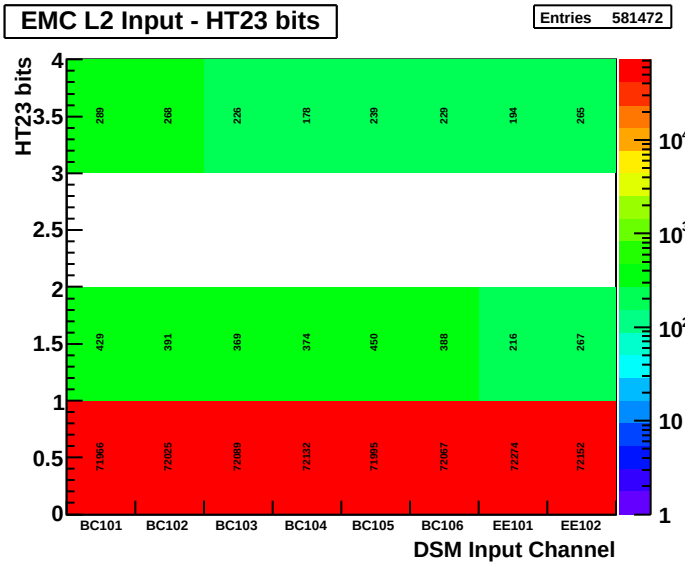

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

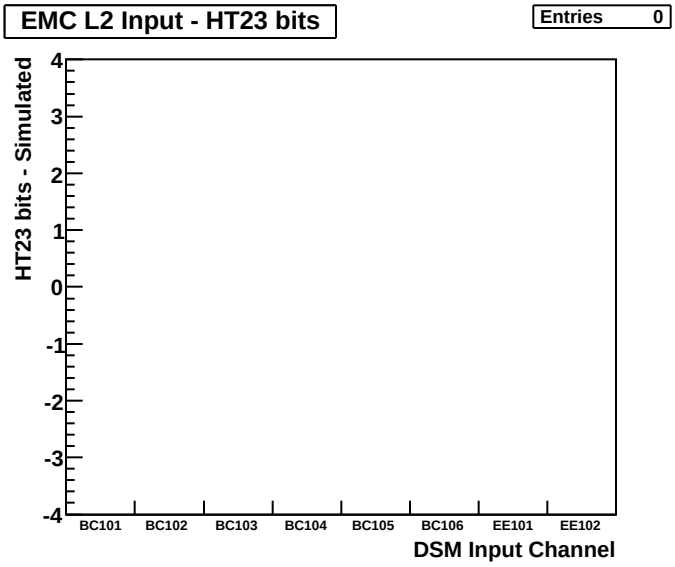

EMC L2 Input - Partial JP Sum
Entries 581472
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

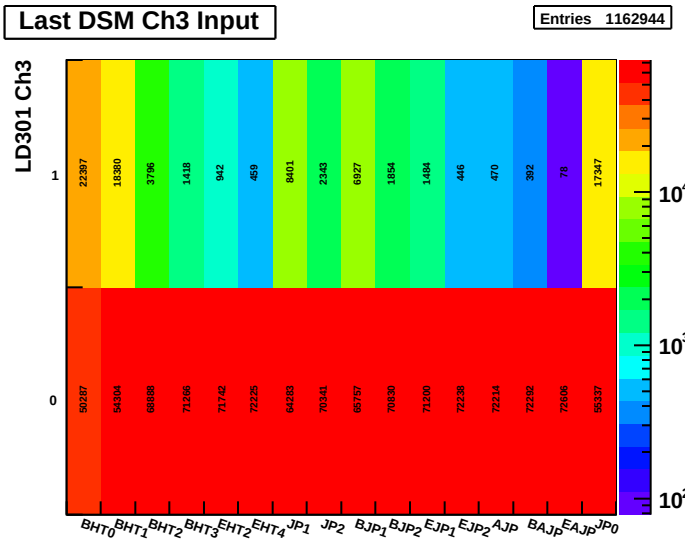

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

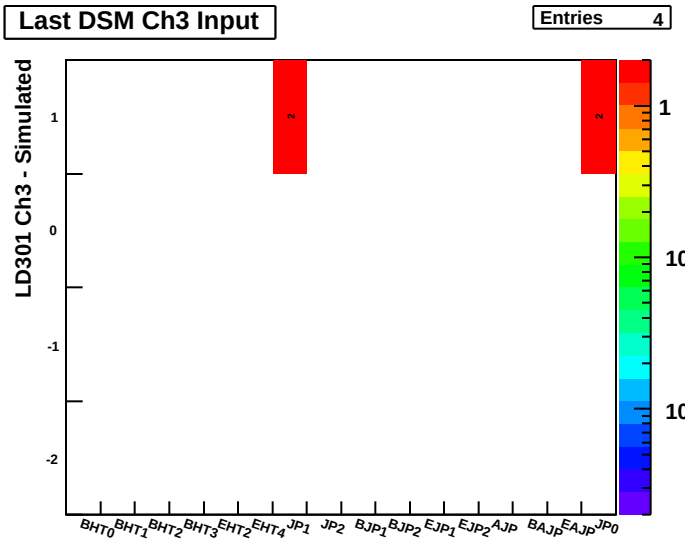

EMC L2 Input - HT01 bits/Partial JP ID
Entries 581472
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

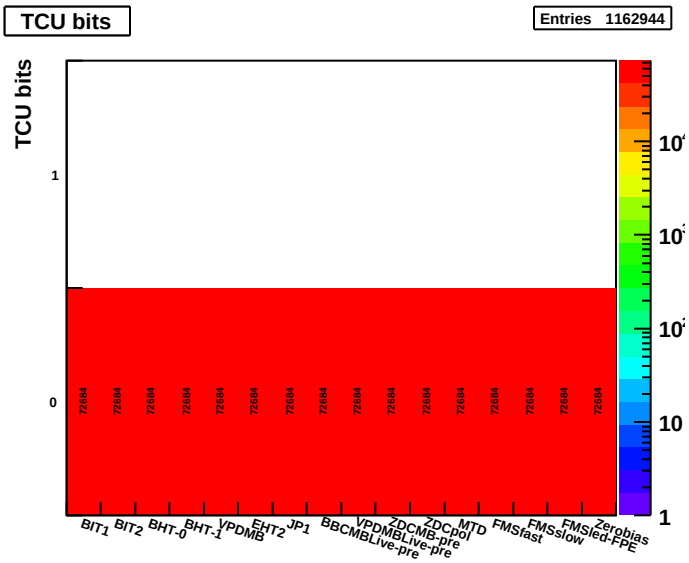

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

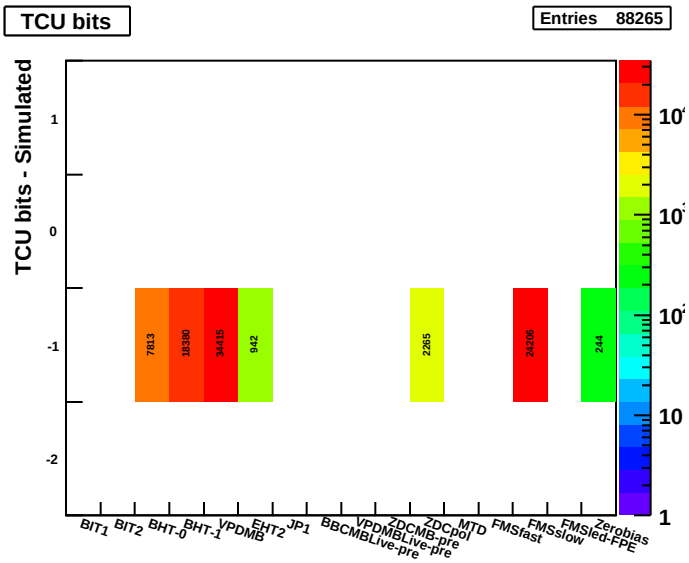

EMC L2 Input - HT23 bits
Entries 581472
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

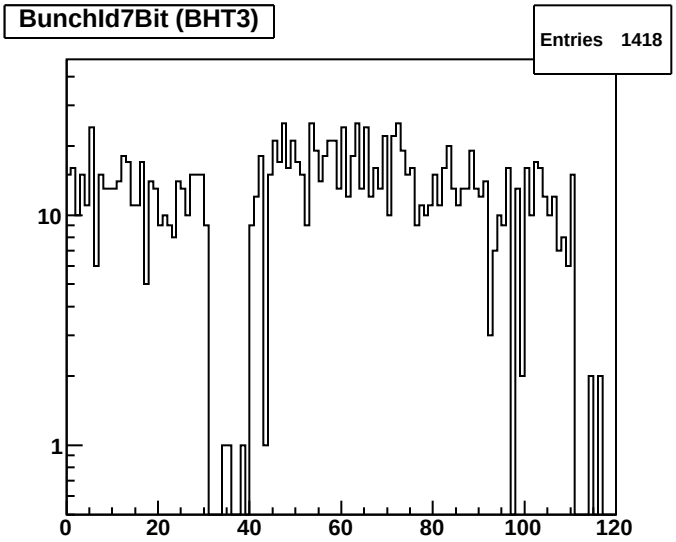

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

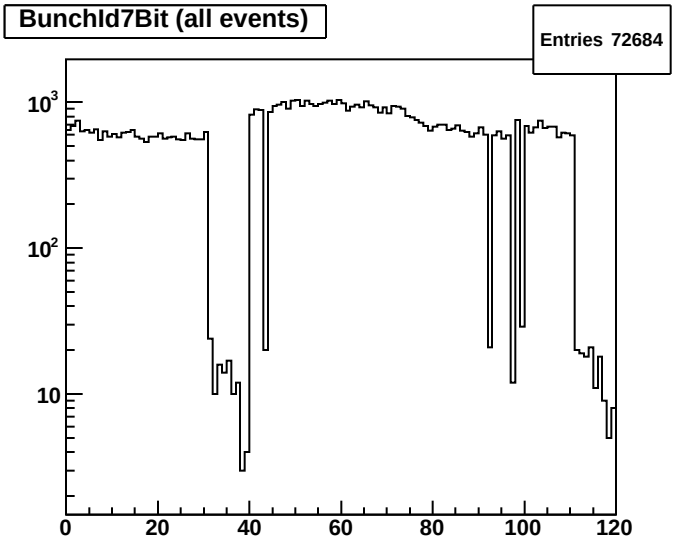

Last DSM Ch3 Input
Entries 1162944
LD301 Ch3
22397 18380 3796 1418 942 459 8401 2343 6927 1854 1484 446 470 392 78 17347
50267 54304 68888 71266 71742 72225 64283 70341 65757 70830 71200 72238 72214 72292 72606 55337
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

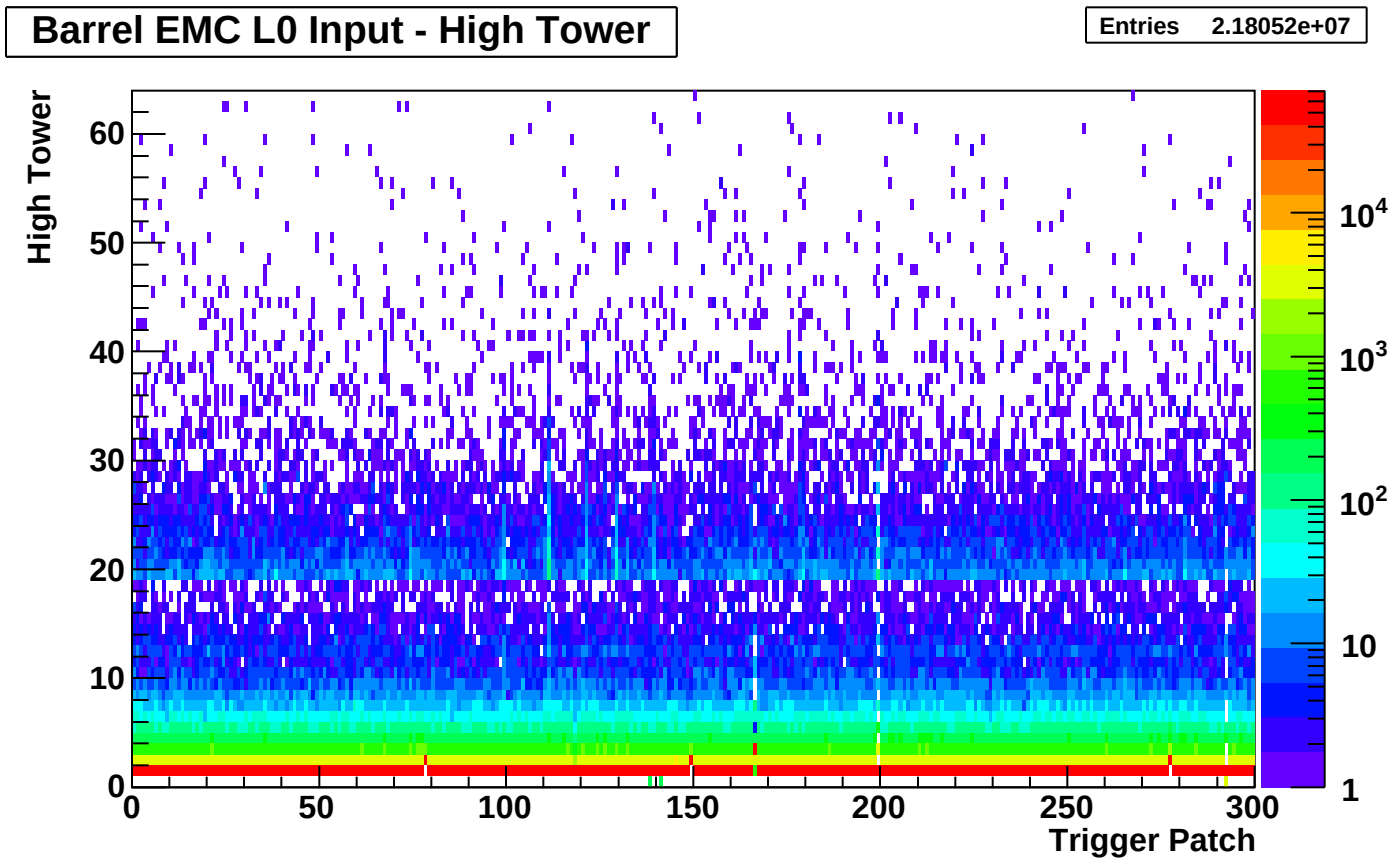

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
2 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

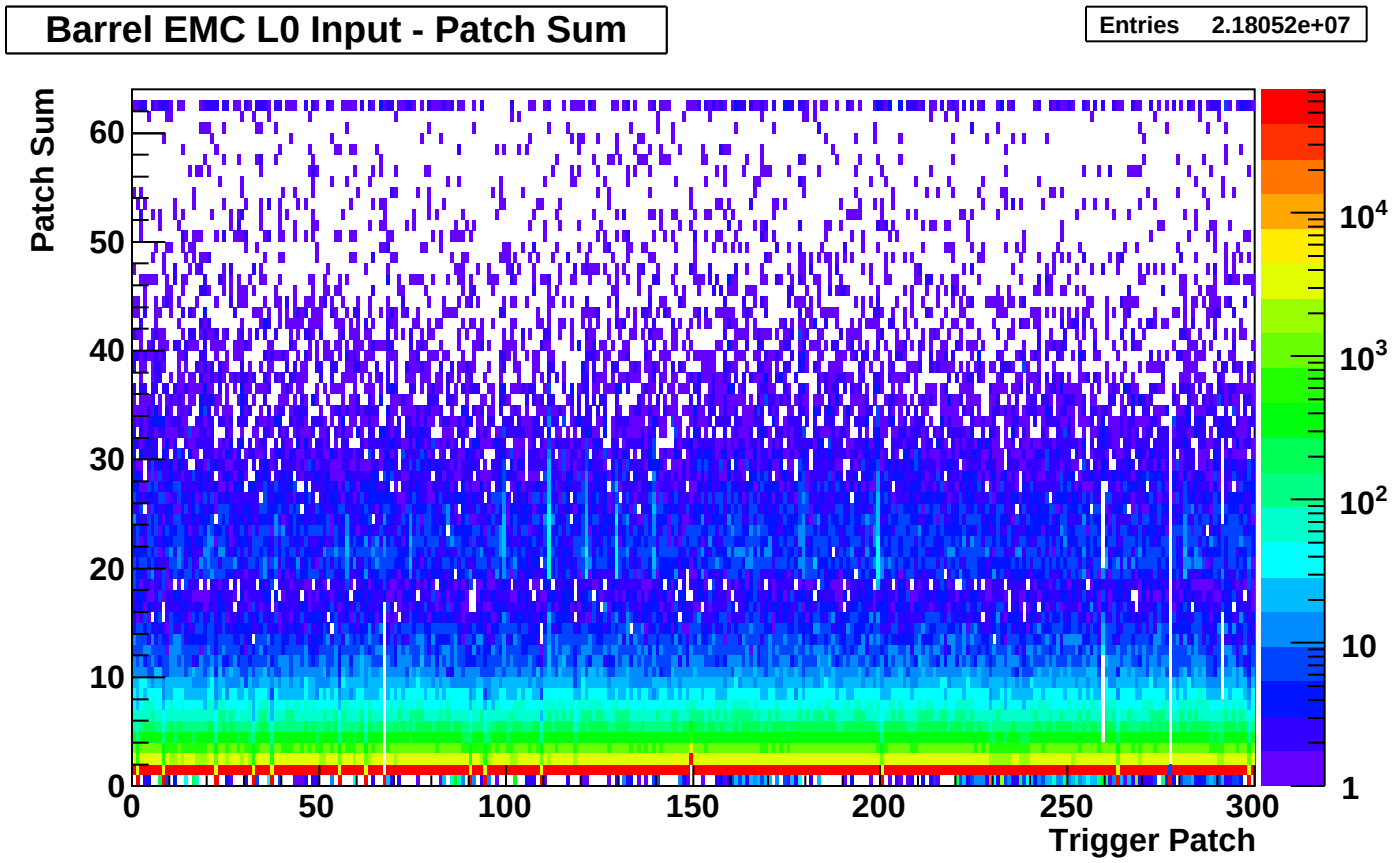

TCU bits
Entries 1162944
TCU bits
72684 72684 72684 72684 72684 72684 72684 72684 72684 72684 72684 72684 72684 72684 72684 72684
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

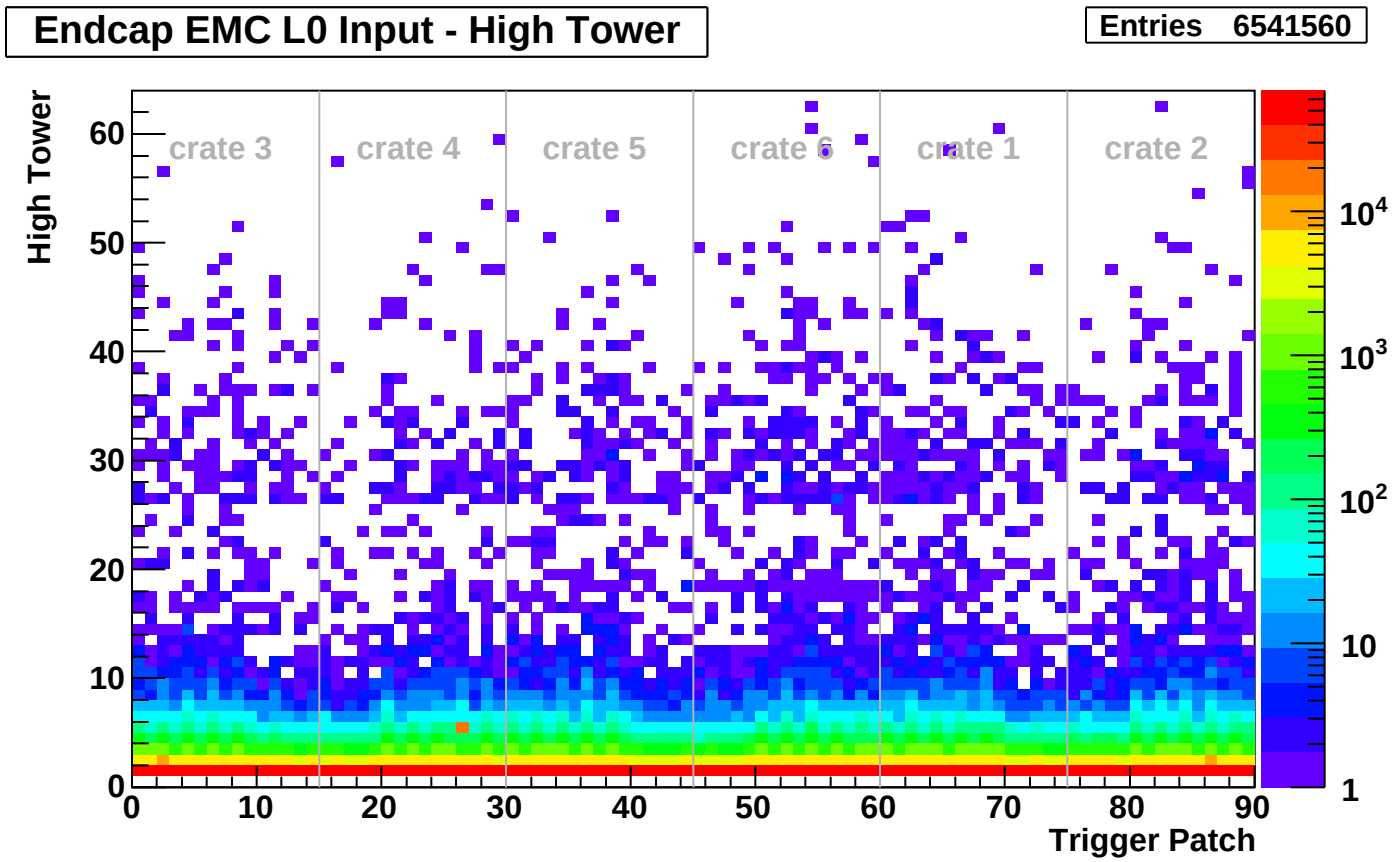

TCU bits
Entries 88265
TCU bits - Simulated
7813 18380 34415 942 2265 24206 244
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

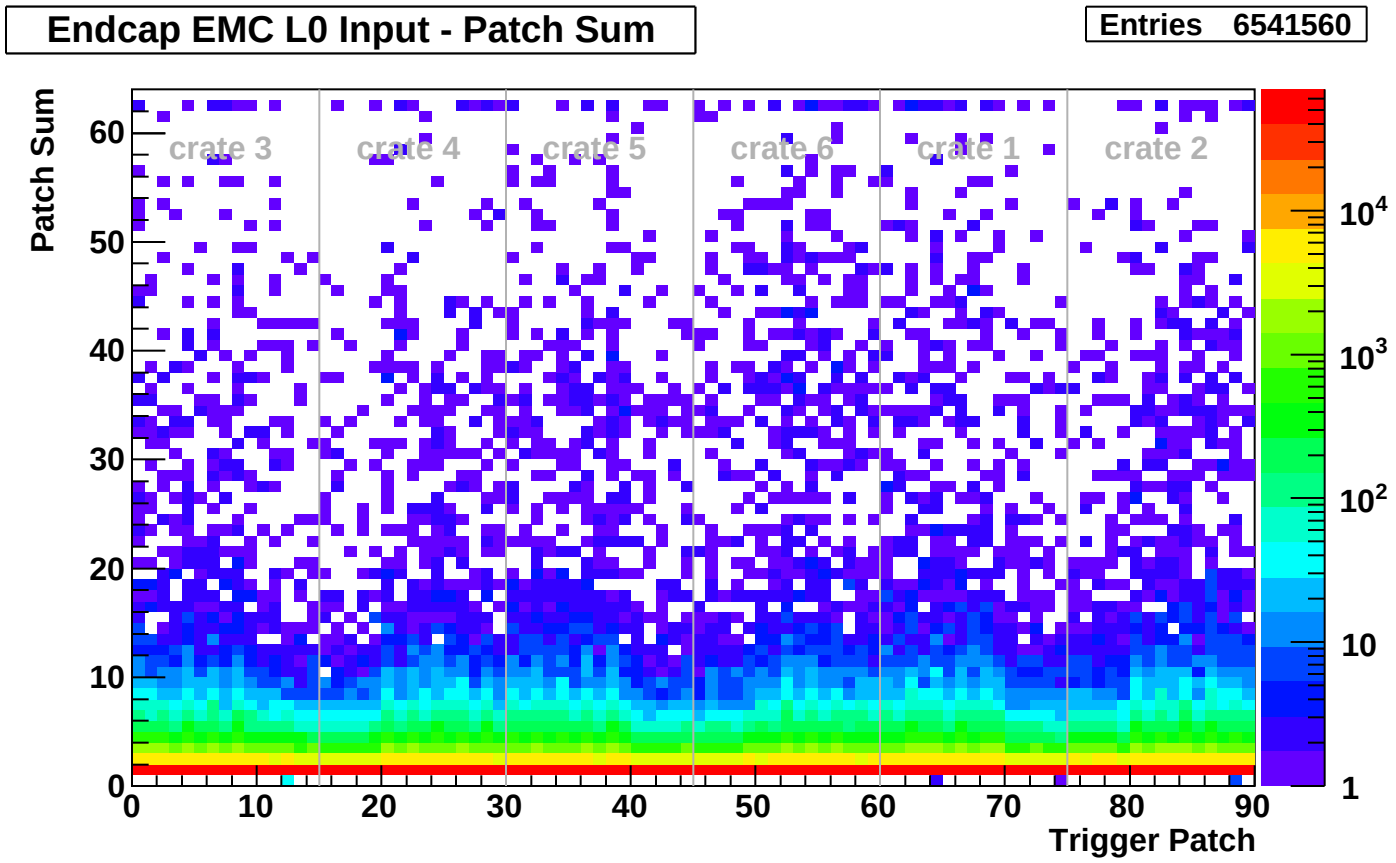

BunchId7Bit (BHT3)
Entries 1418

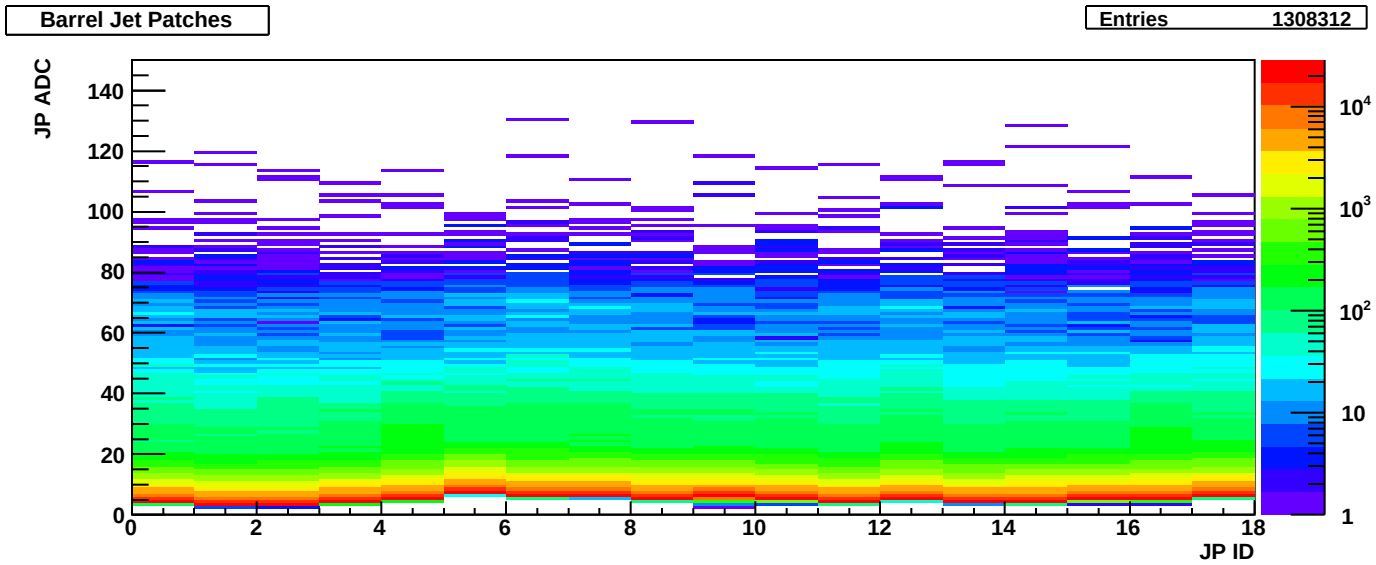

BunchId7Bit (all events)
Entries 72684

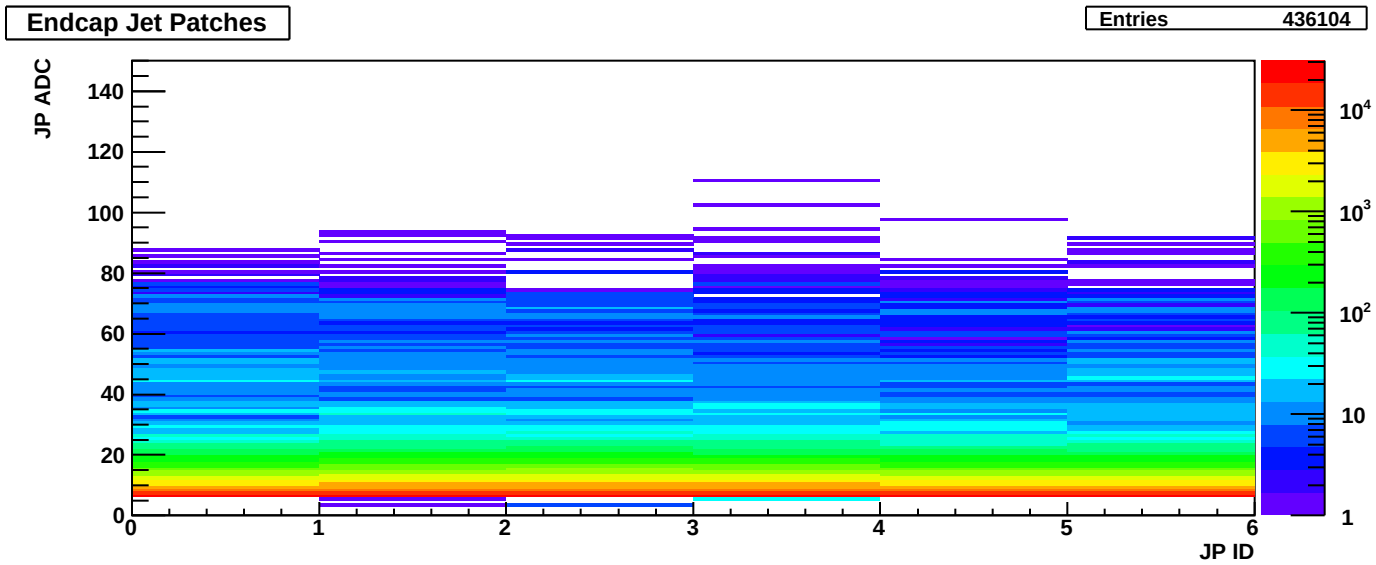

Barrel EMC L0 Input - High Tower
Entries 2.18052e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

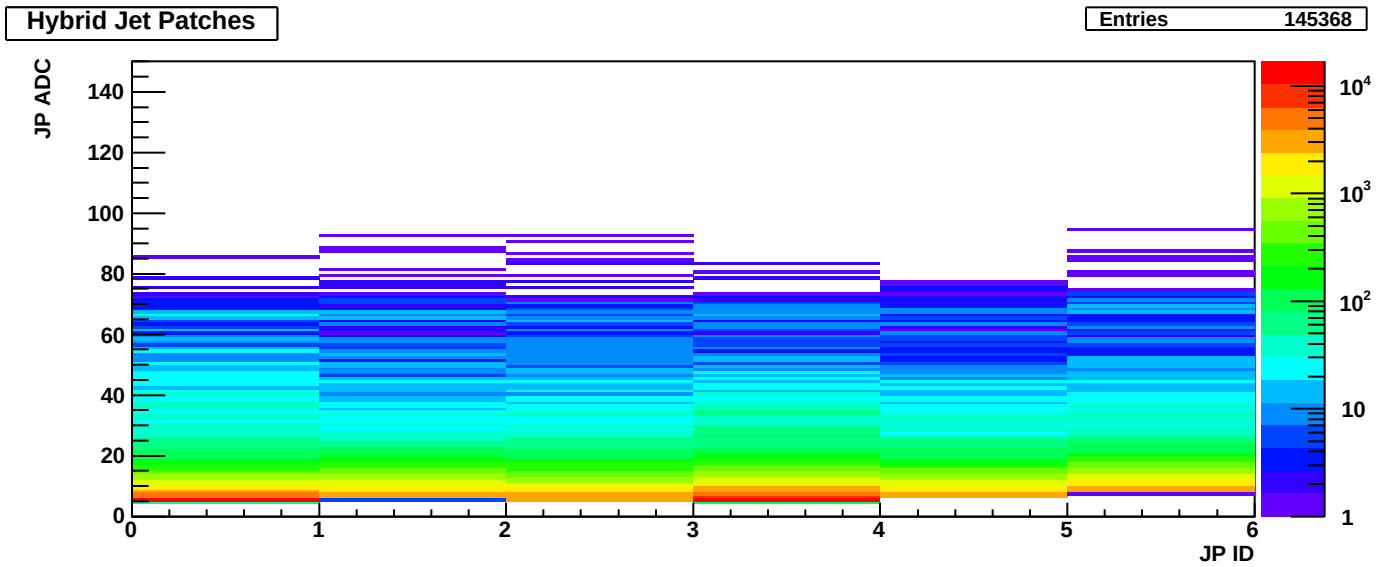

Barrel EMC L0 Input - Patch Sum
Entries 2.18052e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

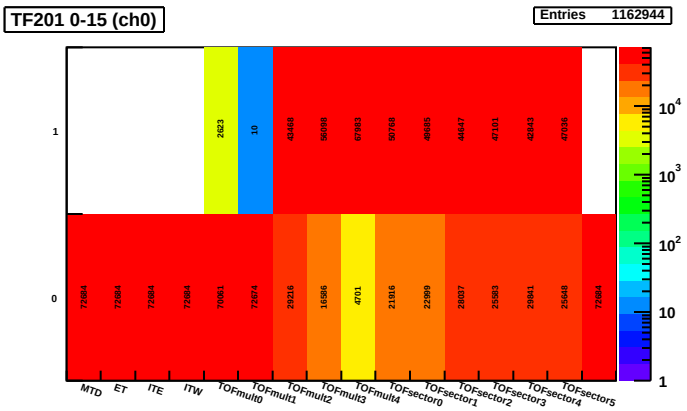

Endcap EMC L0 Input - High Tower
Entries 6541560
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

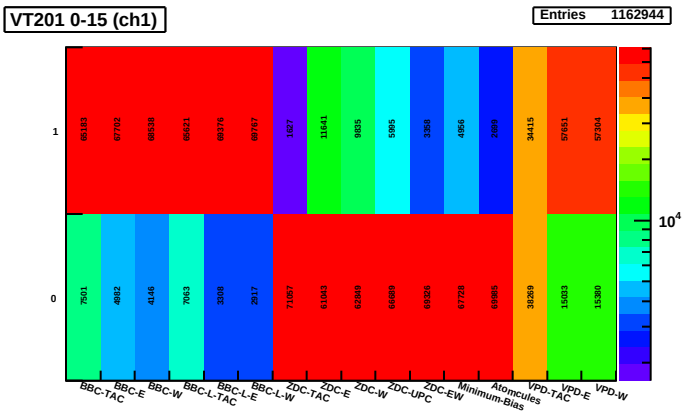

Endcap EMC L0 Input - Patch Sum
Entries 6541560
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

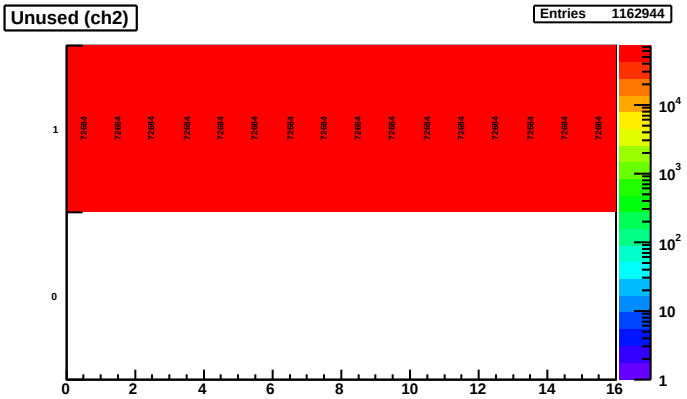

Barrel Jet Patches
Entries 1308312
JP ADC
JP ID

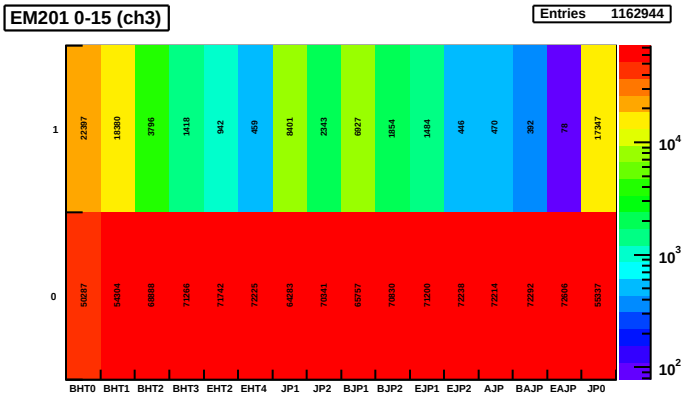

Endcap Jet Patches
Entries 436104
JP ADC
JP ID

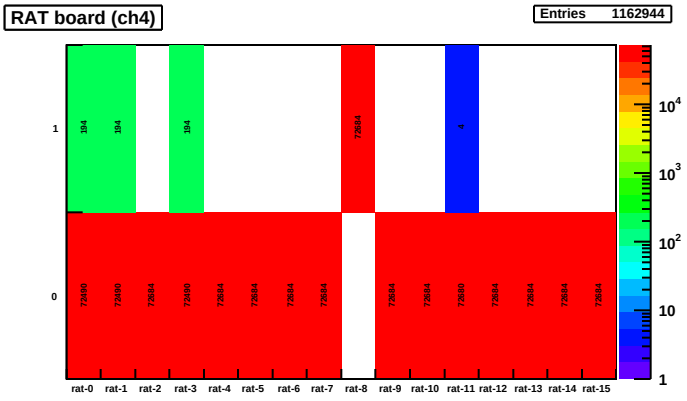

Hybrid Jet Patches
Entries 145368
JP ADC
JP ID

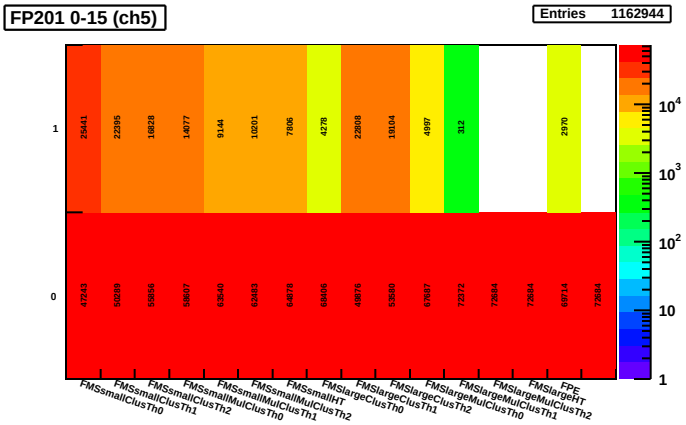

TF201 0-15 (ch0)
Entries 1162944
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

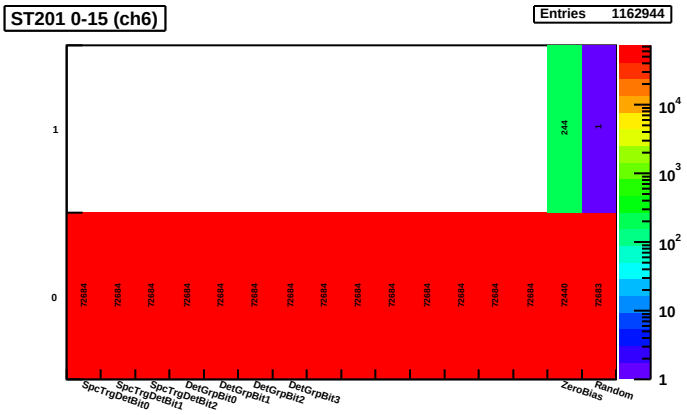

VT201 0-15 (ch1)
Entries 1162944
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

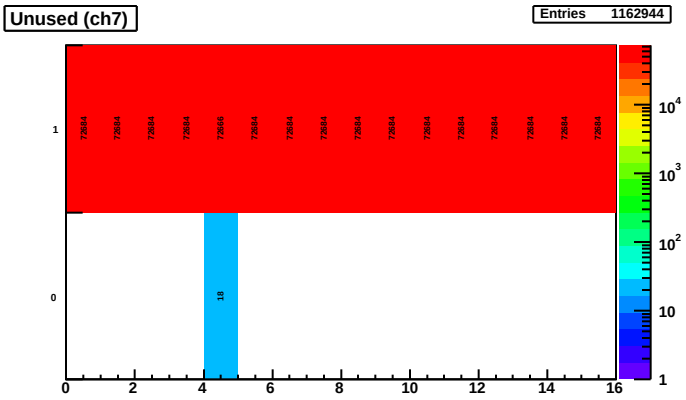

Unused (ch2)
Entries 1162944

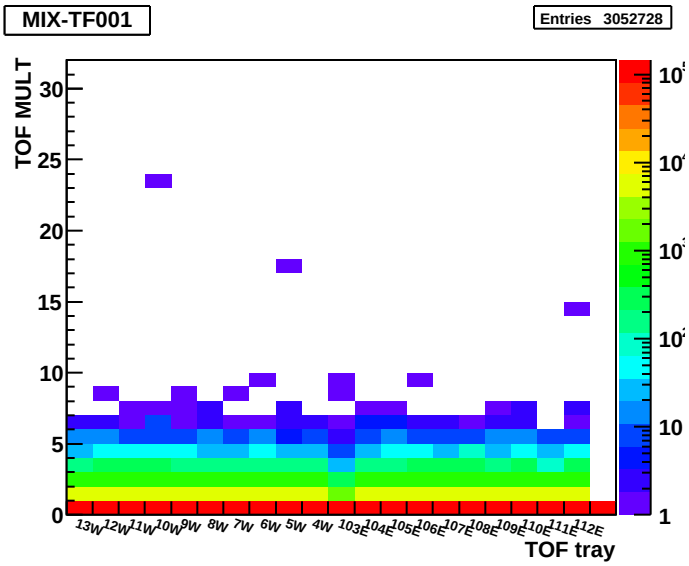

EM201 0-15 (ch3)
Entries 1162944
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

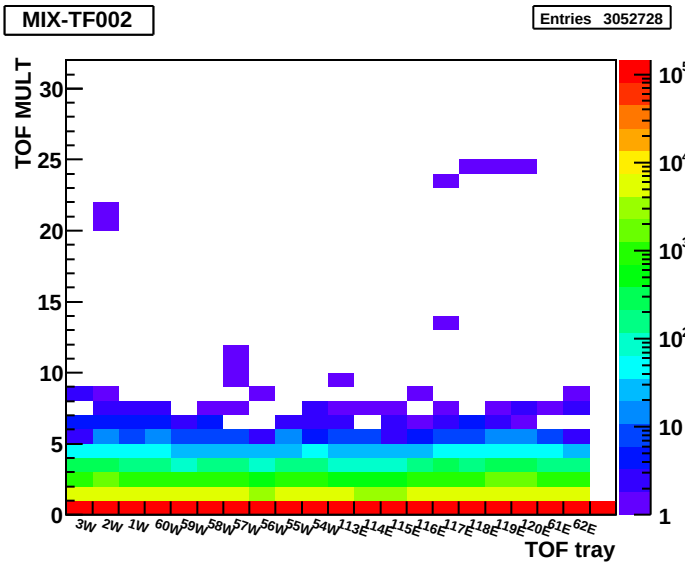

RAT board (ch4)
Entries 1162944
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

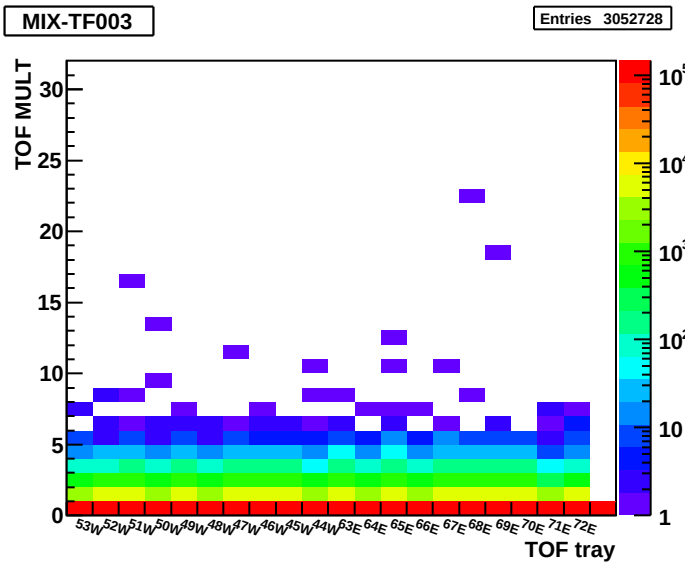

FP201 0-15 (ch5)
Entries 1162944
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

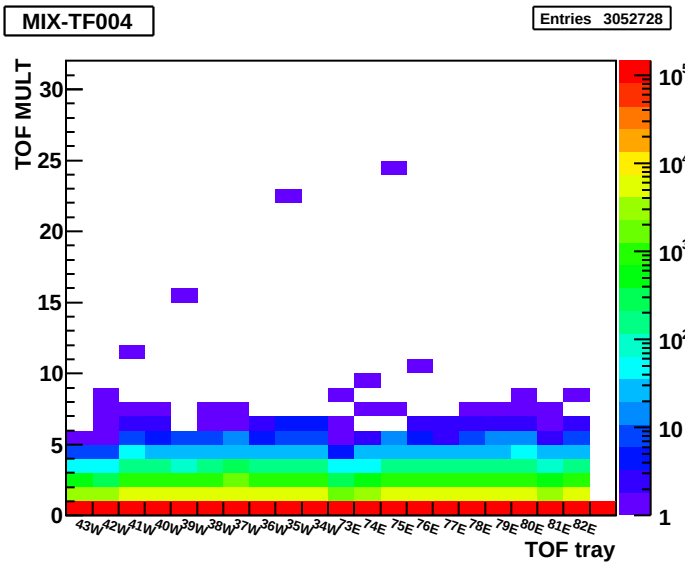

ST201 0-15 (ch6)
Entries 1162944
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

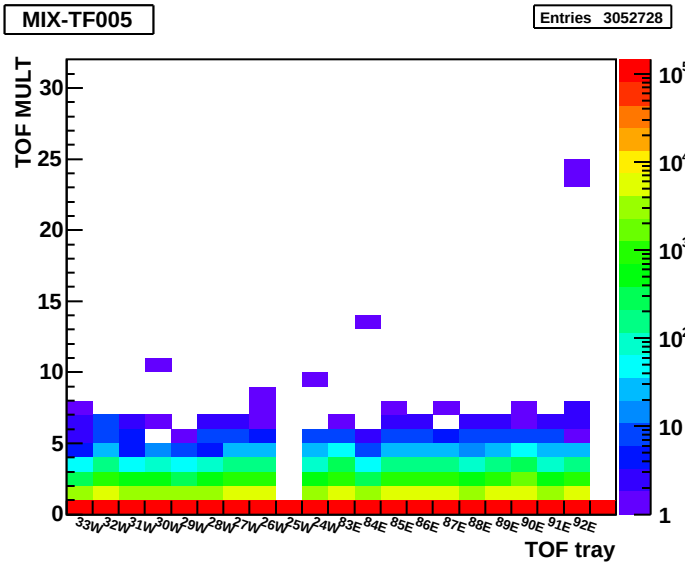

Unused (ch7)
Entries 1162944

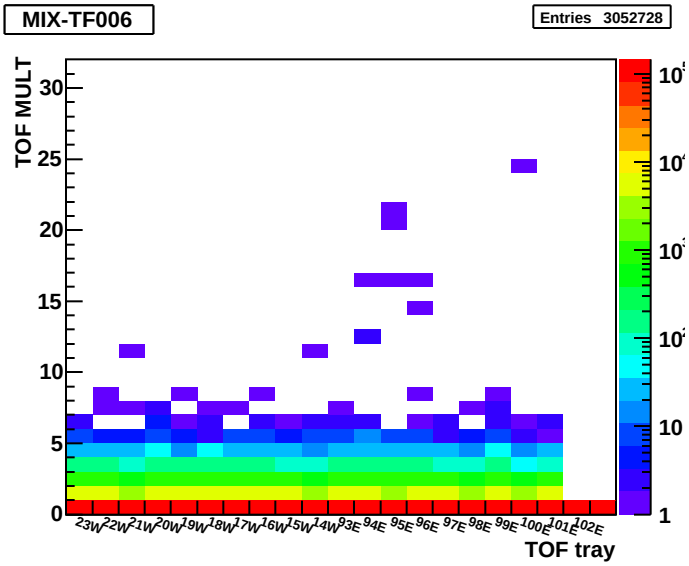

MIX-TF001
Entries 3052728
TOF MULT
TOF tray

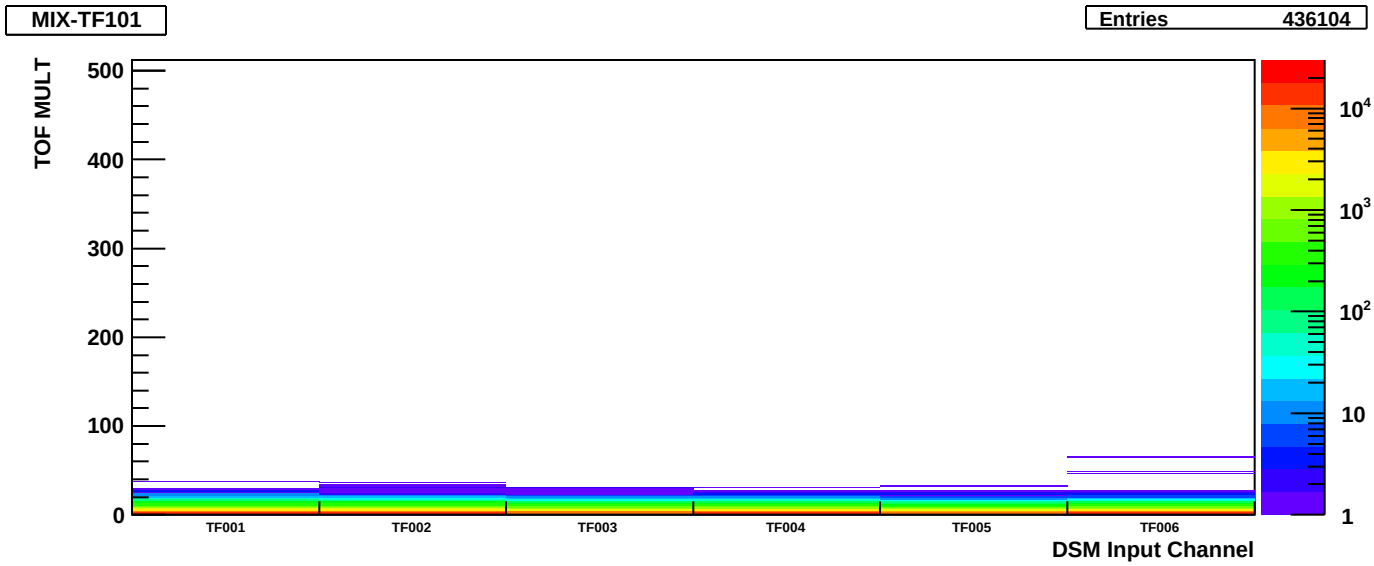

MIX-TF002
Entries 3052728
TOF MULT
TOF tray

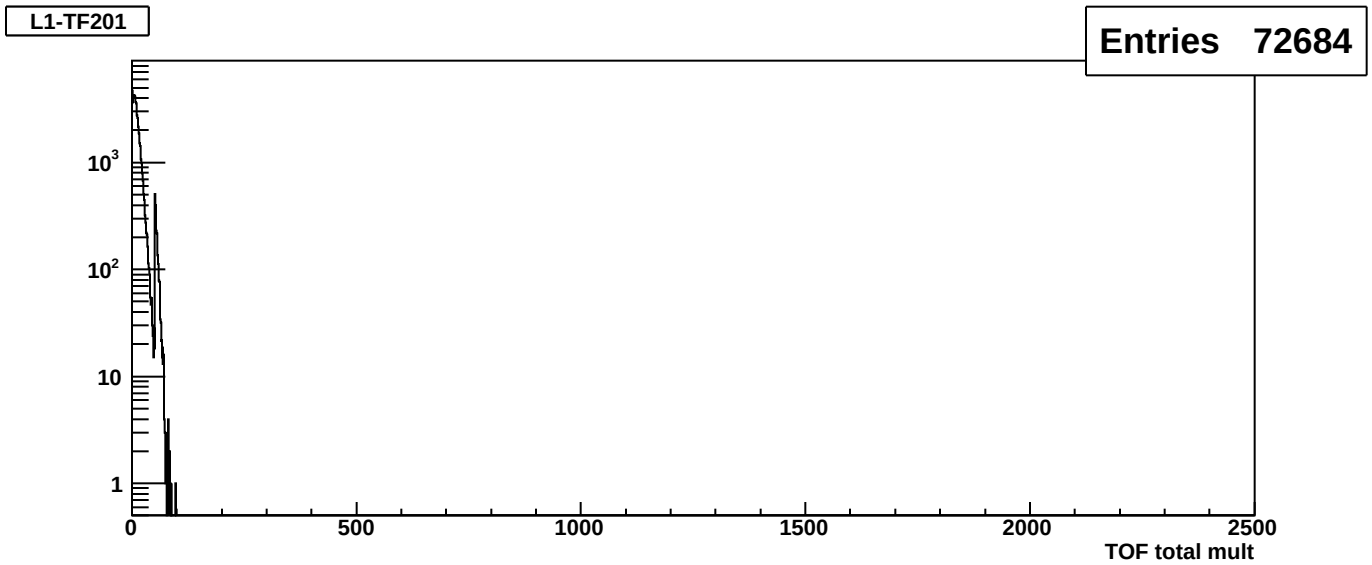

MIX-TF003
Entries 3052728
TOF MULT
TOF tray

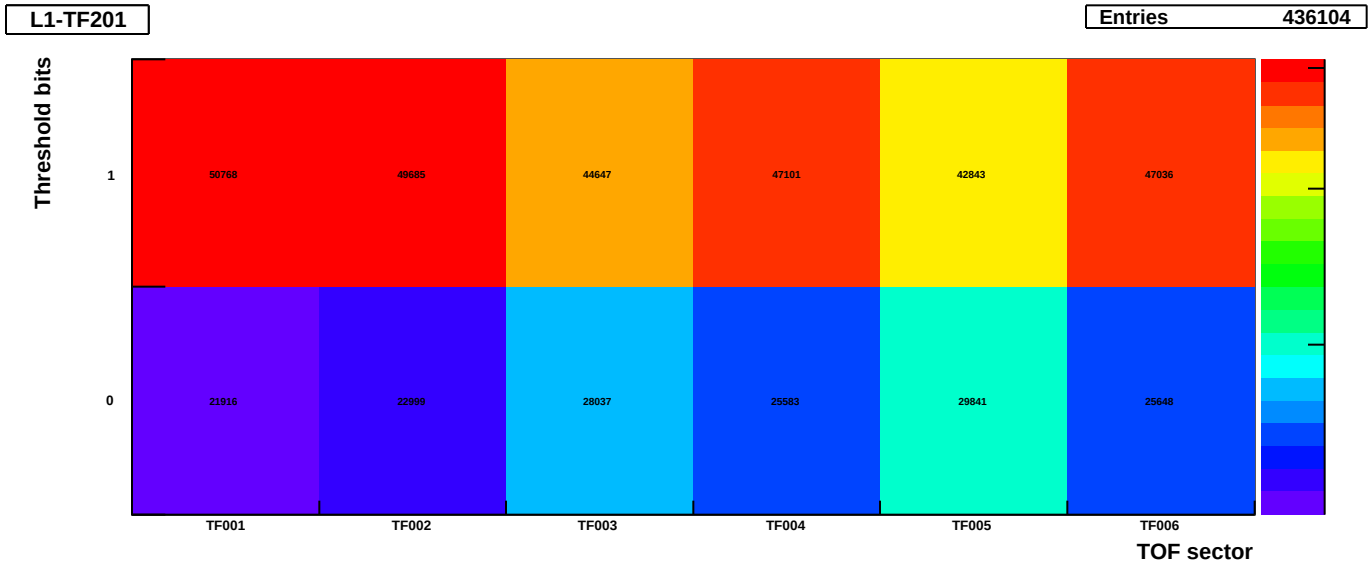

MIX-TF004
Entries 3052728
TOF MULT
TOF tray

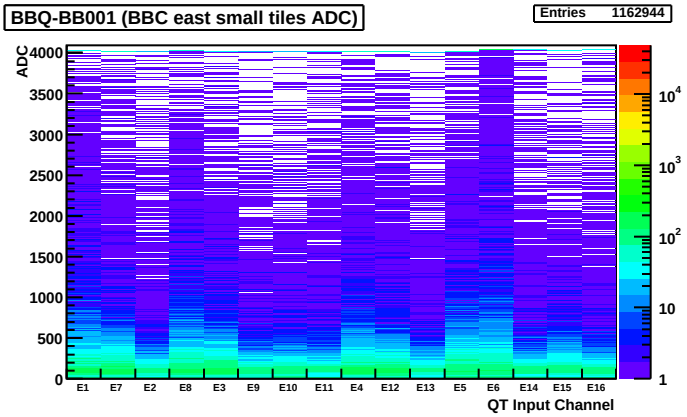

MIX-TF005
Entries 3052728
TOF MULT
TOF tray

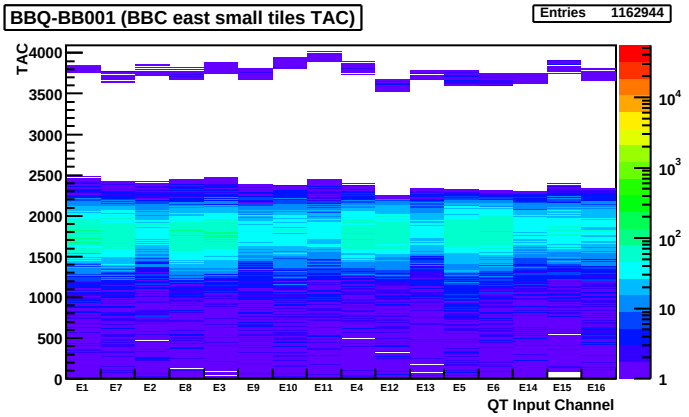

MIX-TF006
Entries 3052728
TOF MULT
TOF tray

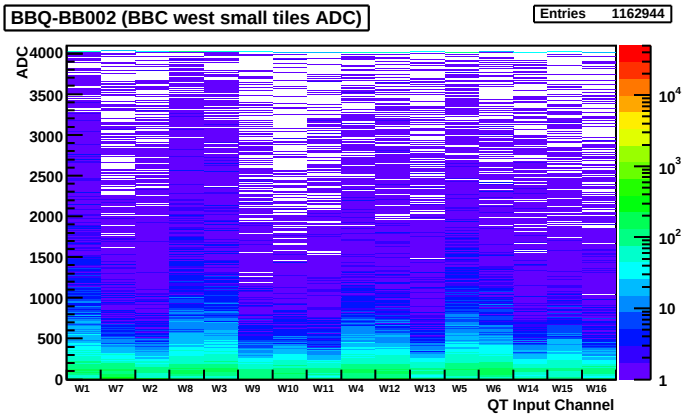

MIX-TF101
Entries 436104
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

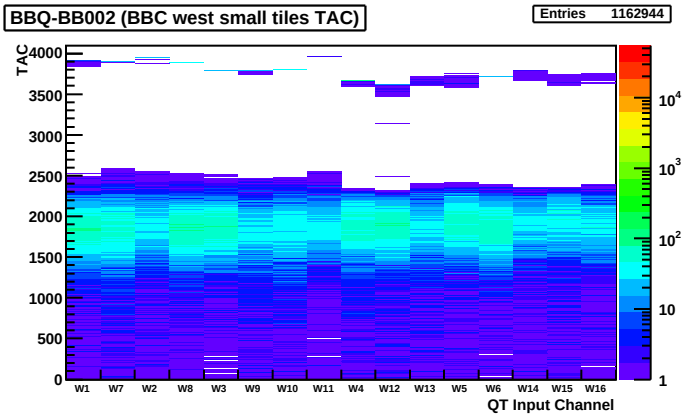

L1-TF201
Entries 72684
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

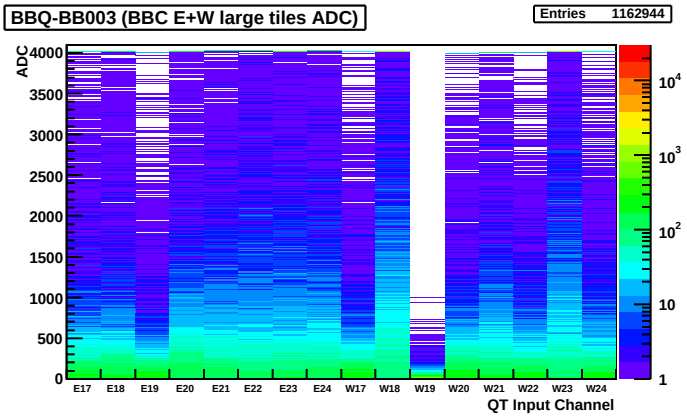

L1-TF201
Entries 436104
Threshold bits
1
0
50768
49685
44647
47101
42843
47036
21916
22999
28037
25583
29841
25648
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

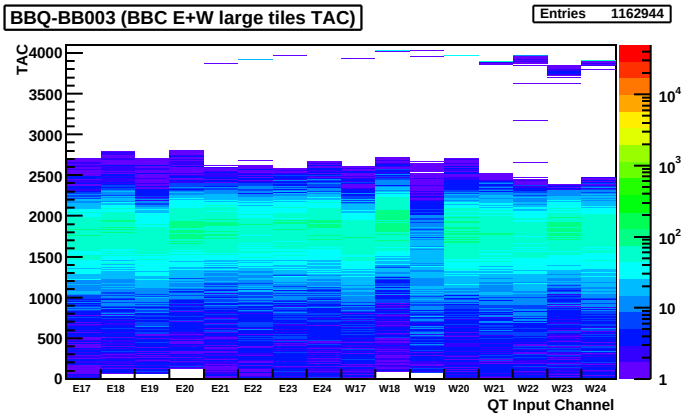

BBQ-BB001 (BBC east small tiles ADC)
Entries 1162944
ADC
QT Input Channel

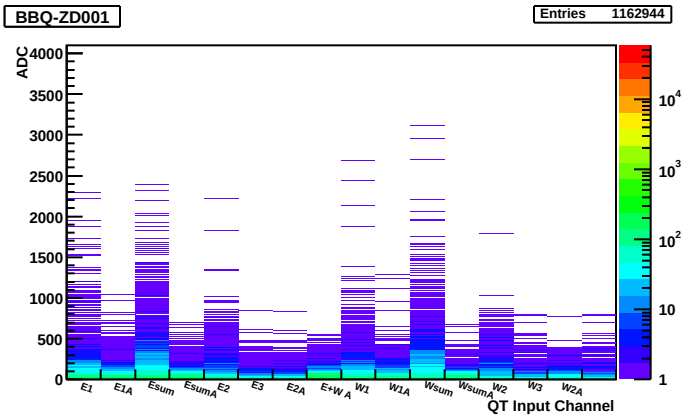

BBQ-BB001 (BBC east small tiles TAC)
Entries 1162944
TAC
QT Input Channel

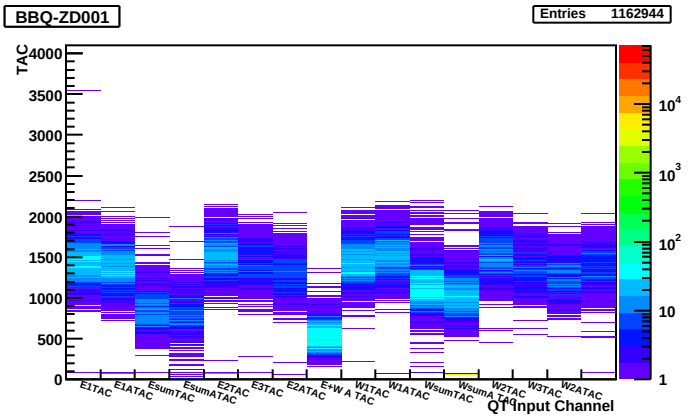

BBQ-BB002 (BBC west small tiles ADC)
Entries 1162944
ADC
QT Input Channel

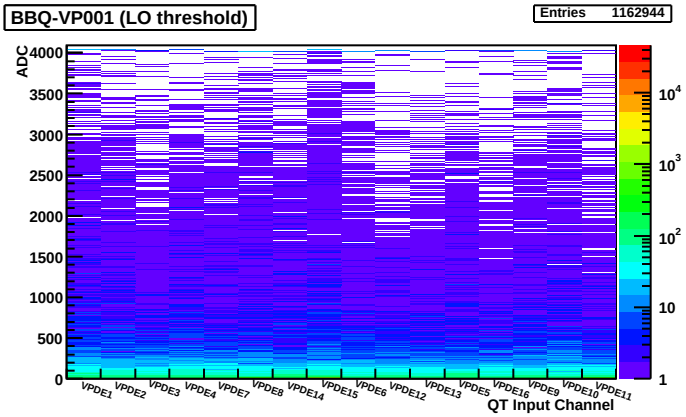

BBQ-BB002 (BBC west small tiles TAC)
Entries 1162944
TAC
QT Input Channel

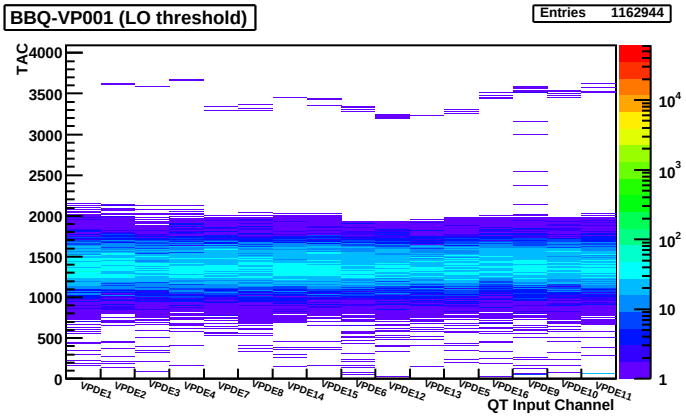

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1162944
ADC
QT Input Channel

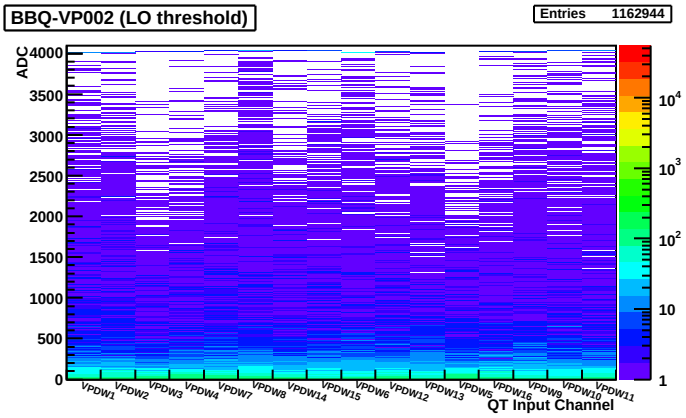

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1162944
TAC
QT Input Channel

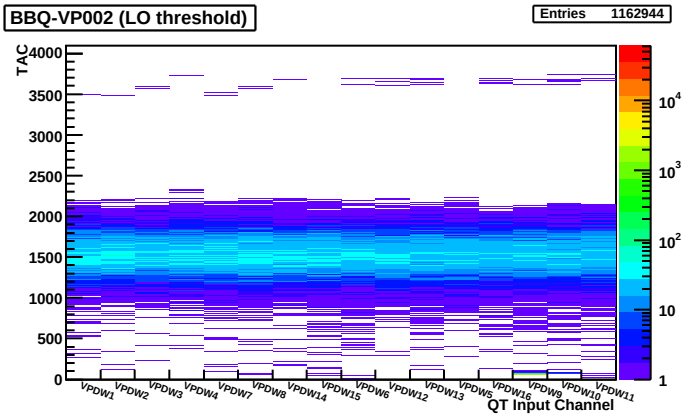

BBQ-ZD001
Entries 1162944
ADC
QT Input Channel

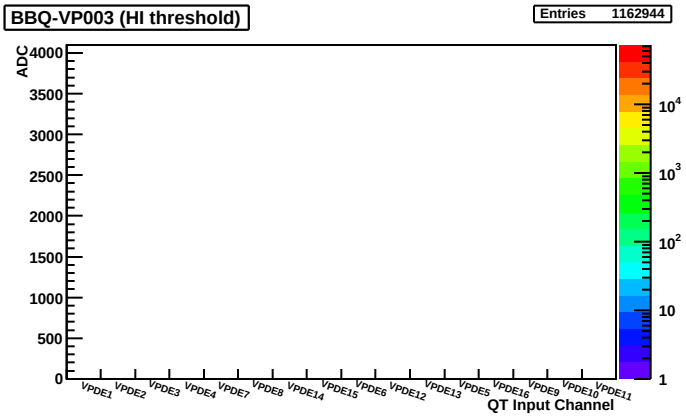

BBQ-ZD001
Entries 1162944
TAC
QT Input Channel

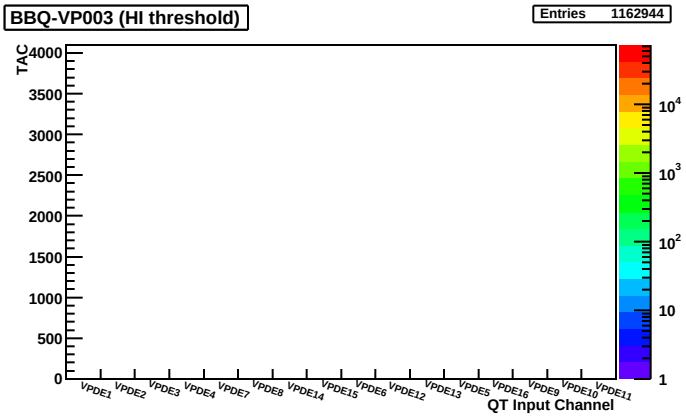

BBQ-VP001 (LO threshold)
Entries 1162944
ADC
QT Input Channel

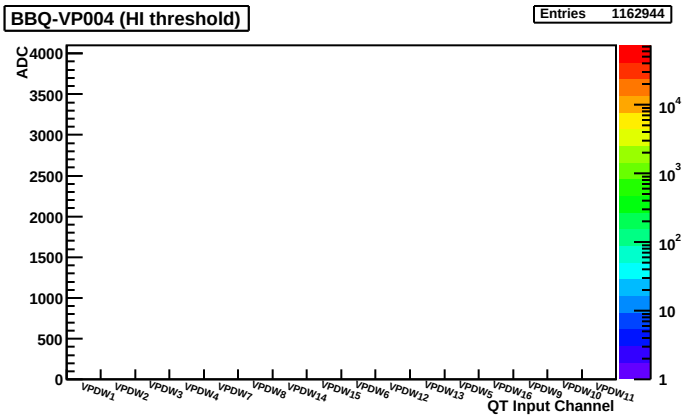

BBQ-VP001 (LO threshold)
Entries 1162944
TAC
QT Input Channel

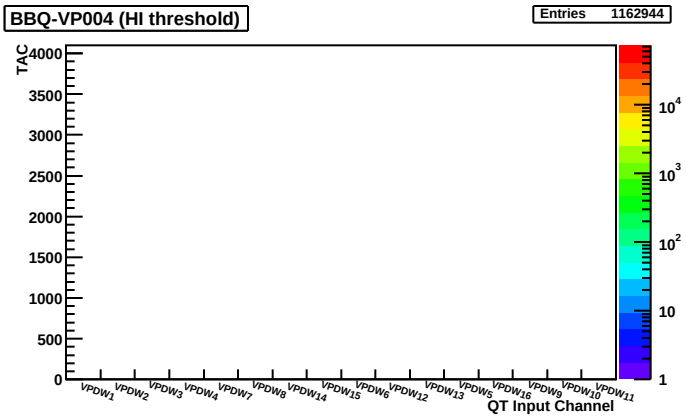

BBQ-VP002 (LO threshold)
Entries 1162944
ADC
QT Input Channel

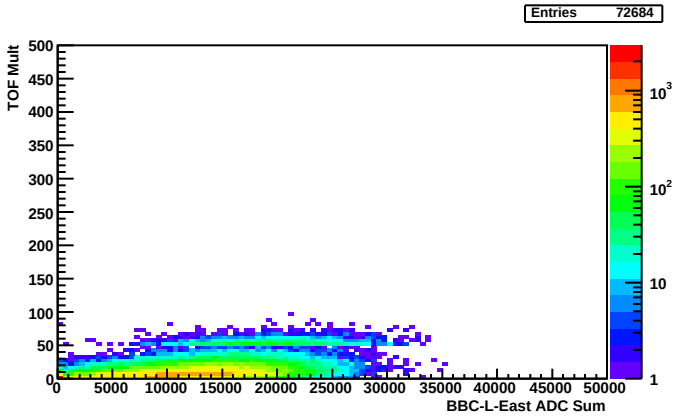

BBQ-VP002 (LO threshold)
Entries 1162944
TAC
QT Input Channel

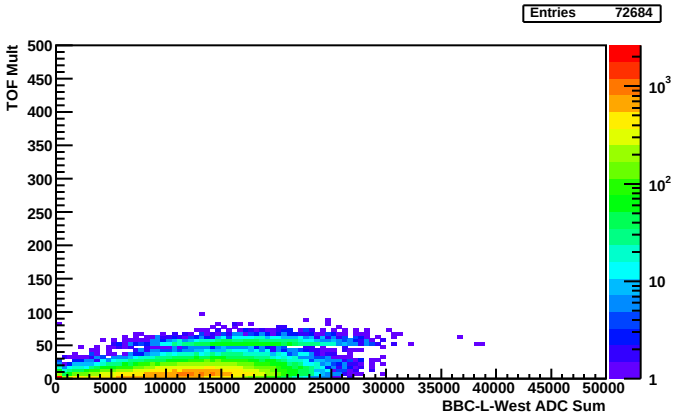

BBQ-VP003 (HI threshold)
Entries 1162944
ADC
QT Input Channel

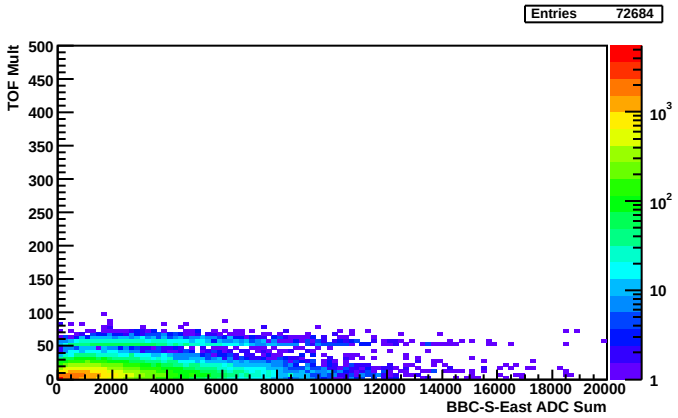

BBQ-VP003 (HI threshold)
Entries 1162944
TAC
QT Input Channel

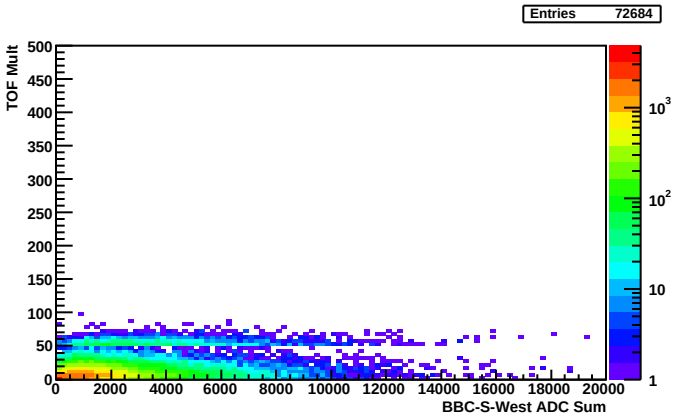

BBQ-VP004 (HI threshold)
Entries 1162944
ADC
QT Input Channel

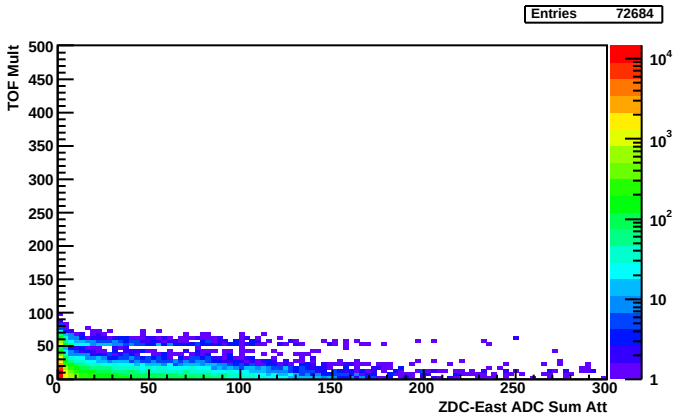

BBQ-VP004 (HI threshold)
Entries 1162944
TAC
QT Input Channel

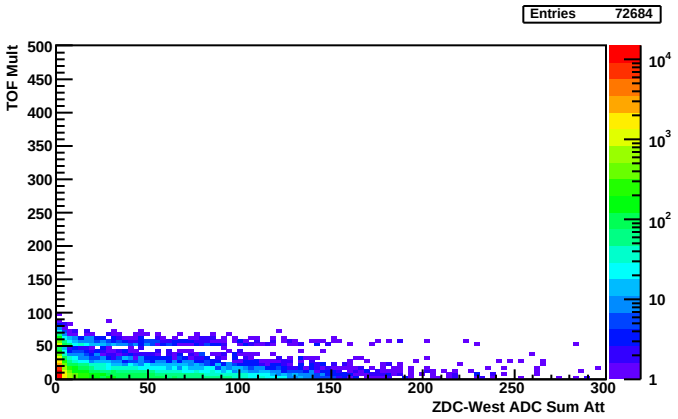

Entries 72684
TOF Mult
BBC-L-East ADC Sum

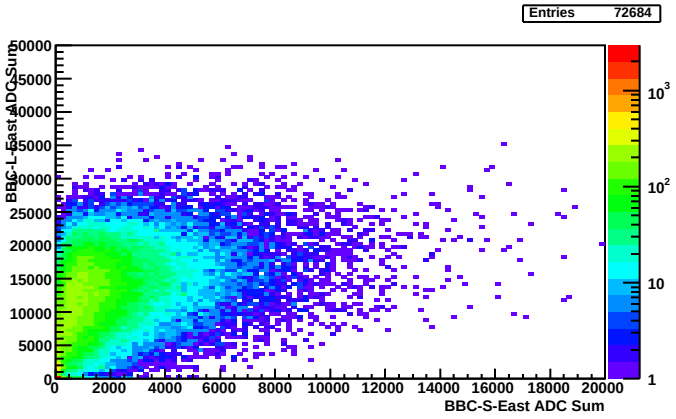

Entries 72684
TOF Mult
BBC-L-West ADC Sum

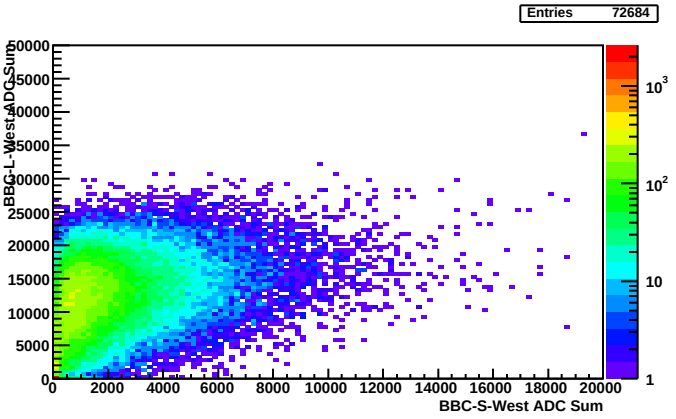

Entries 72684
TOF Mult
BBC-S-East ADC Sum

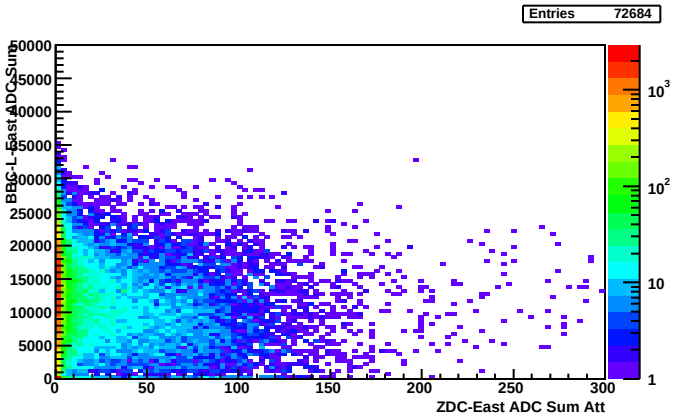

Entries 72684
TOF Mult
BBC-S-West ADC Sum

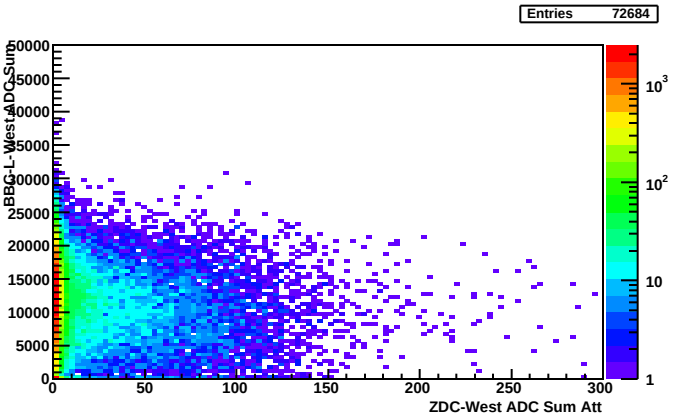

Entries 72684
TOF Mult
ZDC-East ADC Sum Att

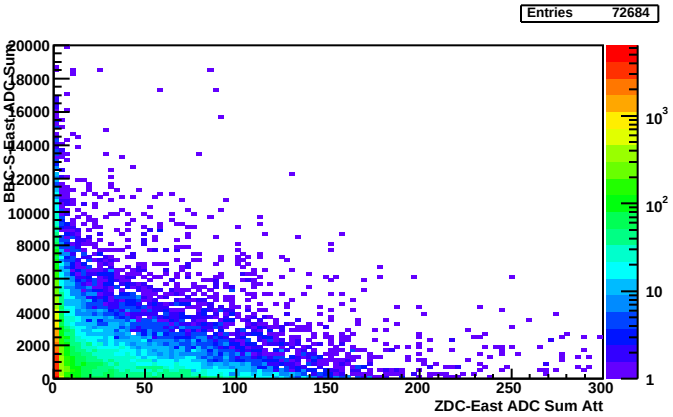

Entries 72684
TOF Mult
ZDC-West ADC Sum Att

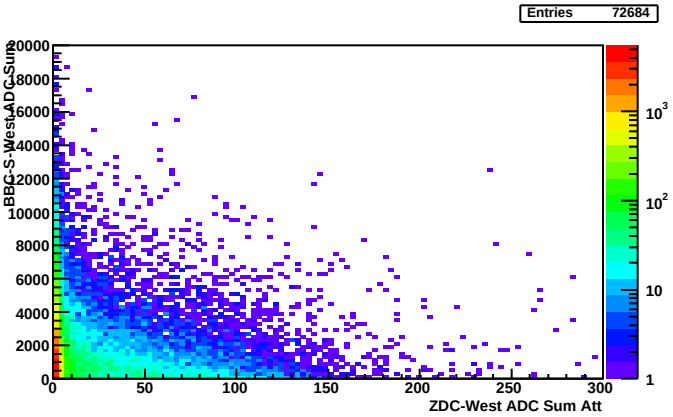

Entries 72684
BBC-L-East ADC Sum
BBC-S-East ADC Sum

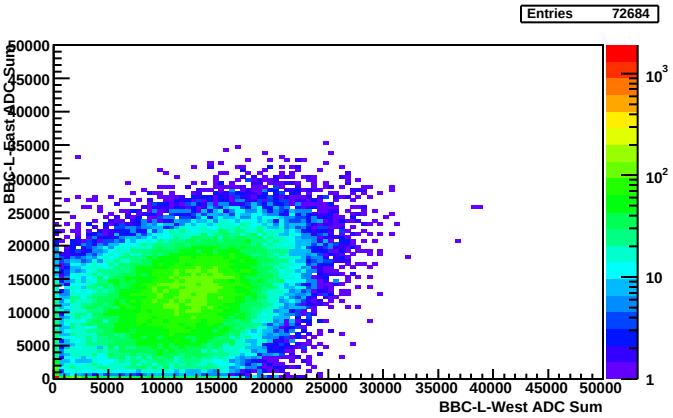

Entries 72684
BBC-L-West ADC Sum
BBC-S-West ADC Sum

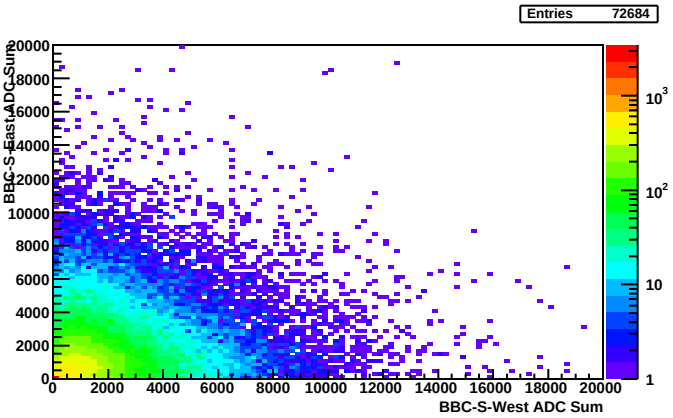

Entries 72684
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

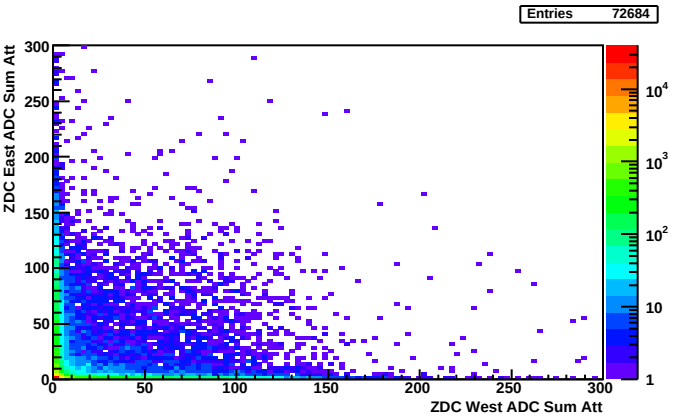

Entries 72684
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

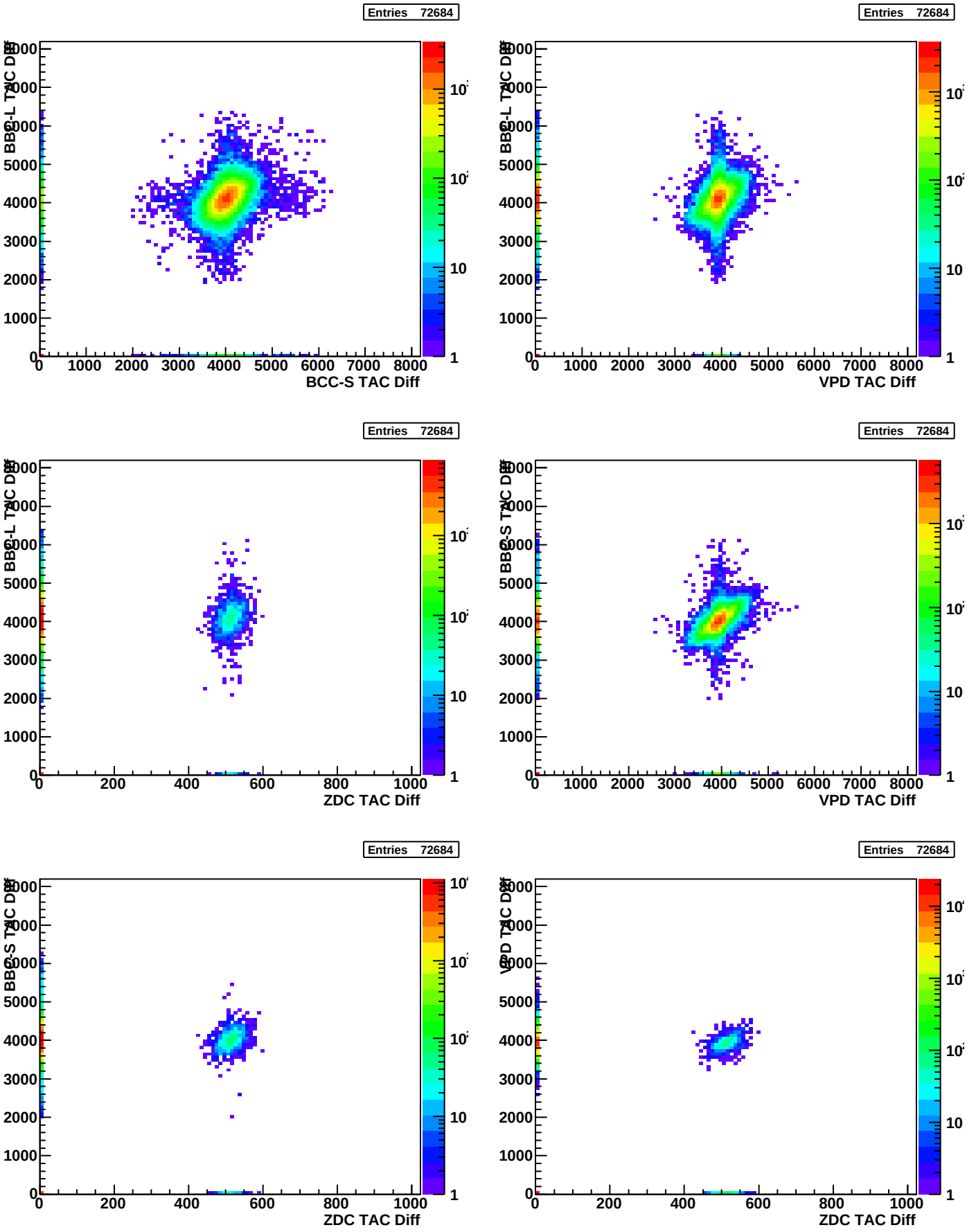

Entries 72684
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

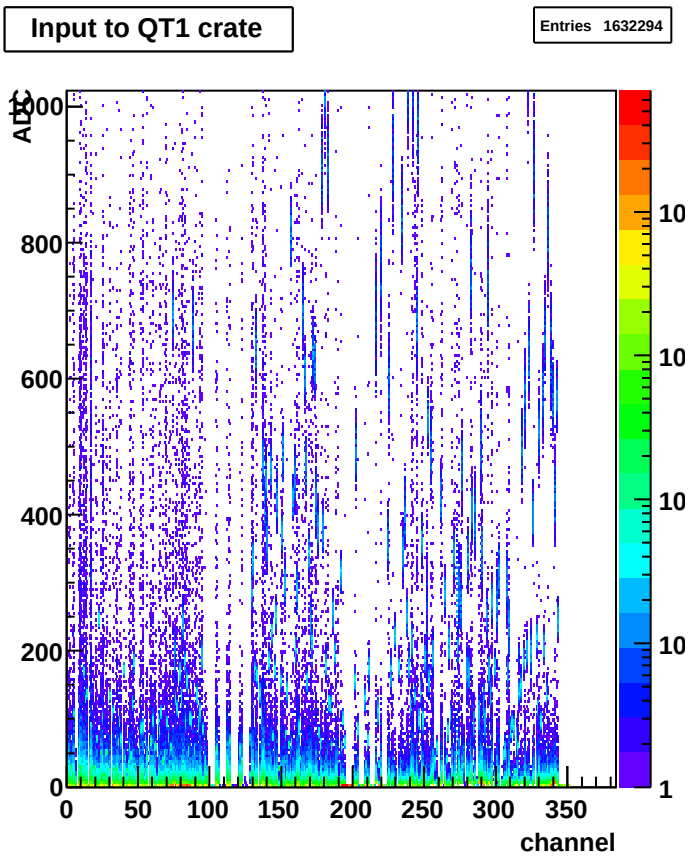

Entries 72684
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

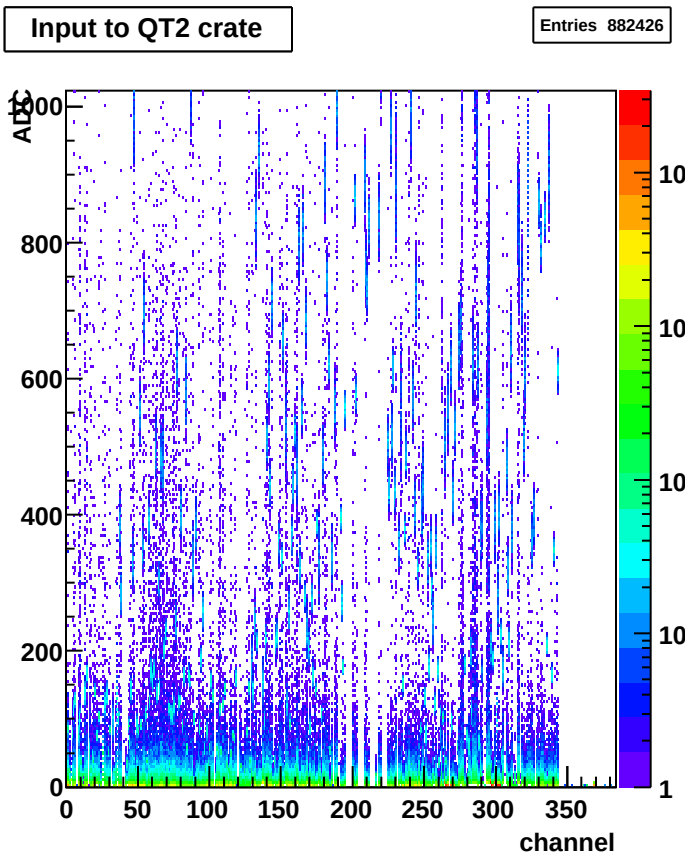

Entries 72684
BBC-L-East ADC Sum
BBC-L-West ADC Sum

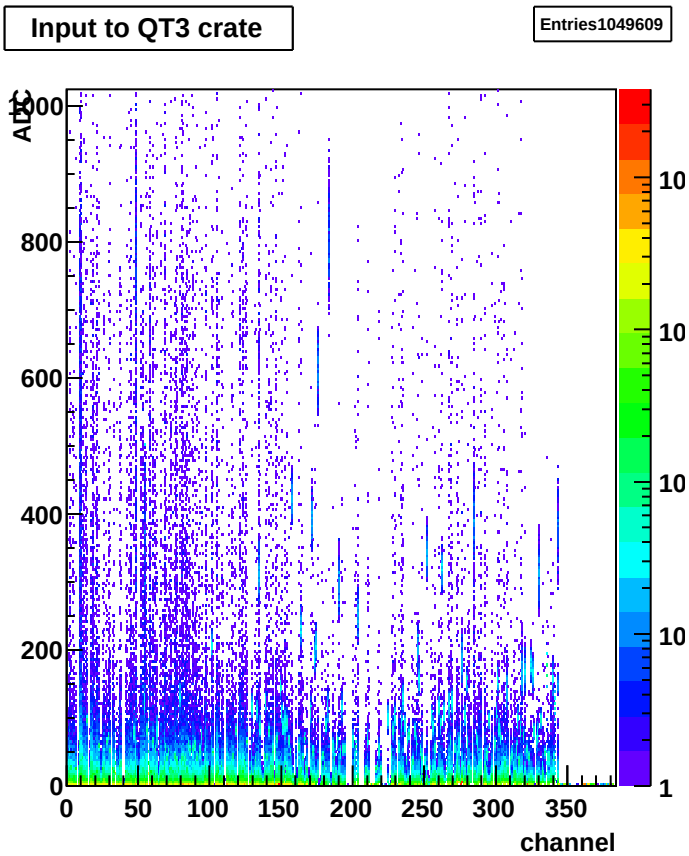

Entries 72684
BBC-S-East ADC Sum
BBC-S-West ADC Sum

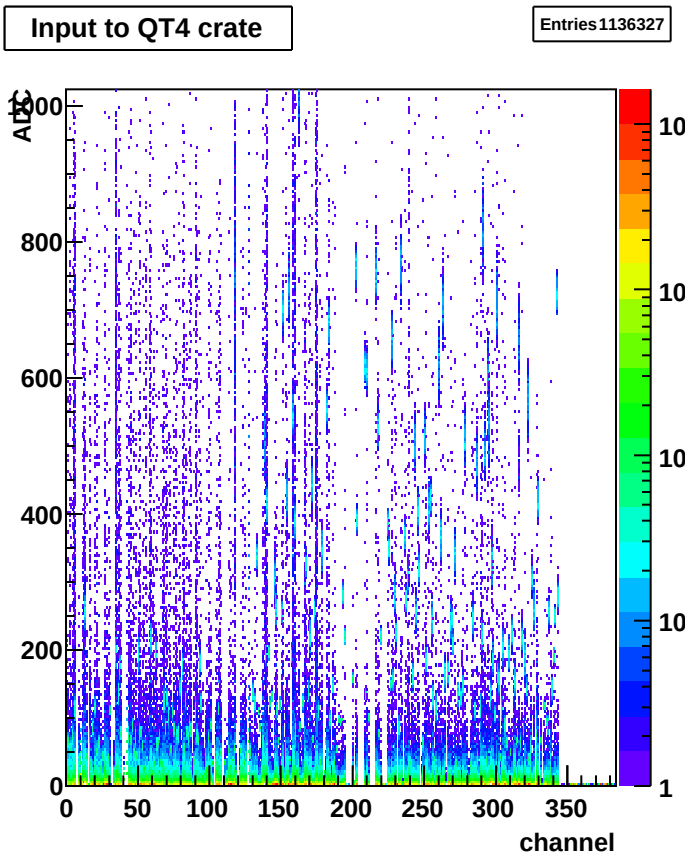

Entries 72684
ZDC East ADC Sum Att
ZDC West ADC Sum Att

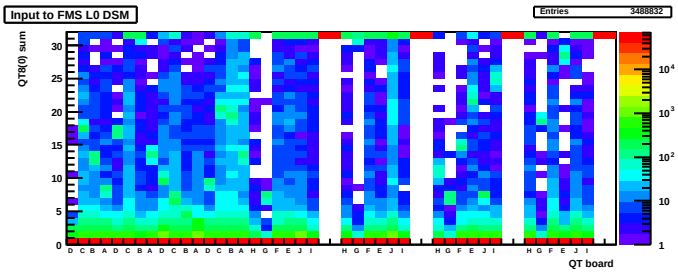

Entries 72684
BBC-L TAC Diff
BCC-S TAC Diff
Entries 72684
BBC-L TAC Diff
VPD TAC Diff
Entries 72684
BBC-L TAC Diff
ZDC TAC Diff
Entries 72684
BBC-S TAC Diff
VPD TAC Diff
Entries 72684
BBC-S TAC Diff
ZDC TAC Diff
Entries 72684
VPD TAC Diff
ZDC TAC Diff

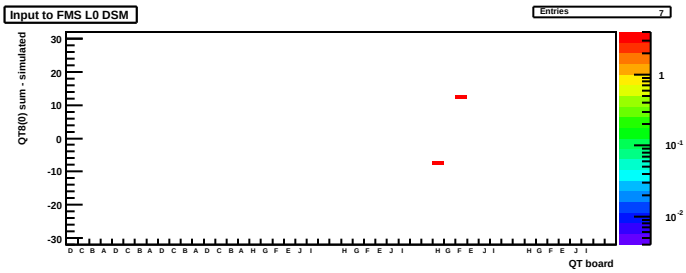

Input to QT1 crate
Entries 1632294
ADC
channel

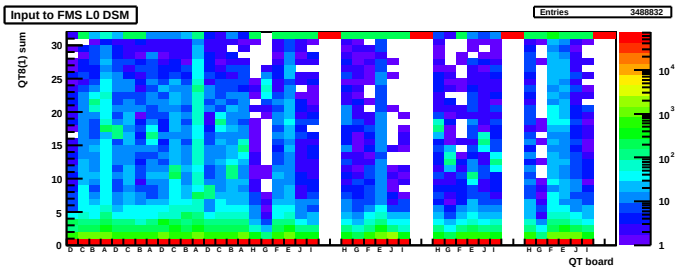

Input to QT2 crate
Entries 882426
ADC
channel

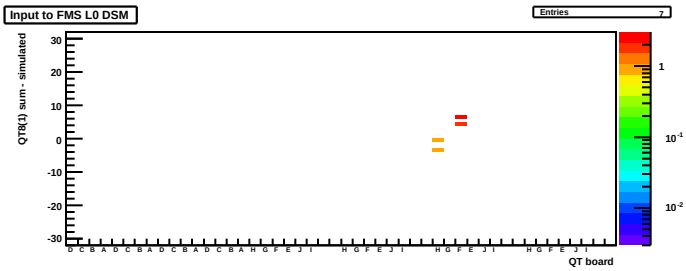

Input to QT3 crate
Entries1049609
ADC
channel

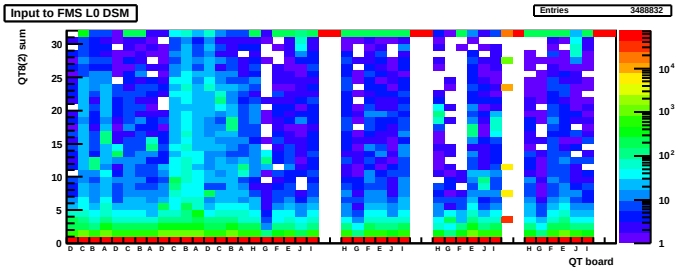

Input to QT4 crate
Entries1136327
ADC
channel

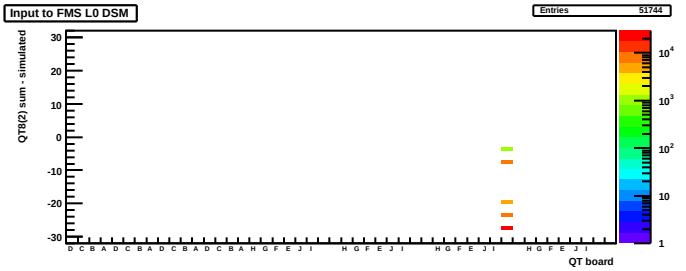

Input to FMS L0 DSM
Entries 3488832
QT8(0) sum
QT board

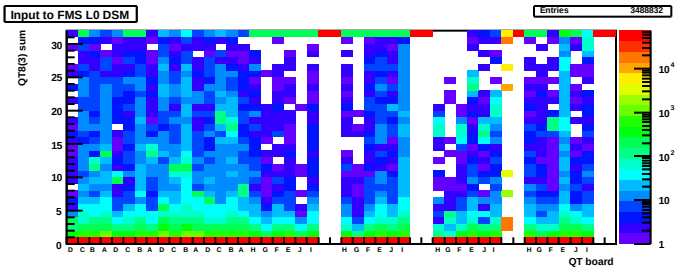

Input to FMS L0 DSM
Entries 7
QT8(0) sum - simulated
QT board

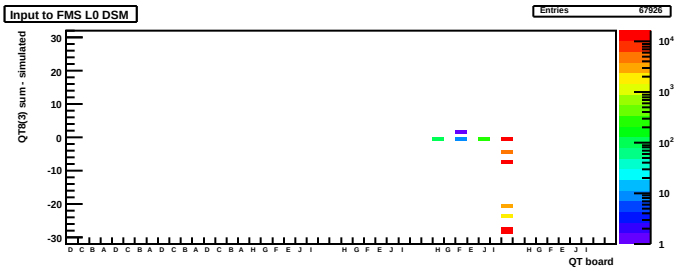

Input to FMS L0 DSM
Entries 3488832
QT8(1) sum
QT board

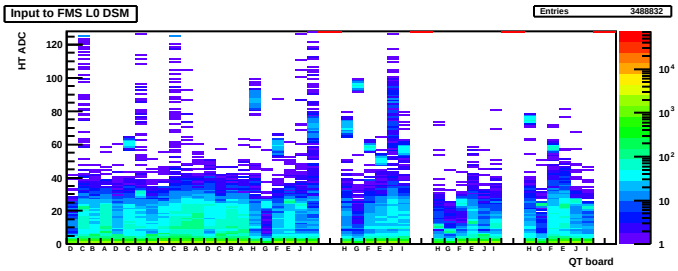

Input to FMS L0 DSM
Entries 7
QT8(1) sum - simulated
QT board

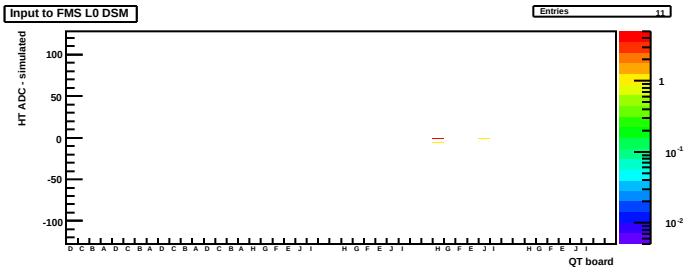

Input to FMS L0 DSM
Entries 3488832
QT8(2) sum
QT board

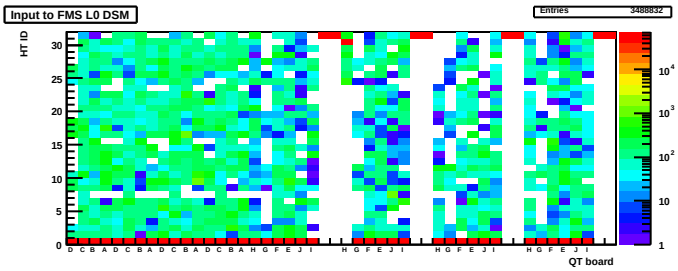

Input to FMS L0 DSM
Entries 51744
QT8(2) sum - simulated
QT board

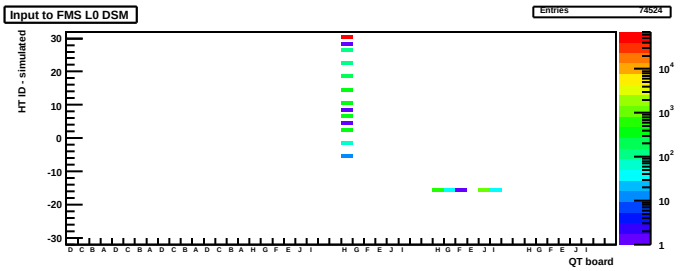

Input to FMS L0 DSM
Entries 3488832
QT8(3) sum
QT board

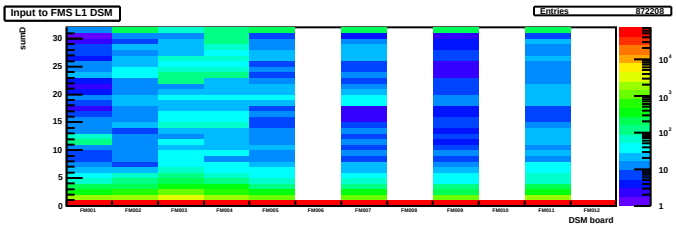

Input to FMS L0 DSM
Entries 67926
QT8(3) sum - simulated
QT board

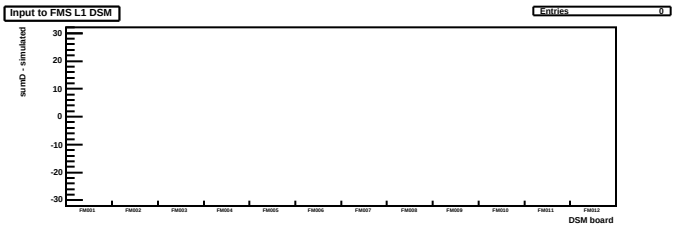

Input to FMS L0 DSM
Entries 3488832
HT ADC
QT board

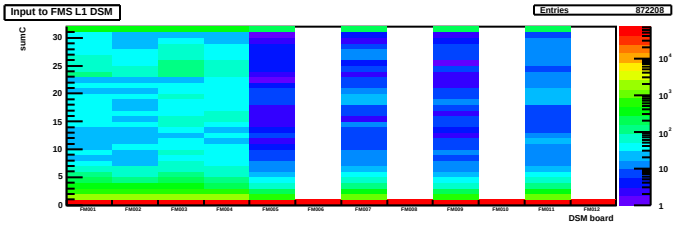

Input to FMS L0 DSM
Entries 11
HT ADC - simulated
QT board

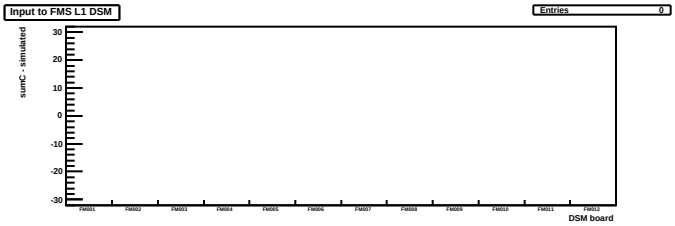

Input to FMS L0 DSM
Entries 3488832
HT ID
QT board

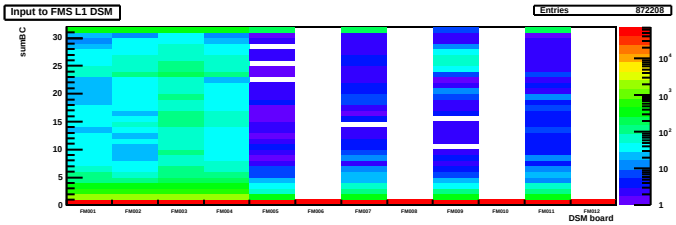

Input to FMS L0 DSM
Entries 74524
HT ID - simulated
QT board

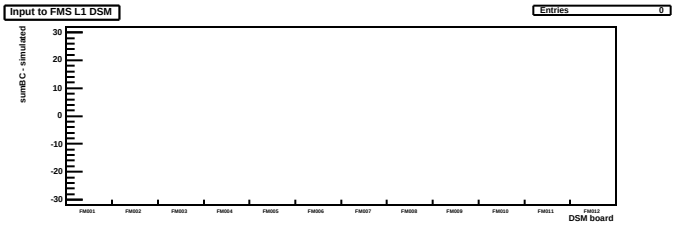

Input to FMS L1 DSM
Entries 872208
sumD
DSM board

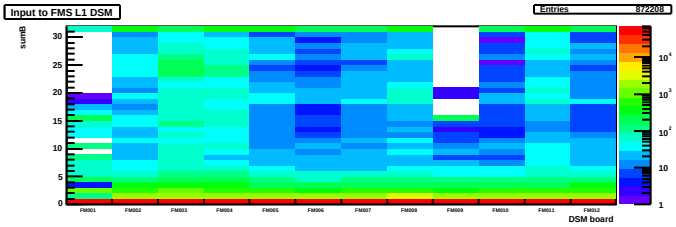

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

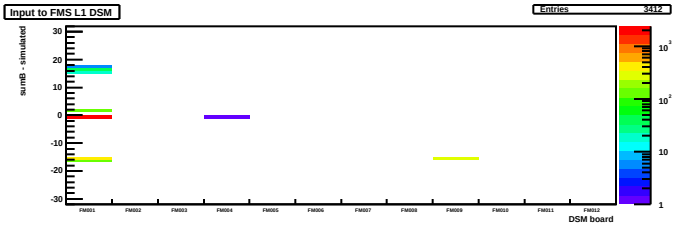

Input to FMS L1 DSM
Entries 872208
sumC
DSM board

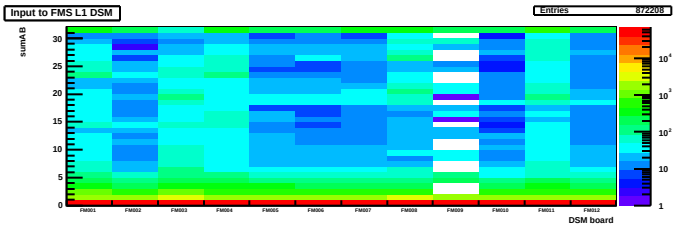

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

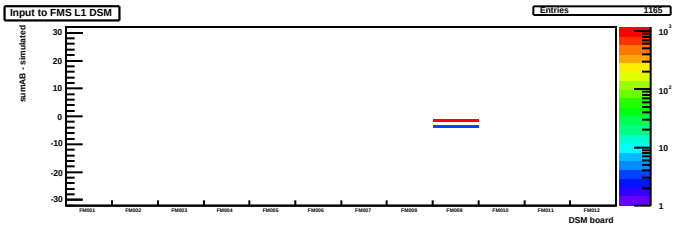

Input to FMS L1 DSM
Entries 872208
sumBC
DSM board

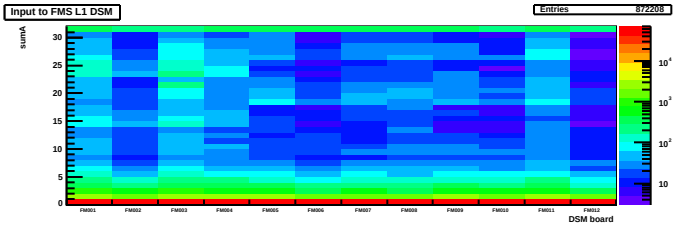

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

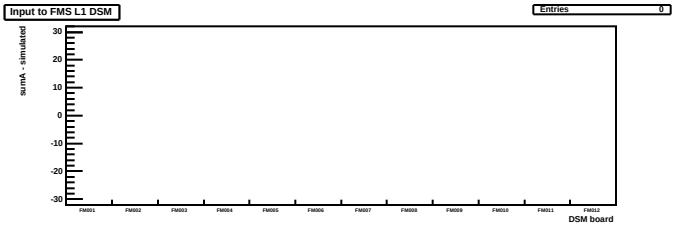

Input to FMS L1 DSM
Entries 872208
sumB
DSM board

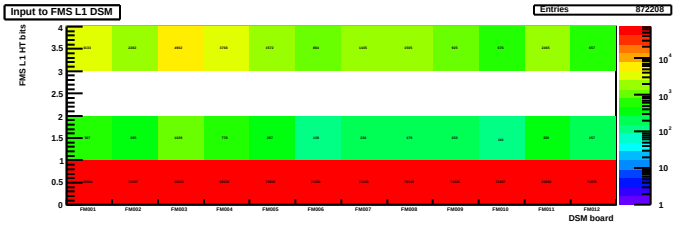

Input to FMS L1 DSM
Entries 3412
sumB - simulated
DSM board

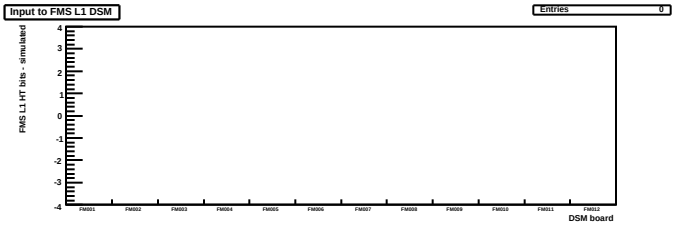

Input to FMS L1 DSM
Entries 872208
sumAB
DSM board

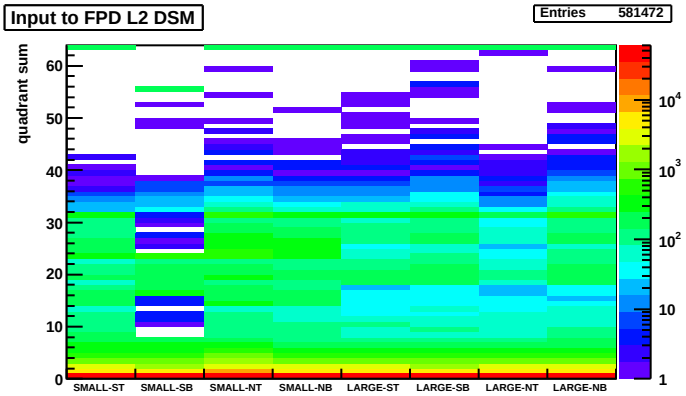

Input to FMS L1 DSM
Entries 1165
sumAB - simulated
DSM board

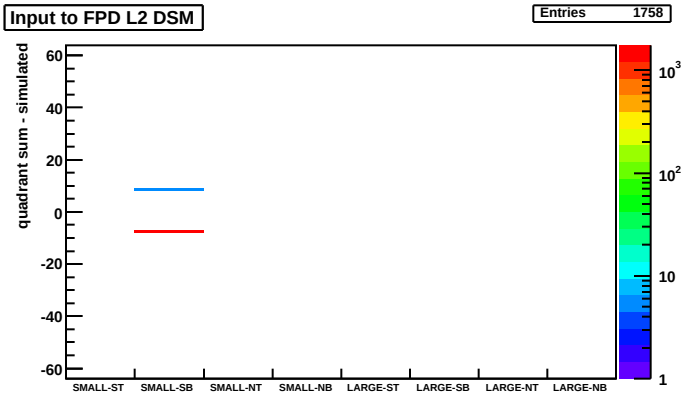

Input to FMS L1 DSM
Entries 872208
sumA
DSM board

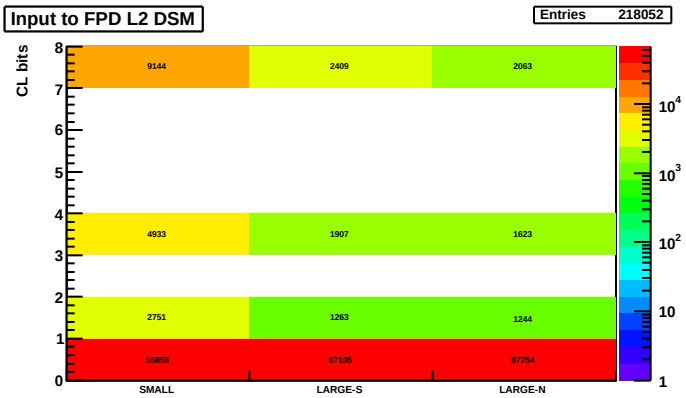

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

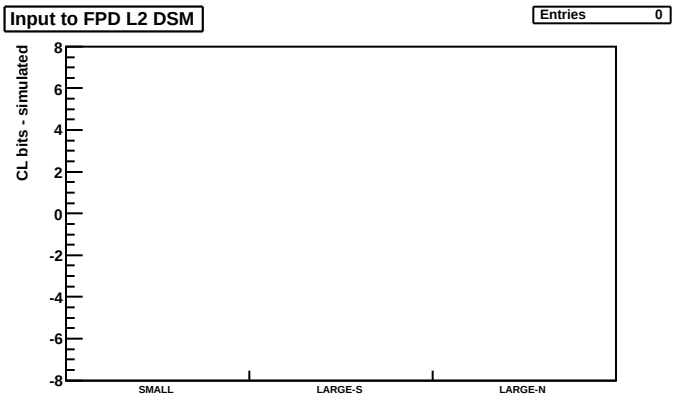

Input to FMS L1 DSM
Entries 872208
FMS L1 HT bits
DSM board

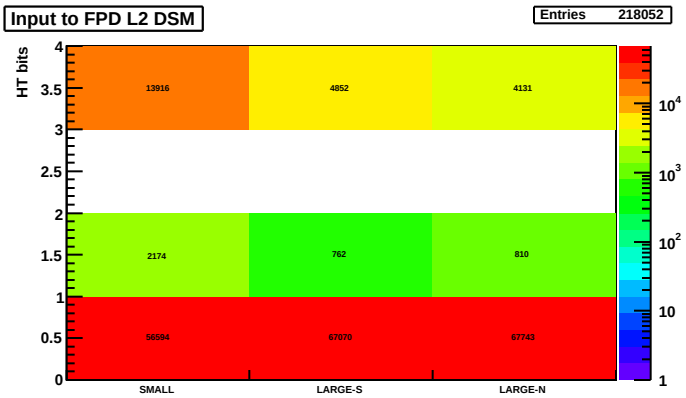

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

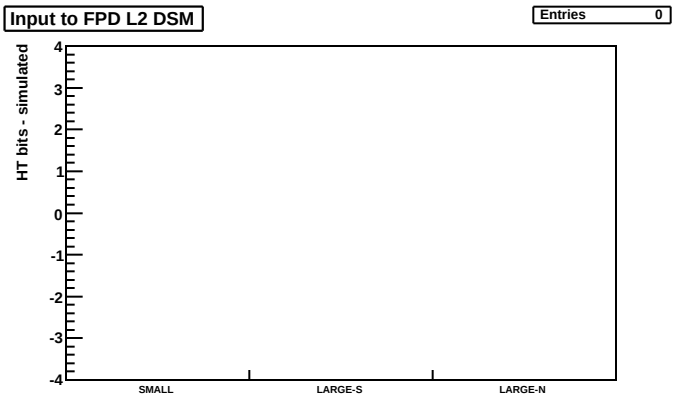

Input to FPD L2 DSM
Entries 581472
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

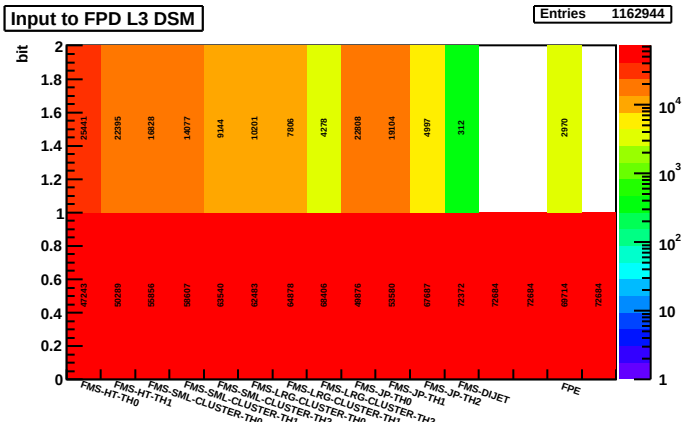

Input to FPD L2 DSM
Entries 1758
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

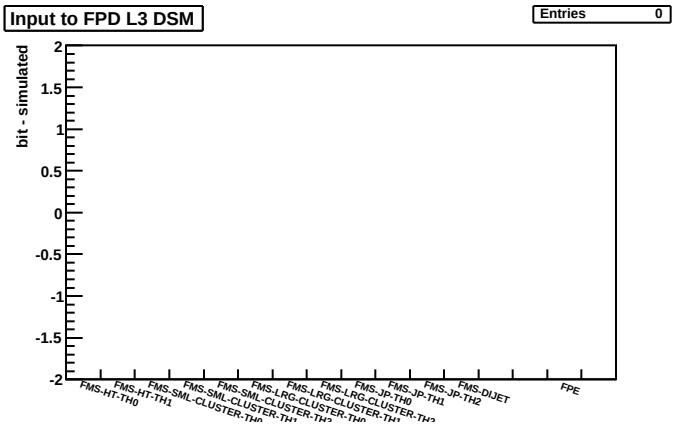

Input to FPD L2 DSM
Entries 218052
CL bits
9144 2409 2063
4933 1907 1623
2751 1263 1244
55856 67105 67754
SMALL LARGE-S LARGE-N

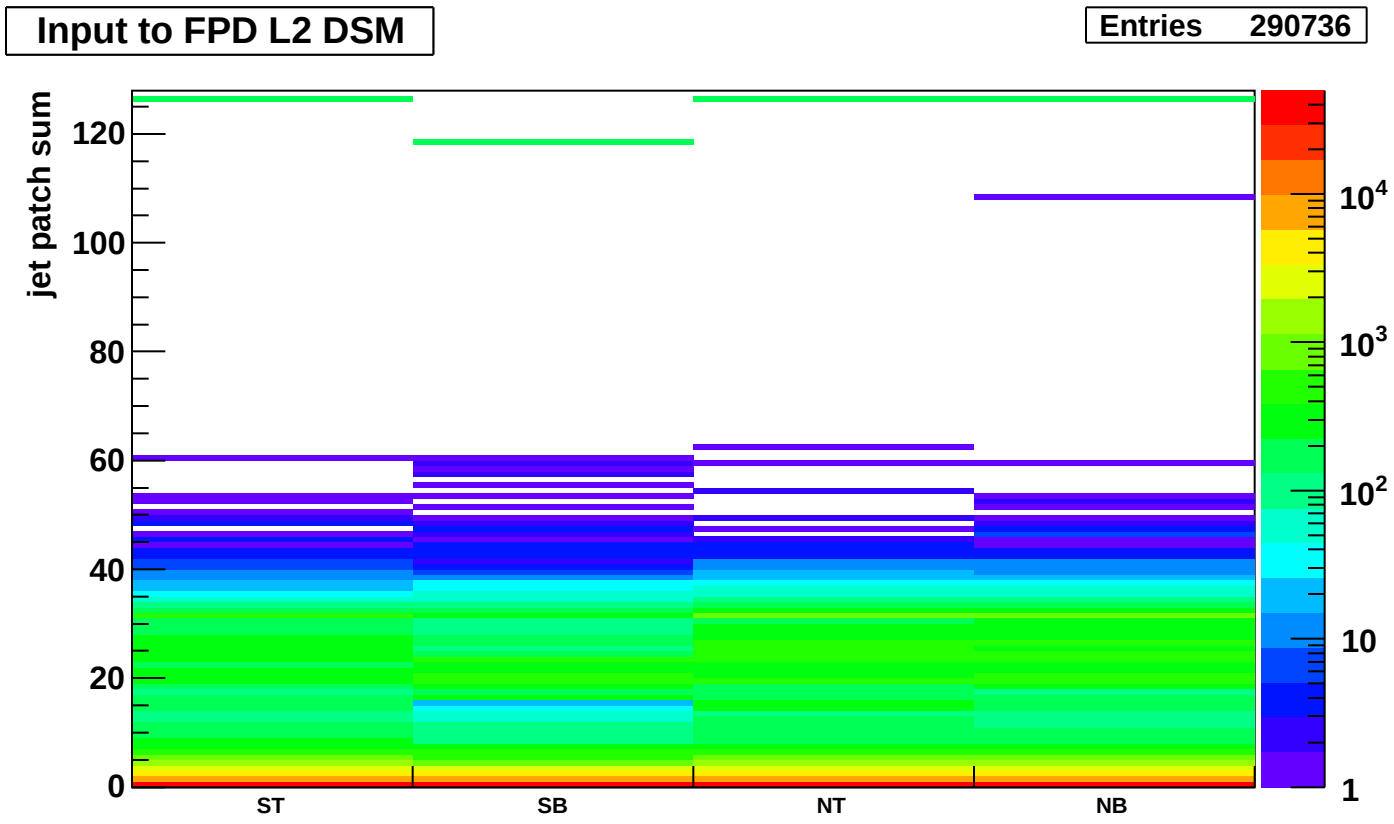

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

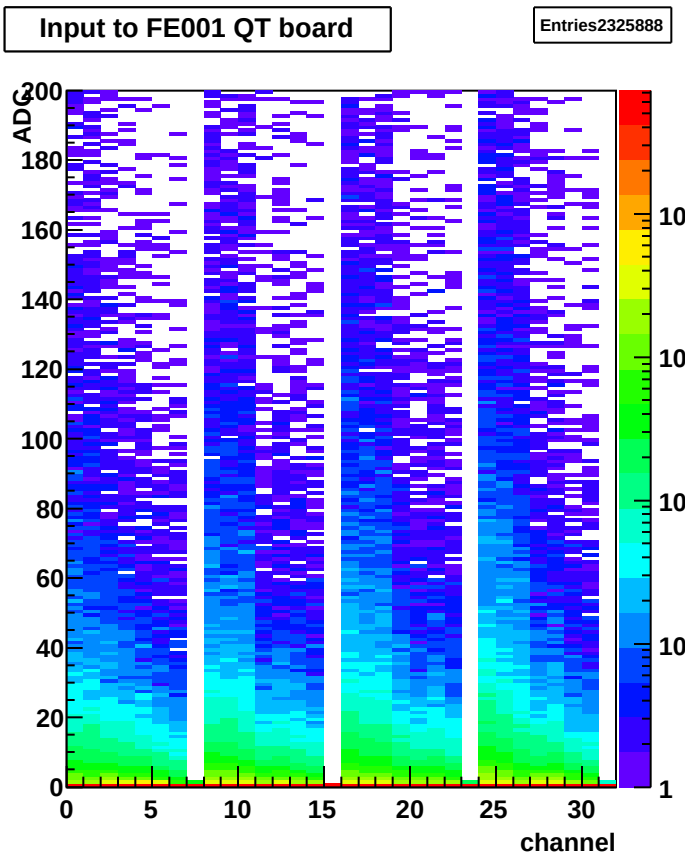

Input to FPD L2 DSM
Entries 218052
HT bits
13916 4852 4131
2174 762 810
56594 67070 67743
SMALL LARGE-S LARGE-N

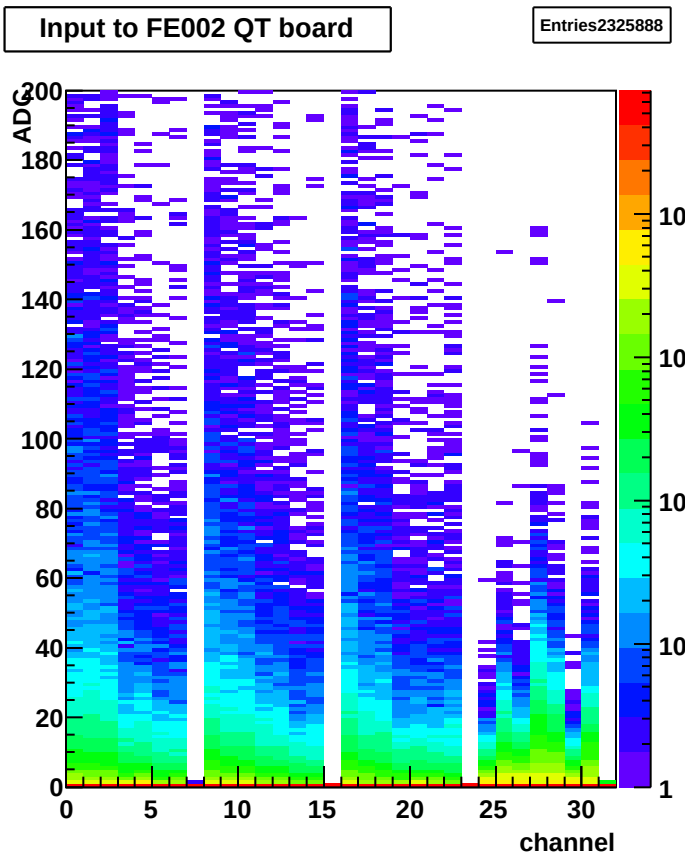

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

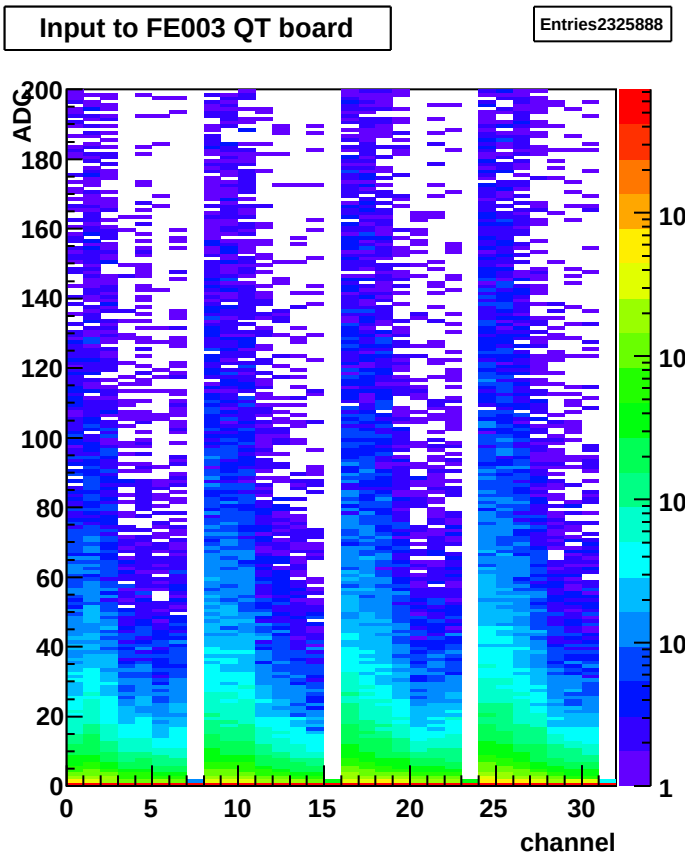

Input to FPD L3 DSM
Entries 1162944
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

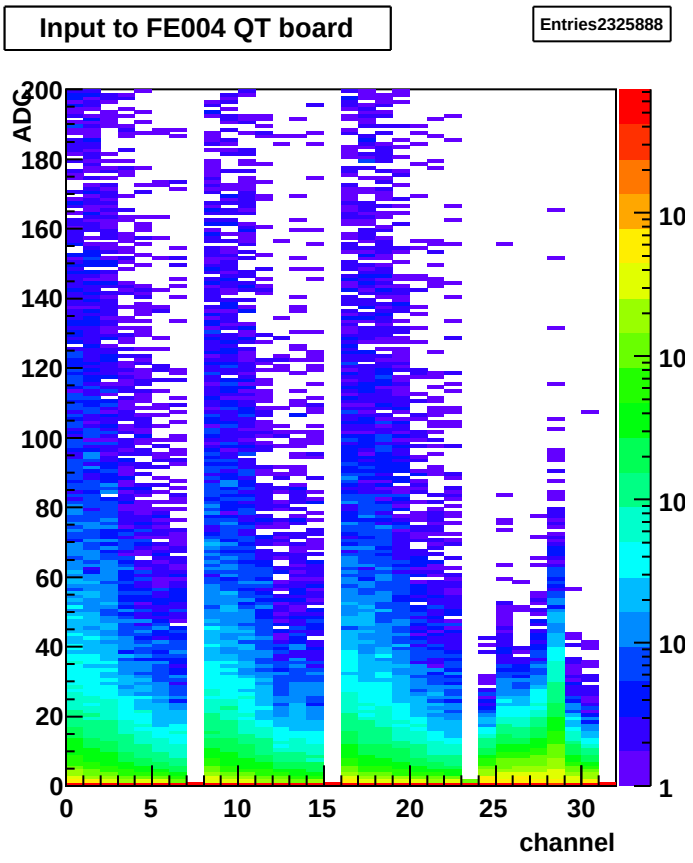

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

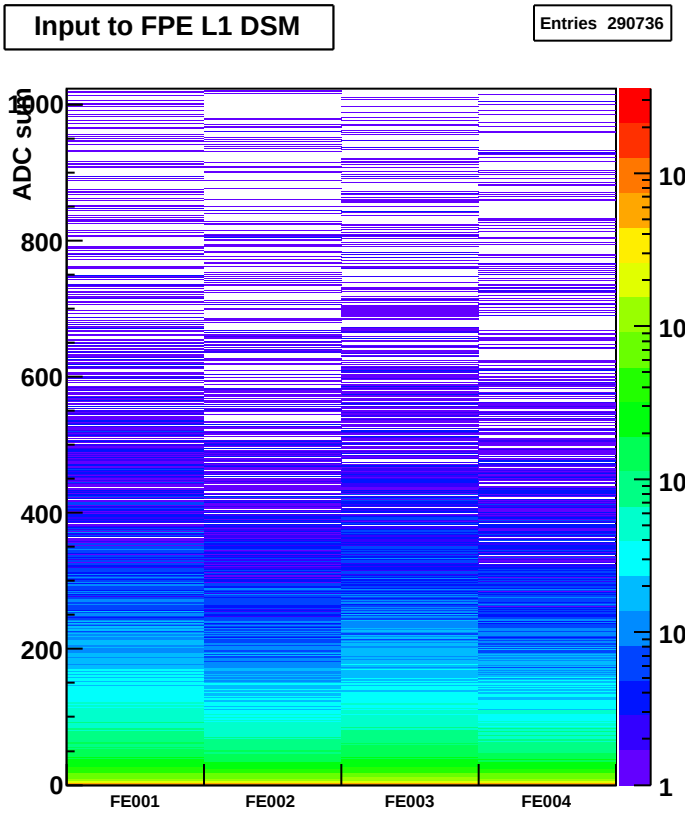

Input to FPD L2 DSM
Entries 290736
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

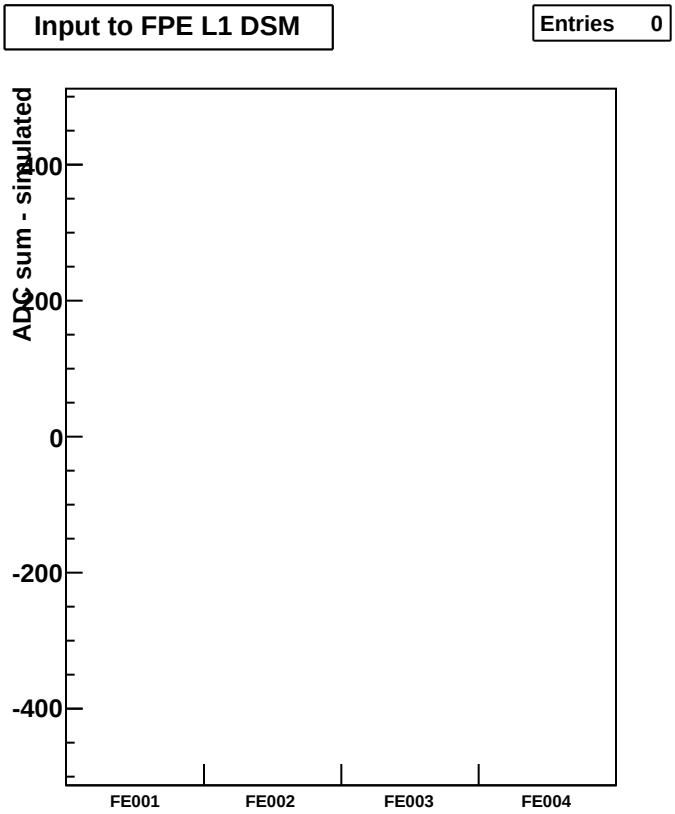

Input to FE001 QT board
Entries2325888
ADC
channel

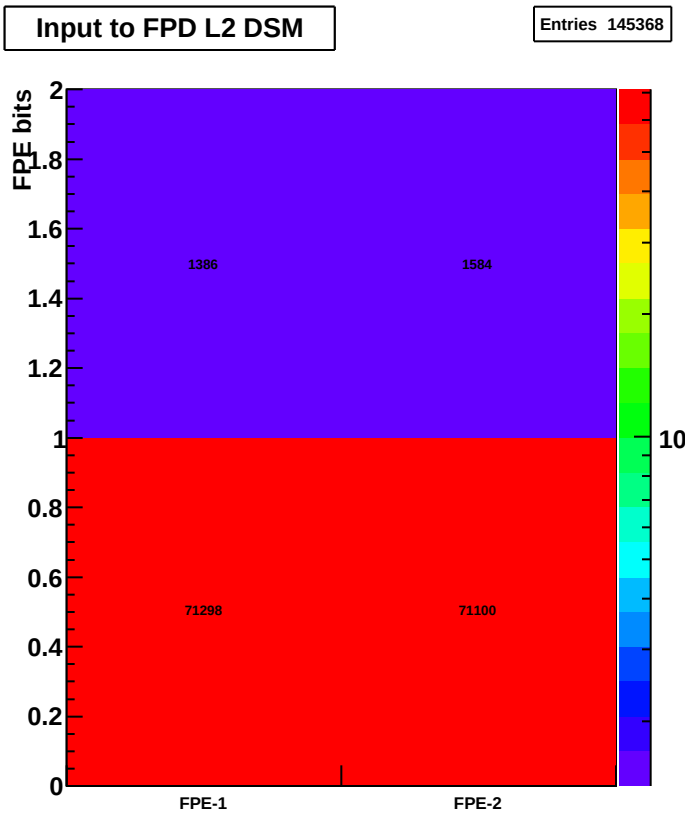

Input to FE002 QT board
Entries2325888
ADC
channel

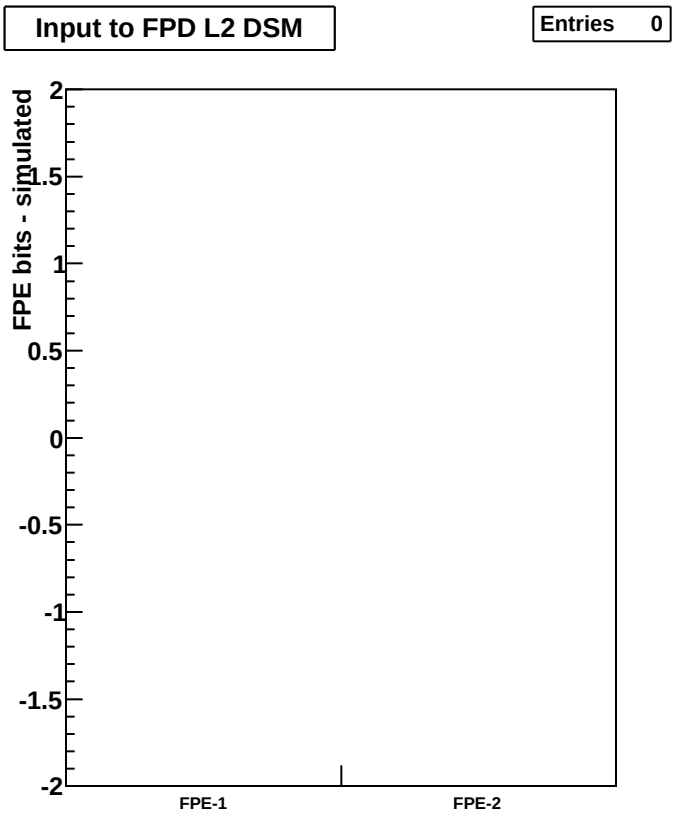

Input to FE003 QT board
Entries2325888
ADC
channel


Input to FE004 QT board
Entries2325888
ADC
channel


Input to FPE L1 DSM
Entries 290736
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 145368
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
1386
1584
71298
71100
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2