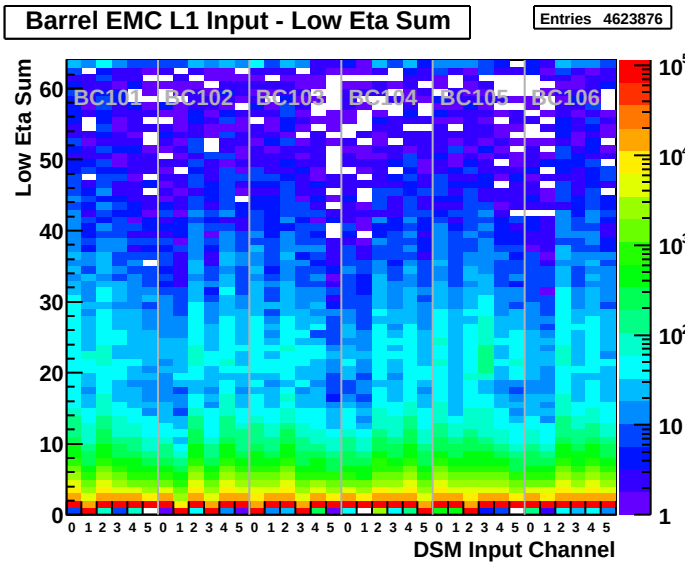
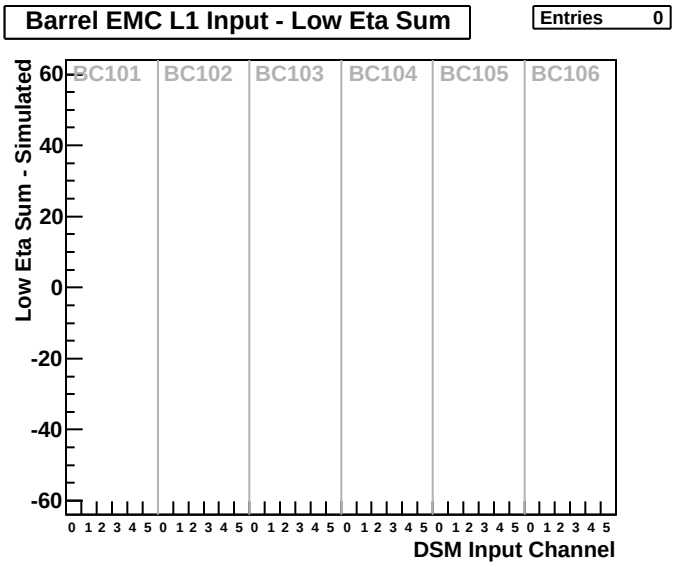
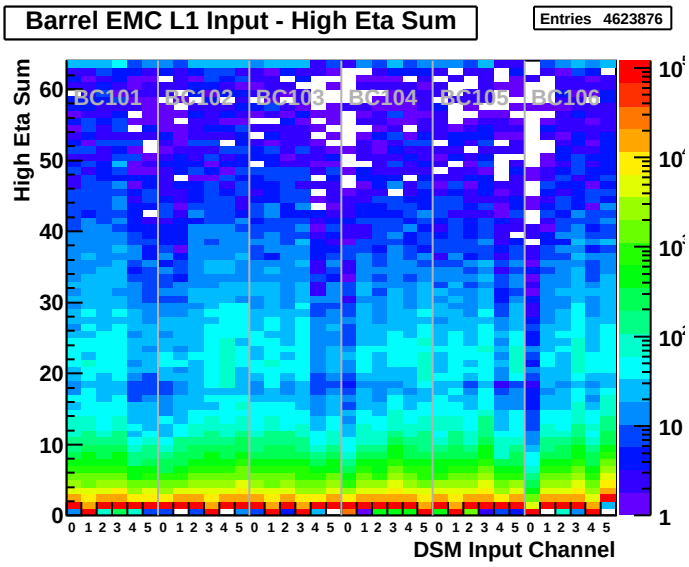
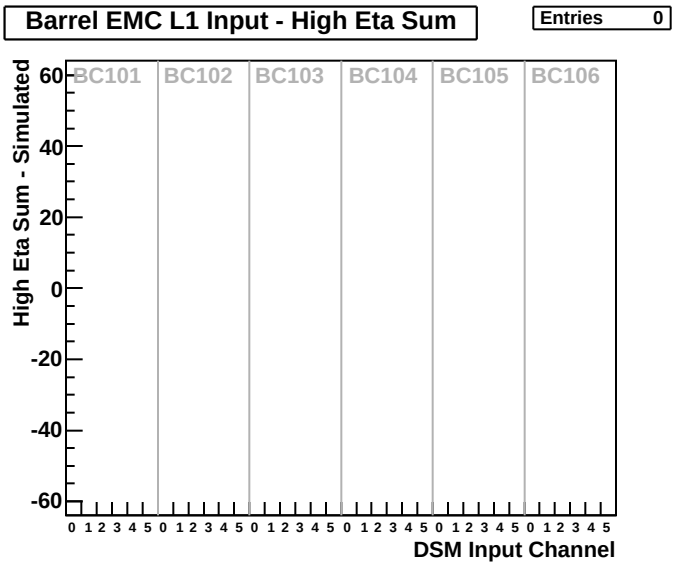
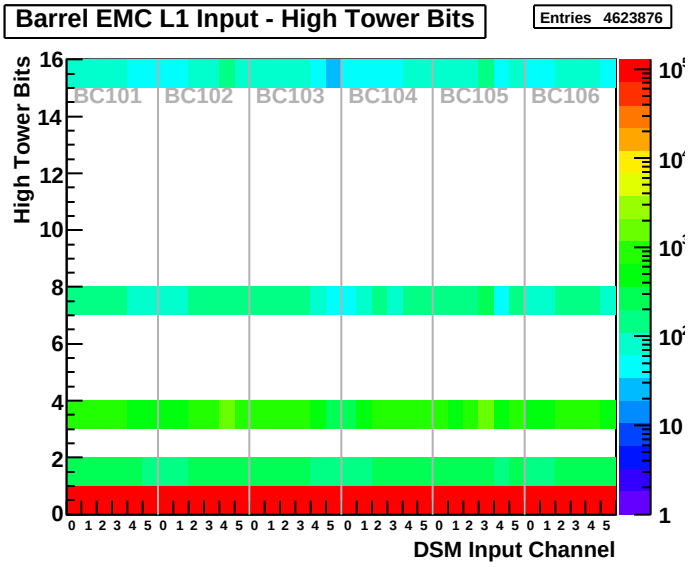
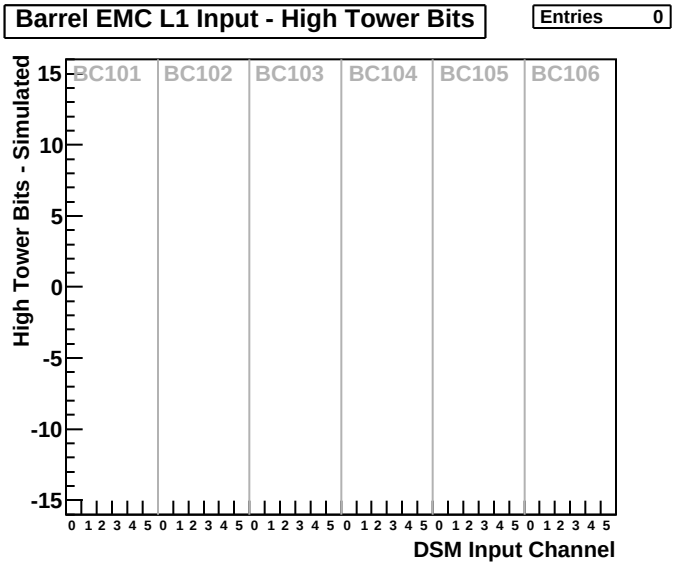

Barrel EMC L1 Input - Low Eta Sum
Entries 4623876
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

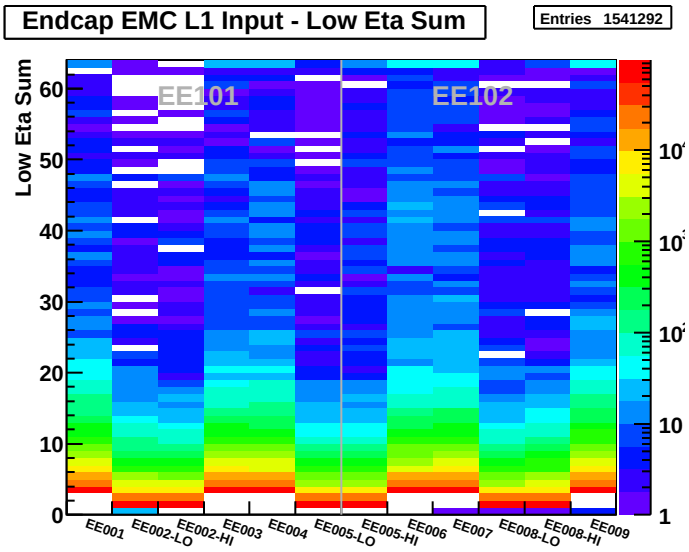

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

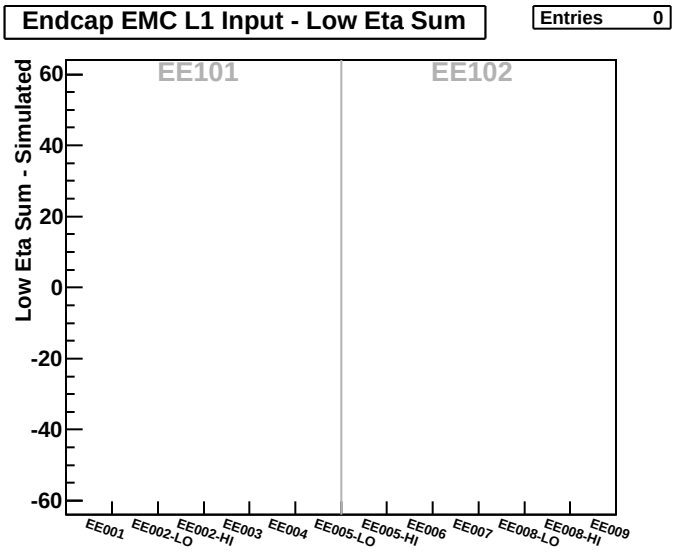

Barrel EMC L1 Input - High Eta Sum
Entries 4623876
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

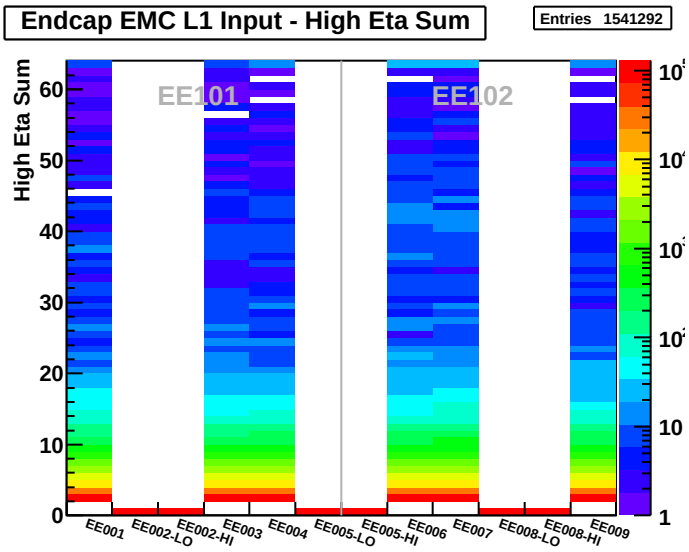

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

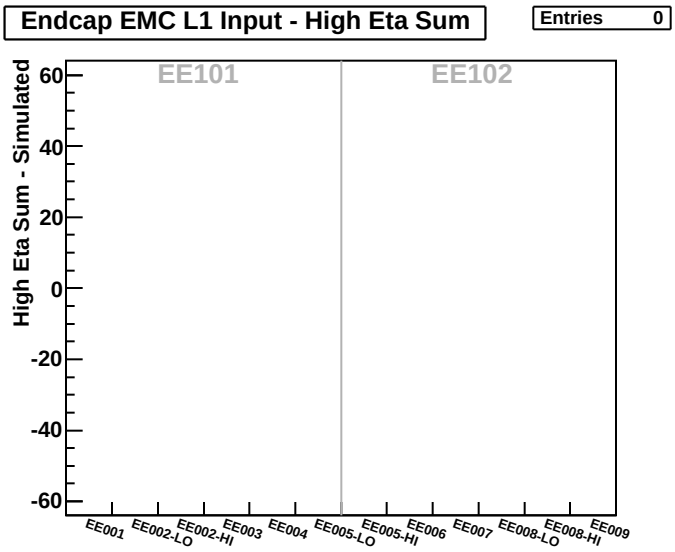

Barrel EMC L1 Input - High Tower Bits
Entries 4623876
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

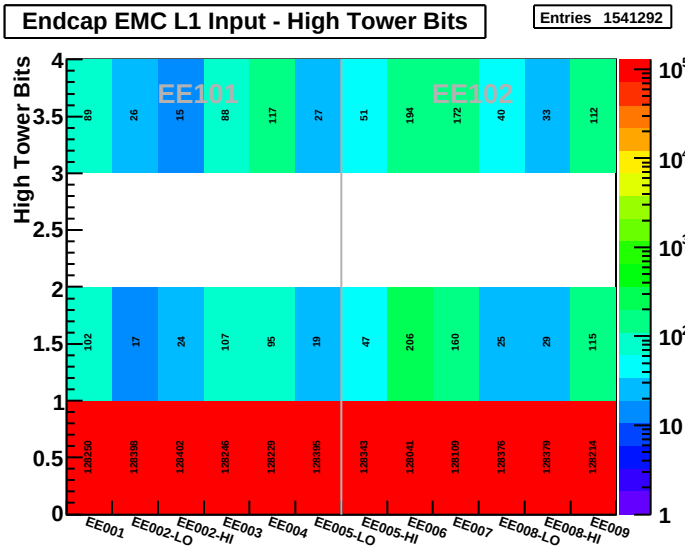

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

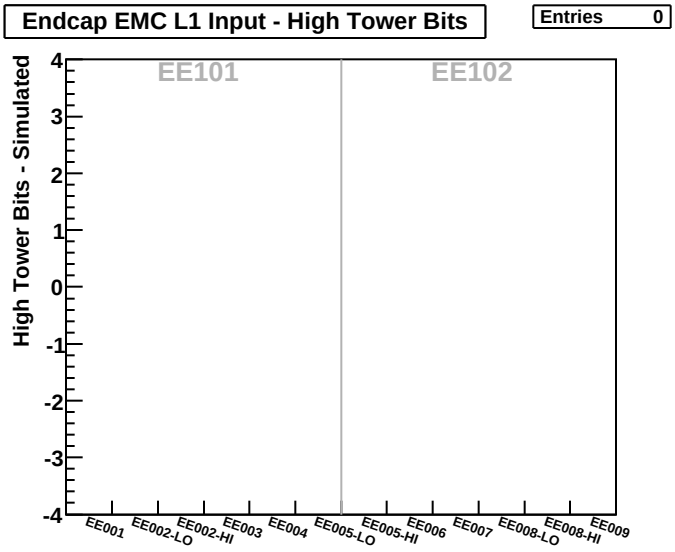

Endcap EMC L1 Input - Low Eta Sum
Entries 1541292
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

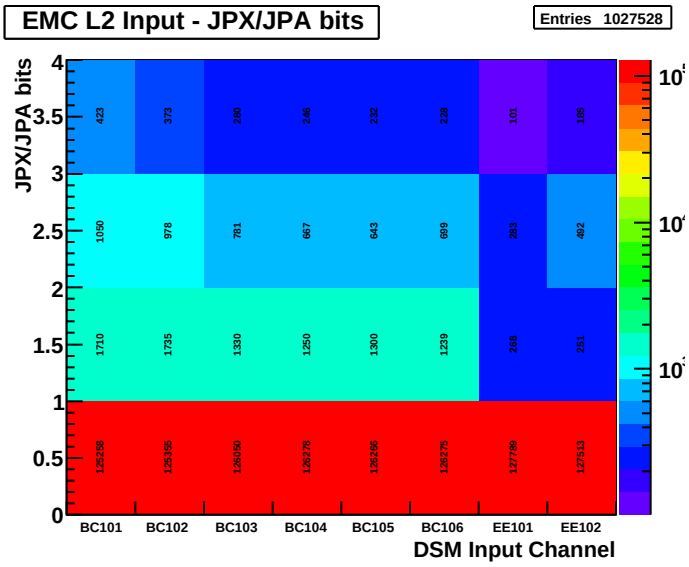

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

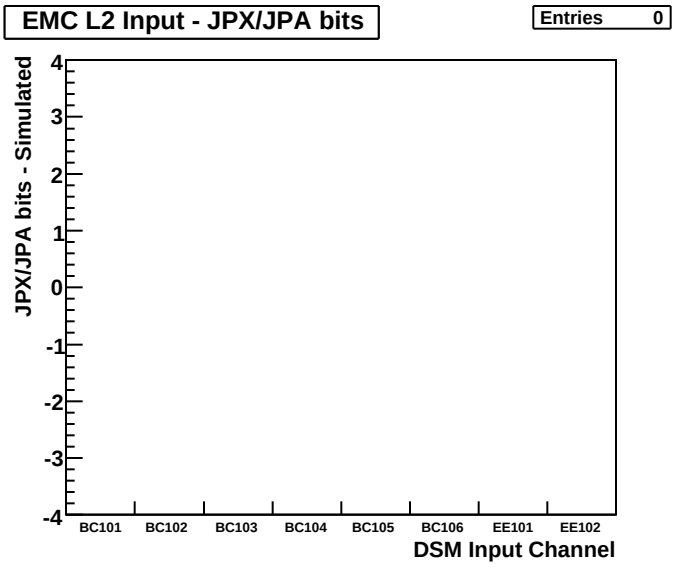

Endcap EMC L1 Input - High Eta Sum
Entries 1541292
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

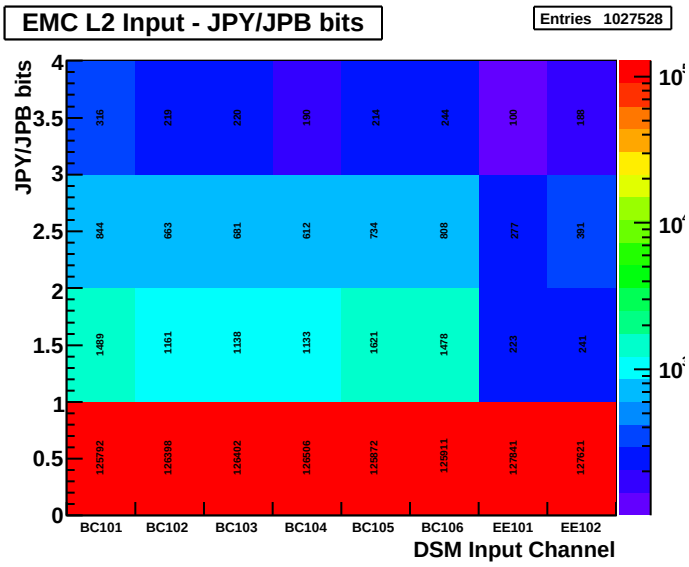

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

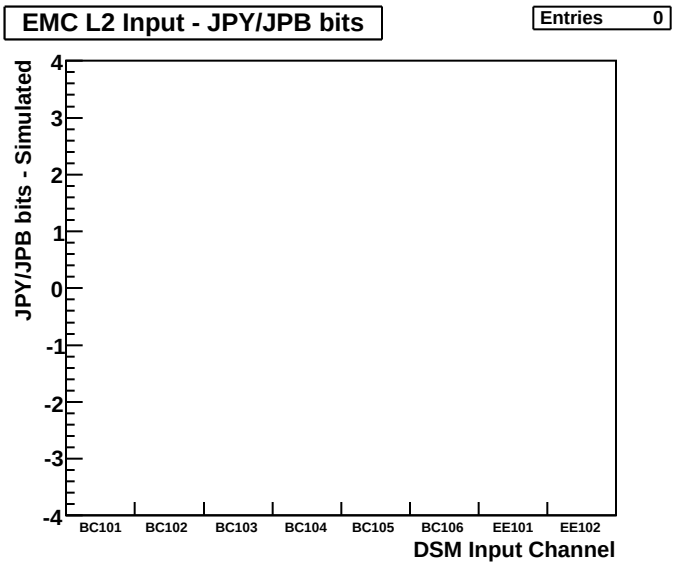

Endcap EMC L1 Input - High Tower Bits
Entries 1541292
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

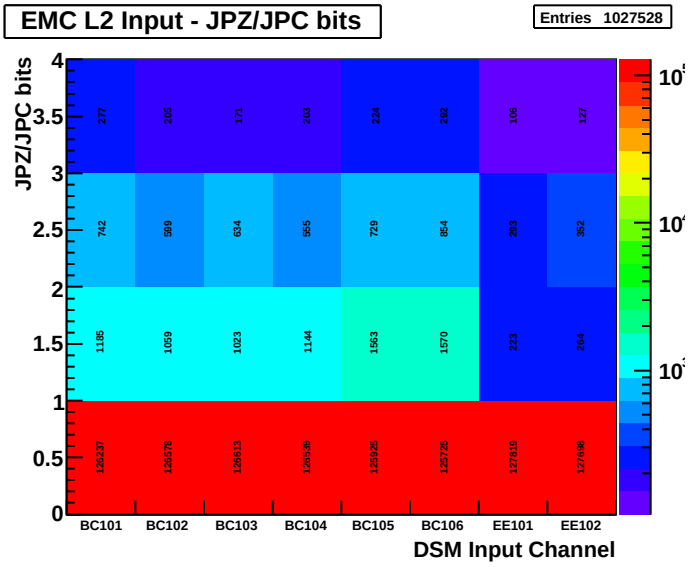

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

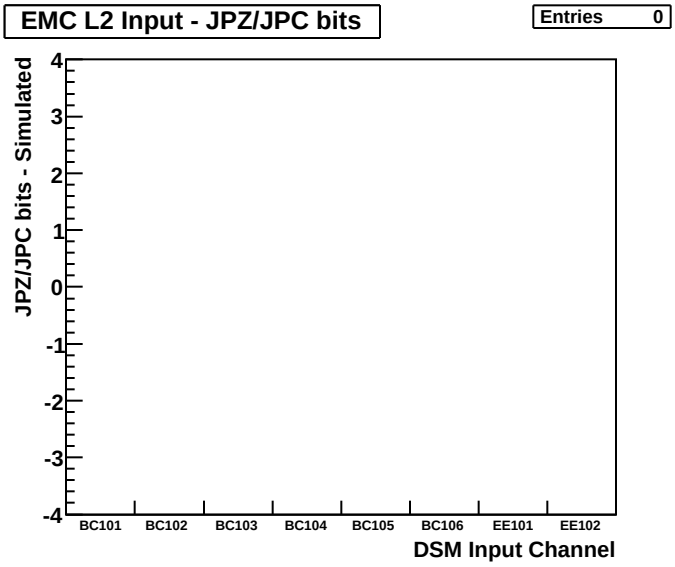

EMC L2 Input - JPX/JPA bits
Entries 1027528
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
423 373 280 246 232 228 101 185
1050 978 781 667 643 699 283 492
1710 1735 1330 1250 1300 1239 268 251
125298 125385 126050 126278 126266 126275 127789 127513

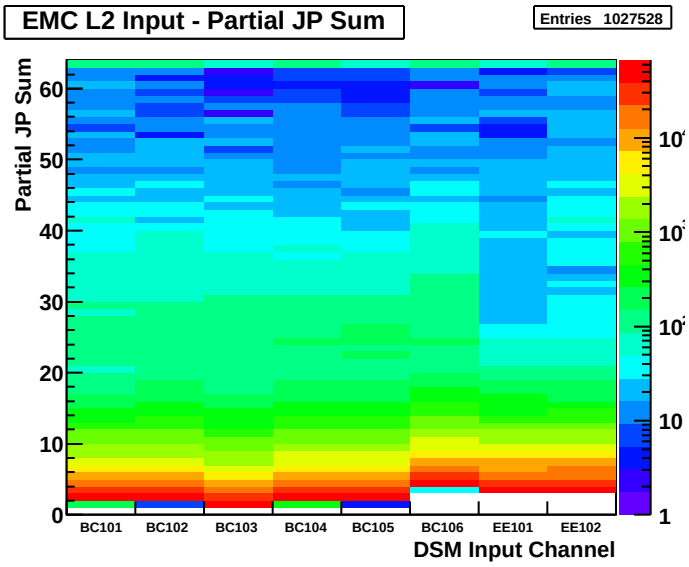

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

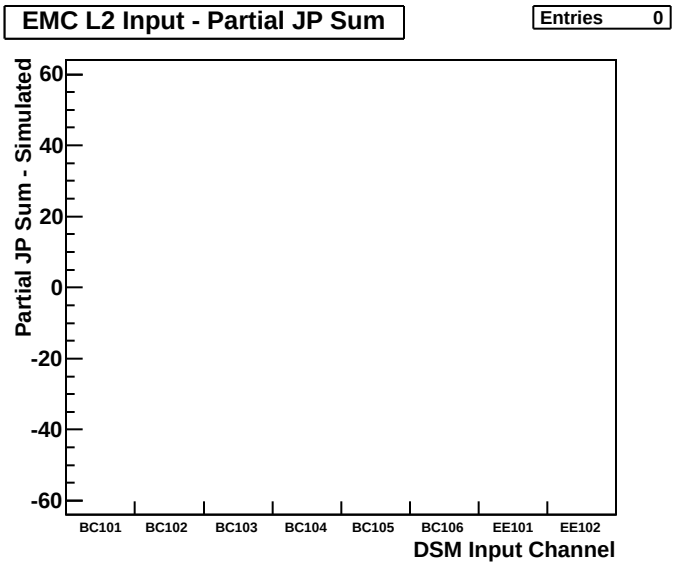

EMC L2 Input - JPY/JPB bits
Entries 1027528
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
316 219 220 190 214 244 100 188
844 663 681 612 734 808 277 391
1489 1161 1138 1133 1621 1478 223 241
125792 126398 126402 126506 125872 125911 127841 127621

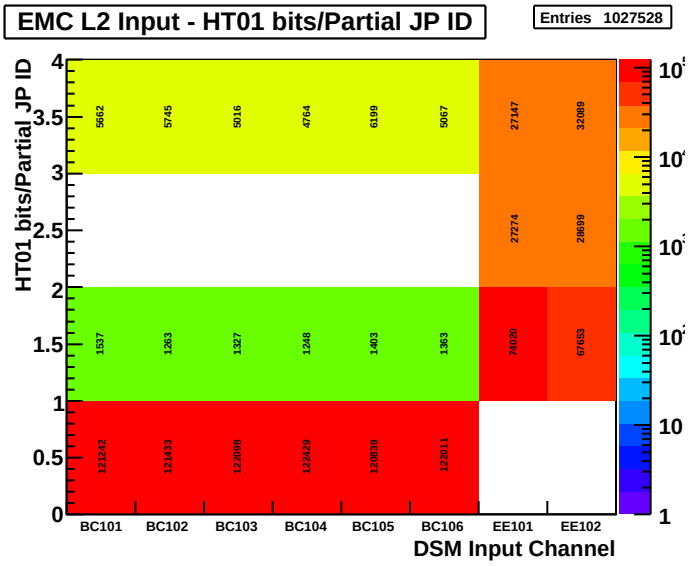

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

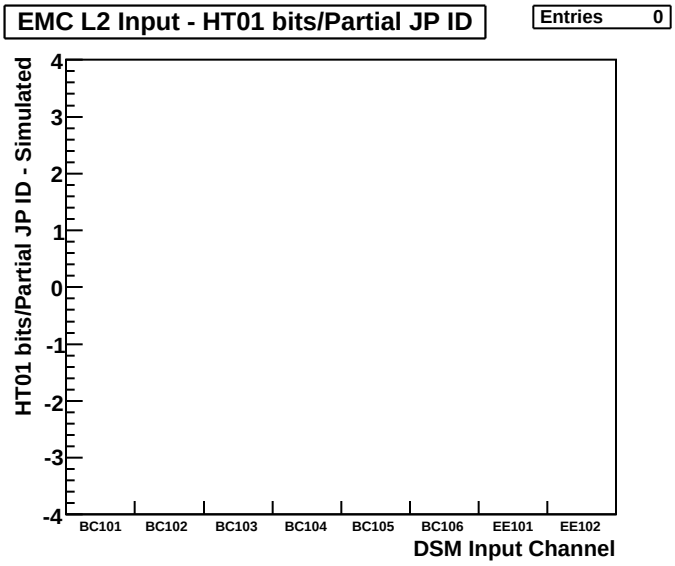

EMC L2 Input - JPZ/JPC bits
Entries 1027528
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
277 205 171 203 224 292 106 127
742 599 634 565 729 854 293 352
1185 1069 1023 1144 1563 1570 223 234
126237 126578 126613 126539 125925 125725 127819 127688

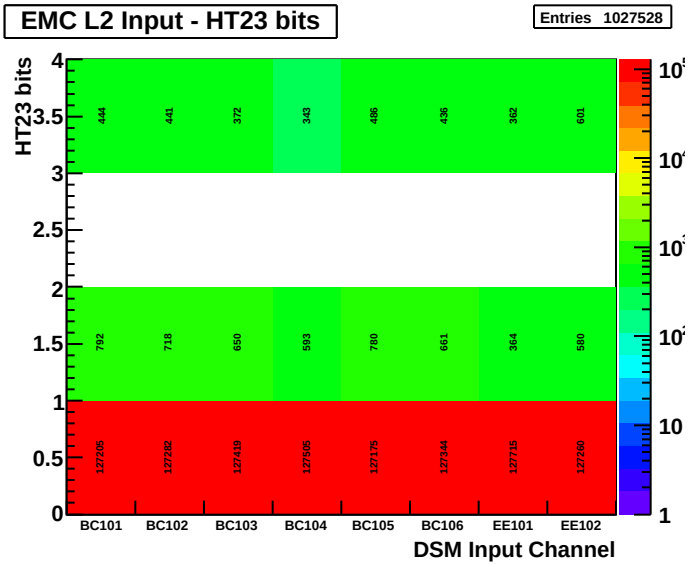

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

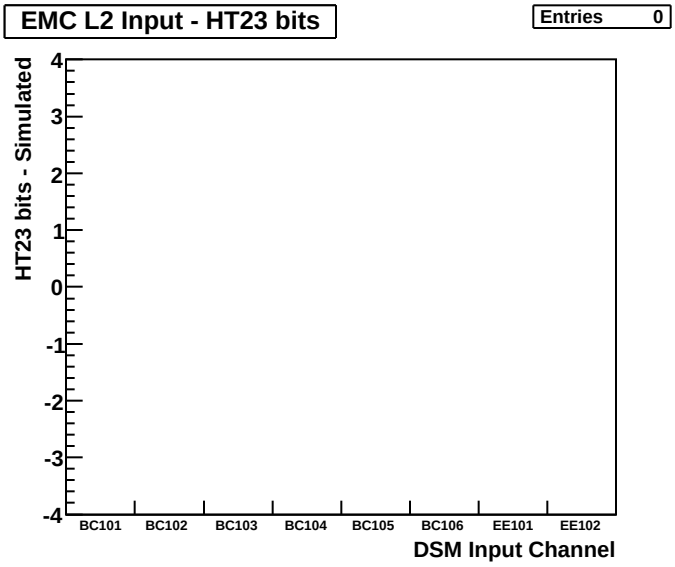

EMC L2 Input - Partial JP Sum
Entries 1027528
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

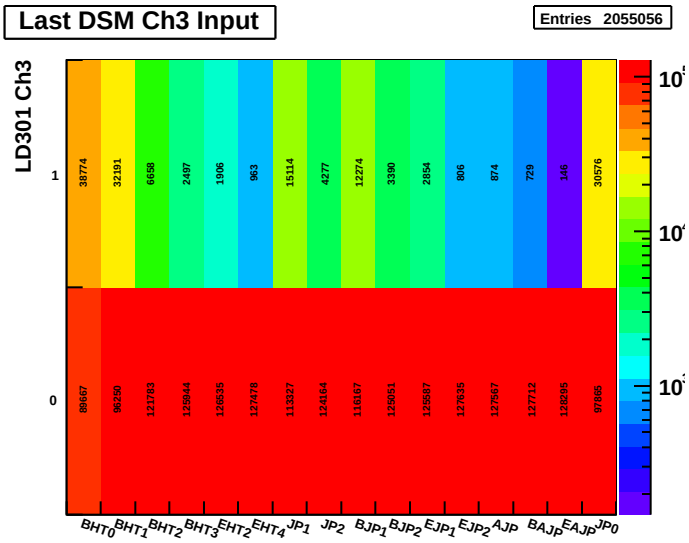

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

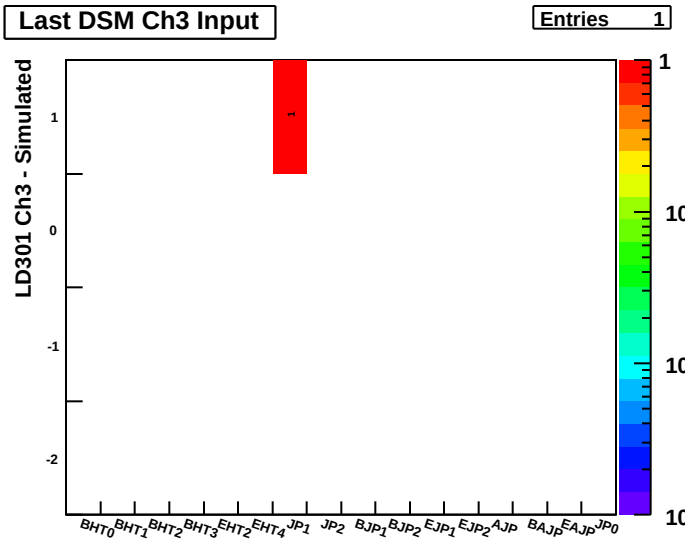

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1027528
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

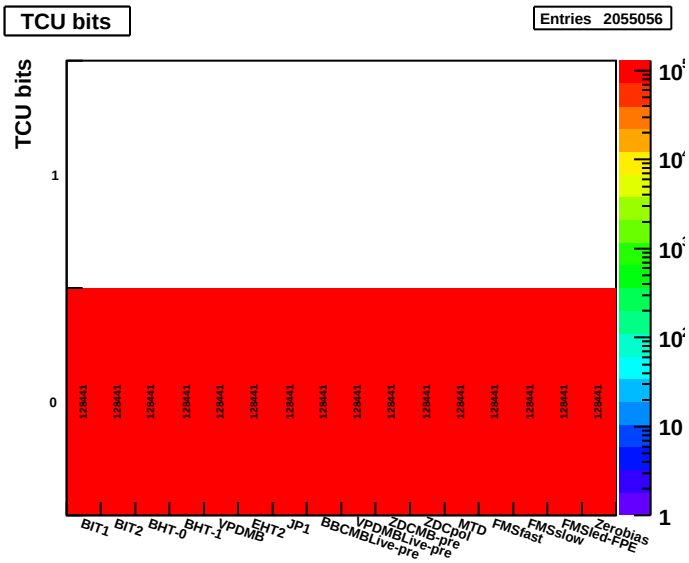

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

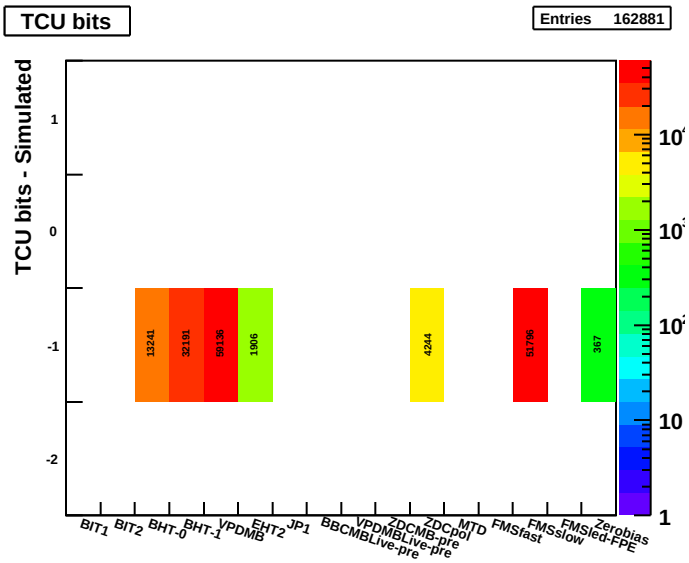

EMC L2 Input - HT23 bits
Entries 1027528
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

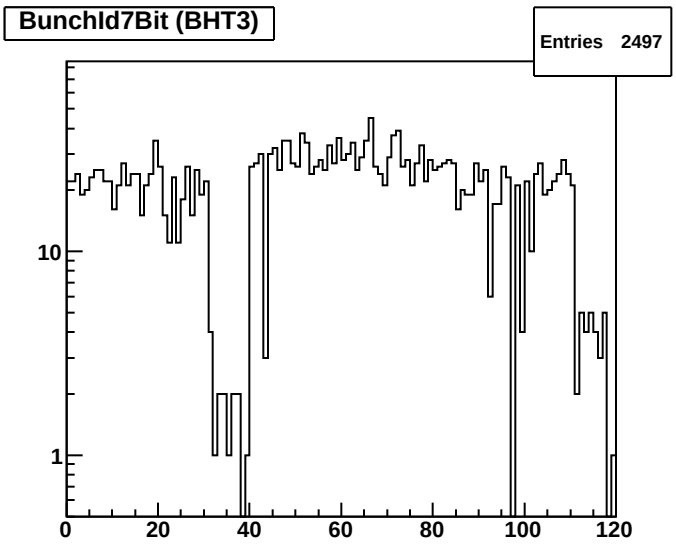

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

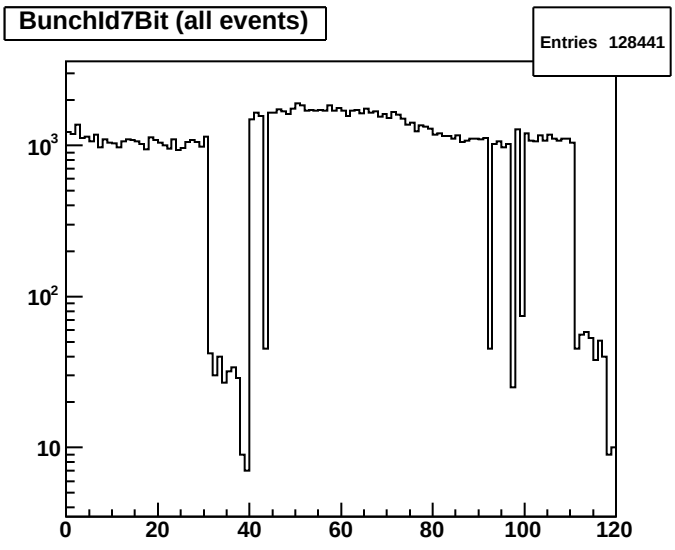

Last DSM Ch3 Input
Entries 2055056
LD301 Ch3
38774 32191 6658 2497 1906 963 15114 4277 12274 3390 2854 806 874 729 146 30576
89667 95250 121783 125944 126535 127478 113327 124164 116167 125051 125587 127635 127567 127712 128295 97865
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

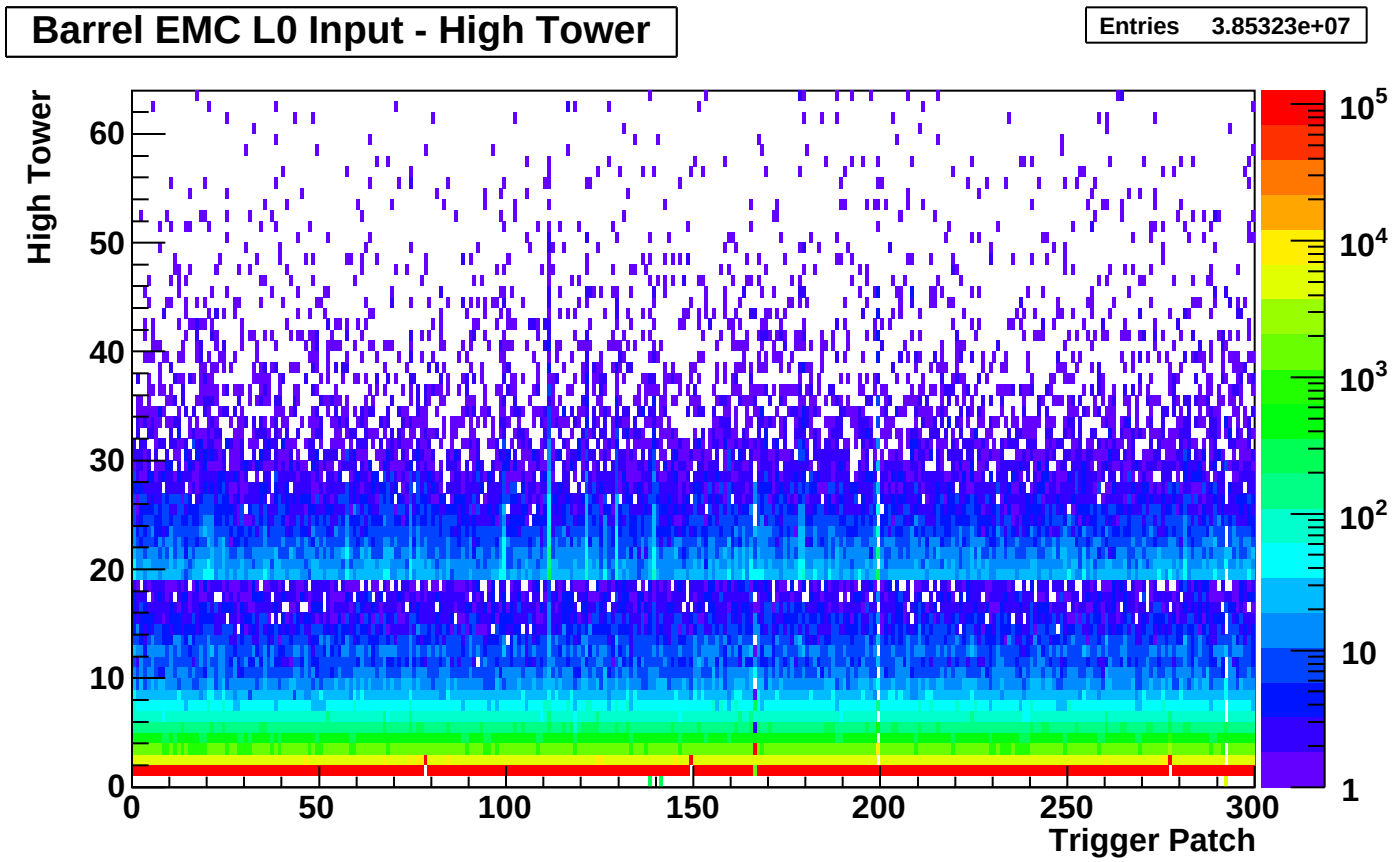

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

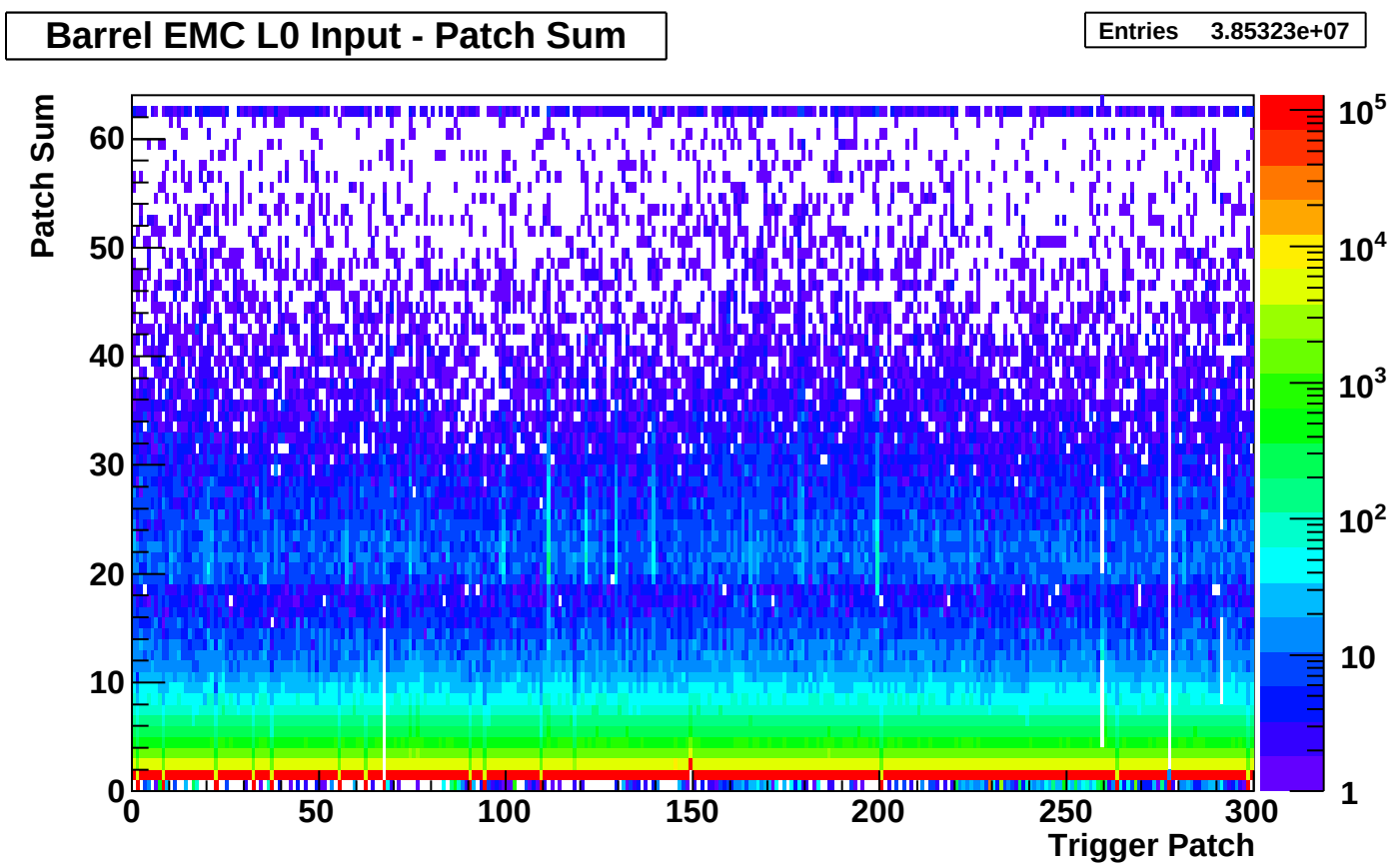

TCU bits
Entries 2055056
TCU bits
128441 128441 128441 128441 128441 128441 128441 128441 128441 128441 128441 128441 128441 128441 128441
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

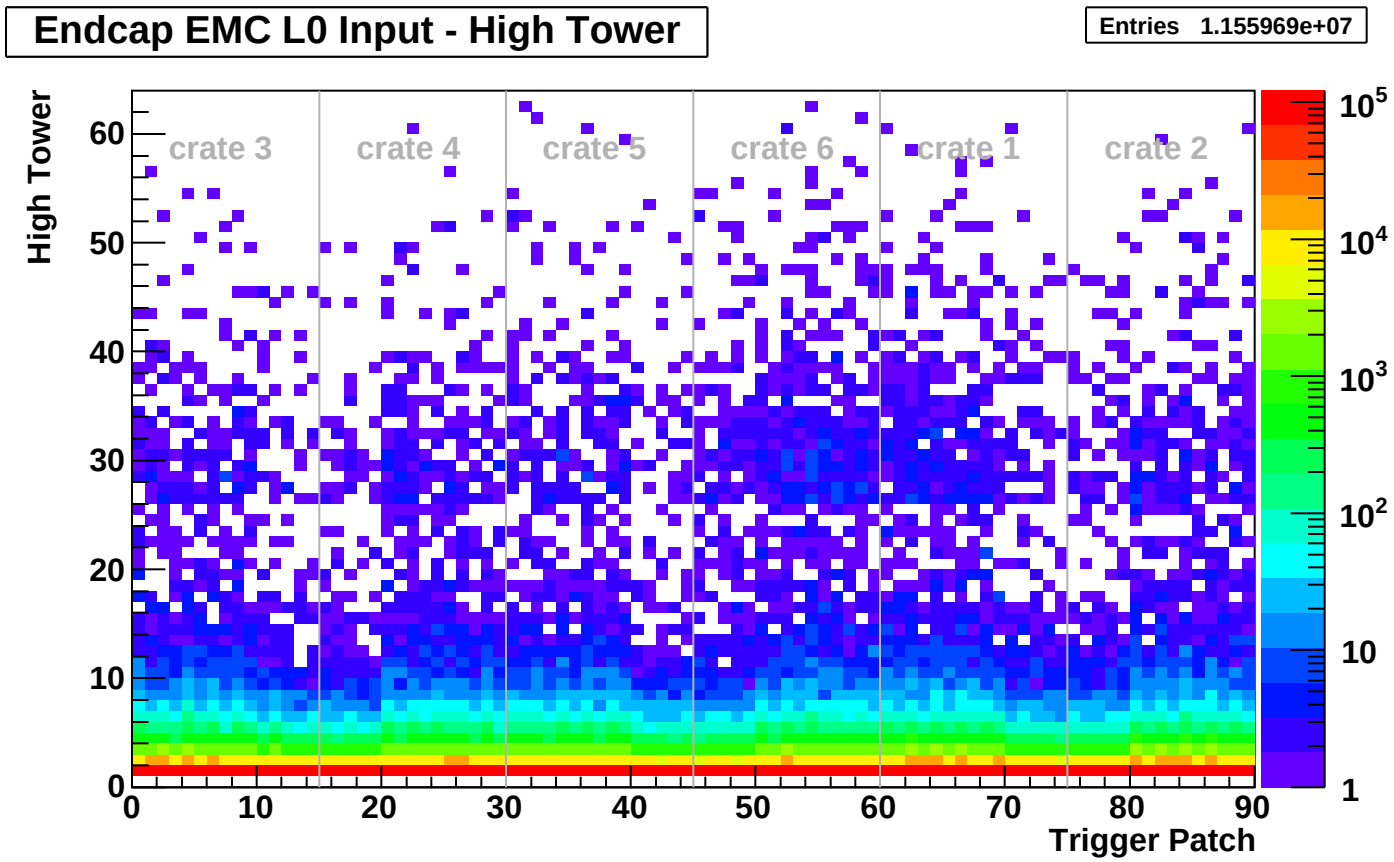

TCU bits
Entries 162881
TCU bits - Simulated
13241 32191 59136 1906 4244 51796 367
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

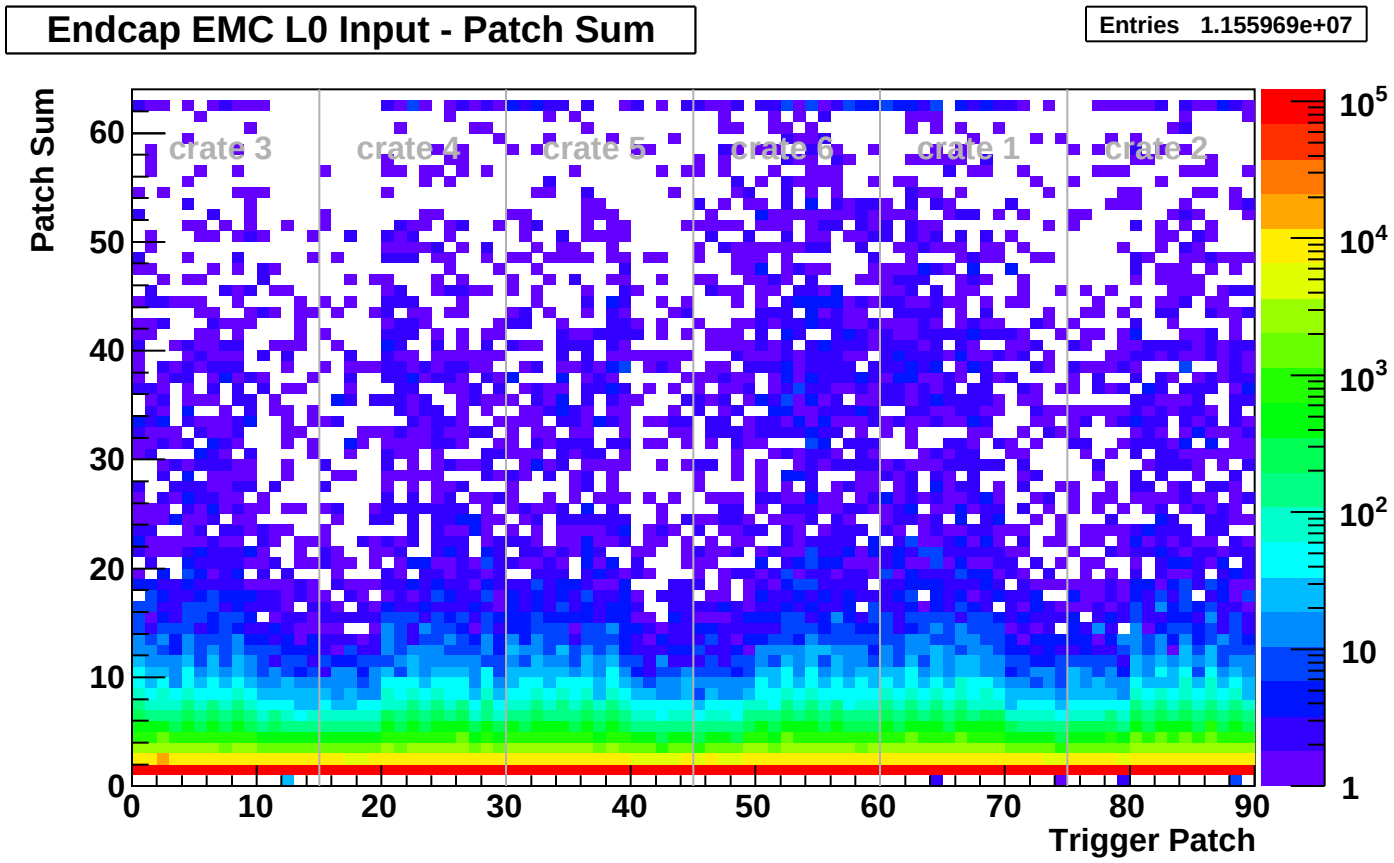

BunchId7Bit (BHT3)
Entries 2497

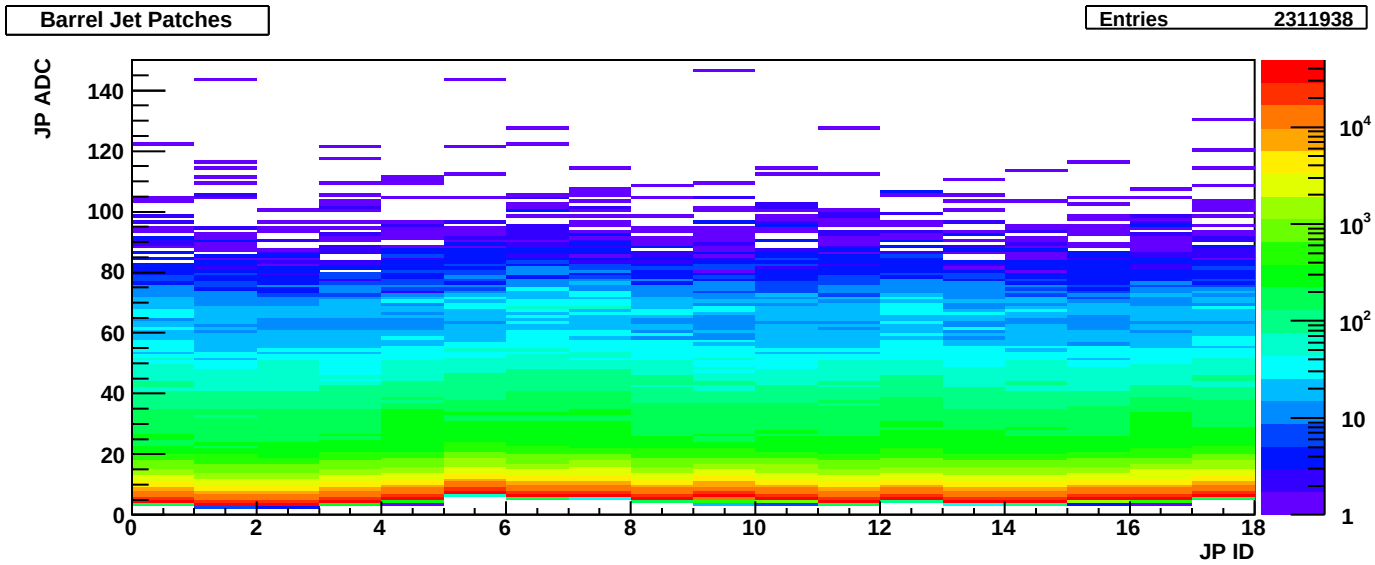

BunchId7Bit (all events)
Entries 128441

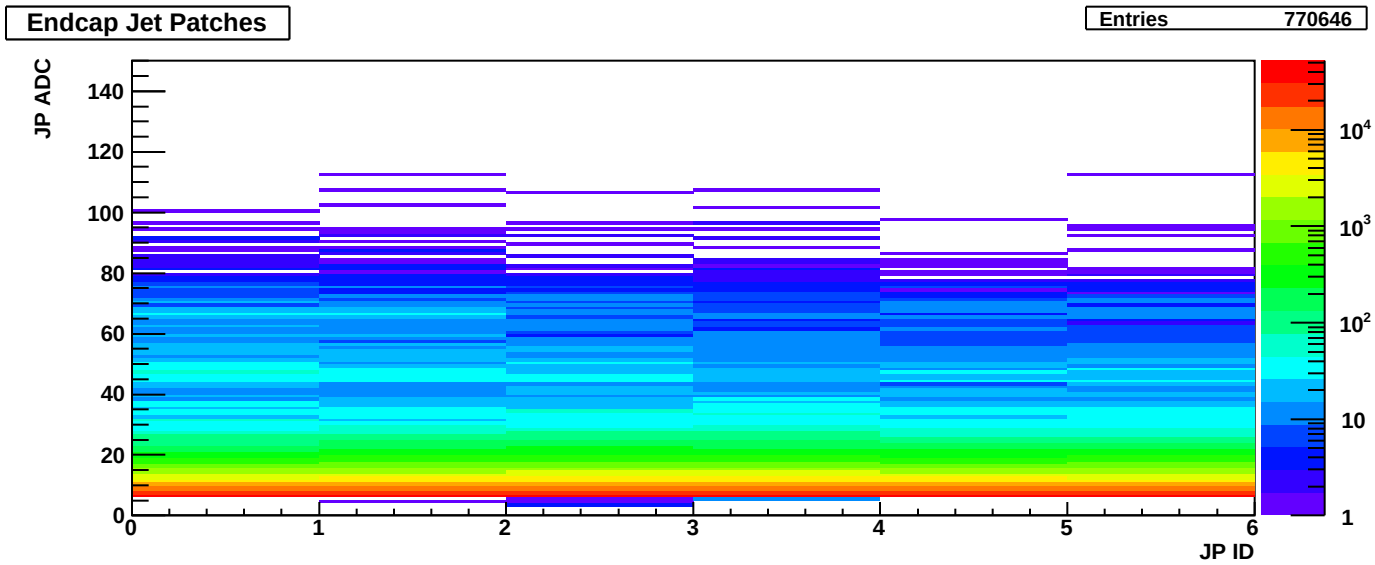

Barrel EMC L0 Input - High Tower
Entries 3.85323e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

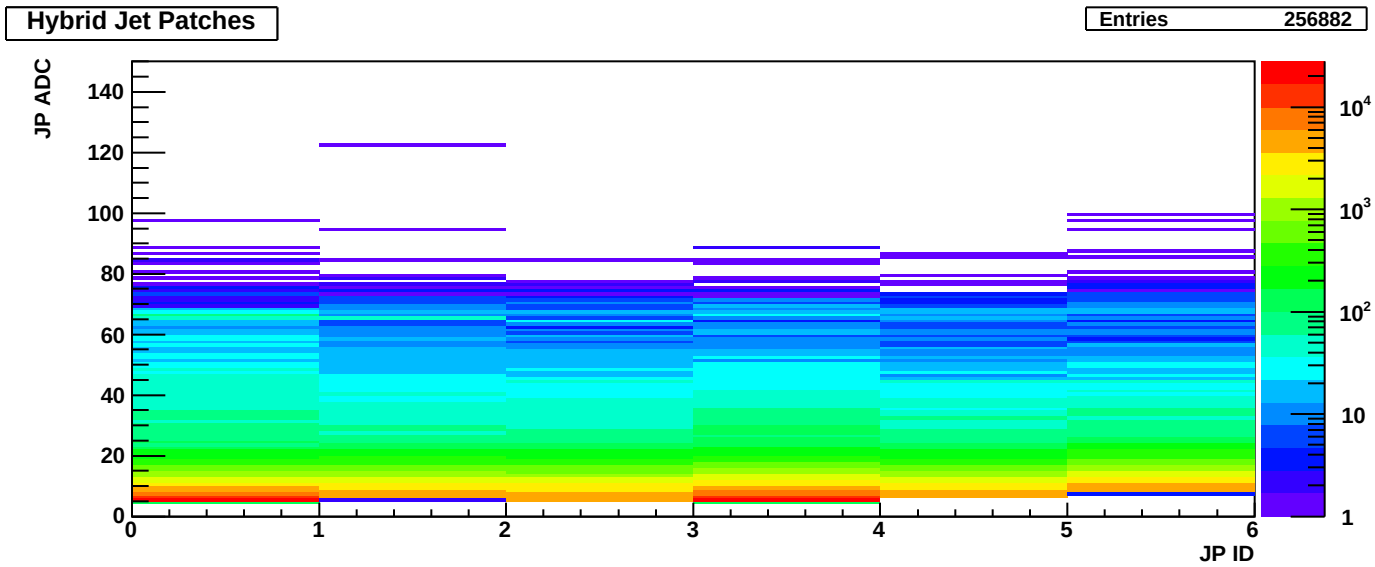

Barrel EMC L0 Input - Patch Sum
Entries 3.85323e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

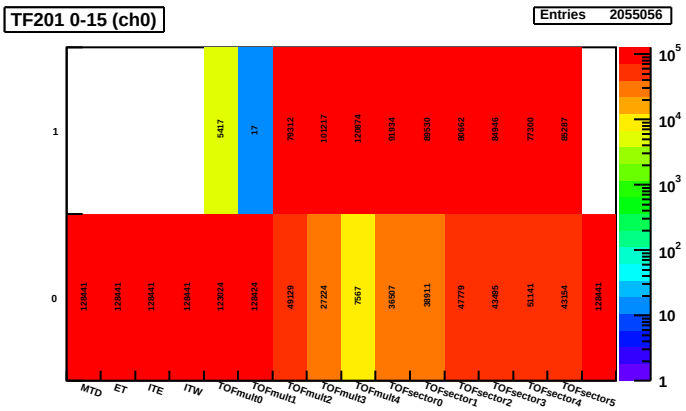

Endcap EMC L0 Input - High Tower
Entries 1.155969e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

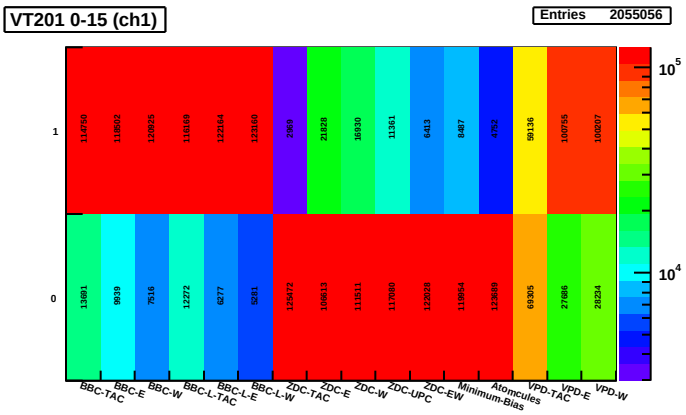

Endcap EMC L0 Input - Patch Sum
Entries 1.155969e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

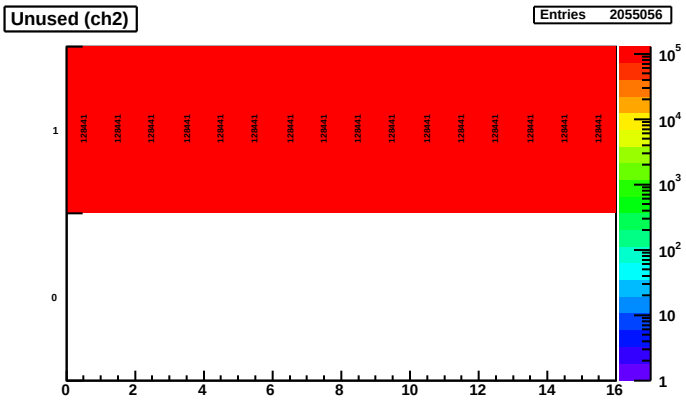

Barrel Jet Patches
Entries 2311938
JP ADC
JP ID

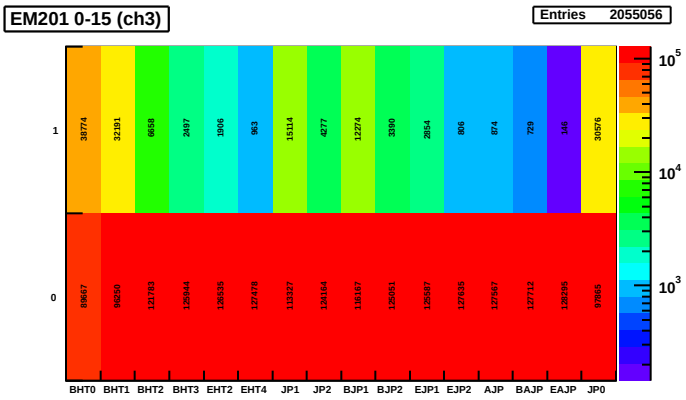

Endcap Jet Patches
Entries 770646
JP ADC
JP ID

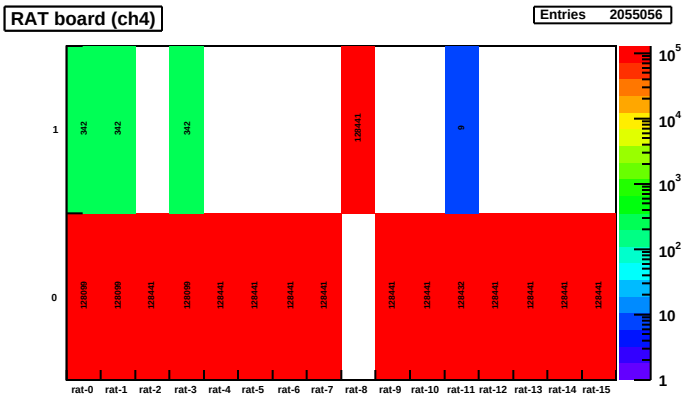

Hybrid Jet Patches
Entries 256882
JP ADC
JP ID

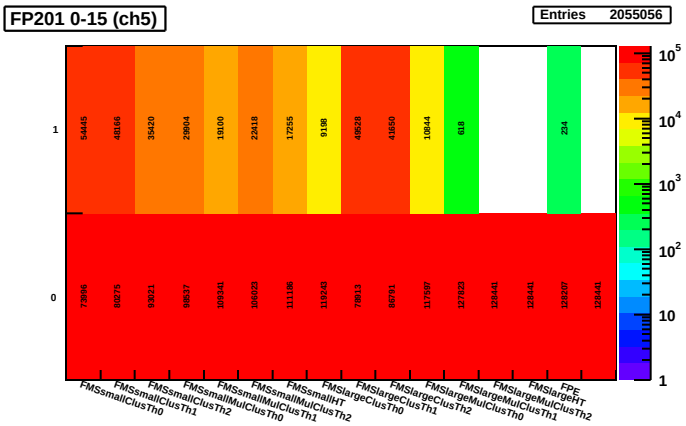

TF201 0-15 (ch0)
Entries 2055056
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

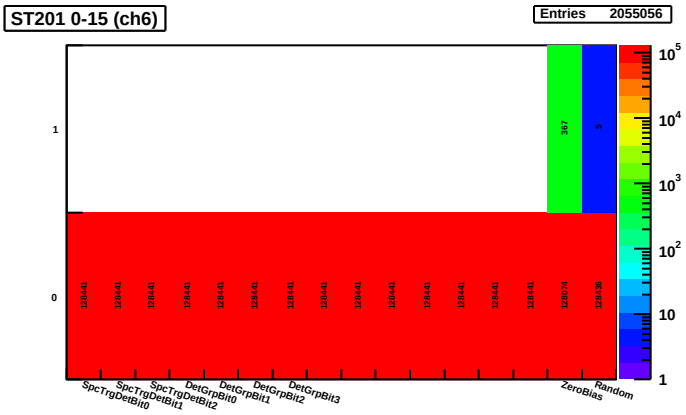

VT201 0-15 (ch1)
Entries 2055056
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

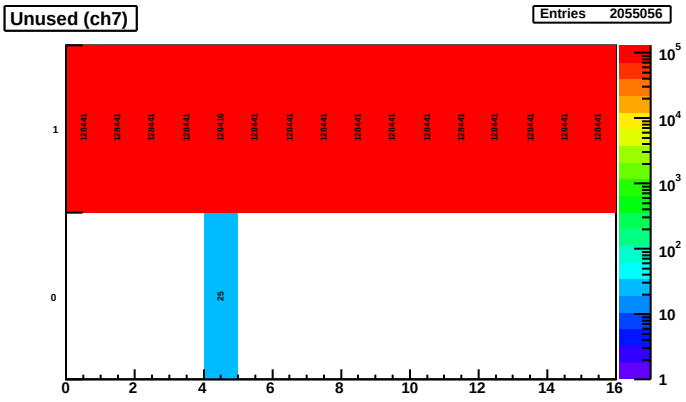

Unused (ch2)
Entries 2055056

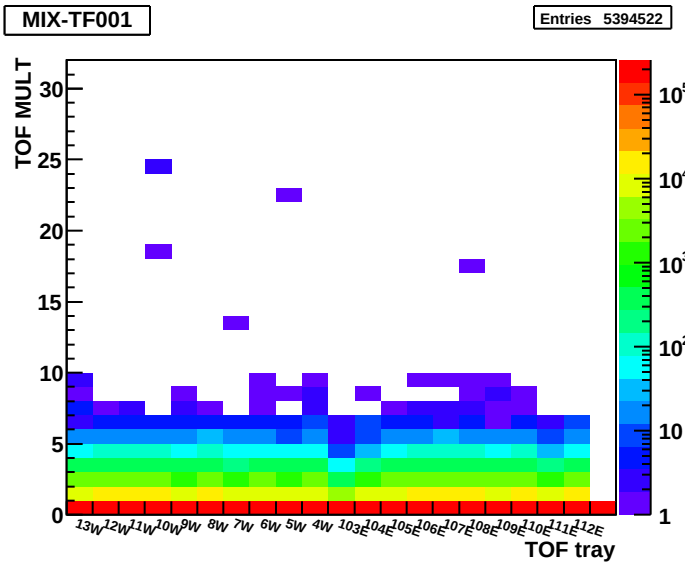

EM201 0-15 (ch3)
Entries 2055056
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

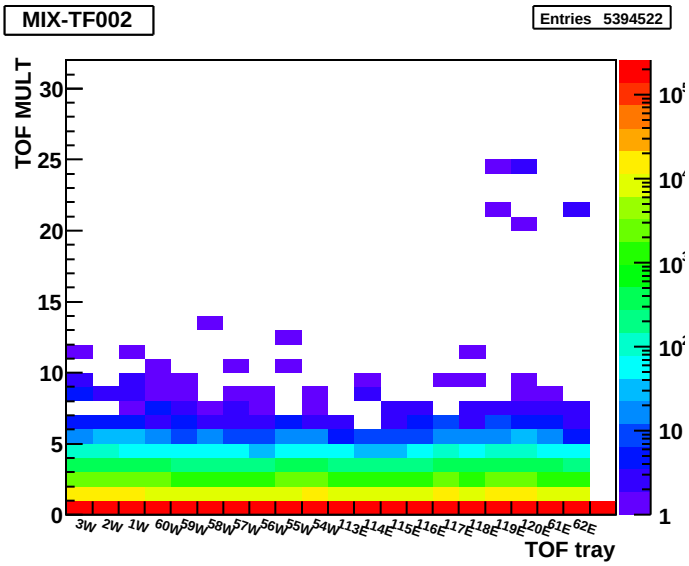

RAT board (ch4)
Entries 2055056
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

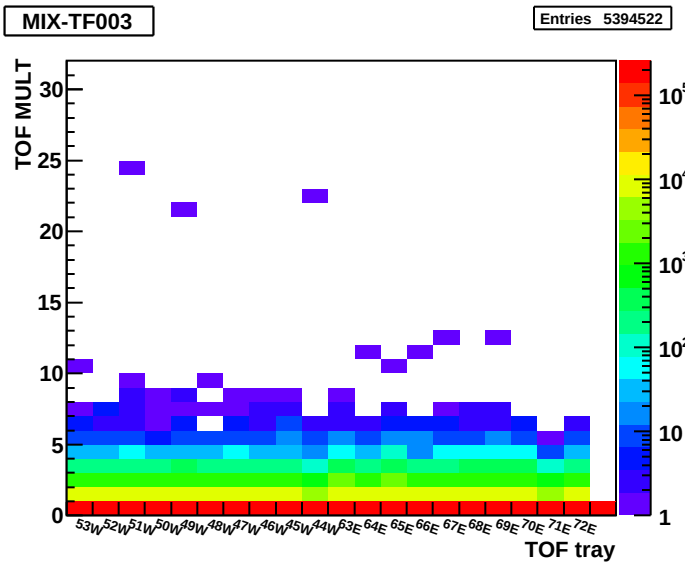

FP201 0-15 (ch5)
Entries 2055056
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

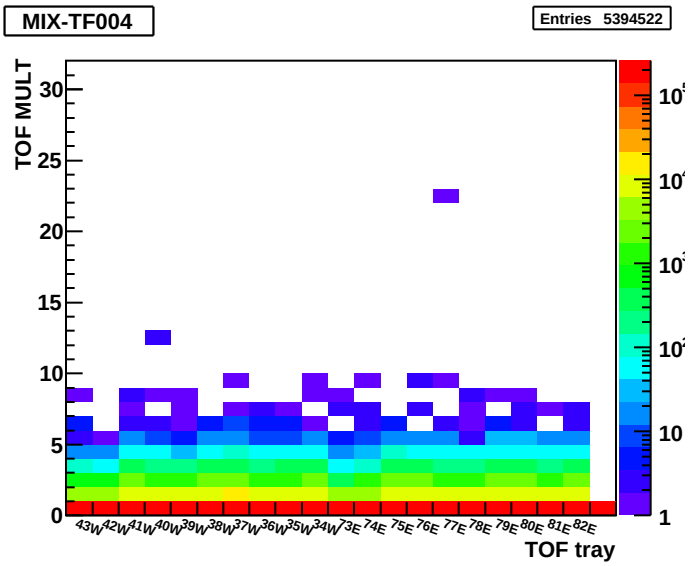

ST201 0-15 (ch6)
Entries 2055056
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

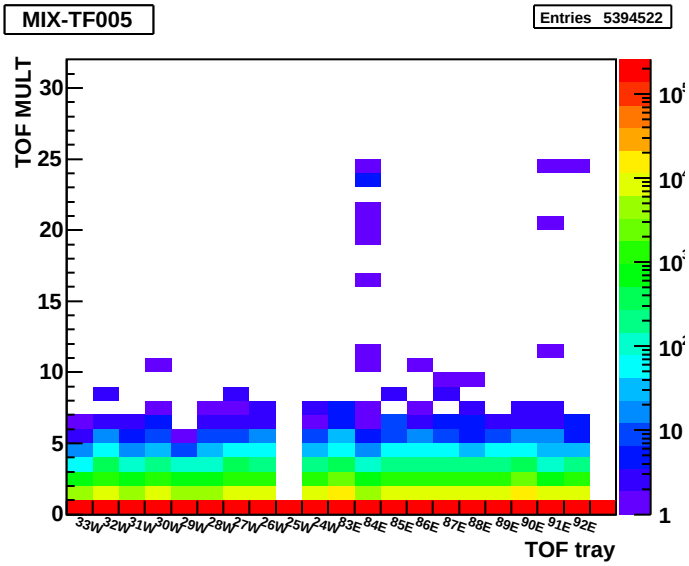

Unused (ch7)
Entries 2055056

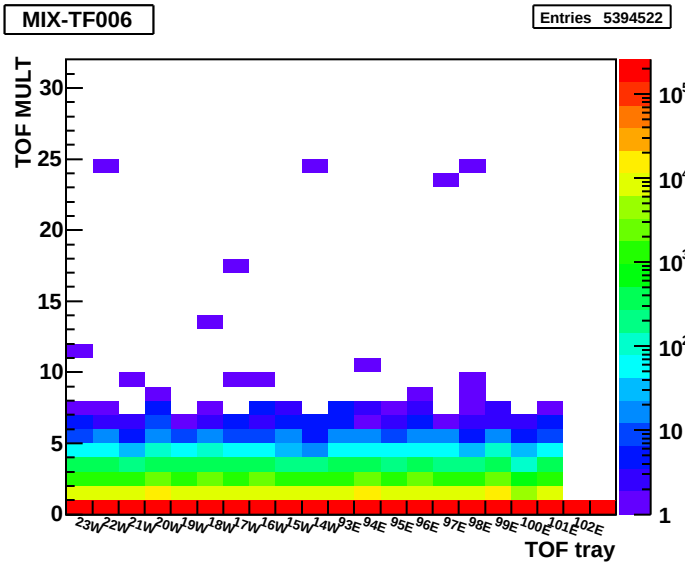

MIX-TF001
Entries 5394522
TOF MULT
TOF tray

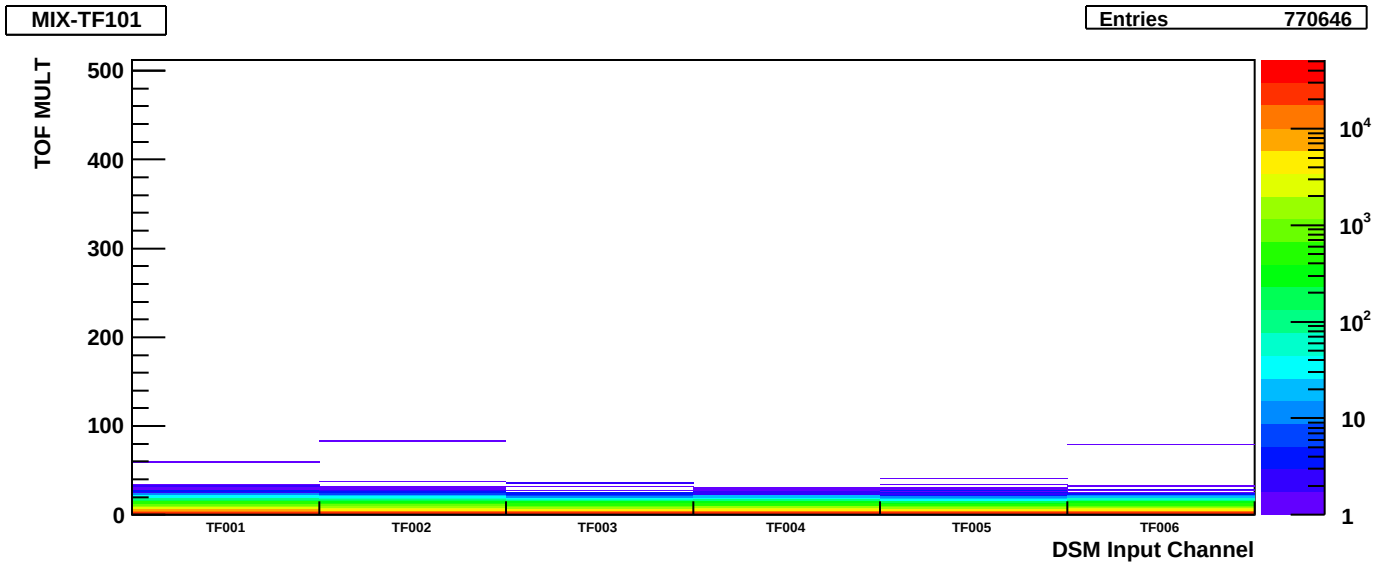

MIX-TF002
Entries 5394522
TOF MULT
TOF tray

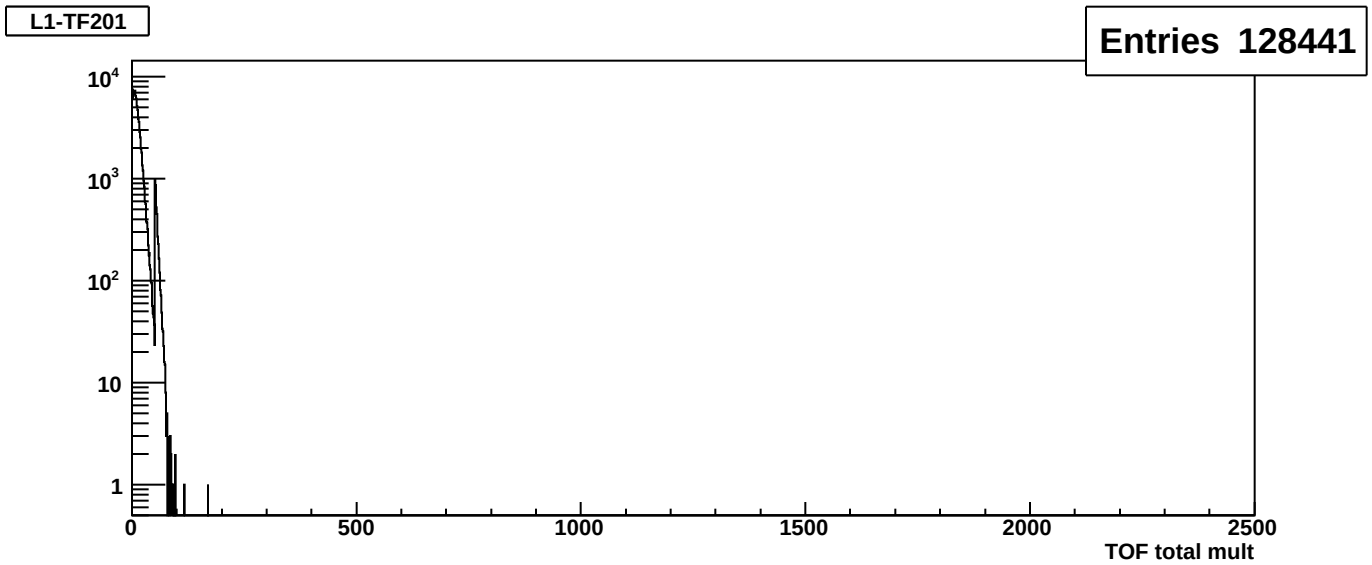

MIX-TF003
Entries 5394522
TOF MULT
TOF tray

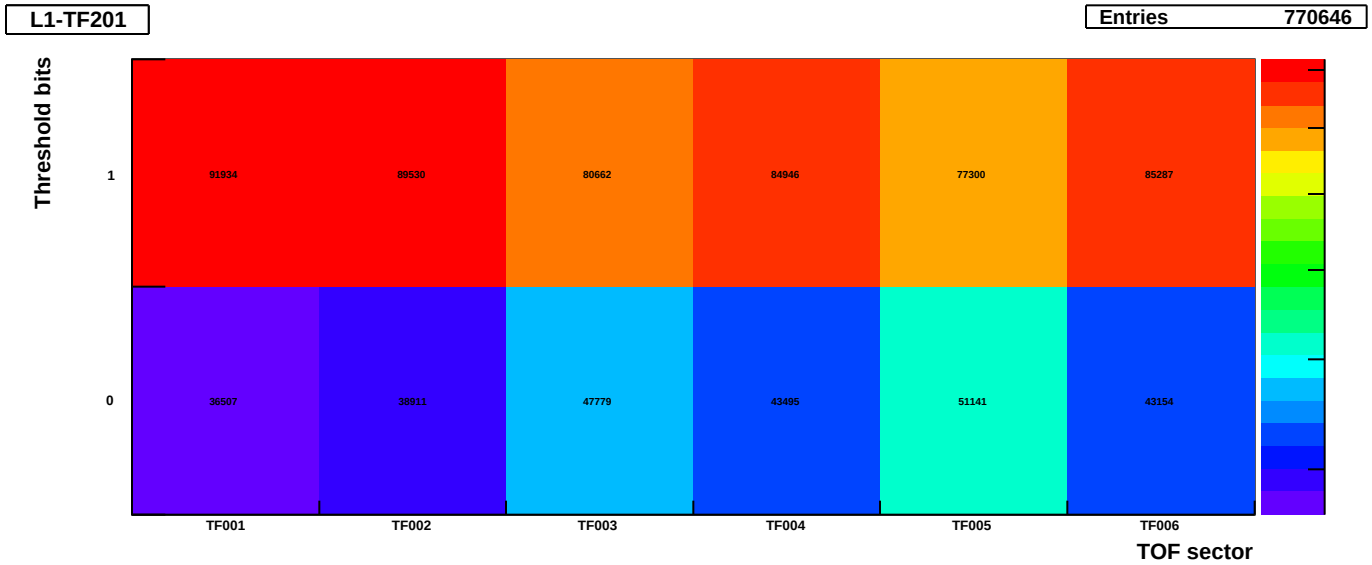

MIX-TF004
Entries 5394522
TOF MULT
TOF tray

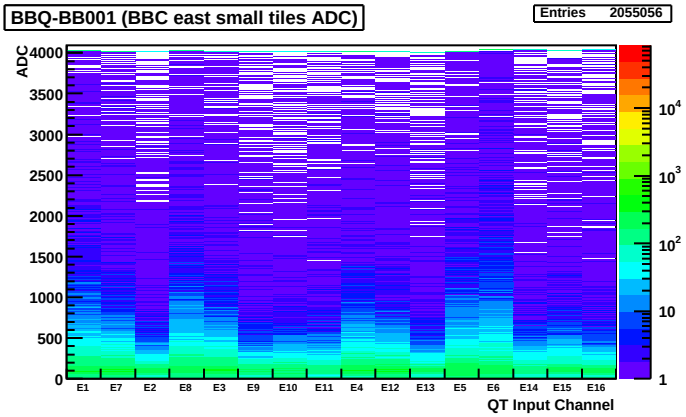

MIX-TF005
Entries 5394522
TOF MULT
TOF tray

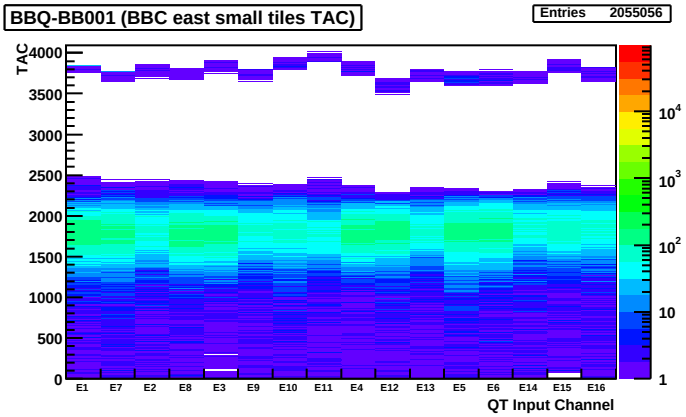

MIX-TF006
Entries 5394522
TOF MULT
TOF tray

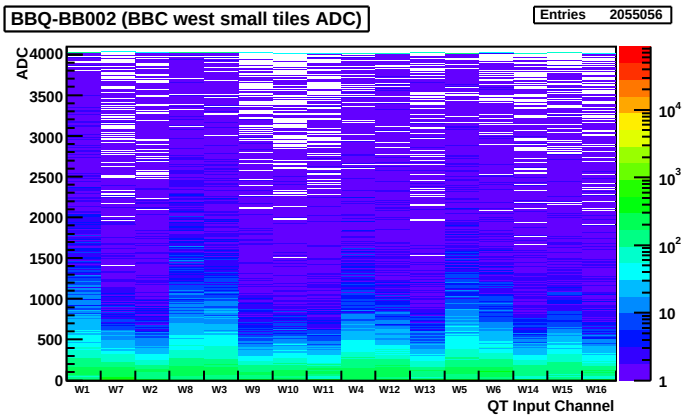

MIX-TF101
Entries 770646
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

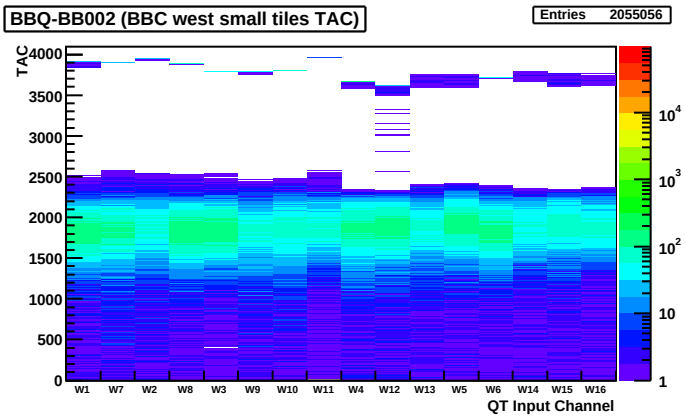

L1-TF201
Entries 128441
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

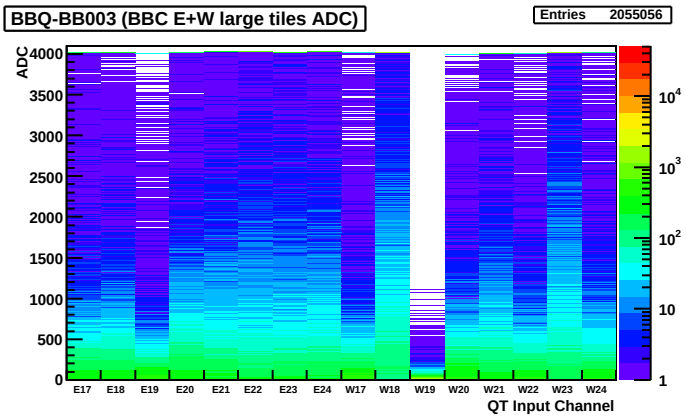

L1-TF201
Entries 770646
Threshold bits
1
0
91934
89530
80662
84946
77300
85287
36507
38911
47779
43495
51141
43154
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

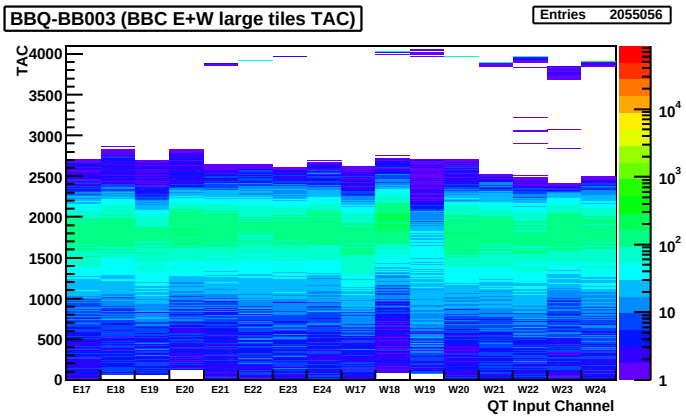

BBQ-BB001 (BBC east small tiles ADC)
Entries 2055056
ADC
QT Input Channel

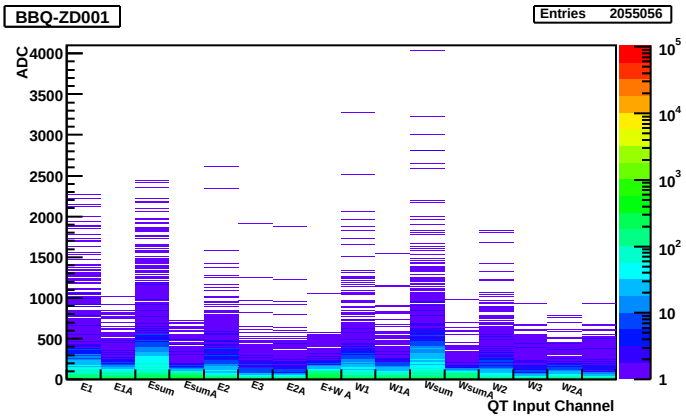

BBQ-BB001 (BBC east small tiles TAC)
Entries 2055056
TAC
QT Input Channel

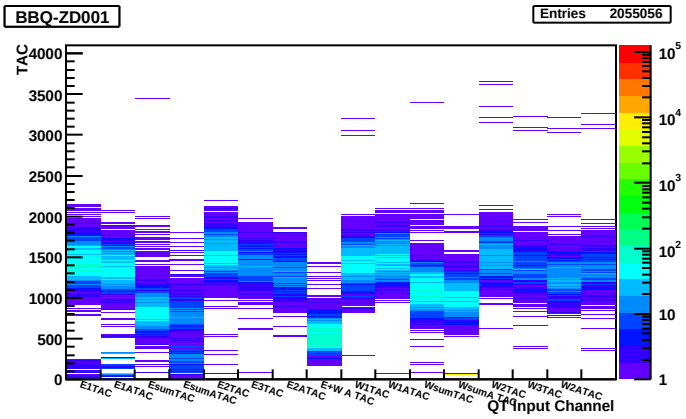

BBQ-BB002 (BBC west small tiles ADC)
Entries 2055056
ADC
QT Input Channel

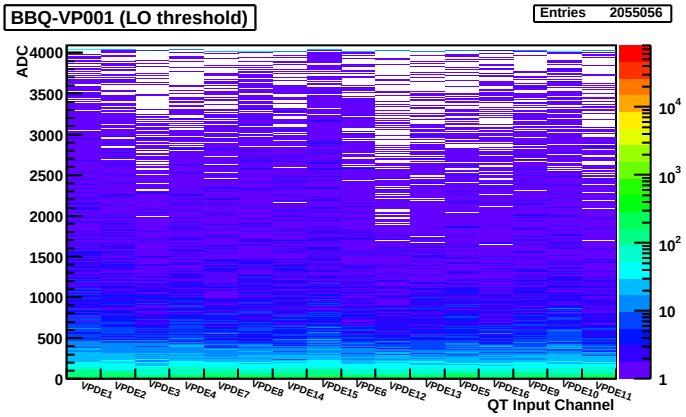

BBQ-BB002 (BBC west small tiles TAC)
Entries 2055056
TAC
QT Input Channel

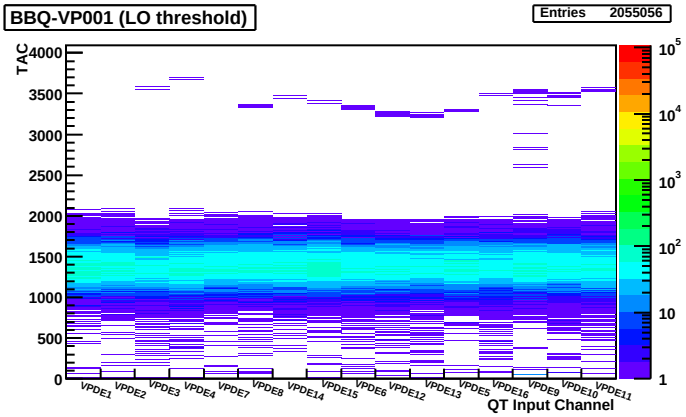

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2055056
ADC
QT Input Channel

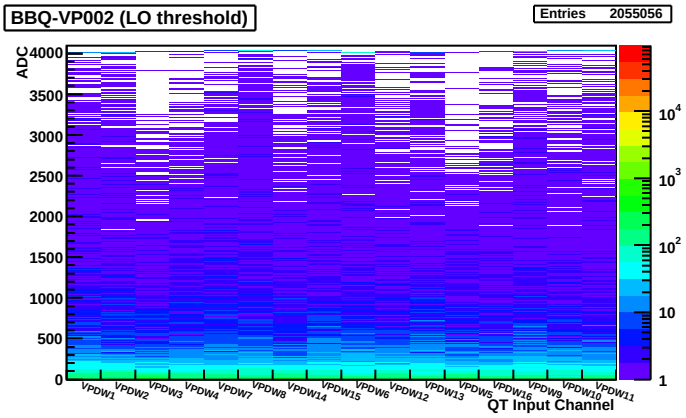

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2055056
TAC
QT Input Channel

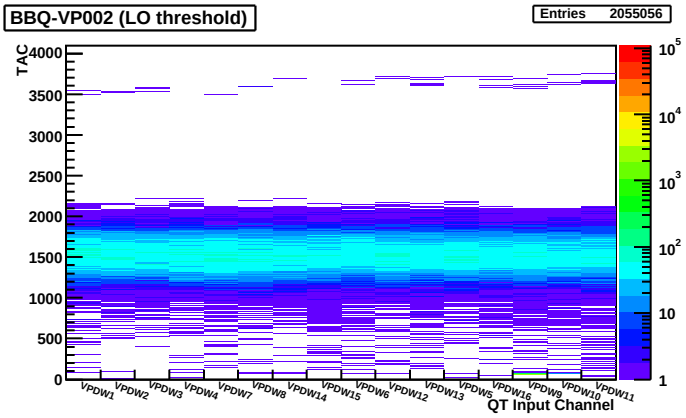

BBQ-ZD001
Entries 2055056
ADC
QT Input Channel

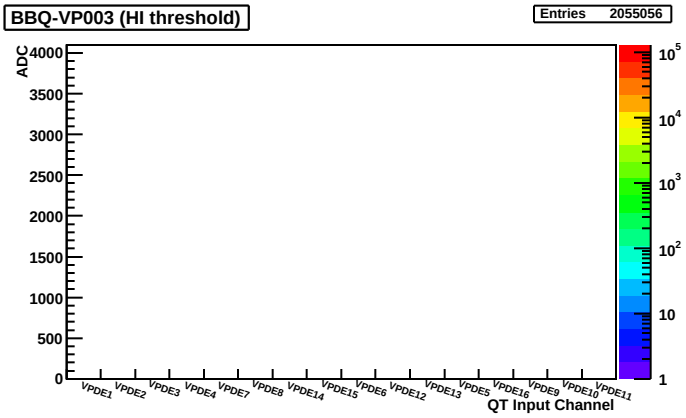

BBQ-ZD001
Entries 2055056
TAC
QT Input Channel

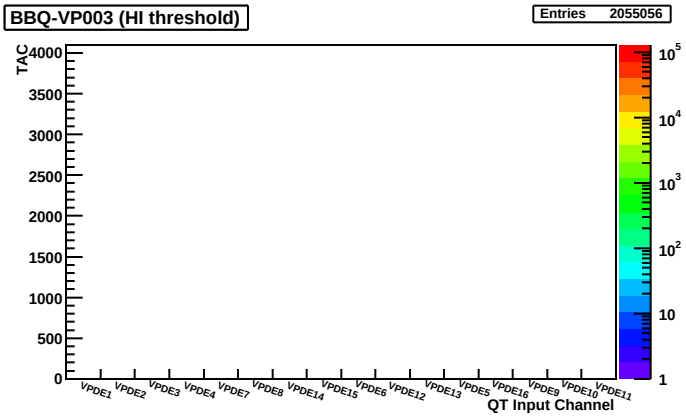

BBQ-VP001 (LO threshold)
Entries 2055056
ADC
QT Input Channel

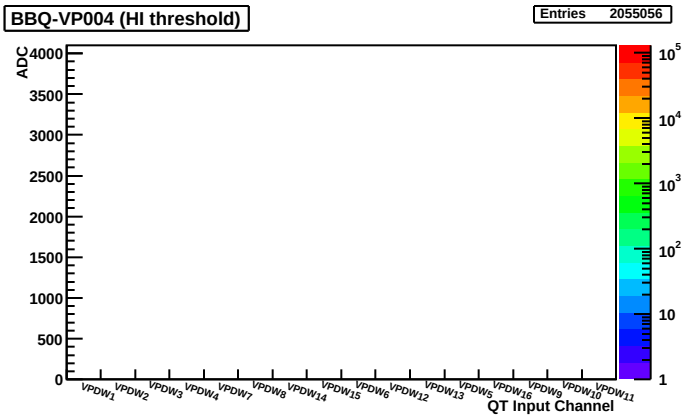

BBQ-VP001 (LO threshold)
Entries 2055056
TAC
QT Input Channel

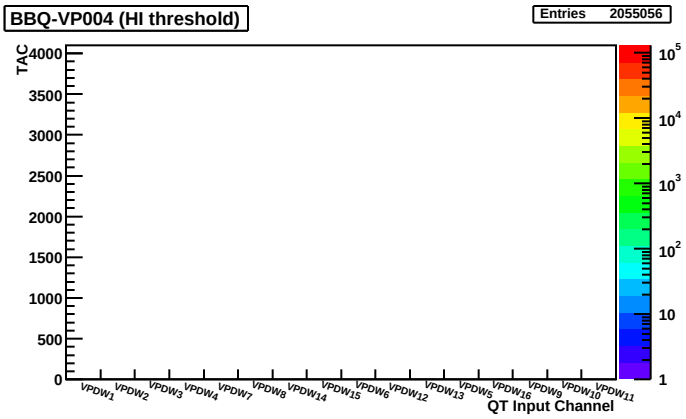

BBQ-VP002 (LO threshold)
Entries 2055056
ADC
QT Input Channel

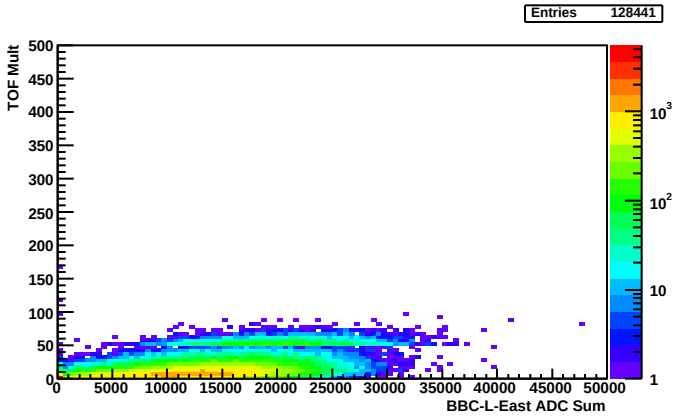

BBQ-VP002 (LO threshold)
Entries 2055056
TAC
QT Input Channel

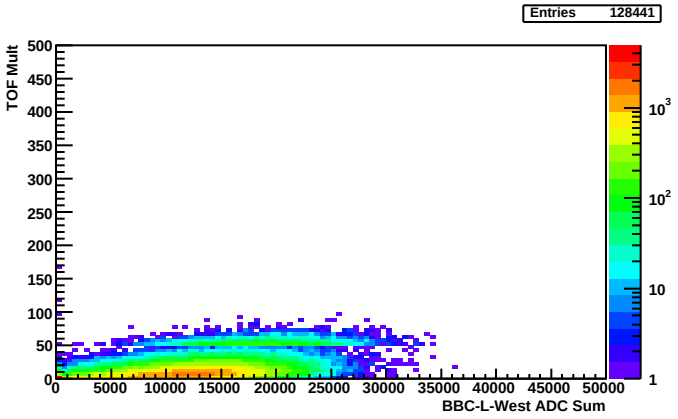

BBQ-VP003 (HI threshold)
Entries 2055056
ADC
QT Input Channel

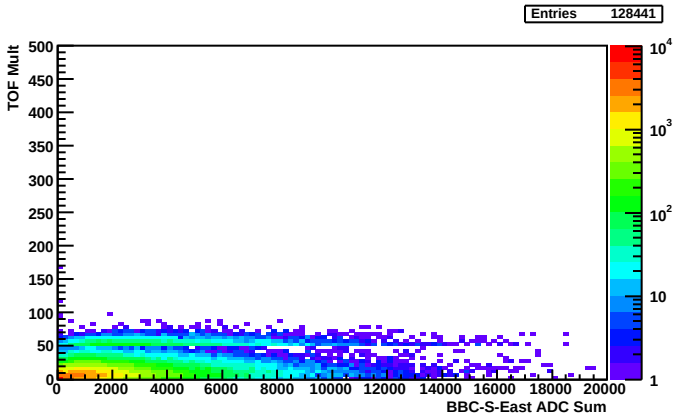

BBQ-VP003 (HI threshold)
Entries 2055056
TAC
QT Input Channel

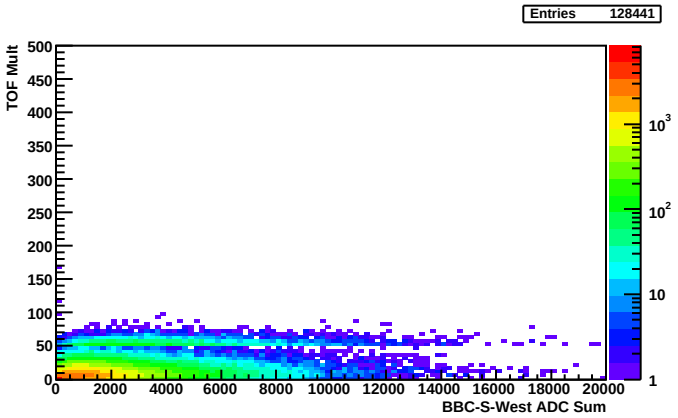

BBQ-VP004 (HI threshold)
Entries 2055056
ADC
QT Input Channel

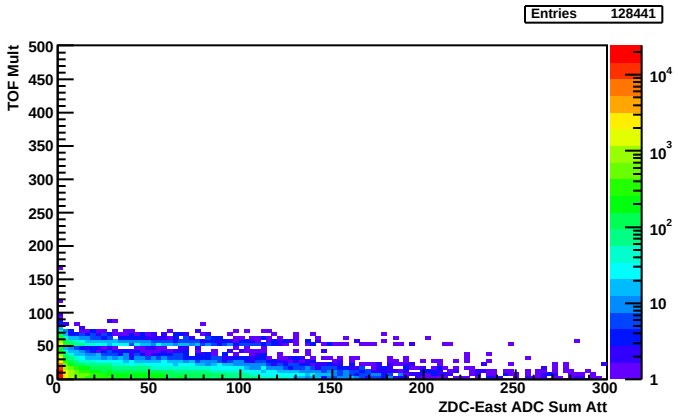

BBQ-VP004 (HI threshold)
Entries 2055056
TAC
QT Input Channel

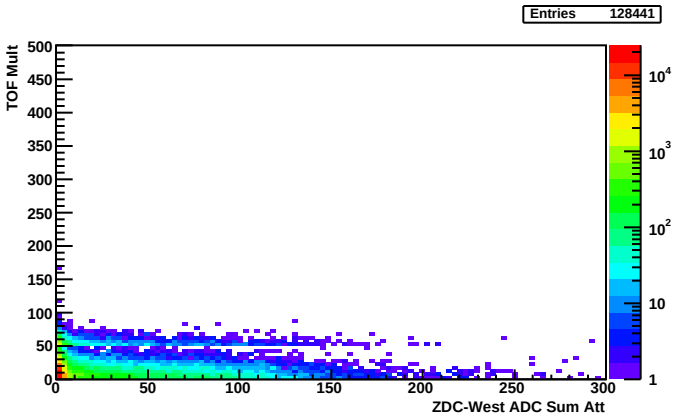

Entries 128441
TOF Mult
BBC-L-East ADC Sum

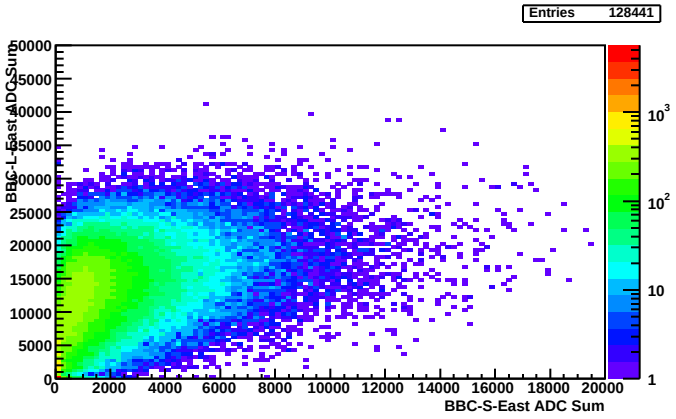

Entries 128441
TOF Mult
BBC-L-West ADC Sum

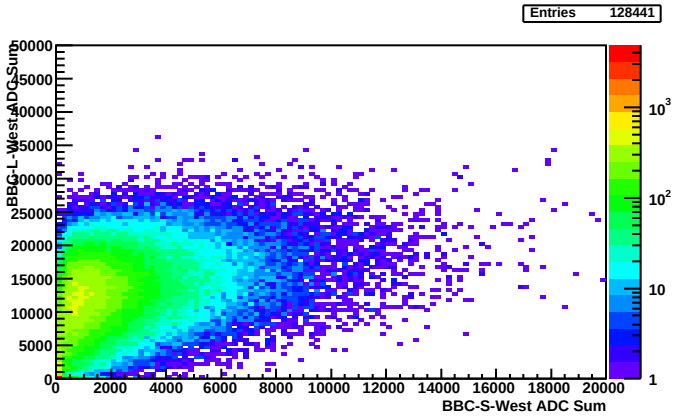

Entries 128441
TOF Mult
BBC-S-East ADC Sum

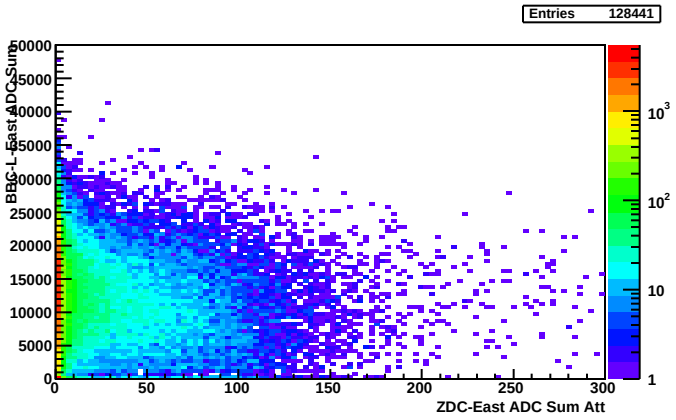

Entries 128441
TOF Mult
BBC-S-West ADC Sum

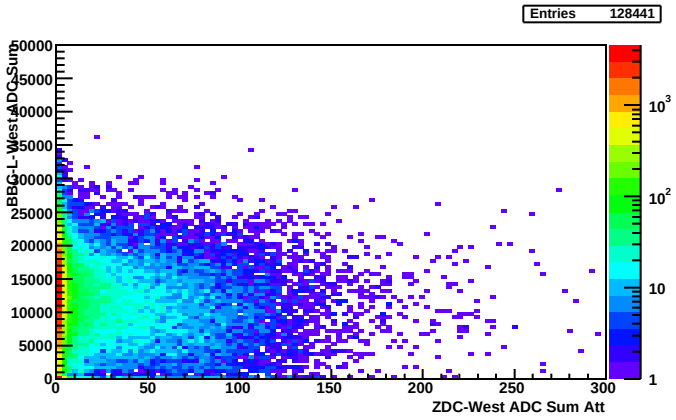

Entries 128441
TOF Mult
ZDC-East ADC Sum Att

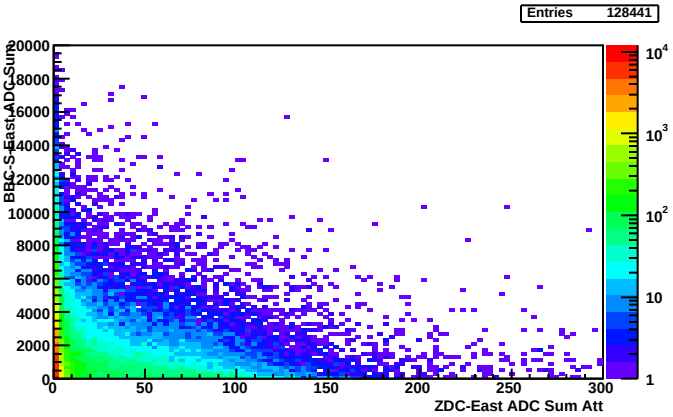

Entries 128441
TOF Mult
ZDC-West ADC Sum Att

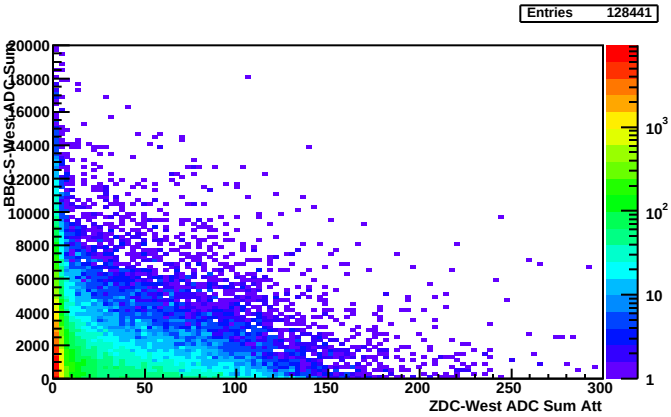

Entries 128441
BBC-L-East ADC Sum
BBC-S-East ADC Sum

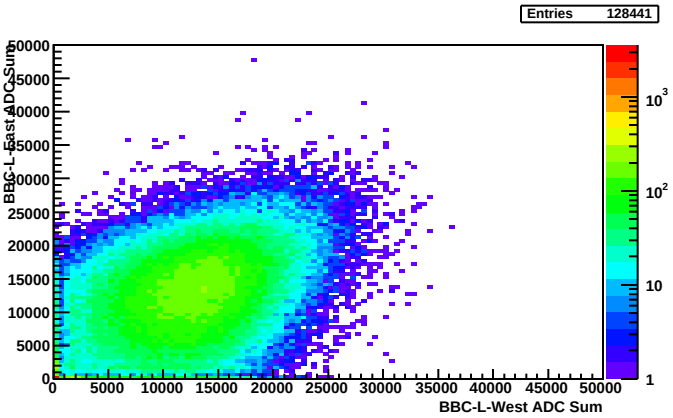

Entries 128441
BBC-L-West ADC Sum
BBC-S-West ADC Sum

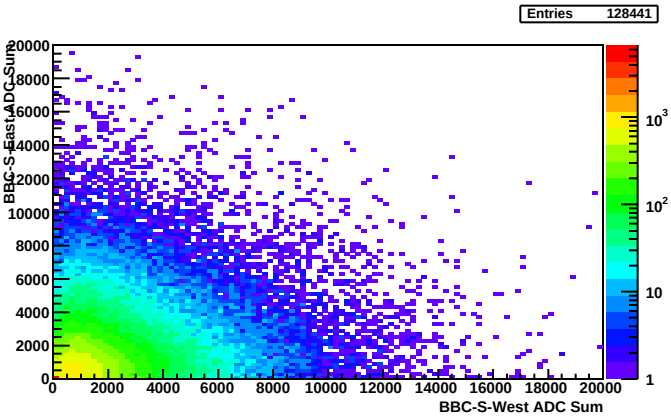

Entries 128441
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

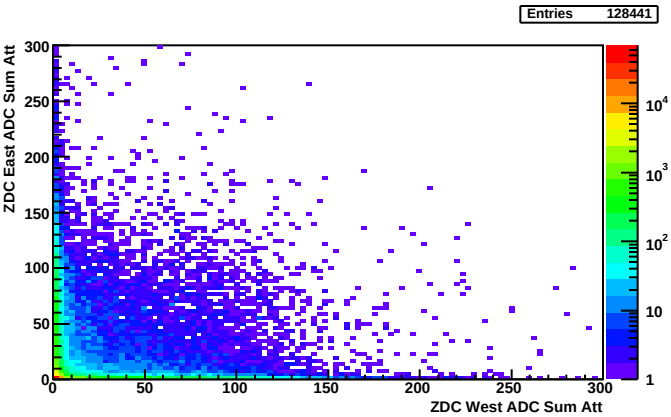

Entries 128441
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

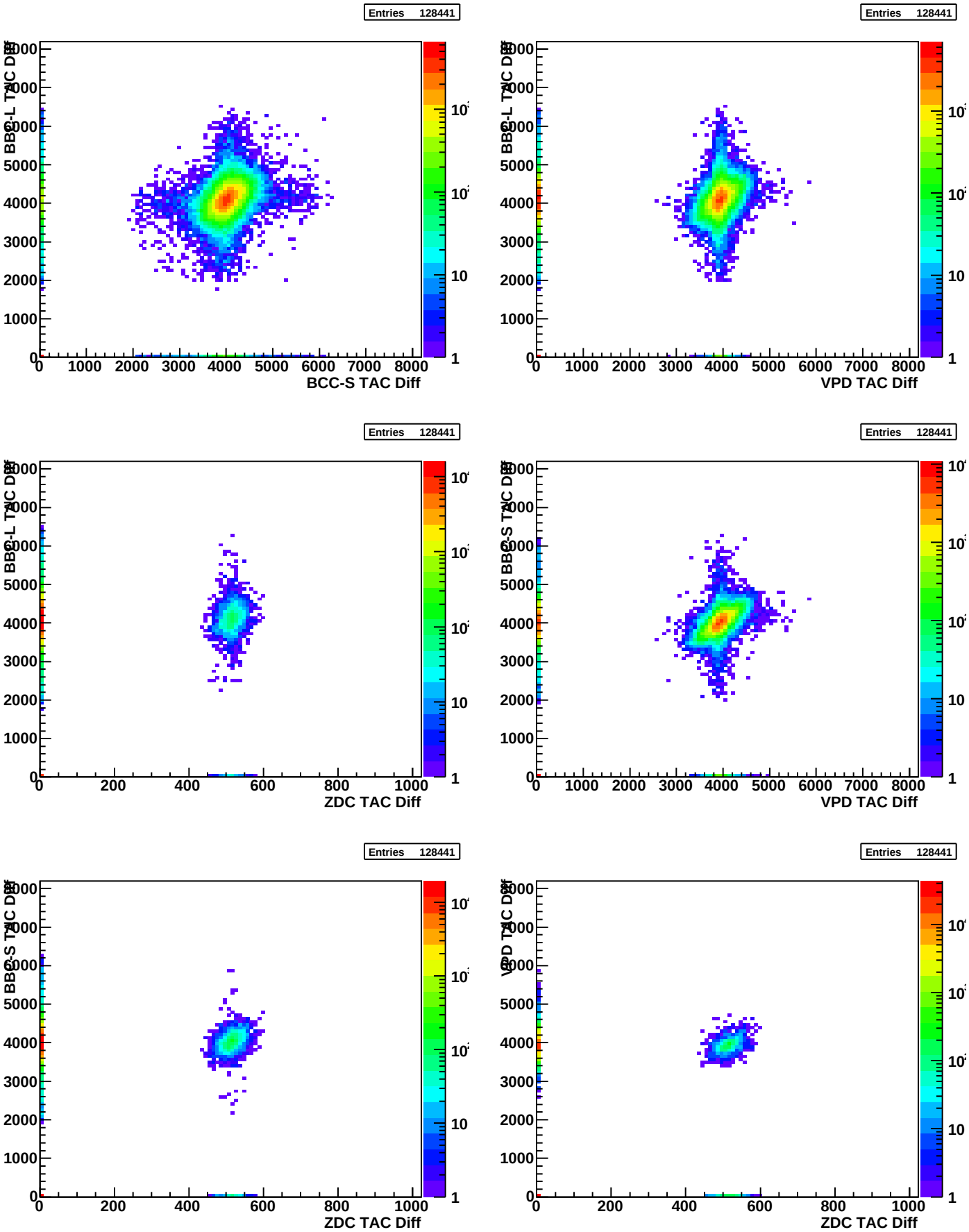

Entries 128441
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

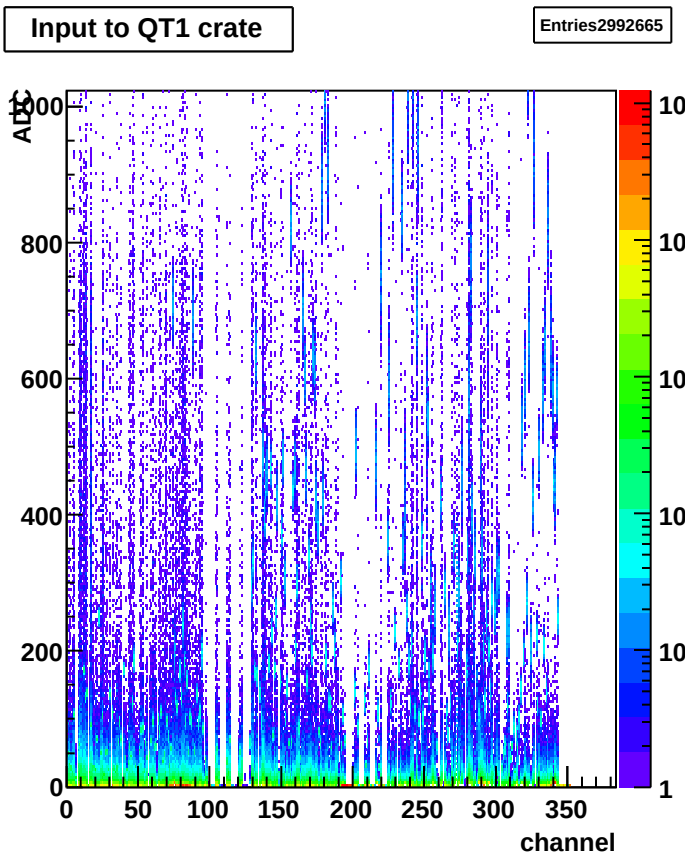

Entries 128441
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

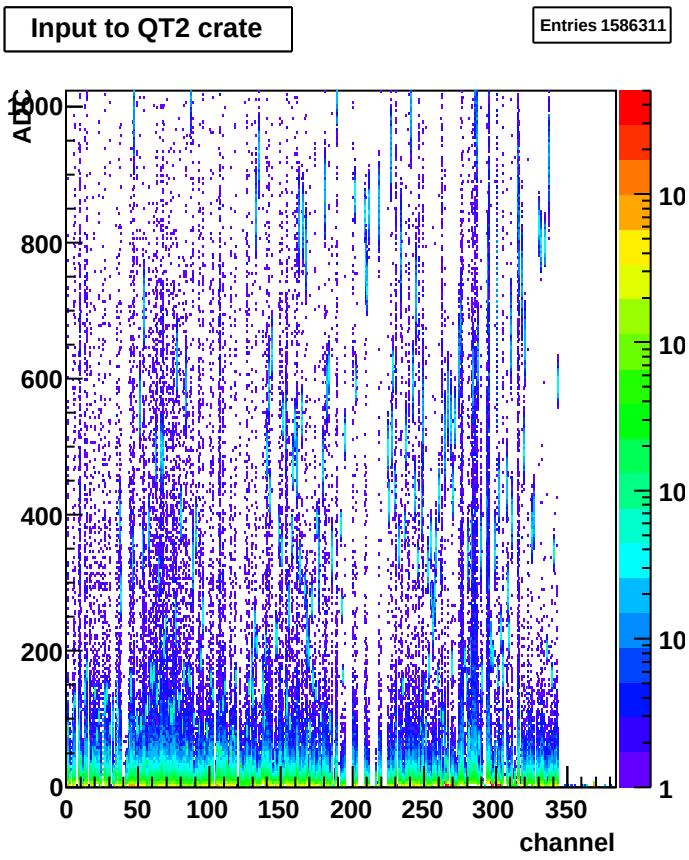

Entries 128441
BBC-L-East ADC Sum
BBC-L-West ADC Sum

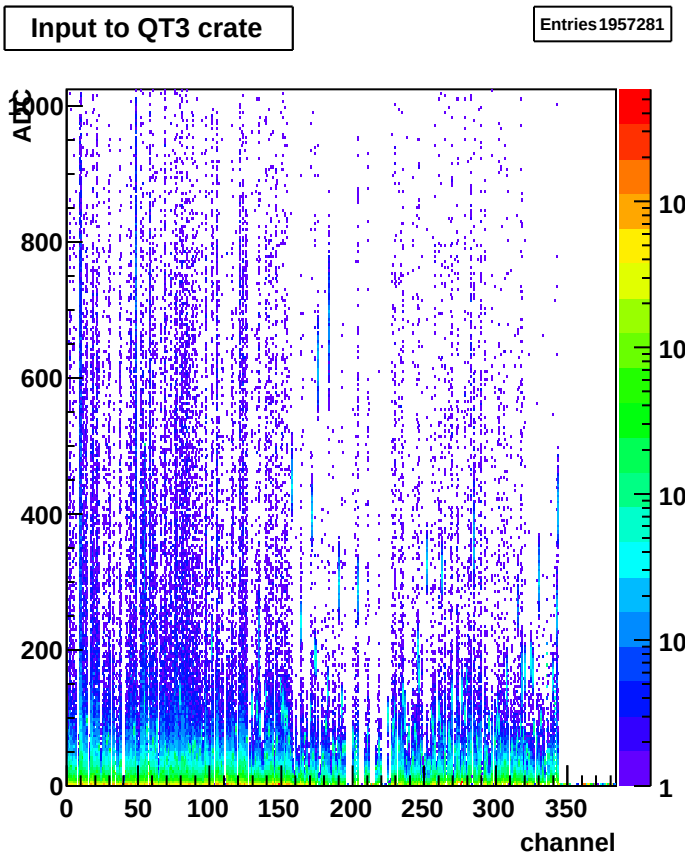

Entries 128441
BBC-S-East ADC Sum
BBC-S-West ADC Sum

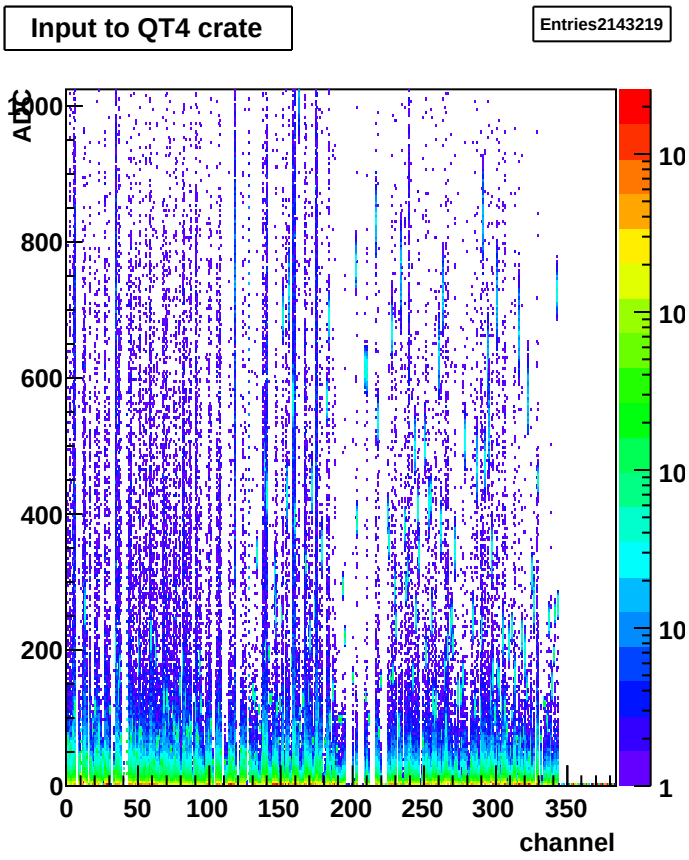

Entries 128441
ZDC East ADC Sum Att
ZDC West ADC Sum Att

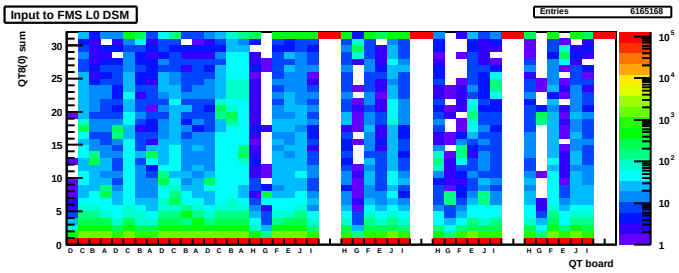

Entries 128441
BBC-L TAC Diff
BCC-S TAC Diff
Entries 128441
BBC-L TAC Diff
VPD TAC Diff
Entries 128441
BBC-L TAC Diff
ZDC TAC Diff
Entries 128441
BBC-S TAC Diff
VPD TAC Diff
Entries 128441
BBC-S TAC Diff
ZDC TAC Diff
Entries 128441
VPD TAC Diff
ZDC TAC Diff

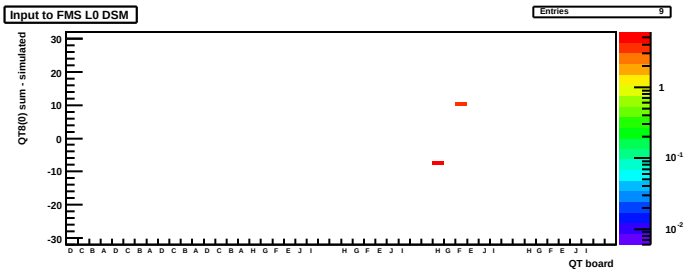

Input to QT1 crate
Entries2992665
ADC
channel

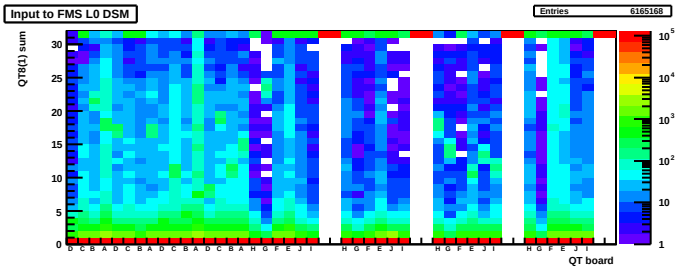

Input to QT2 crate
Entries 1586311
ADC
channel

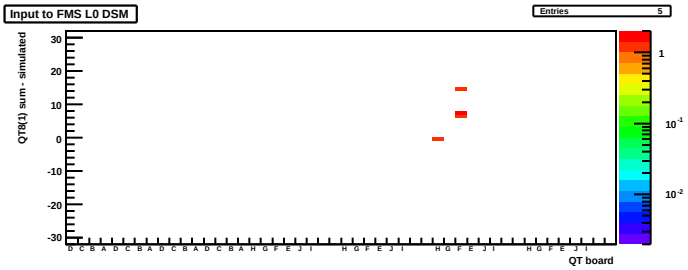

Input to QT3 crate
Entries1957281
ADC
channel

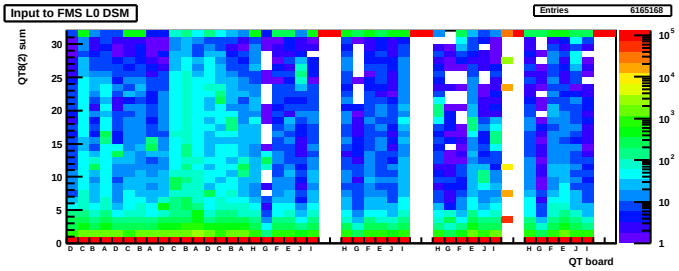

Input to QT4 crate
Entries2143219
ADC
channel

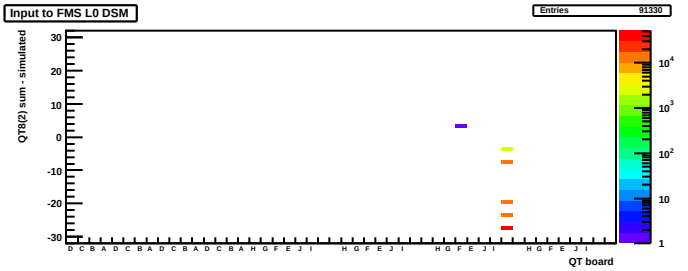

Input to FMS L0 DSM
Entries 6165168
QT8(0) sum
QT board

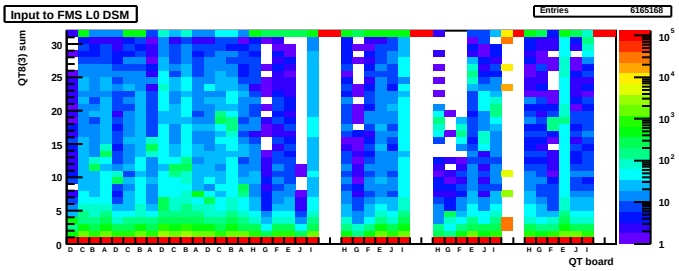

Input to FMS L0 DSM
Entries 9
QT8(0) sum - simulated
QT board

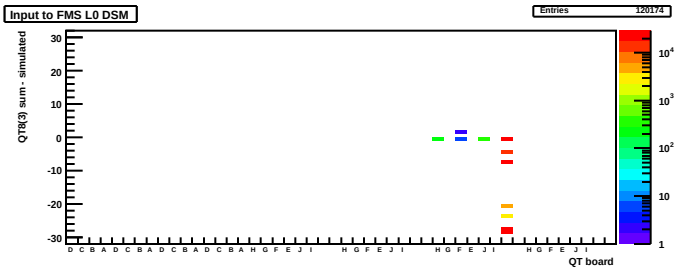

Input to FMS L0 DSM
Entries 6165168
QT8(1) sum
QT board

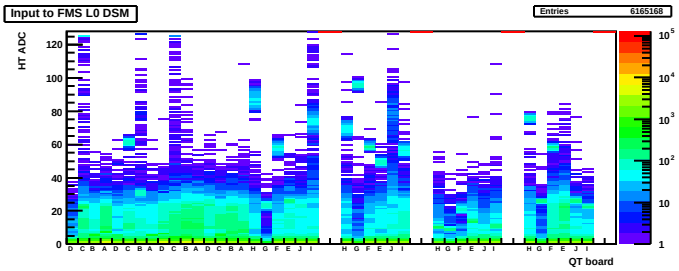

Input to FMS L0 DSM
Entries 5
QT8(1) sum - simulated
QT board

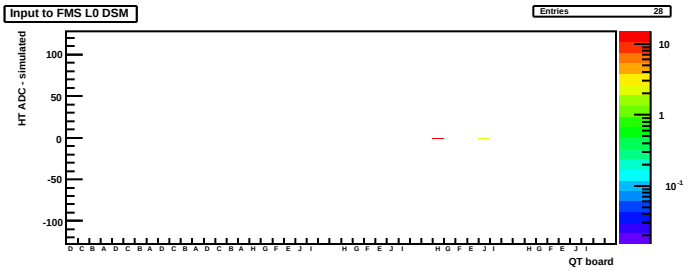

Input to FMS L0 DSM
Entries 6165168
QT8(2) sum
QT board

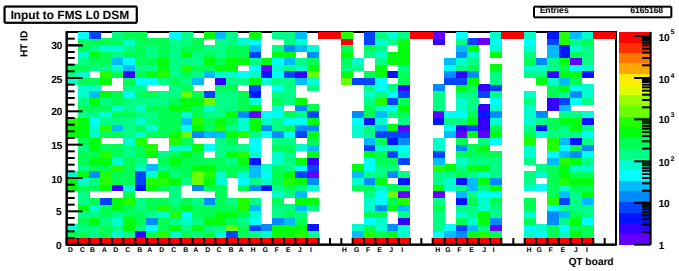

Input to FMS L0 DSM
Entries 91330
QT8(2) sum - simulated
QT board

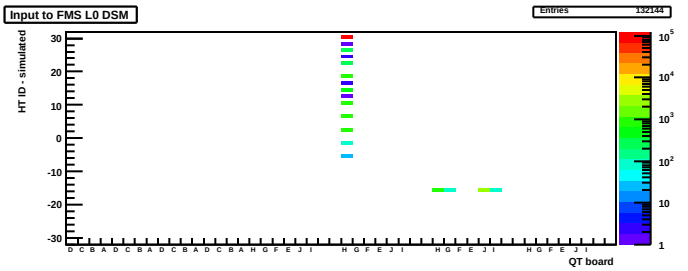

Input to FMS L0 DSM
Entries 6165168
QT8(3) sum
QT board

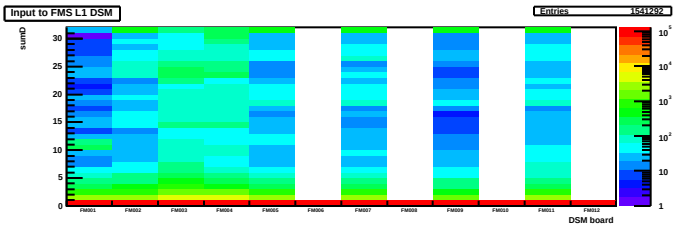

Input to FMS L0 DSM
Entries 120174
QT8(3) sum - simulated
QT board

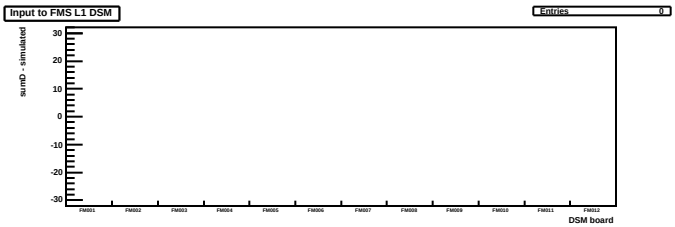

Input to FMS L0 DSM
Entries 6165168
HT ADC
QT board

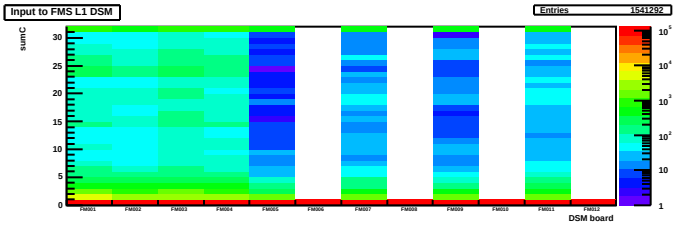

Input to FMS L0 DSM
Entries 28
HT ADC - simulated
QT board

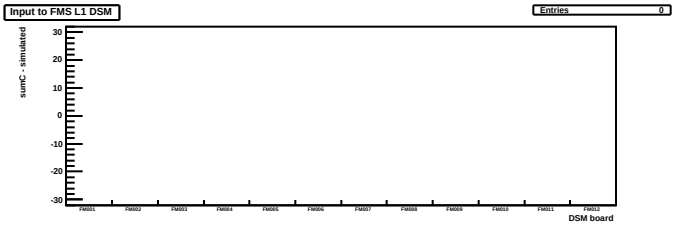

Input to FMS L0 DSM
Entries 6165168
HT ID
QT board

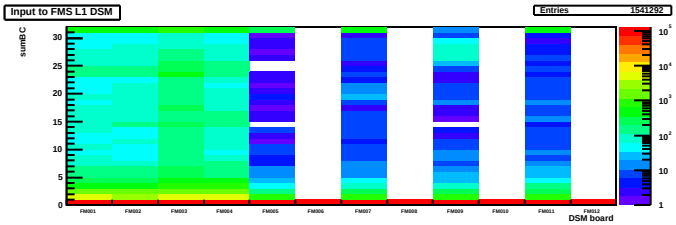

Input to FMS L0 DSM
Entries 132144
HT ID - simulated
QT board

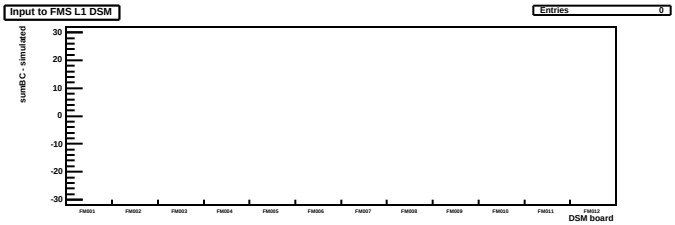

Input to FMS L1 DSM
Entries 1541292
sumD
DSM board

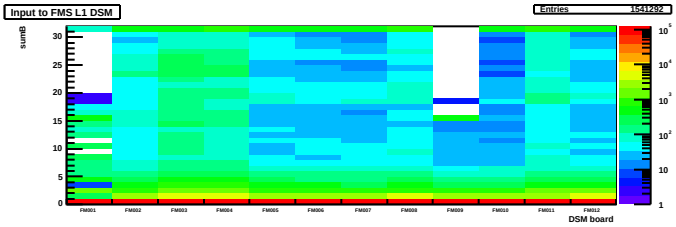

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

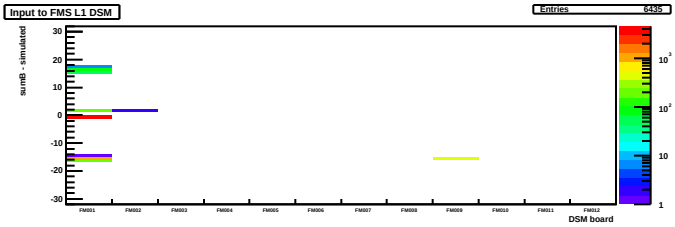

Input to FMS L1 DSM
Entries 1541292
sumC
DSM board

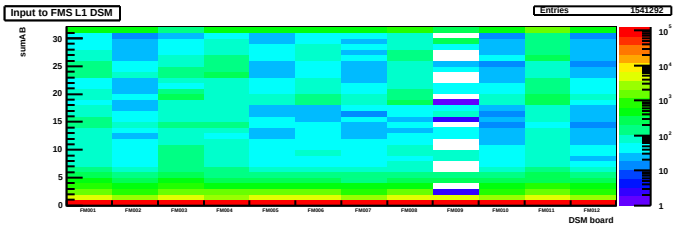

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

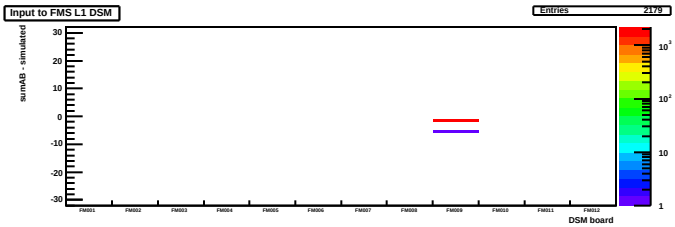

Input to FMS L1 DSM
Entries 1541292
sumBC
DSM board

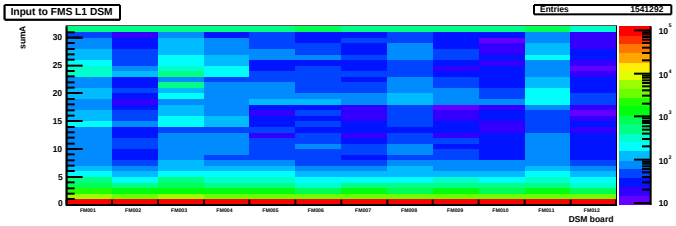

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

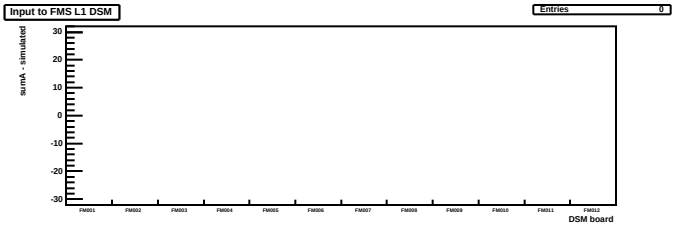

Input to FMS L1 DSM
Entries 1541292
sumB
DSM board

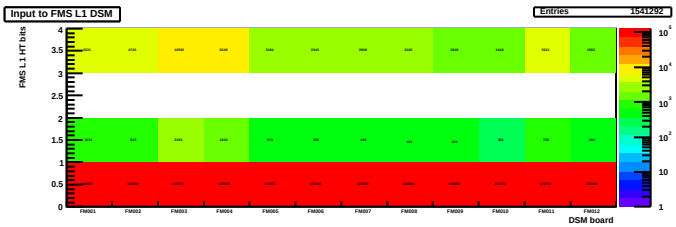

Input to FMS L1 DSM
Entries 6435
sumB - simulated
DSM board

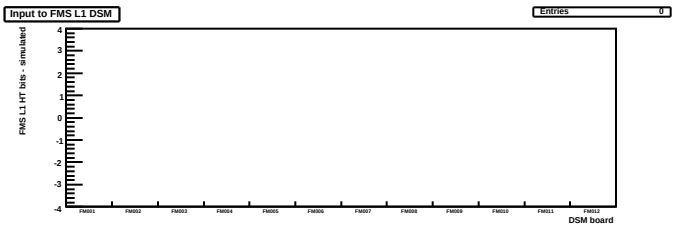

Input to FMS L1 DSM
Entries 1541292
sumAB
DSM board

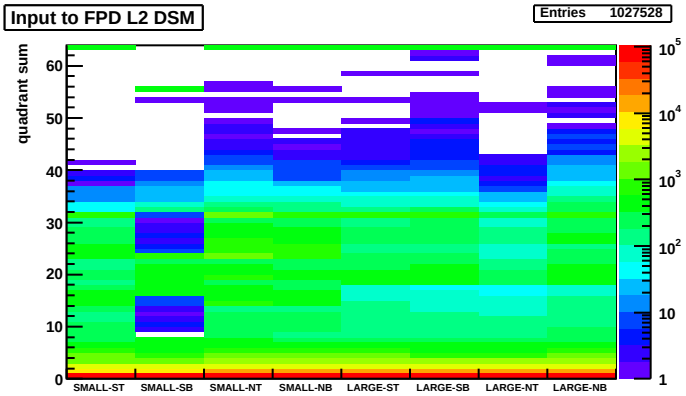

Input to FMS L1 DSM
Entries 2179
sumAB - simulated
DSM board

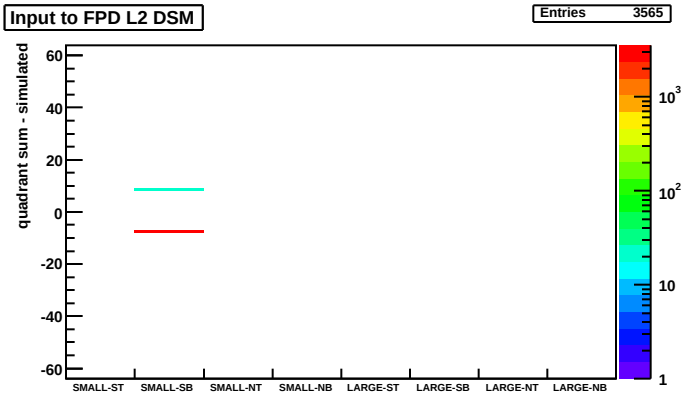

Input to FMS L1 DSM
Entries 1541292
sumA
DSM board

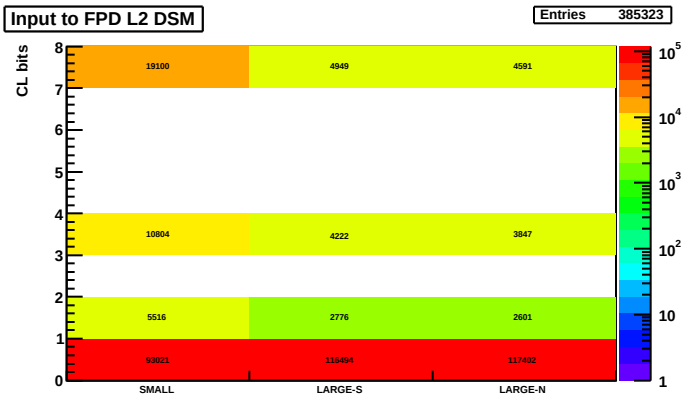

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

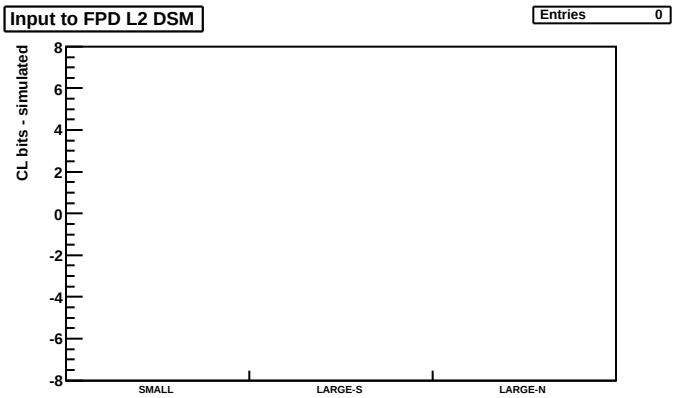

Input to FMS L1 DSM
Entries 1541292
FMS L1 HT bits
DSM board

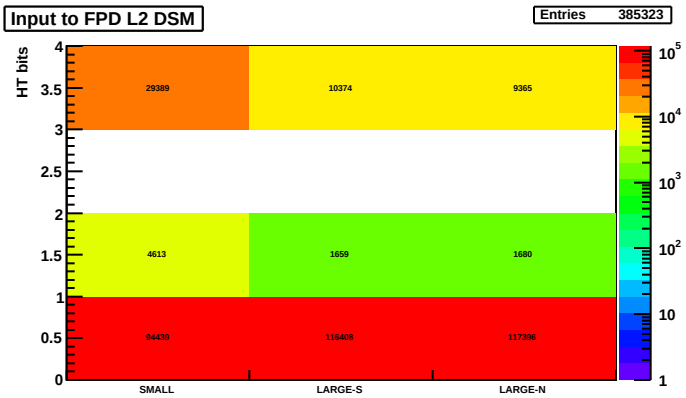

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

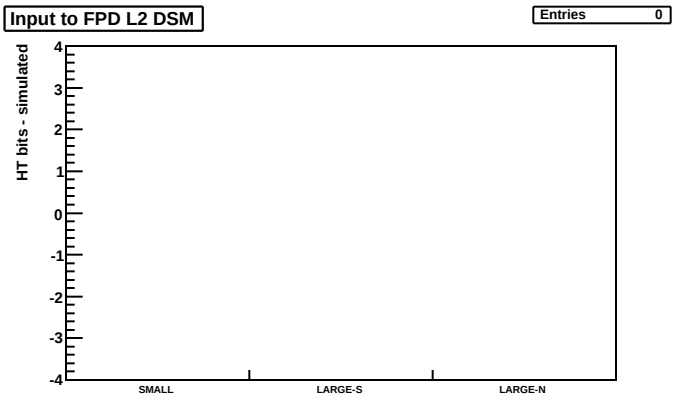

Input to FPD L2 DSM
Entries 1027528
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

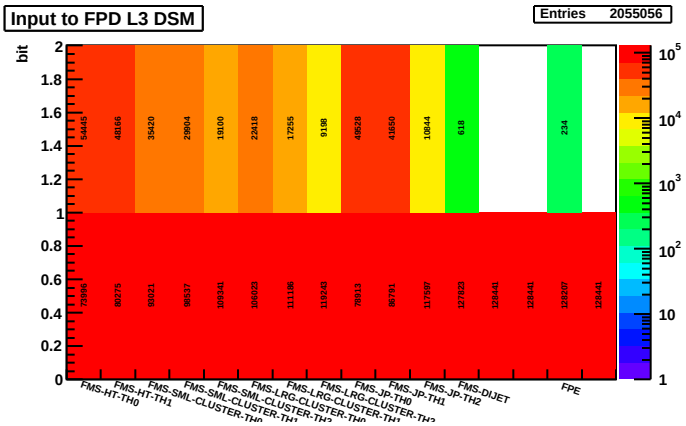

Input to FPD L2 DSM
Entries 3565
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

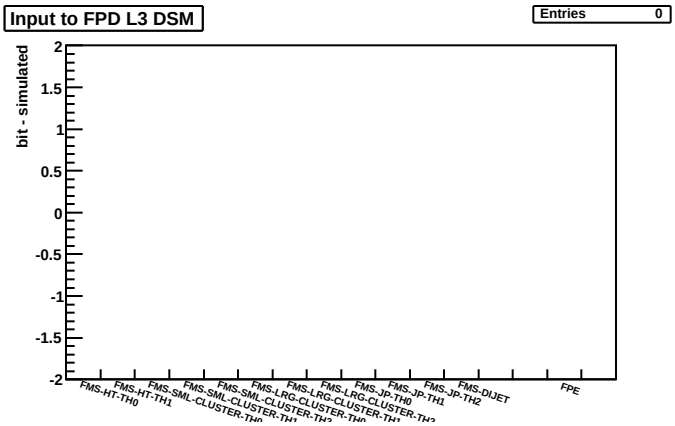

Input to FPD L2 DSM
Entries 385323
CL bits
19100 4949 4591
10804 4222 3847
5516 2776 2601
93021 116494 117402
SMALL LARGE-S LARGE-N

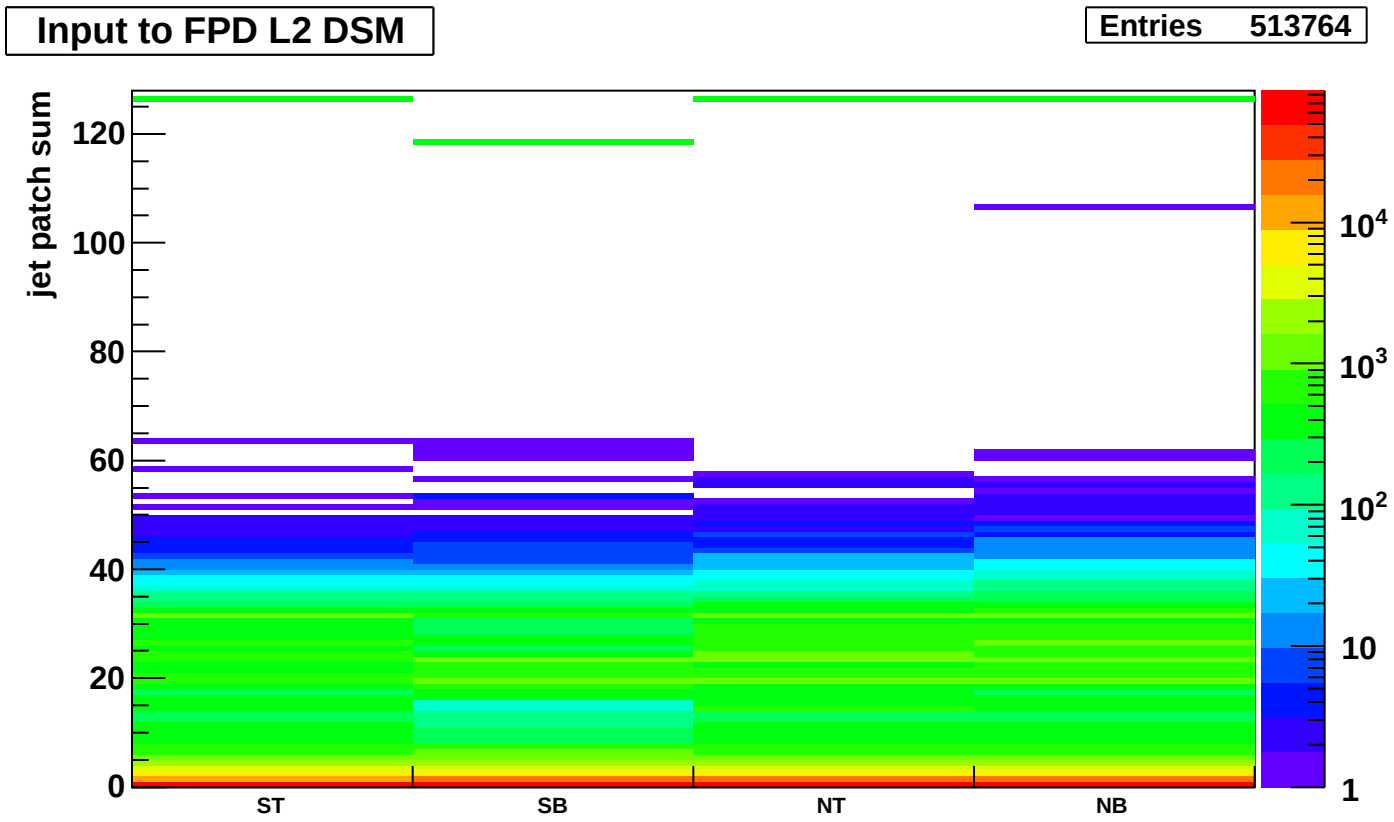

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

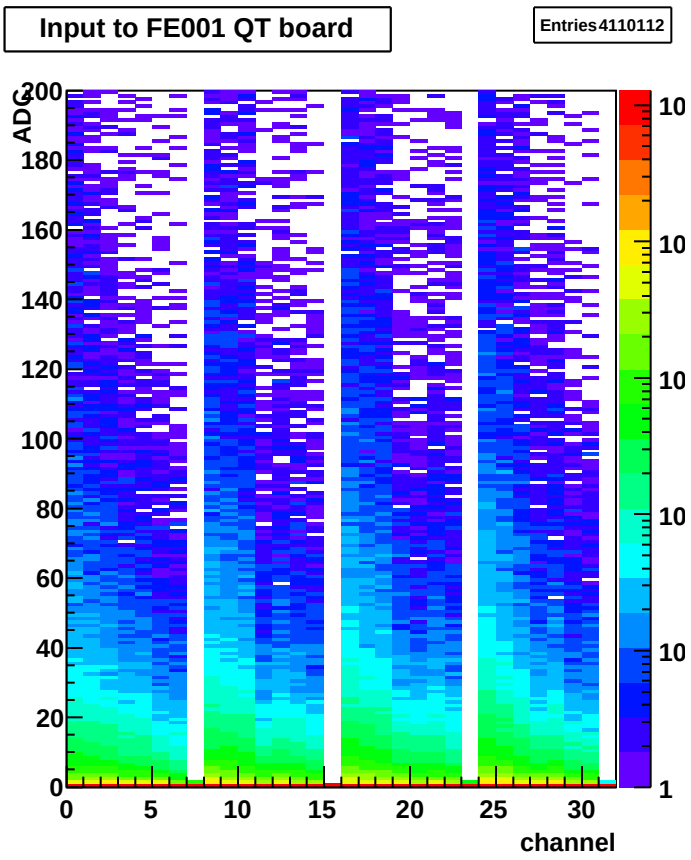

Input to FPD L2 DSM
Entries 385323
HT bits
29389 10374 9365
4613 1659 1680
94439 116408 117396
SMALL LARGE-S LARGE-N

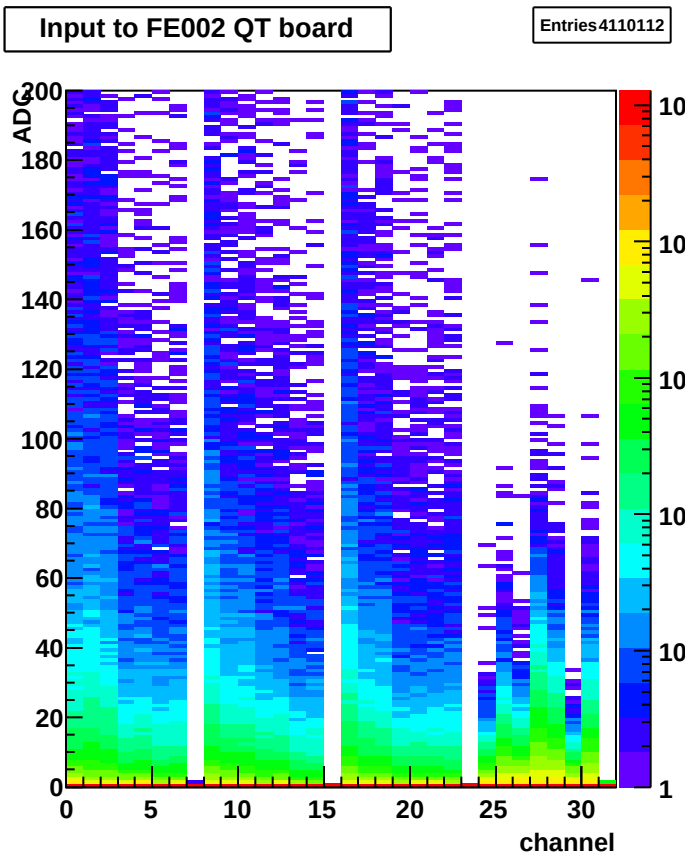

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

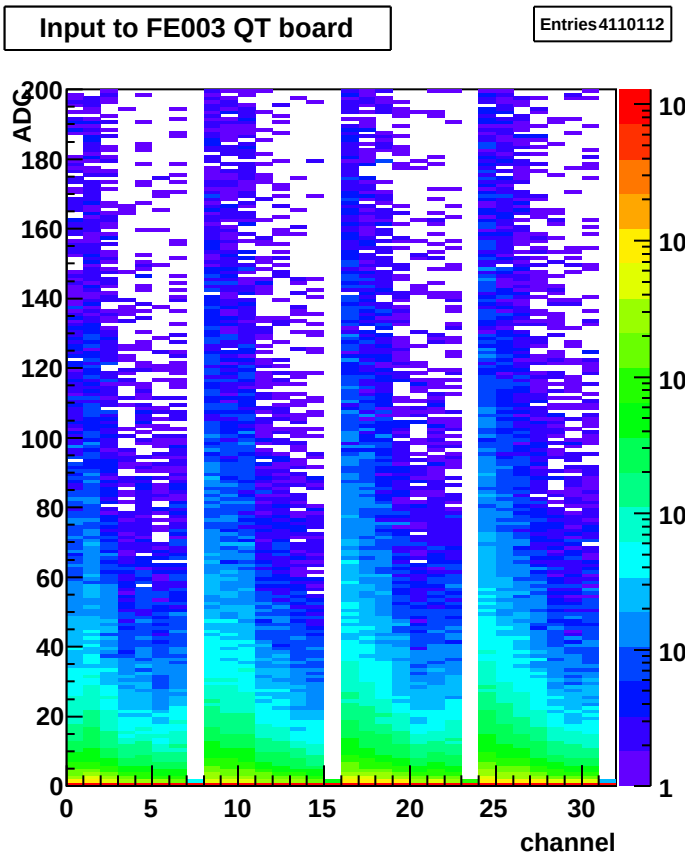

Input to FPD L3 DSM
Entries 2055056
bit
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

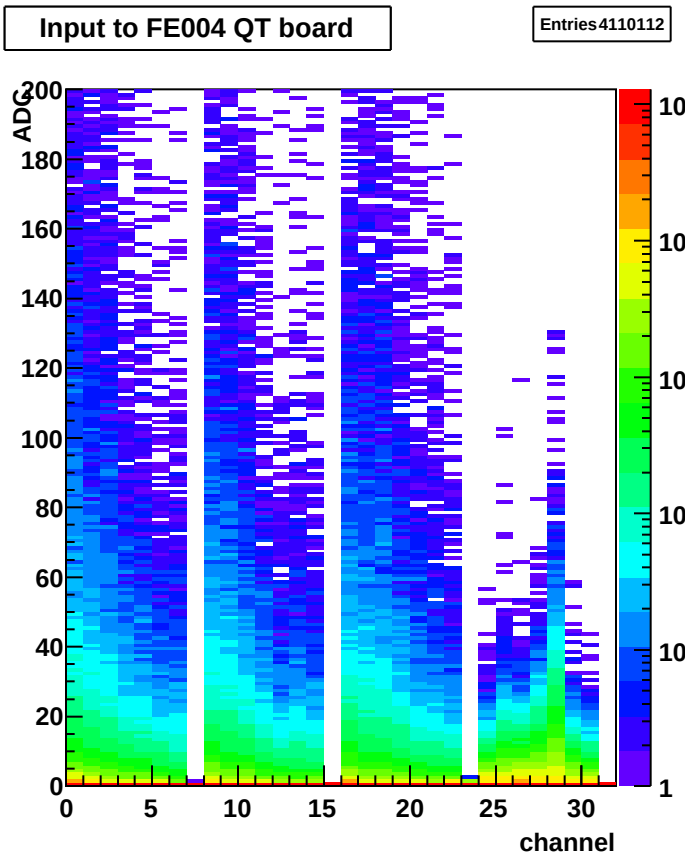

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

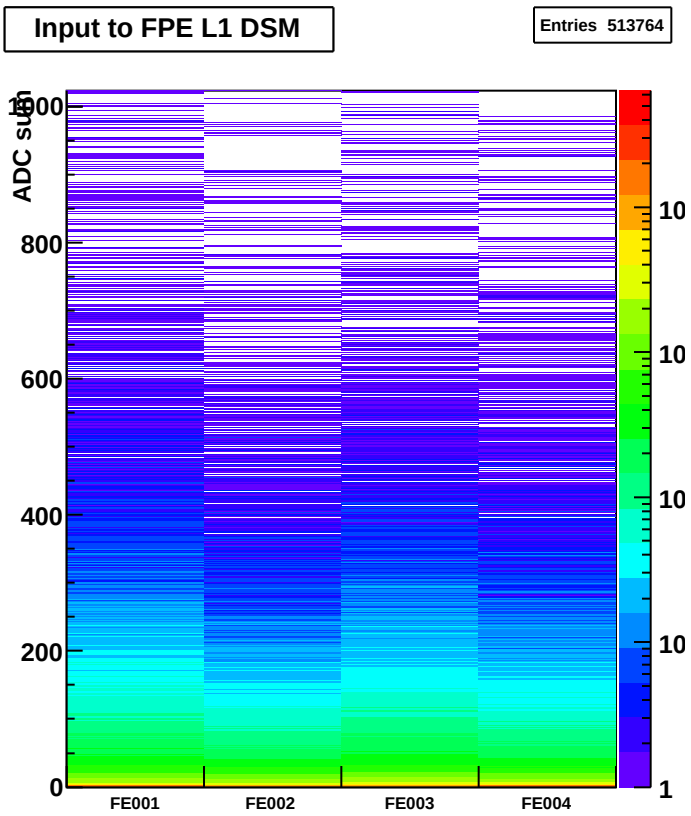

Input to FPD L2 DSM
Entries 513764
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

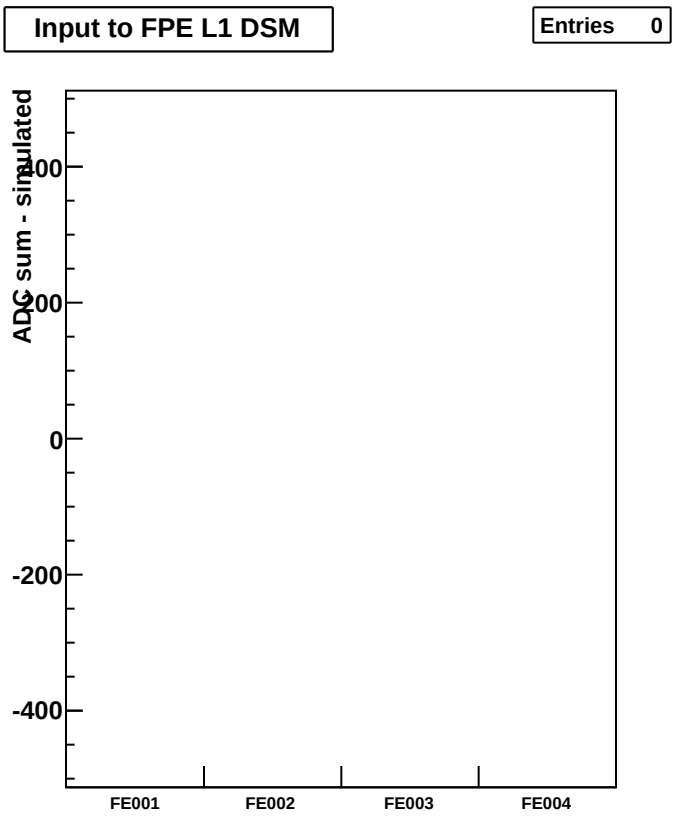

Input to FE001 QT board
Entries 4110112
ADC
channel

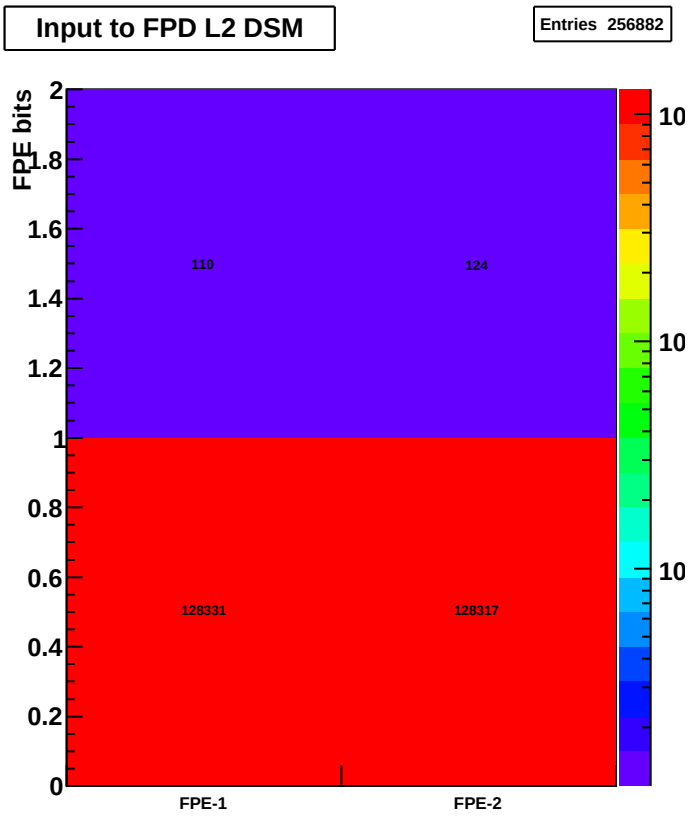

Input to FE002 QT board
Entries 4110112
ADC
channel

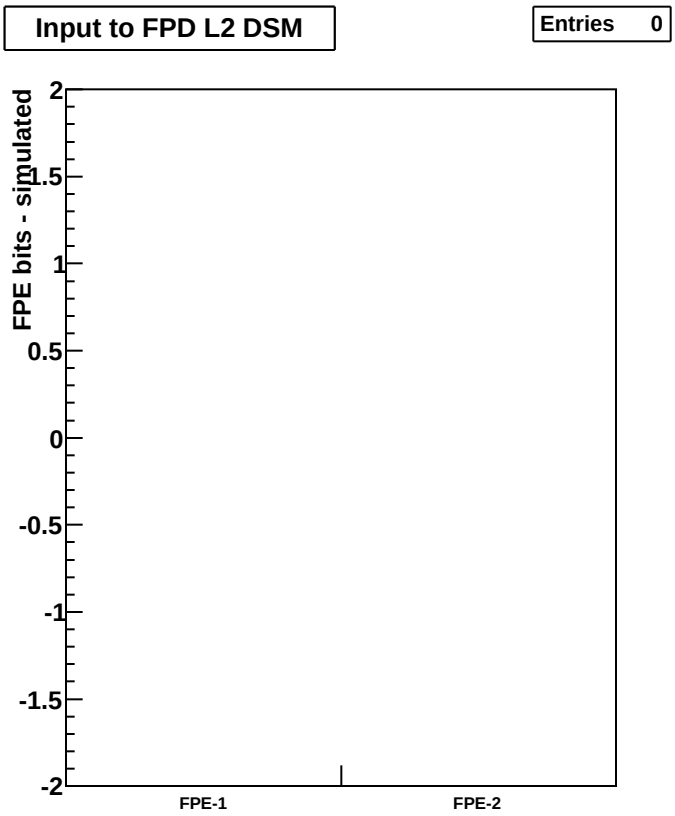

Input to FE003 QT board
Entries 4110112
ADC
channel


Input to FE004 QT board
Entries 4110112
ADC
channel


Input to FPE L1 DSM
Entries 513764
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 256882
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
110
124
128331
128317
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2