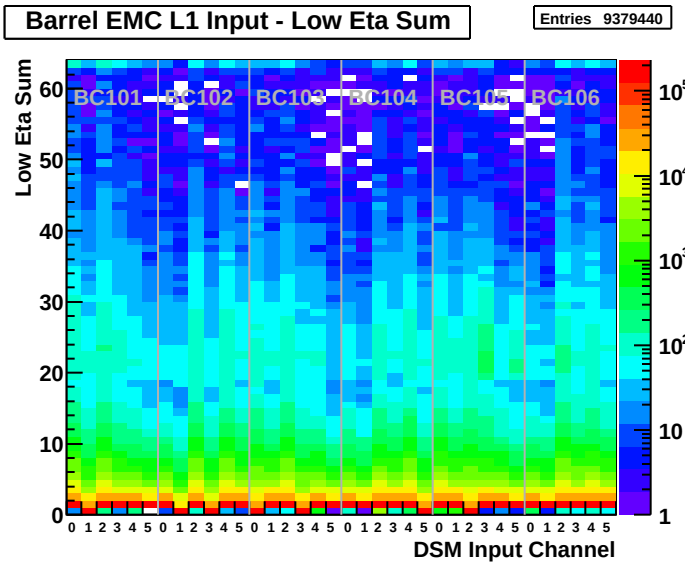
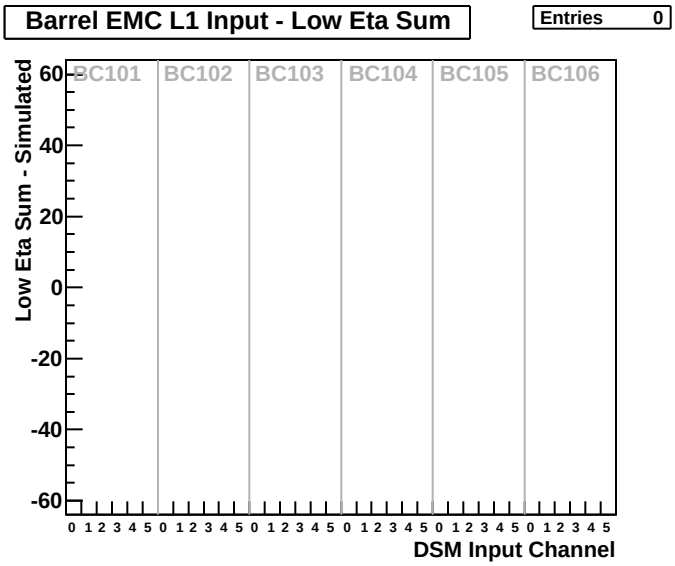
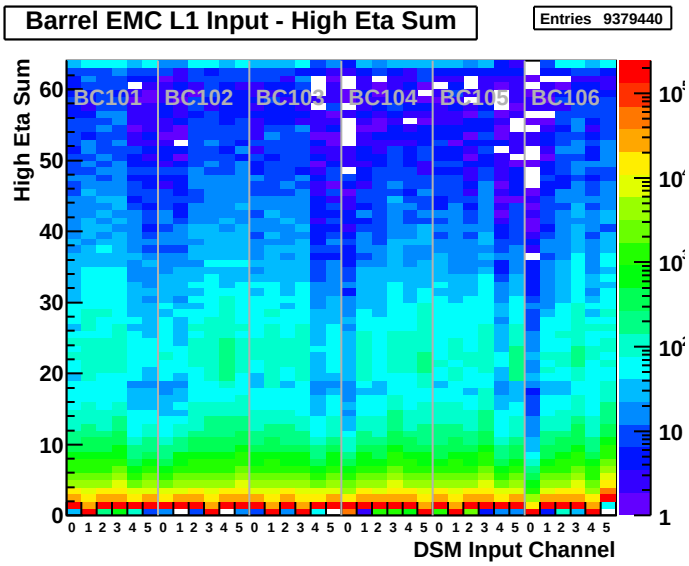
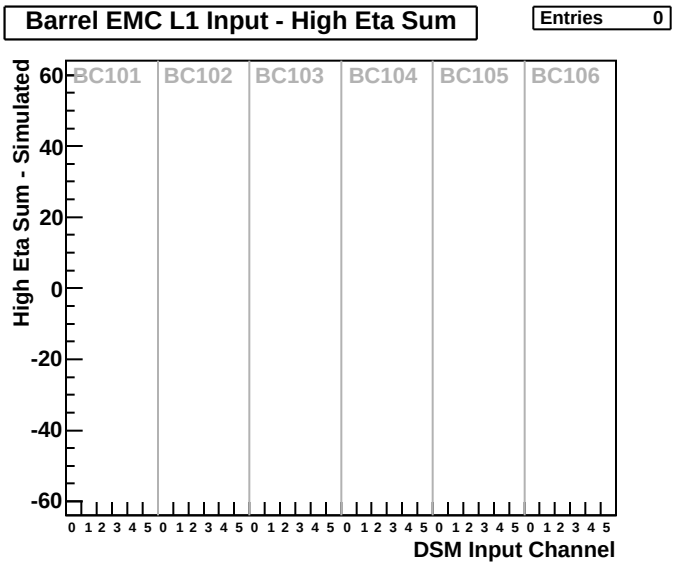
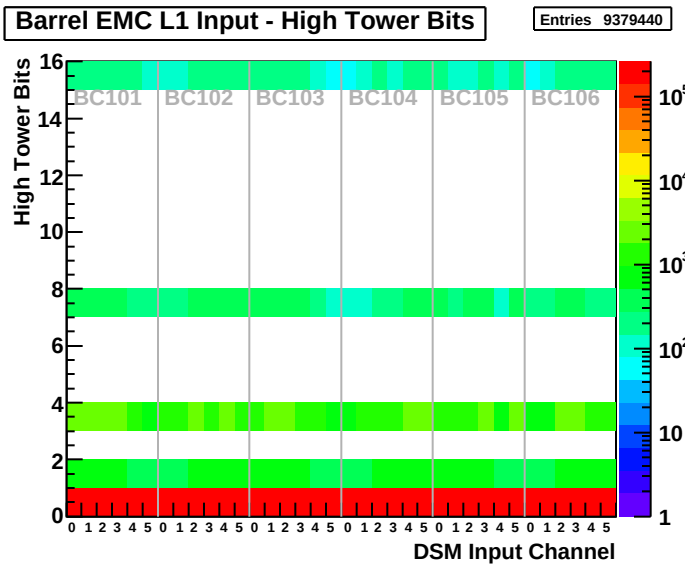
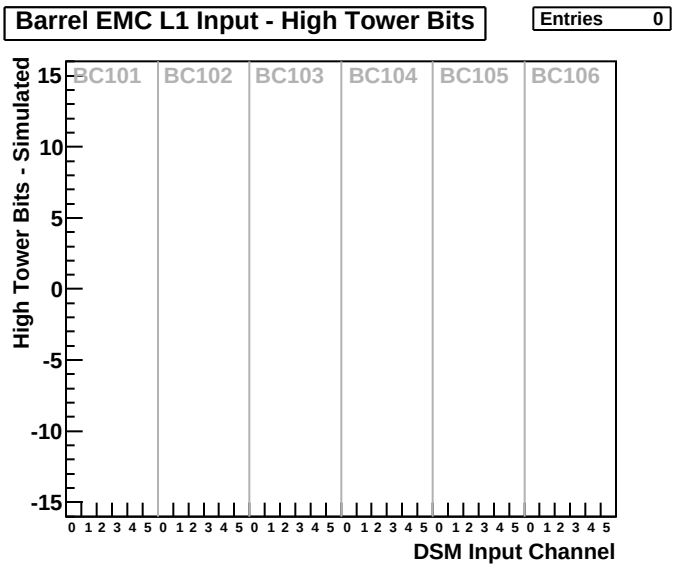

Barrel EMC L1 Input - Low Eta Sum
Entries 9379440
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

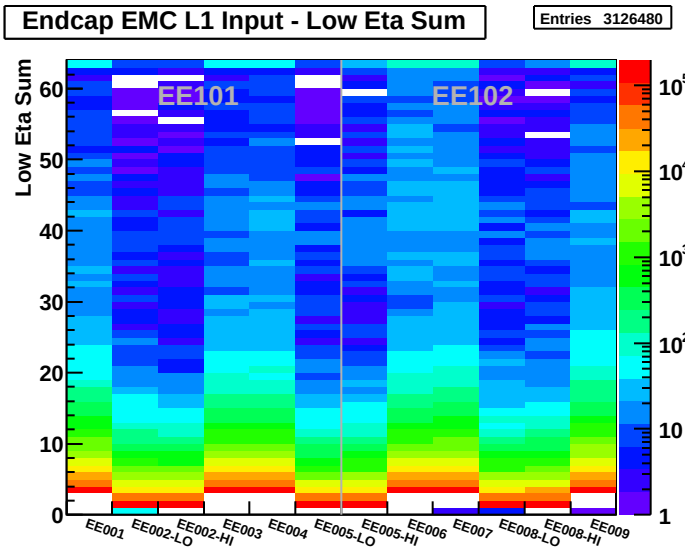

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

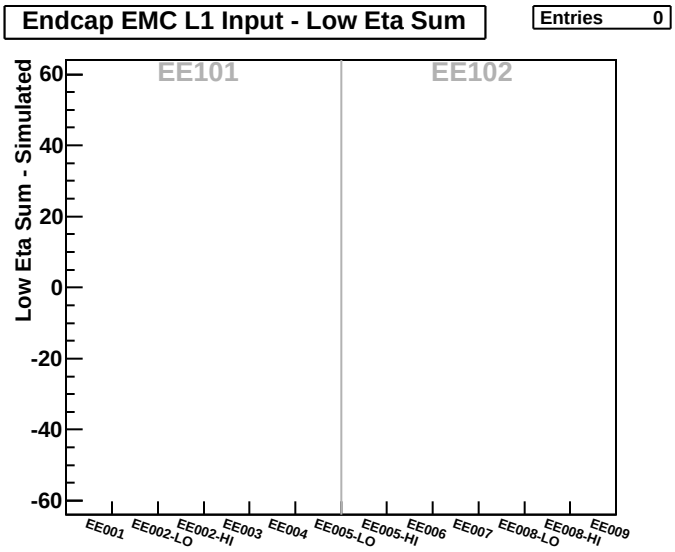

Barrel EMC L1 Input - High Eta Sum
Entries 9379440
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

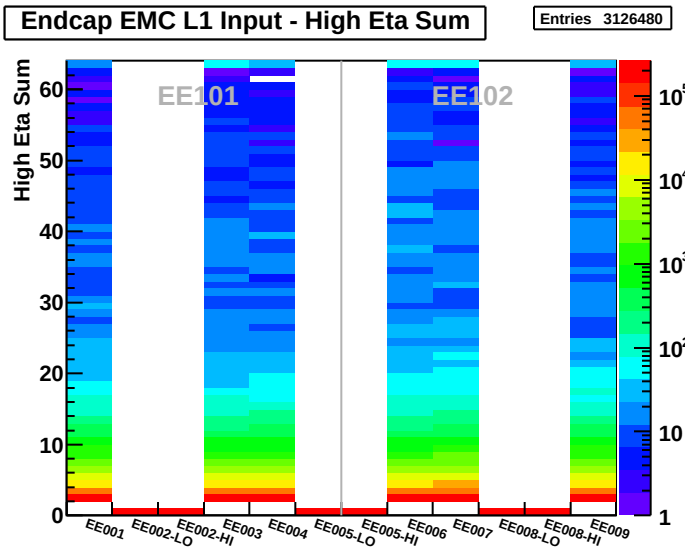

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

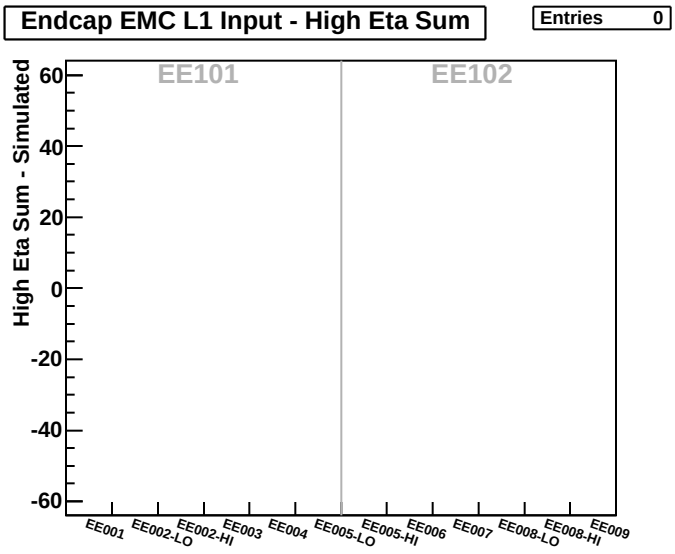

Barrel EMC L1 Input - High Tower Bits
Entries 9379440
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

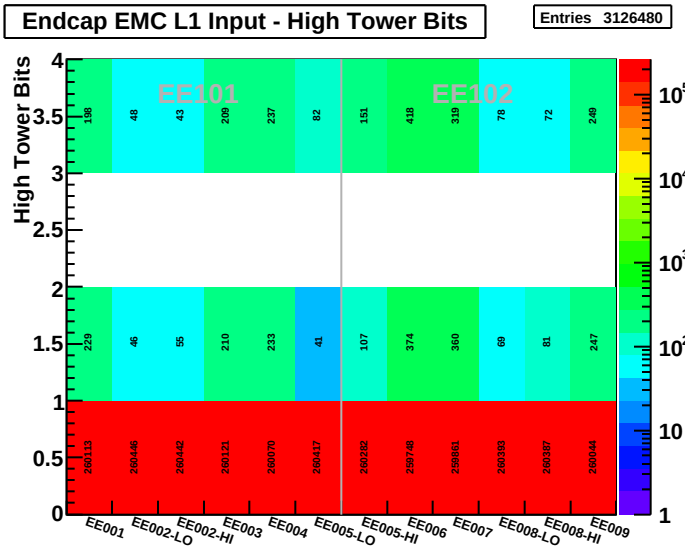

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

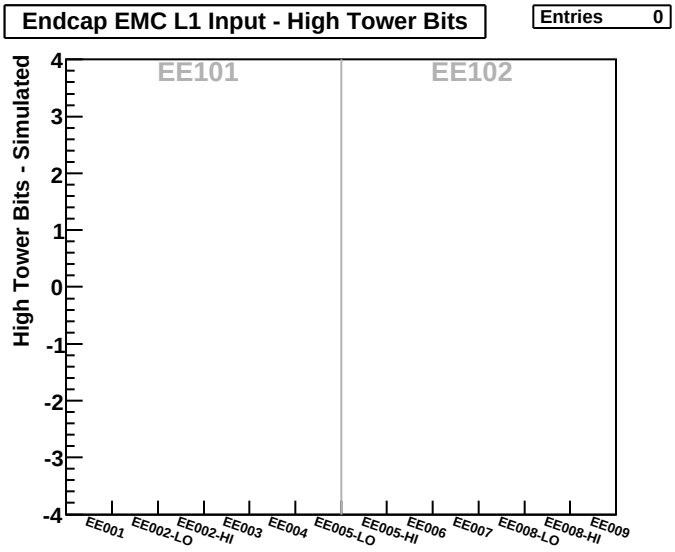

Endcap EMC L1 Input - Low Eta Sum
Entries 3126480
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

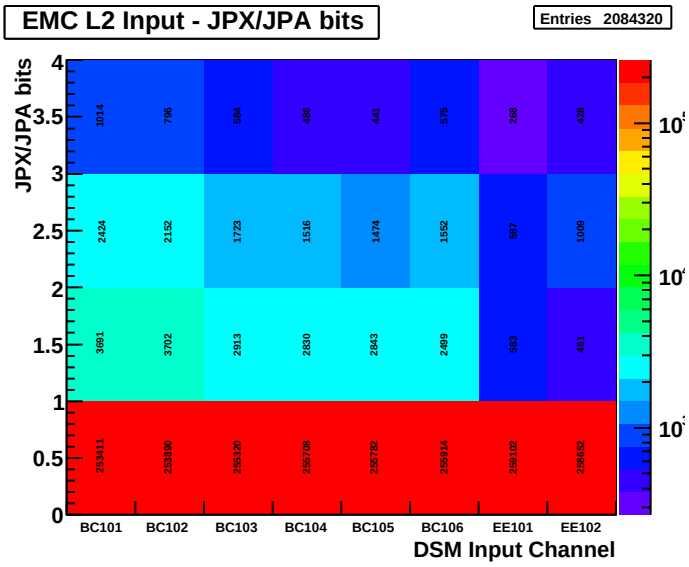

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

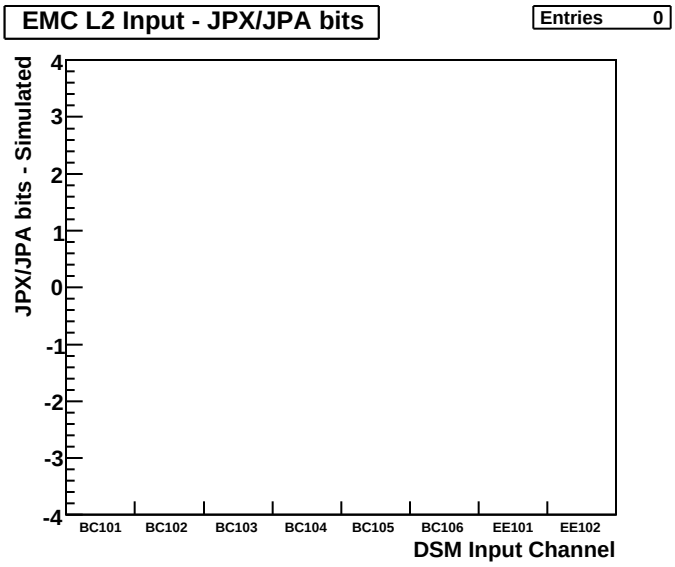

Endcap EMC L1 Input - High Eta Sum
Entries 3126480
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

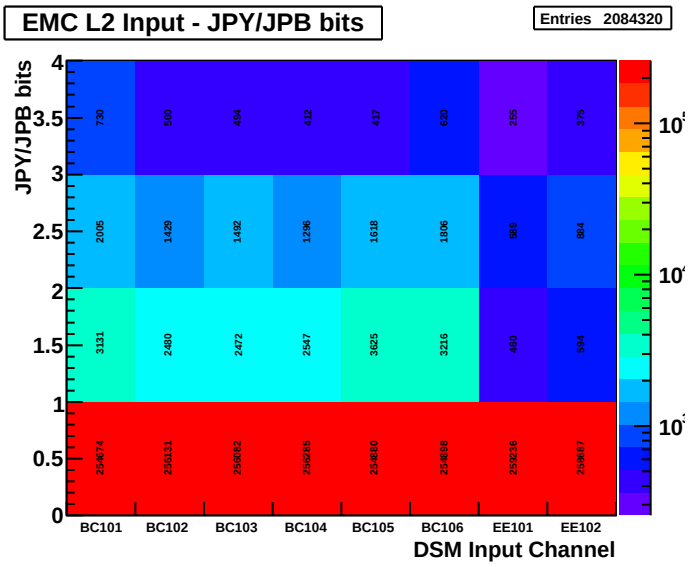

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

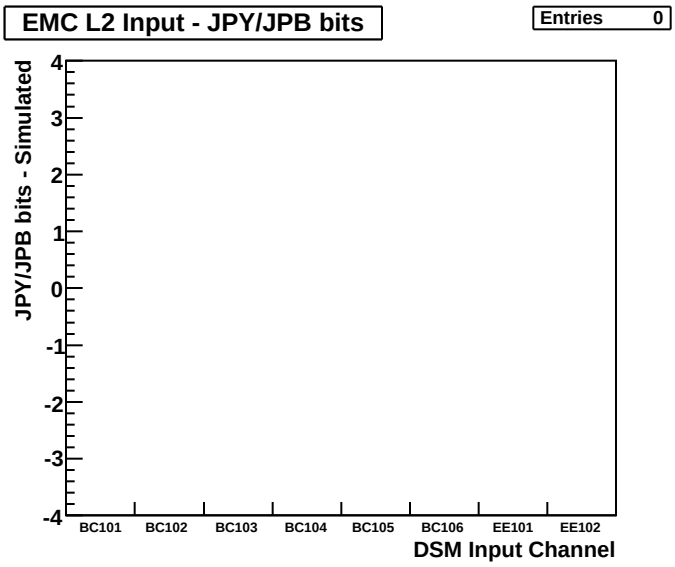

Endcap EMC L1 Input - High Tower Bits
Entries 3126480
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

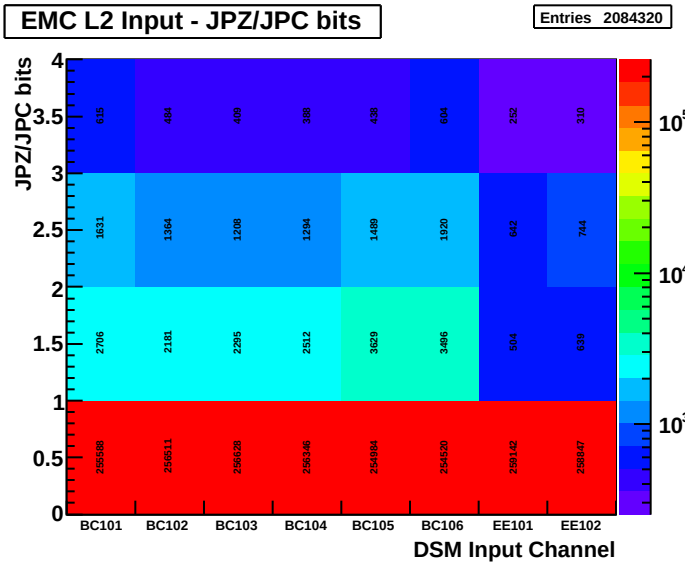

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

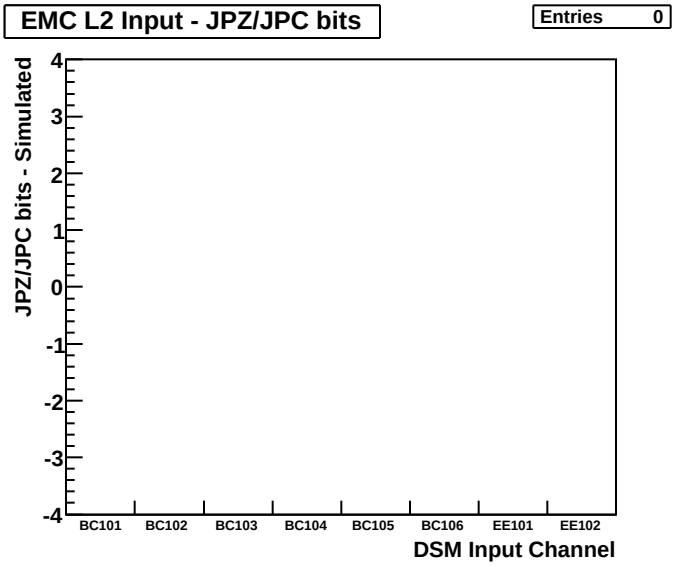

EMC L2 Input - JPX/JPA bits
Entries 2084320
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1014 796 584 486 441 575 268 428
2424 2152 1723 1516 1474 1552 987 1009
3691 3702 2913 2830 2843 2499 583 451
253411 253890 255320 255708 255782 255914 259102 259652

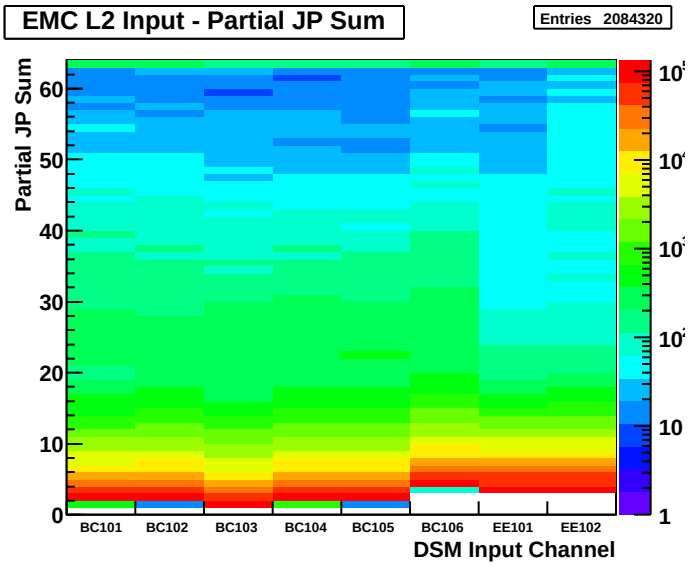

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

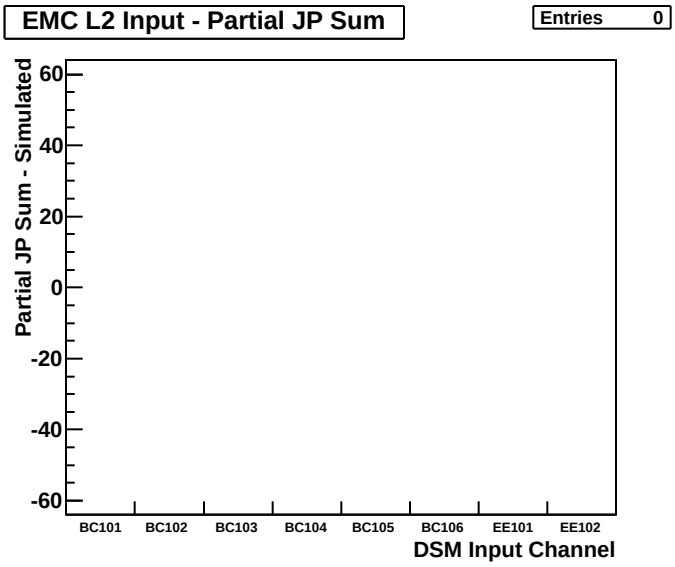

EMC L2 Input - JPY/JPB bits
Entries 2084320
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
730 500 494 412 417 620 255 375
2005 1429 1492 1296 1618 1806 589 884
3131 2480 2472 2547 3625 3216 460 594
254674 256131 256082 256285 254880 254898 259236 259587

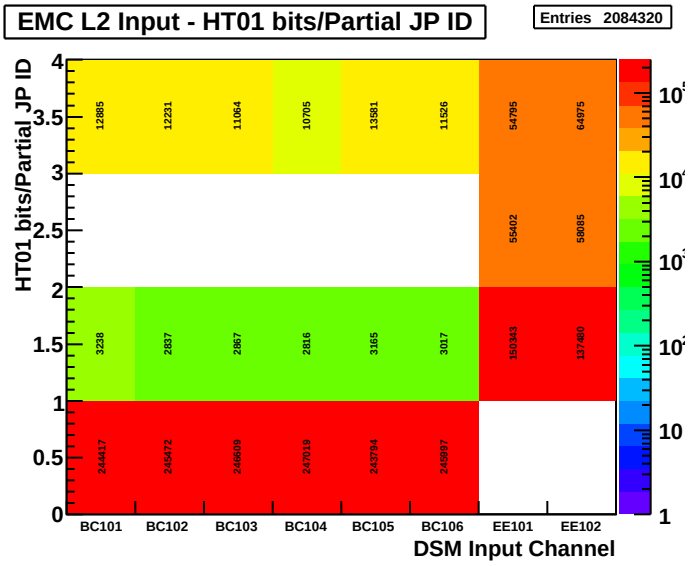

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

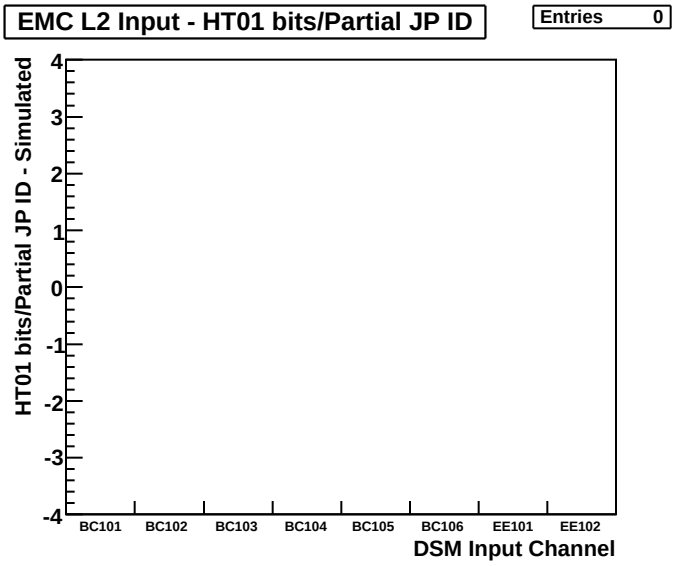

EMC L2 Input - JPZ/JPC bits
Entries 2084320
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
615 484 409 388 438 604 252 310
1631 1364 1208 1204 1489 1920 642 744
2706 2181 2295 2512 3629 3486 504 639
255588 256511 256628 256346 254894 254520 259142 259847

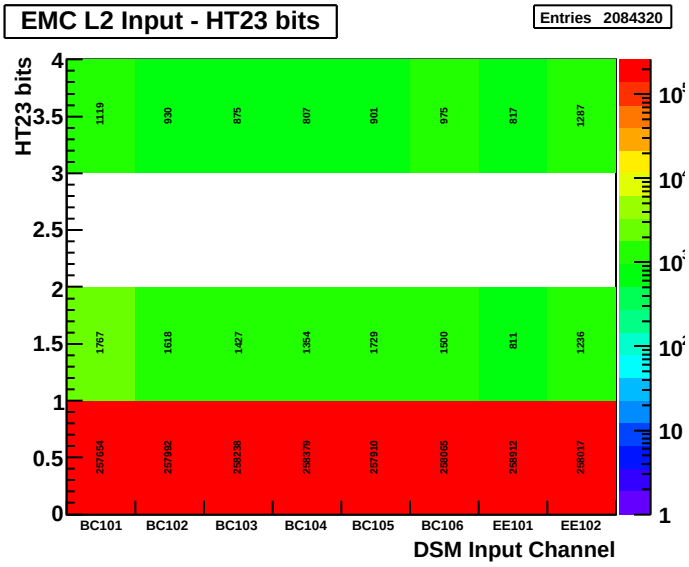

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

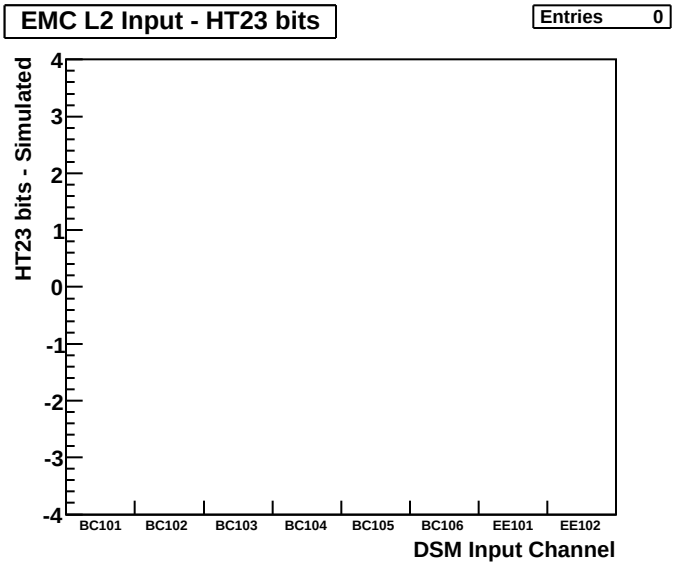

EMC L2 Input - Partial JP Sum
Entries 2084320
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

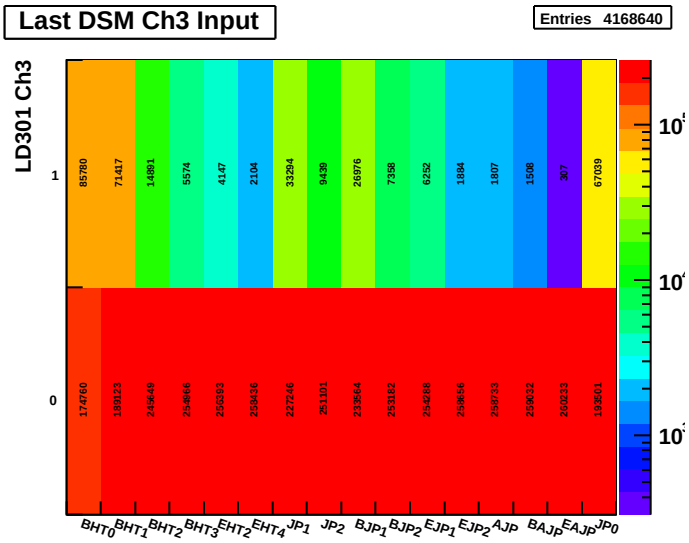

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

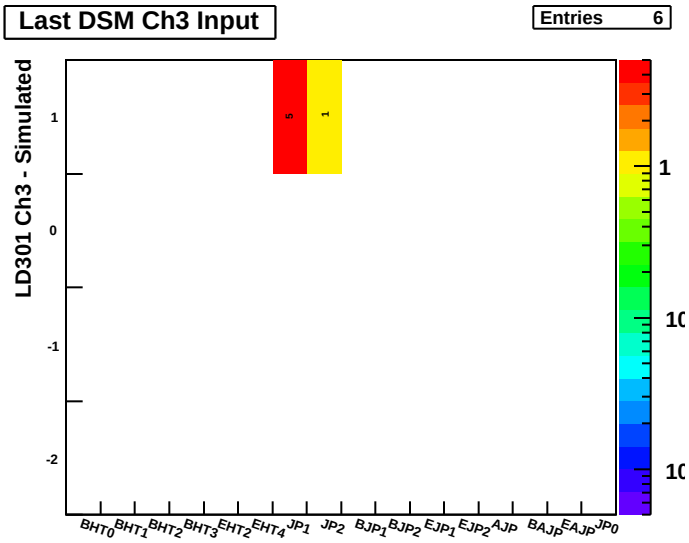

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2084320
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

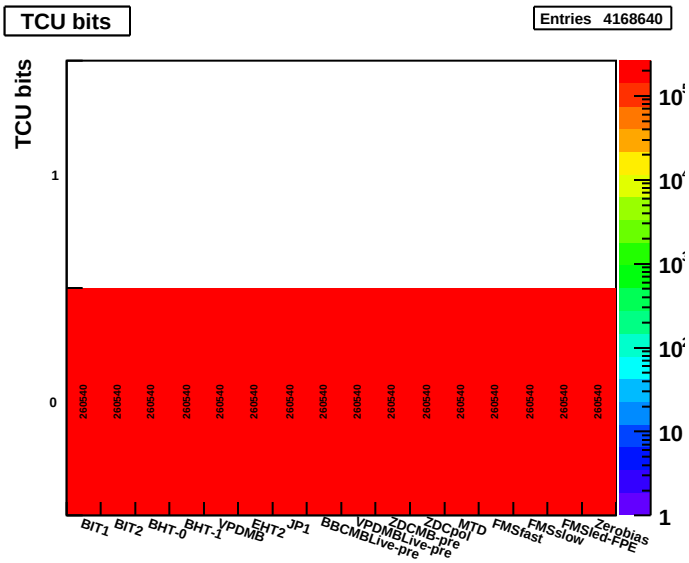

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

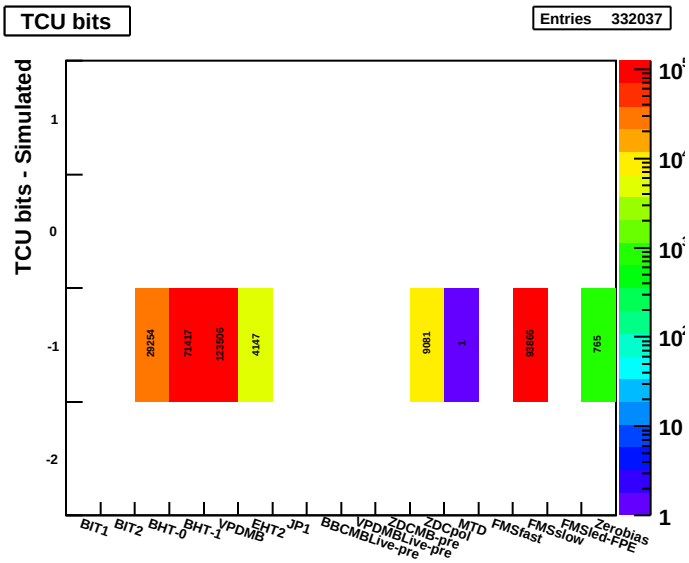

EMC L2 Input - HT23 bits
Entries 2084320
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

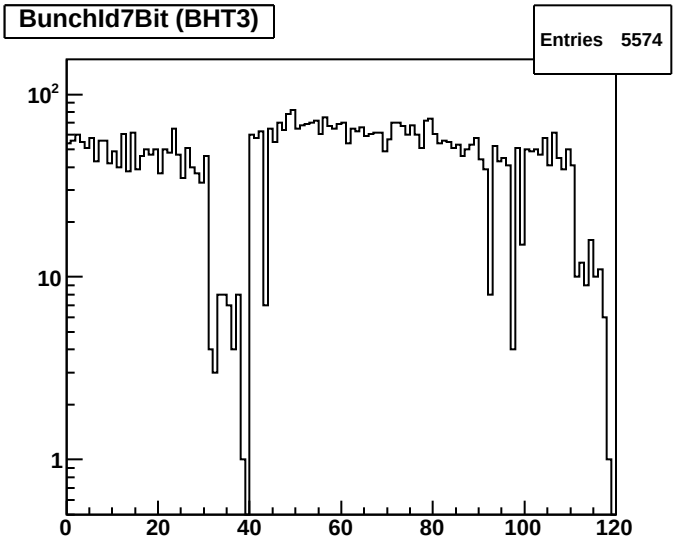

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

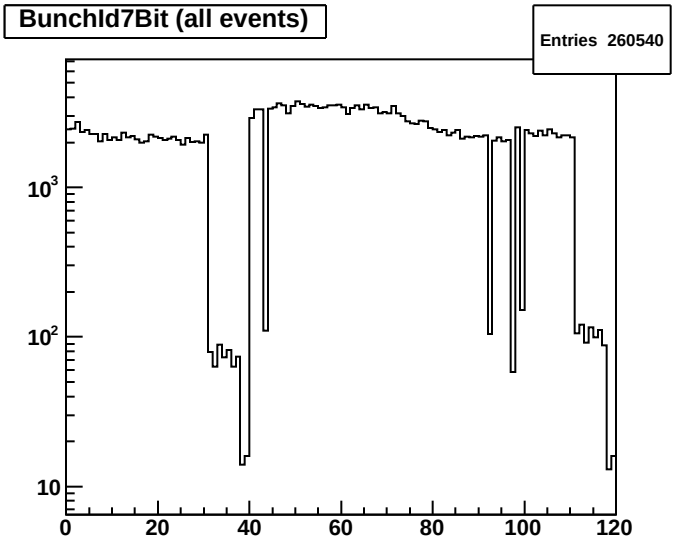

Last DSM Ch3 Input
Entries 4168640
LD301 Ch3
85780 71417 14891 5574 4147 2104 33294 9439 26976 7358 6252 1884 1807 1508 307 67039
174760 189123 245649 254966 256393 258436 227246 251101 233564 253182 254288 258556 258733 259032 260233 193501
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

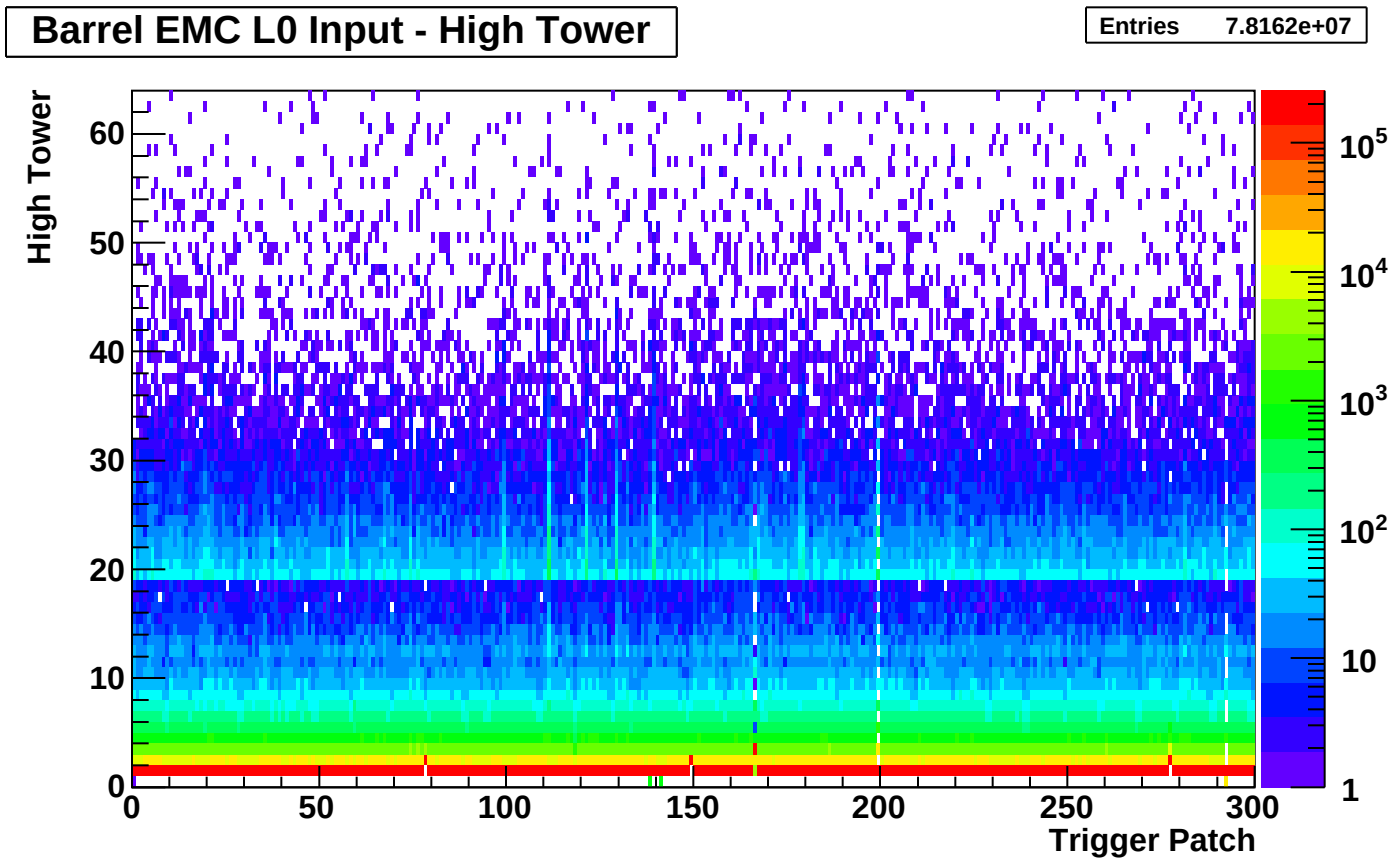

Last DSM Ch3 Input
Entries 6
LD301 Ch3 - Simulated
5 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

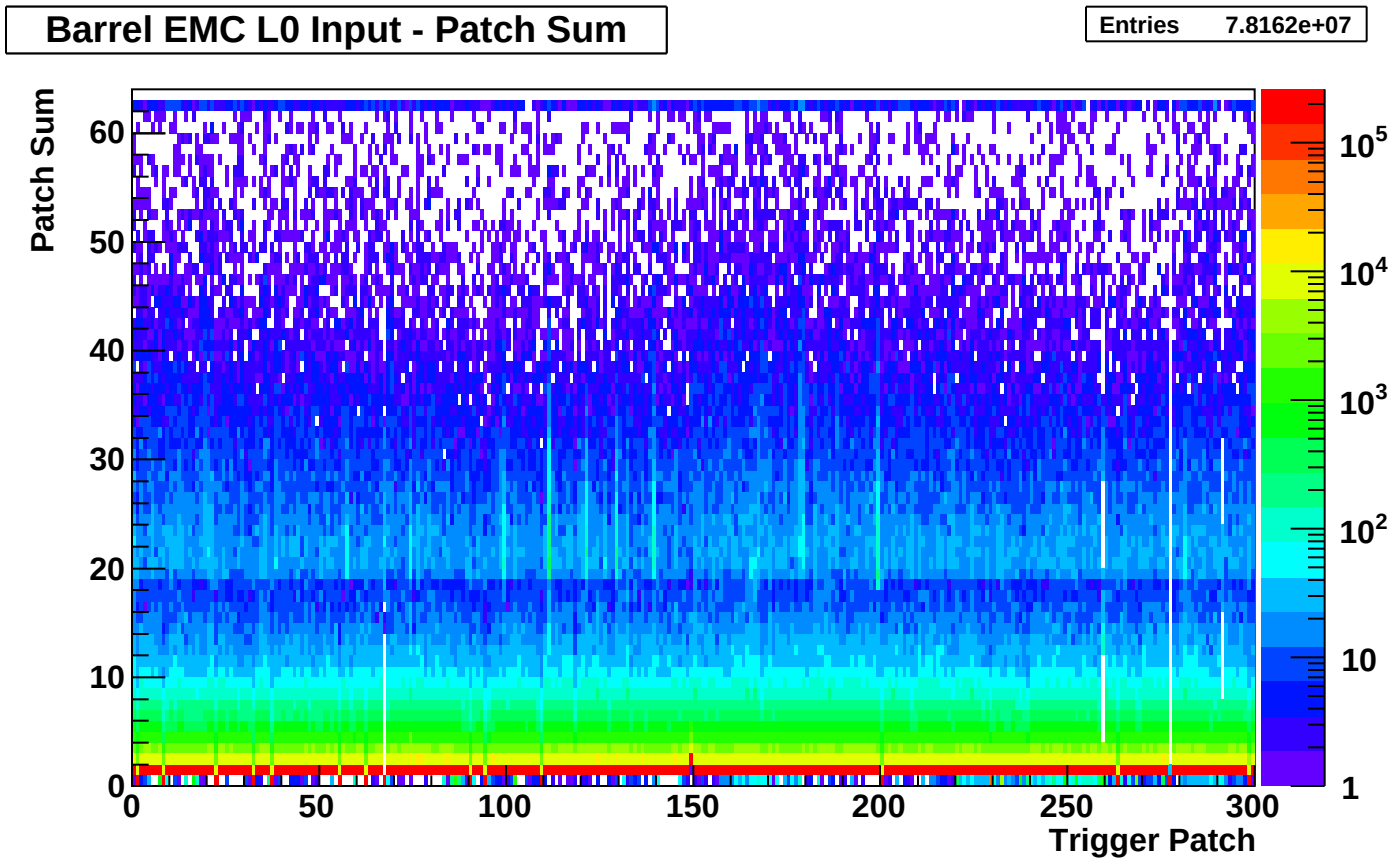

TCU bits
Entries 4168640
TCU bits
260540
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

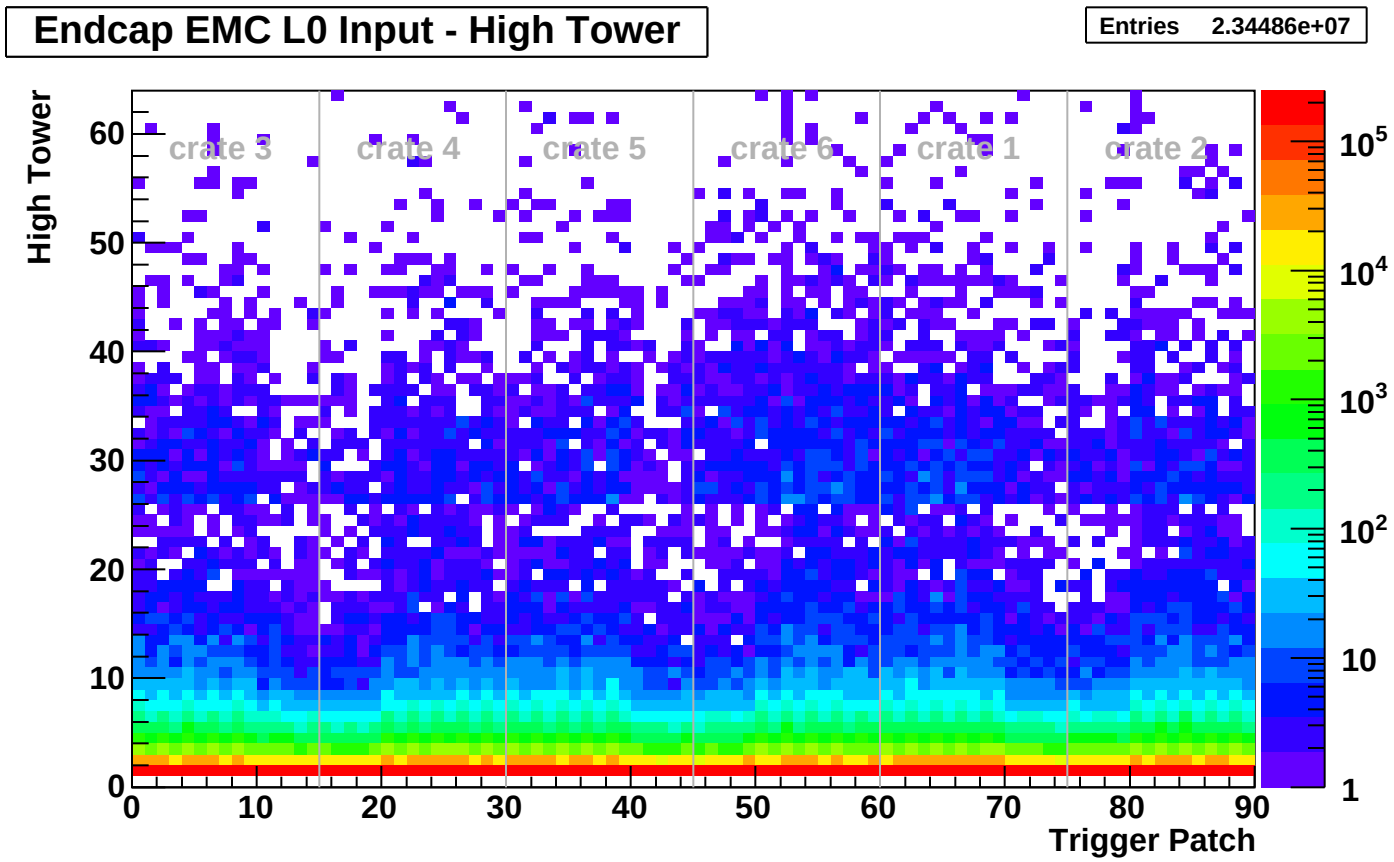

TCU bits
Entries 332037
TCU bits - Simulated
29254 71417 123506 4147 9081 1 93866 765
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

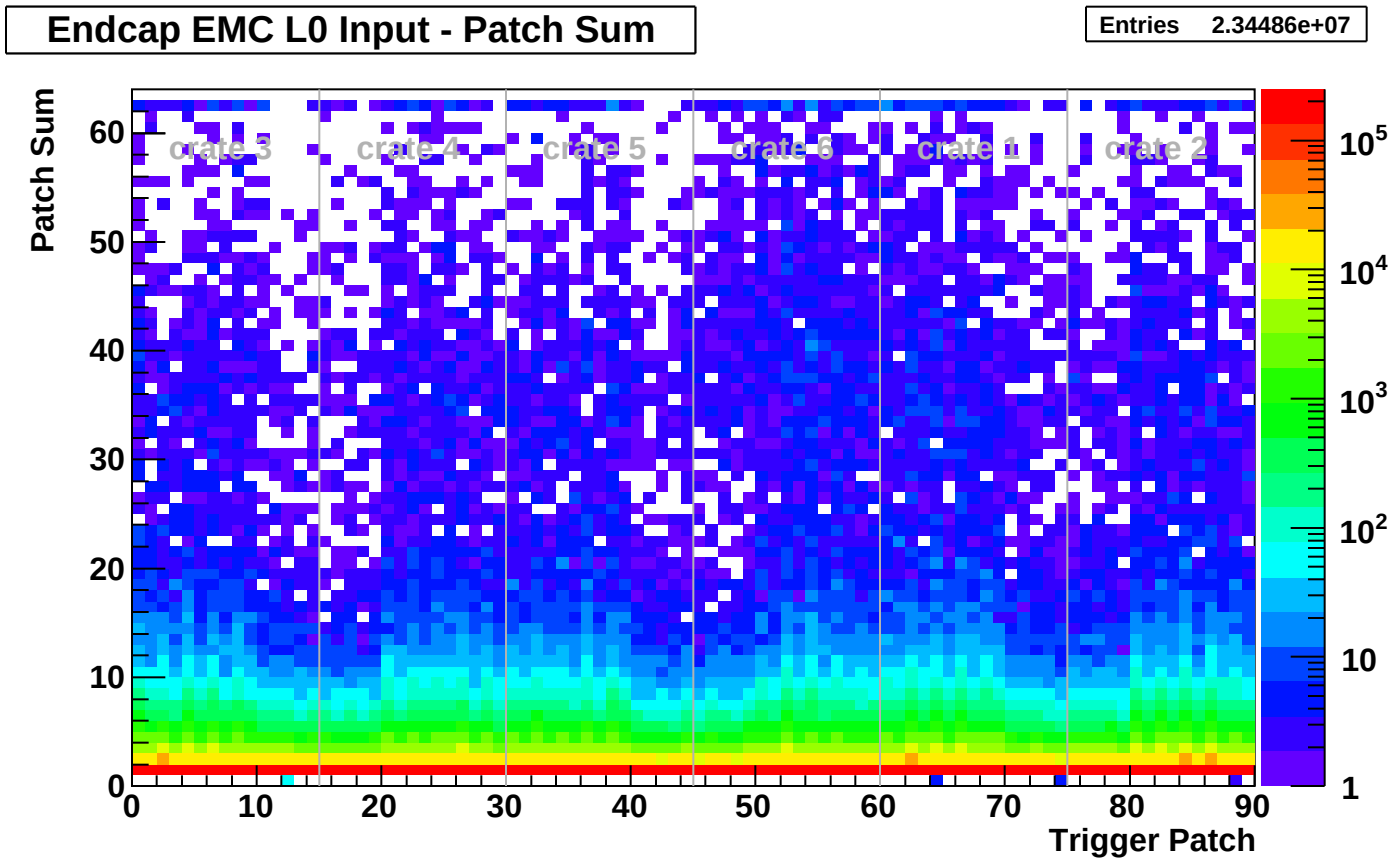

BunchId7Bit (BHT3)
Entries 5574

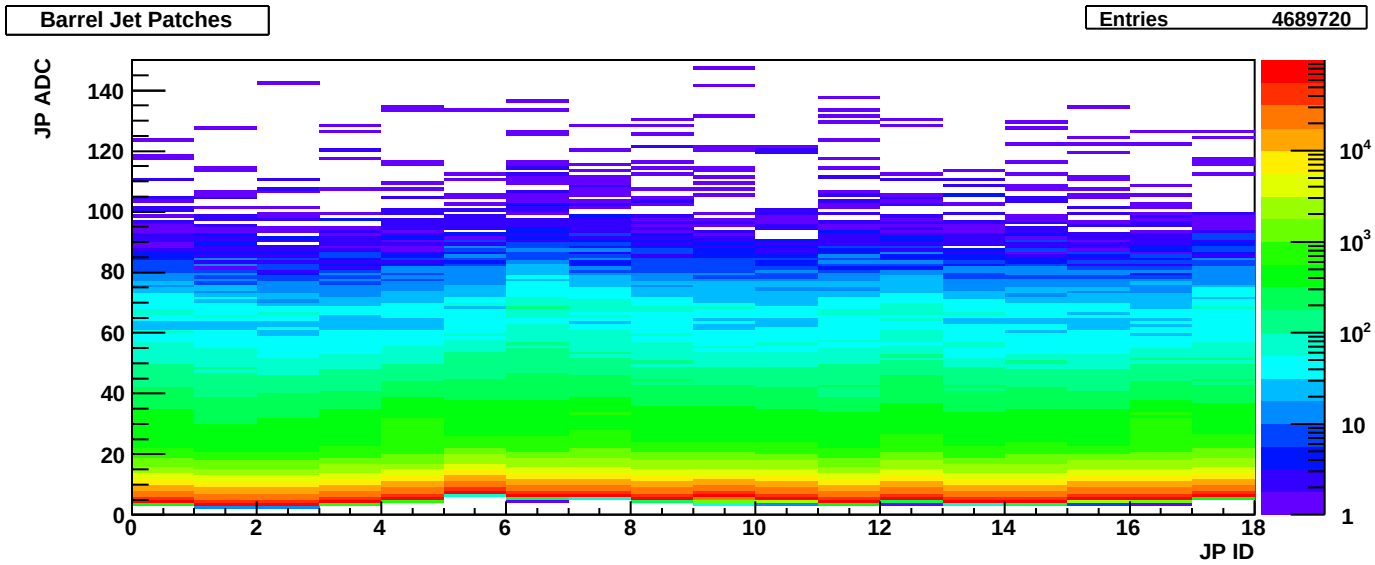

BunchId7Bit (all events)
Entries 260540

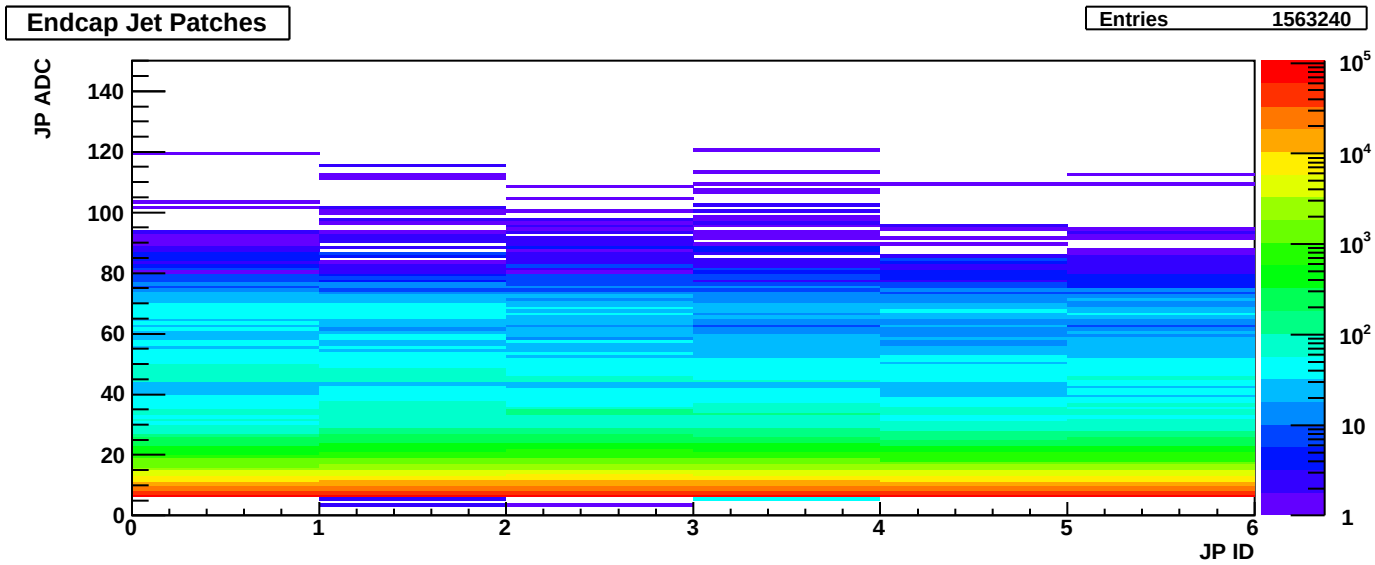

Barrel EMC L0 Input - High Tower
Entries 7.8162e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

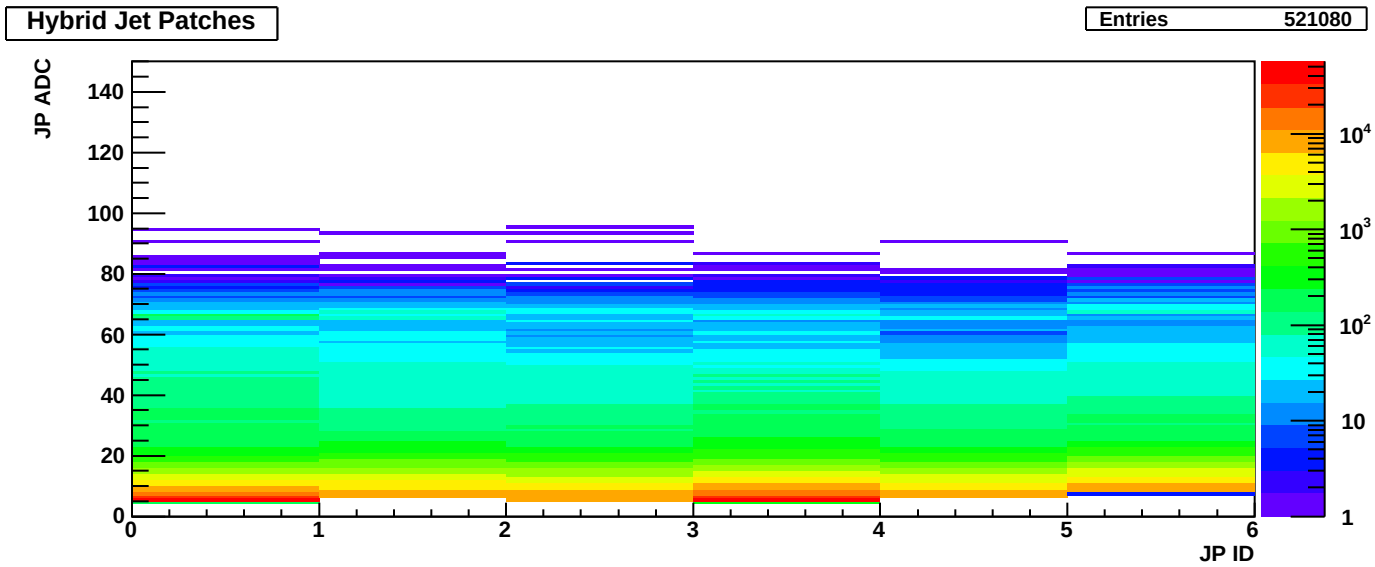

Barrel EMC L0 Input - Patch Sum
Entries 7.8162e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

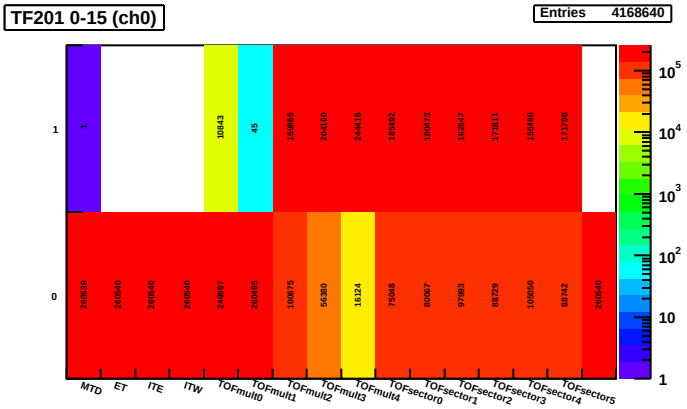

Endcap EMC L0 Input - High Tower
Entries 2.34486e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

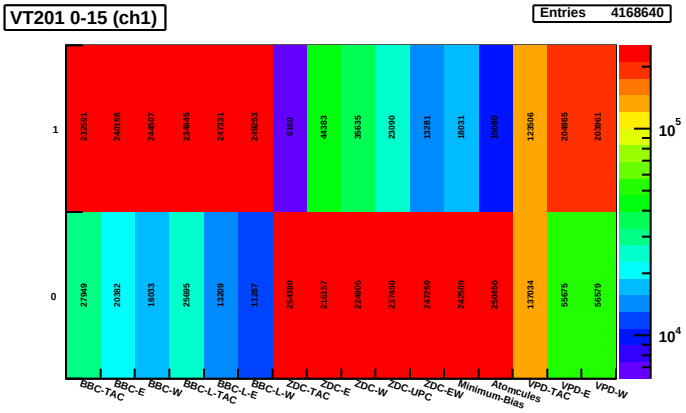

Endcap EMC L0 Input - Patch Sum
Entries 2.34486e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

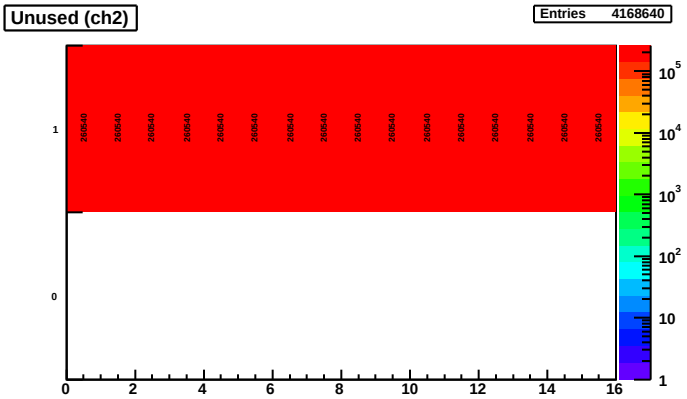

Barrel Jet Patches
Entries 4689720
JP ADC
JP ID

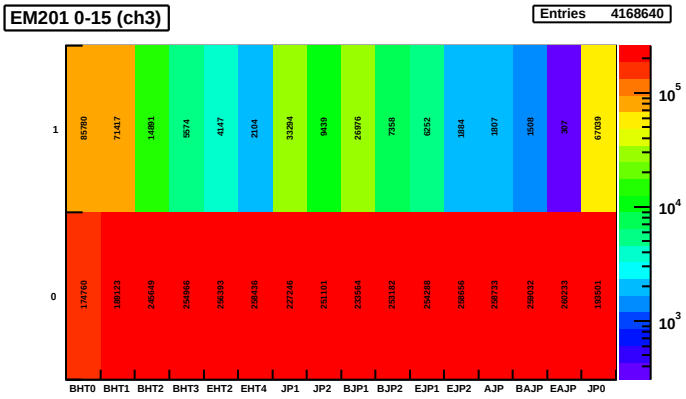

Endcap Jet Patches
Entries 1563240
JP ADC
JP ID

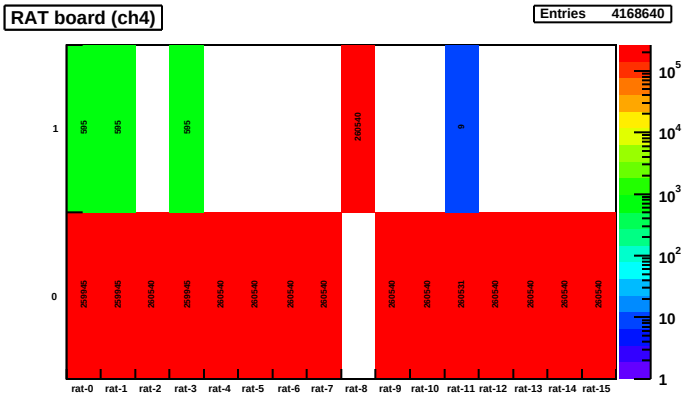

Hybrid Jet Patches
Entries 521080
JP ADC
JP ID

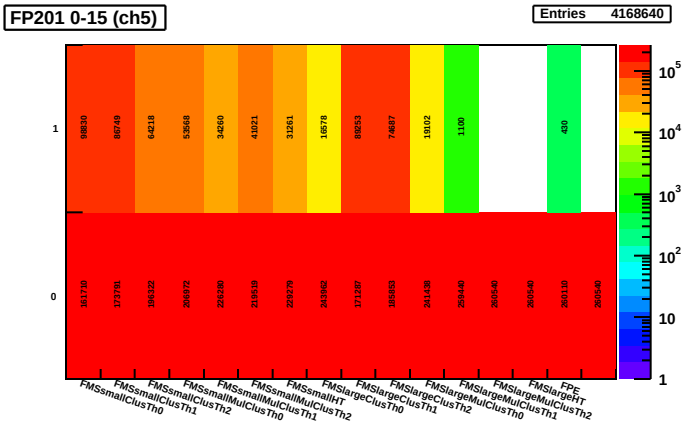

TF201 0-15 (ch0)
Entries 4168640
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

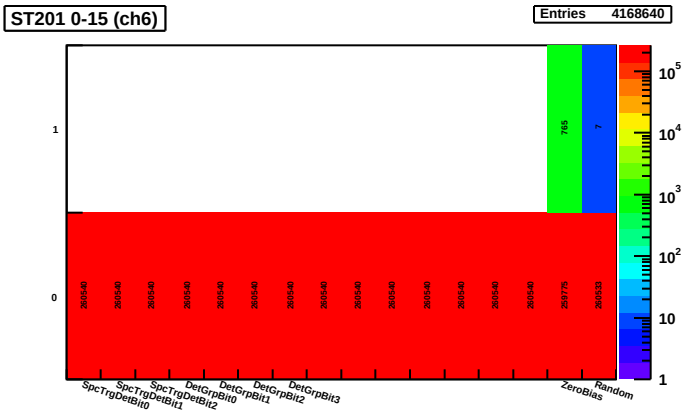

VT201 0-15 (ch1)
Entries 4168640
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

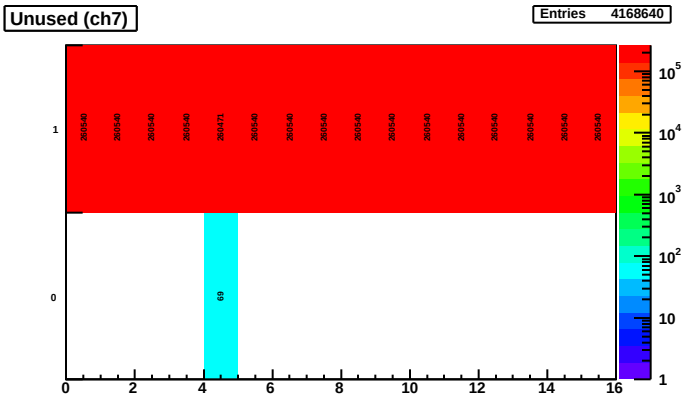

Unused (ch2)
Entries 4168640

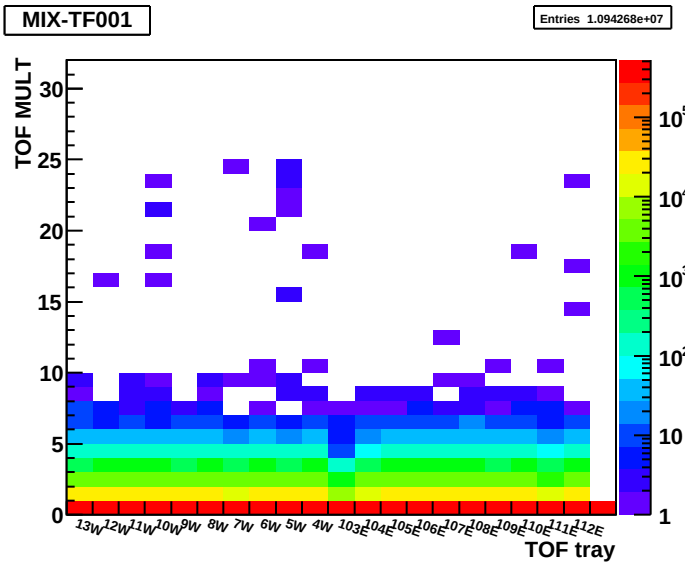

EM201 0-15 (ch3)
Entries 4168640
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

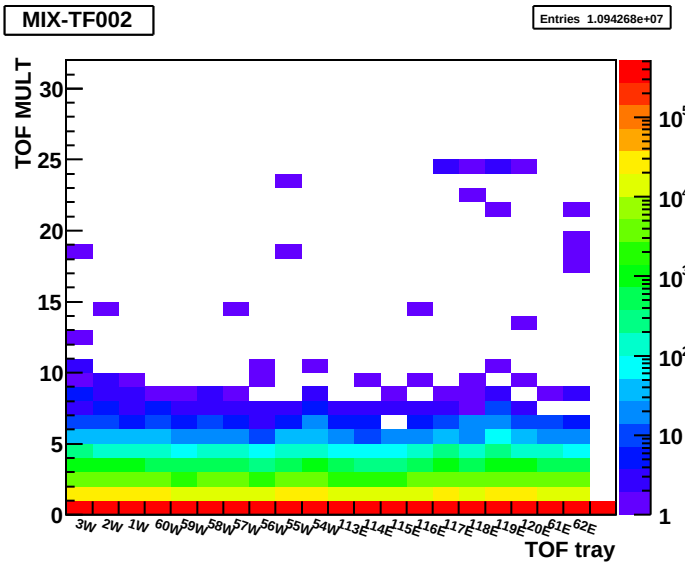

RAT board (ch4)
Entries 4168640
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

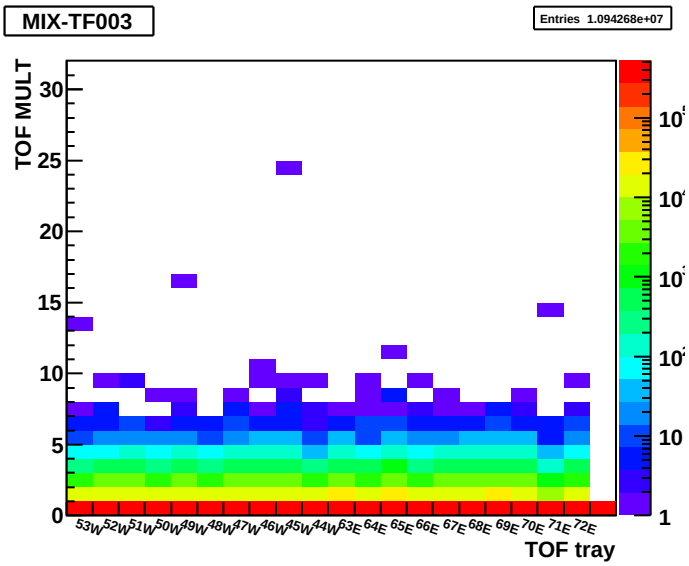

FP201 0-15 (ch5)
Entries 4168640
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

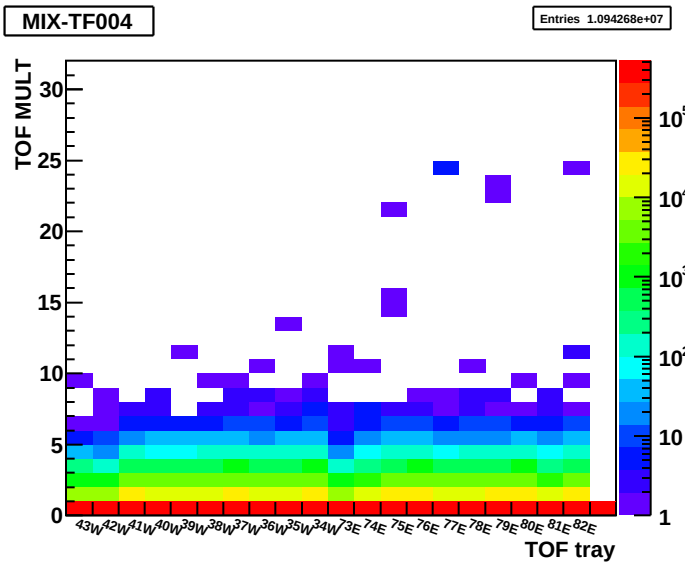

ST201 0-15 (ch6)
Entries 4168640
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

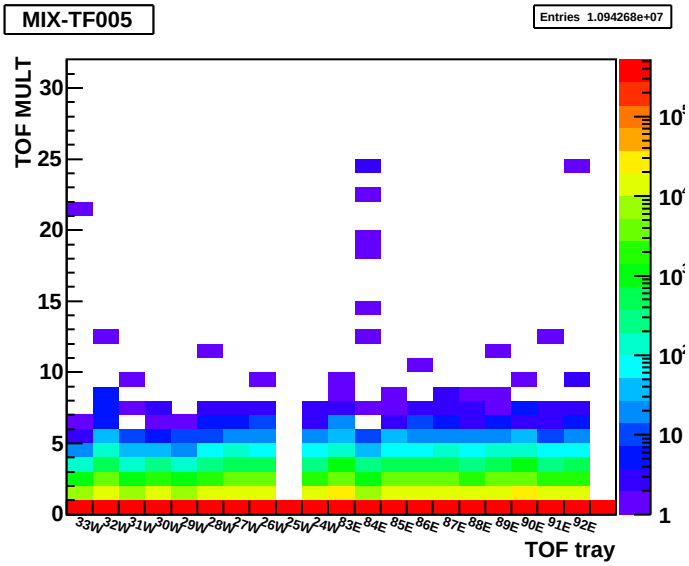

Unused (ch7)
Entries 4168640

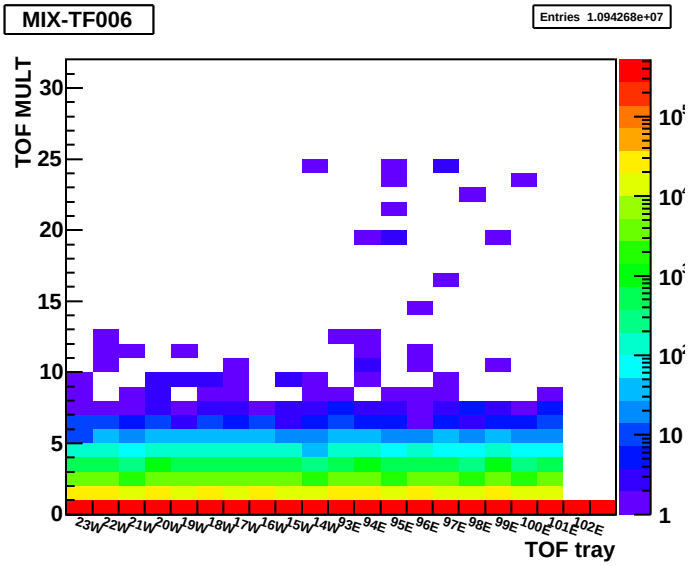

MIX-TF001
Entries 1.094268e+07
TOF MULT
TOF tray

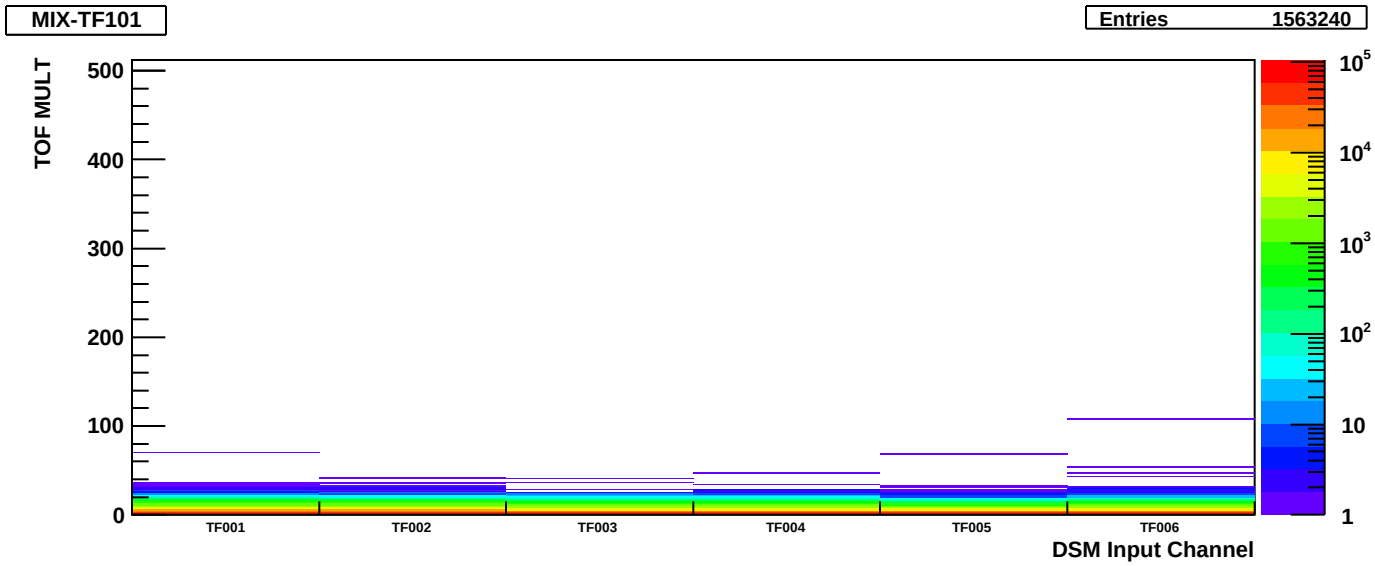

MIX-TF002
Entries 1.094268e+07
TOF MULT
TOF tray

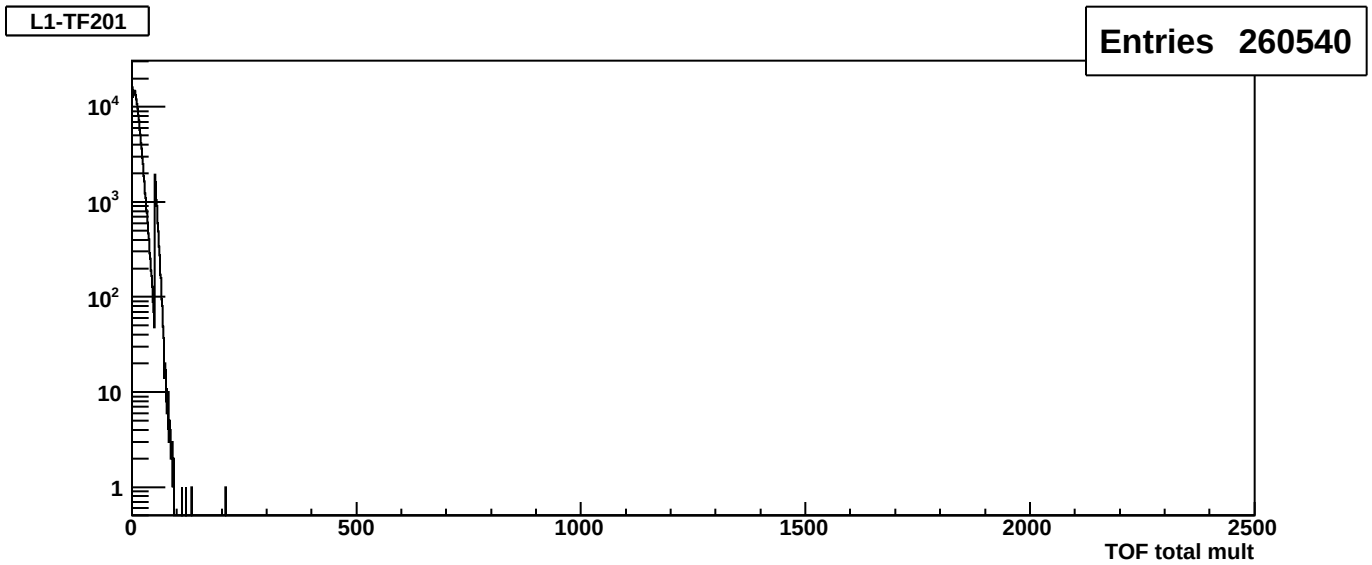

MIX-TF003
Entries 1.094268e+07
TOF MULT
TOF tray

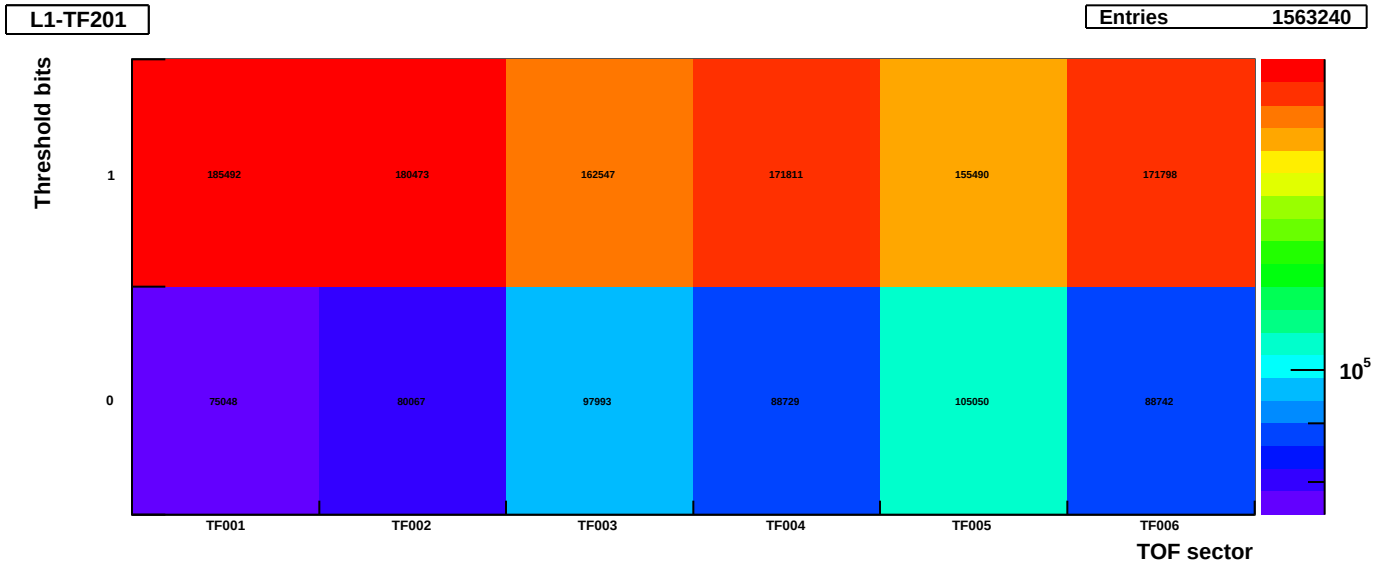

MIX-TF004
Entries 1.094268e+07
TOF MULT
TOF tray

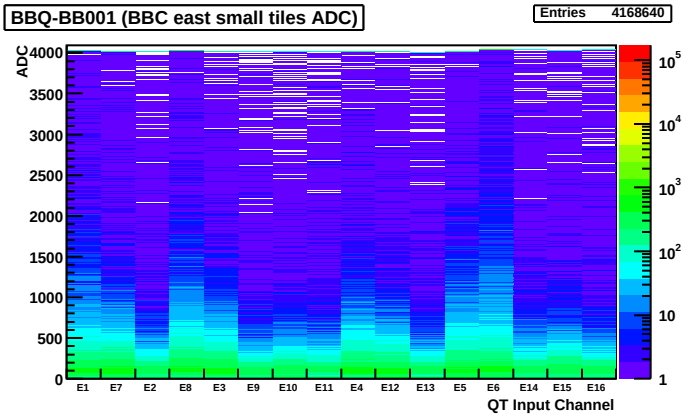

MIX-TF005
Entries 1.094268e+07
TOF MULT
TOF tray

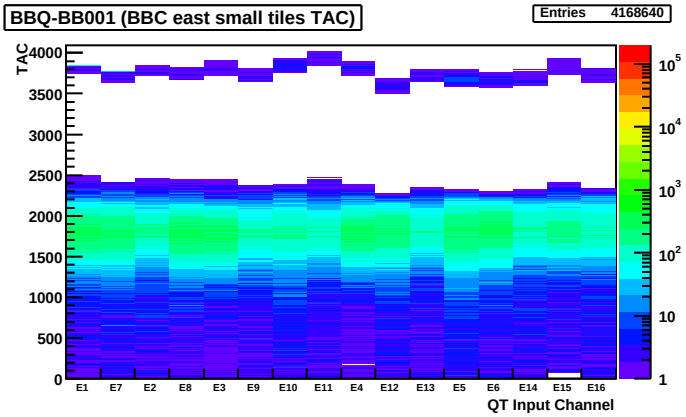

MIX-TF006
Entries 1.094268e+07
TOF MULT
TOF tray

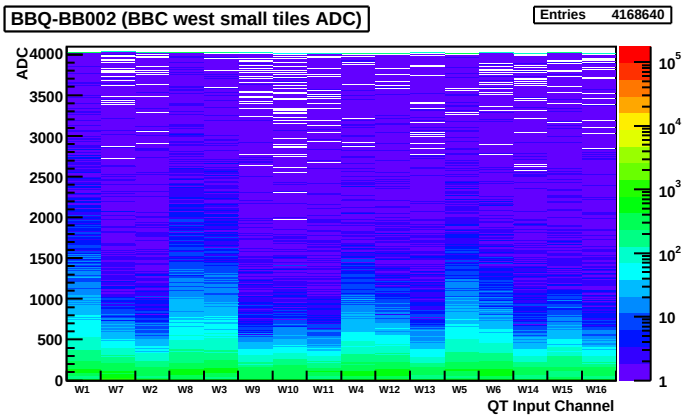

MIX-TF101
Entries 1563240
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

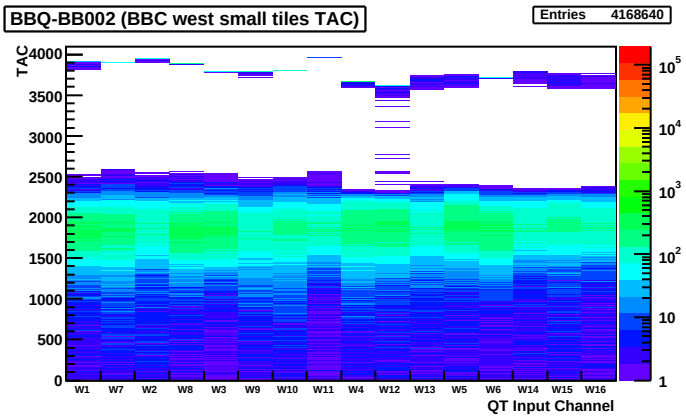

L1-TF201
Entries 260540
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

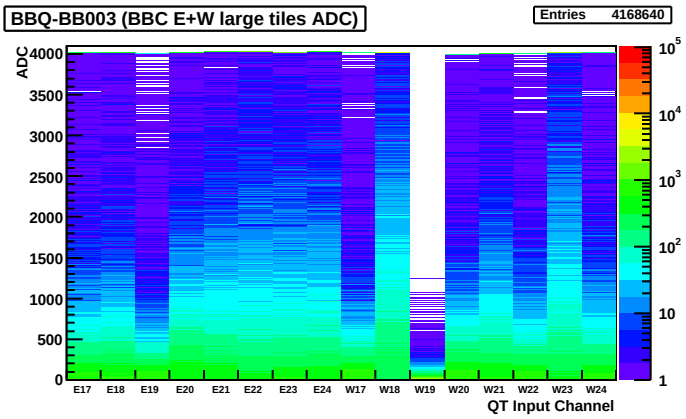

L1-TF201
Entries 1563240
Threshold bits
1
0
185492
180473
162547
171811
155490
171798
75048
80067
97993
88729
105050
88742
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

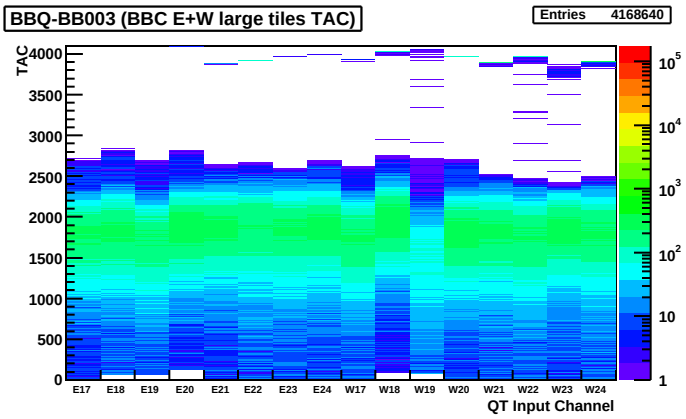

BBQ-BB001 (BBC east small tiles ADC)
Entries 4168640
ADC
QT Input Channel

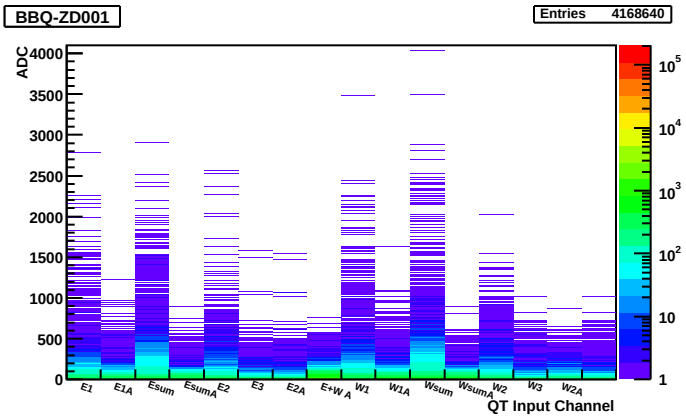

BBQ-BB001 (BBC east small tiles TAC)
Entries 4168640
TAC
QT Input Channel

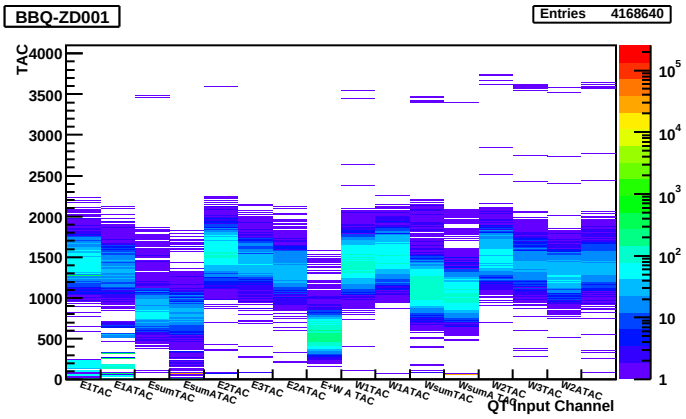

BBQ-BB002 (BBC west small tiles ADC)
Entries 4168640
ADC
QT Input Channel

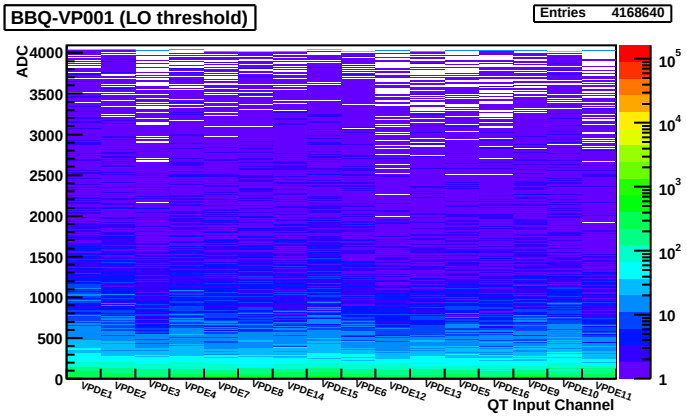

BBQ-BB002 (BBC west small tiles TAC)
Entries 4168640
TAC
QT Input Channel

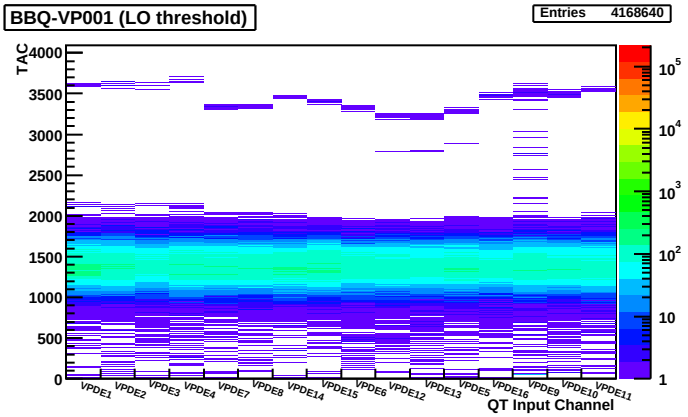

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4168640
ADC
QT Input Channel

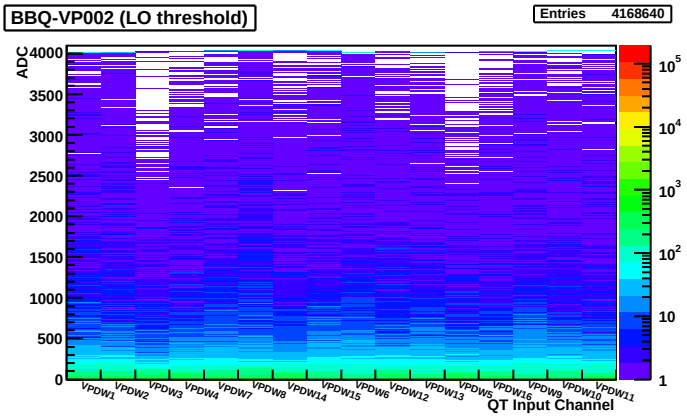

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4168640
TAC
QT Input Channel

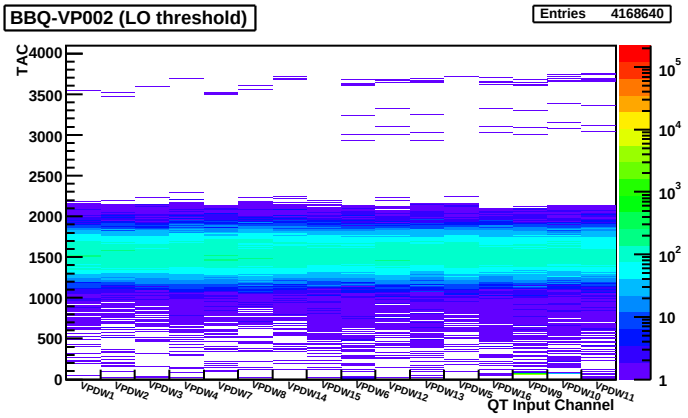

BBQ-ZD001
Entries 4168640
ADC
QT Input Channel

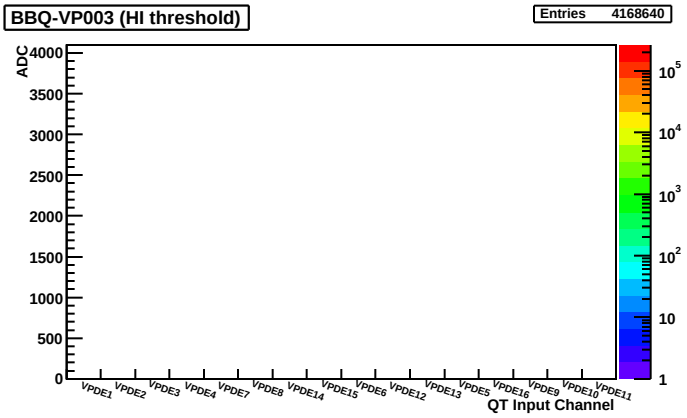

BBQ-ZD001
Entries 4168640
TAC
QT Input Channel

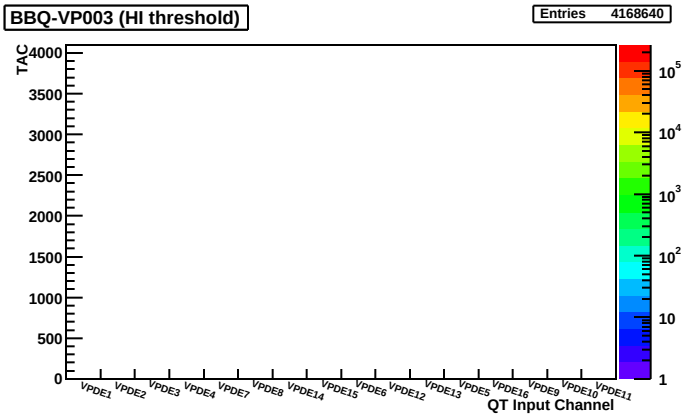

BBQ-VP001 (LO threshold)
Entries 4168640
ADC
QT Input Channel

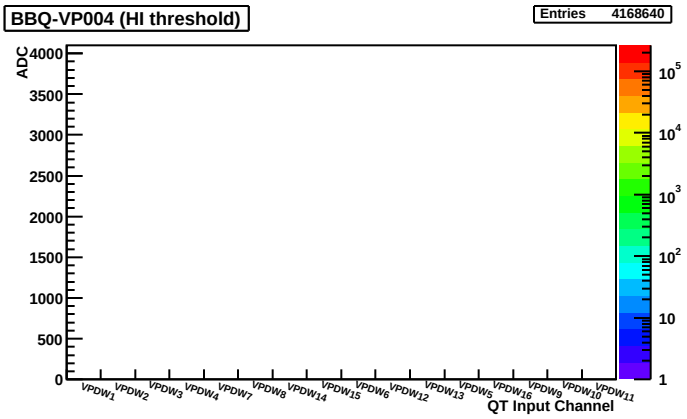

BBQ-VP001 (LO threshold)
Entries 4168640
TAC
QT Input Channel

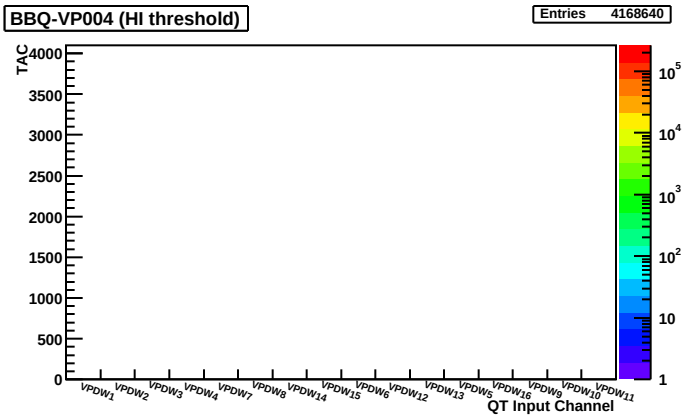

BBQ-VP002 (LO threshold)
Entries 4168640
ADC
QT Input Channel

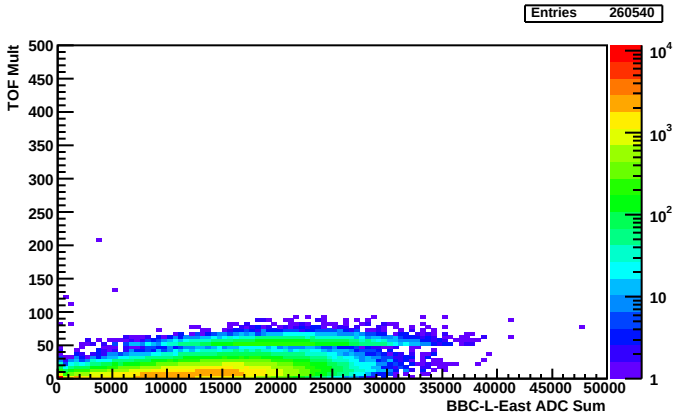

BBQ-VP002 (LO threshold)
Entries 4168640
TAC
QT Input Channel

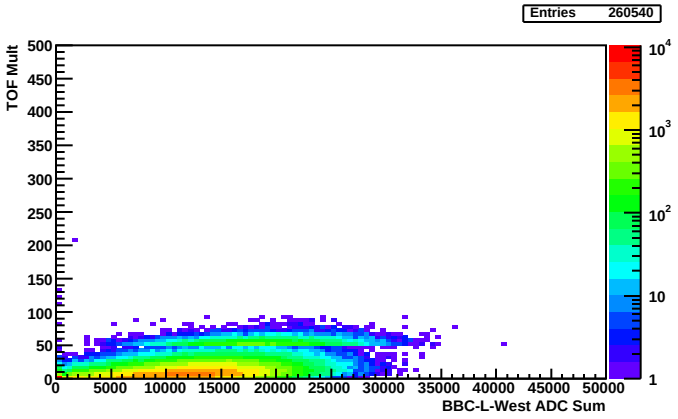

BBQ-VP003 (HI threshold)
Entries 4168640
ADC
QT Input Channel

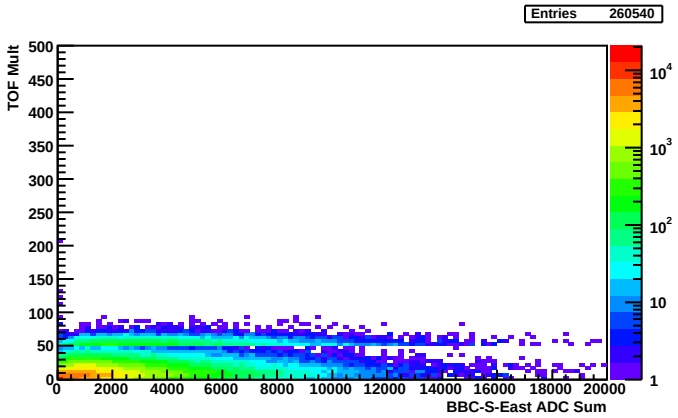

BBQ-VP003 (HI threshold)
Entries 4168640
TAC
QT Input Channel

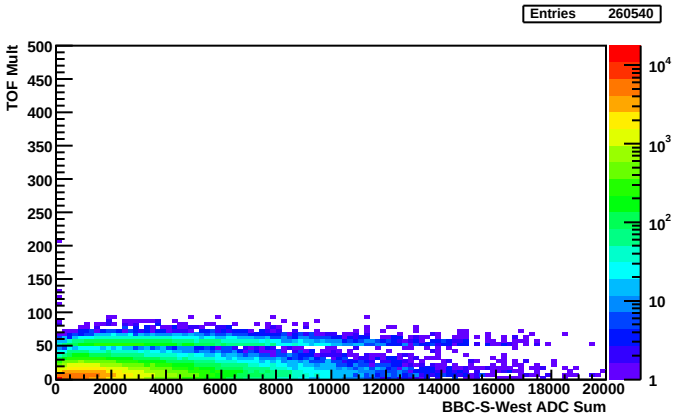

BBQ-VP004 (HI threshold)
Entries 4168640
ADC
QT Input Channel

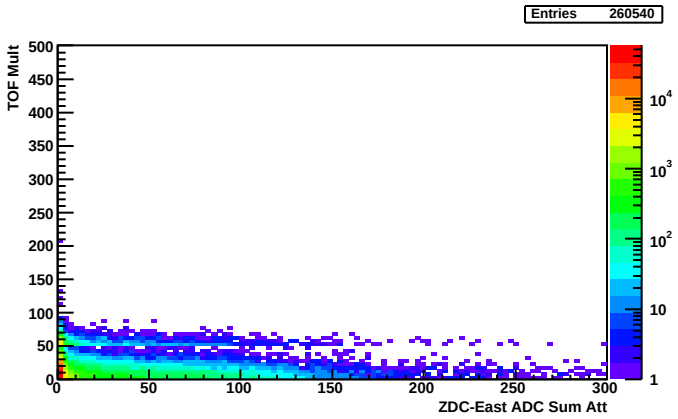

BBQ-VP004 (HI threshold)
Entries 4168640
TAC
QT Input Channel

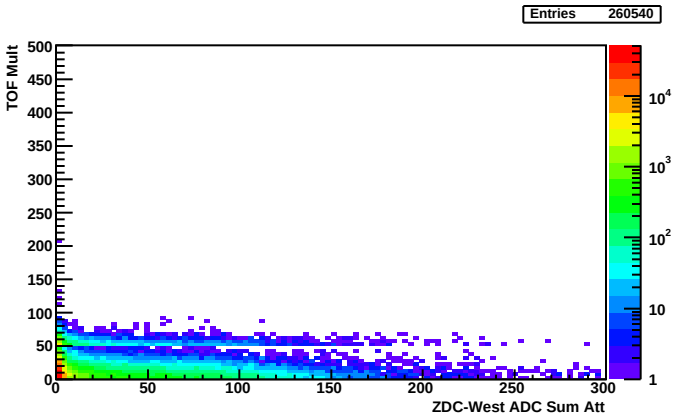

Entries 260540
TOF Mult
BBC-L-East ADC Sum

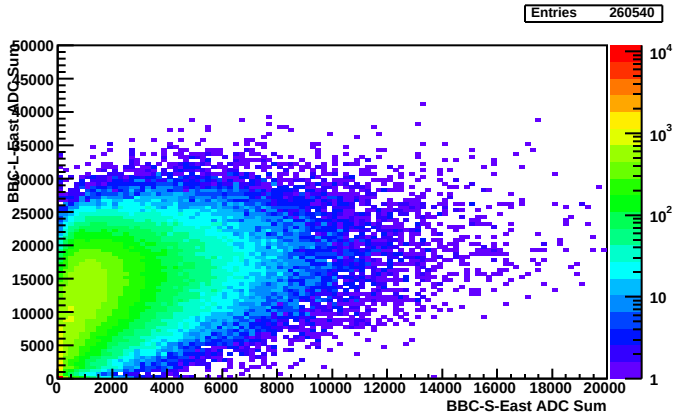

Entries 260540
TOF Mult
BBC-L-West ADC Sum

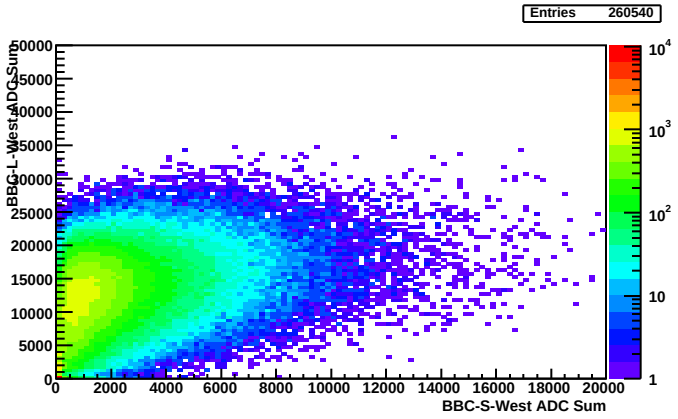

Entries 260540
TOF Mult
BBC-S-East ADC Sum

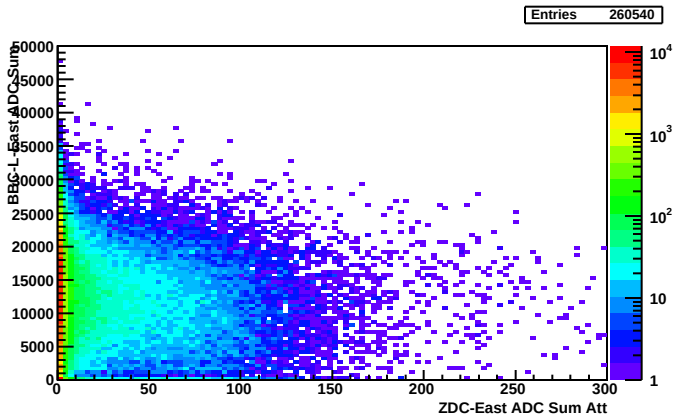

Entries 260540
TOF Mult
BBC-S-West ADC Sum

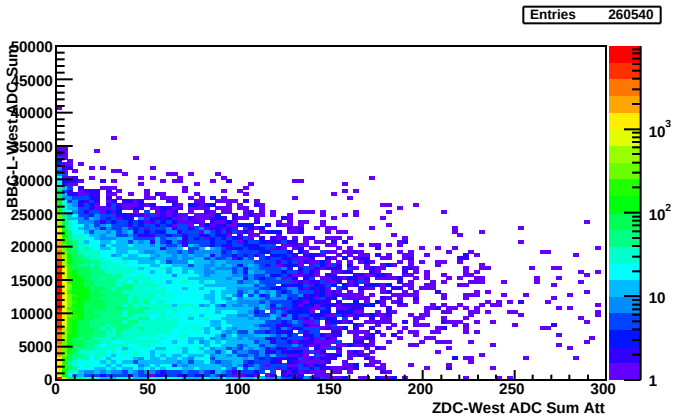

Entries 260540
TOF Mult
ZDC-East ADC Sum Att

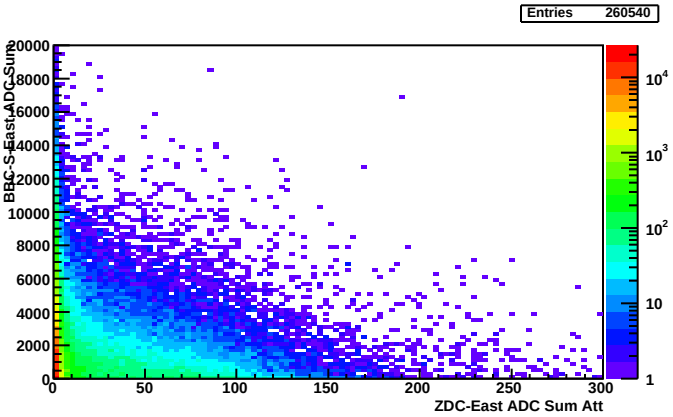

Entries 260540
TOF Mult
ZDC-West ADC Sum Att

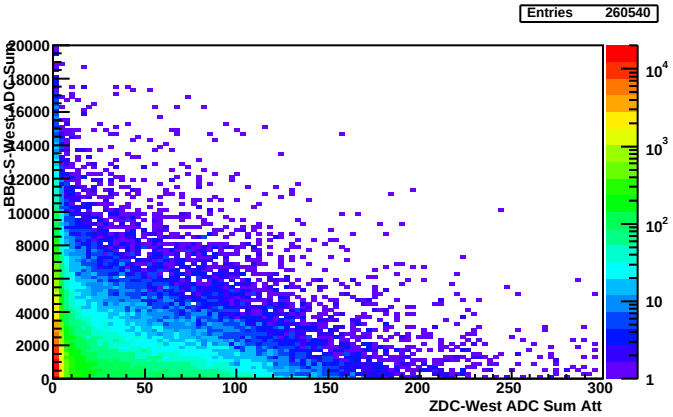

Entries 260540
BBC-L-East ADC Sum
BBC-S-East ADC Sum

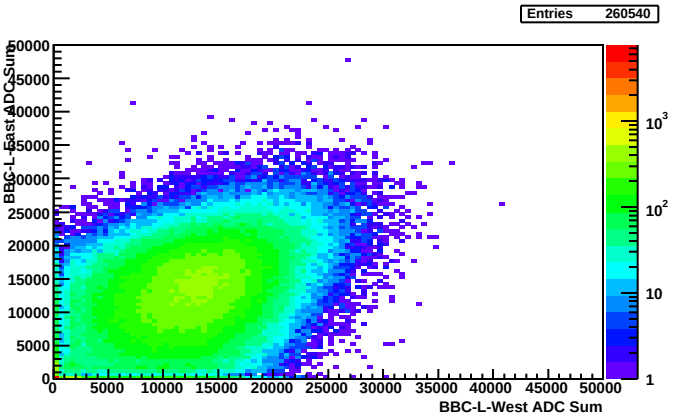

Entries 260540
BBC-L-West ADC Sum
BBC-S-West ADC Sum

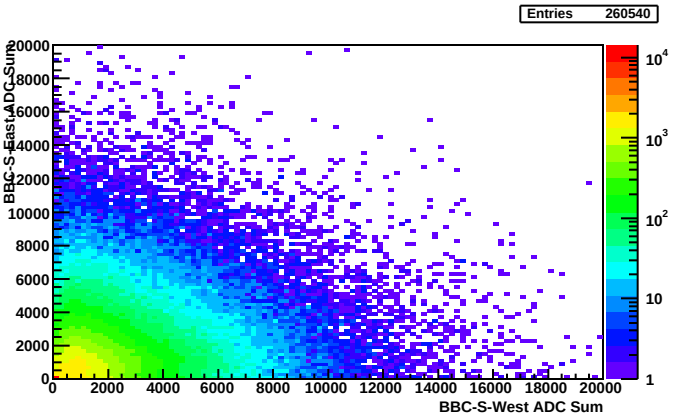

Entries 260540
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

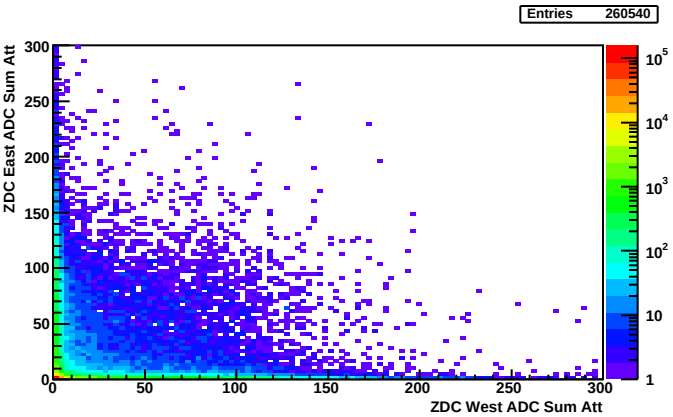

Entries 260540
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

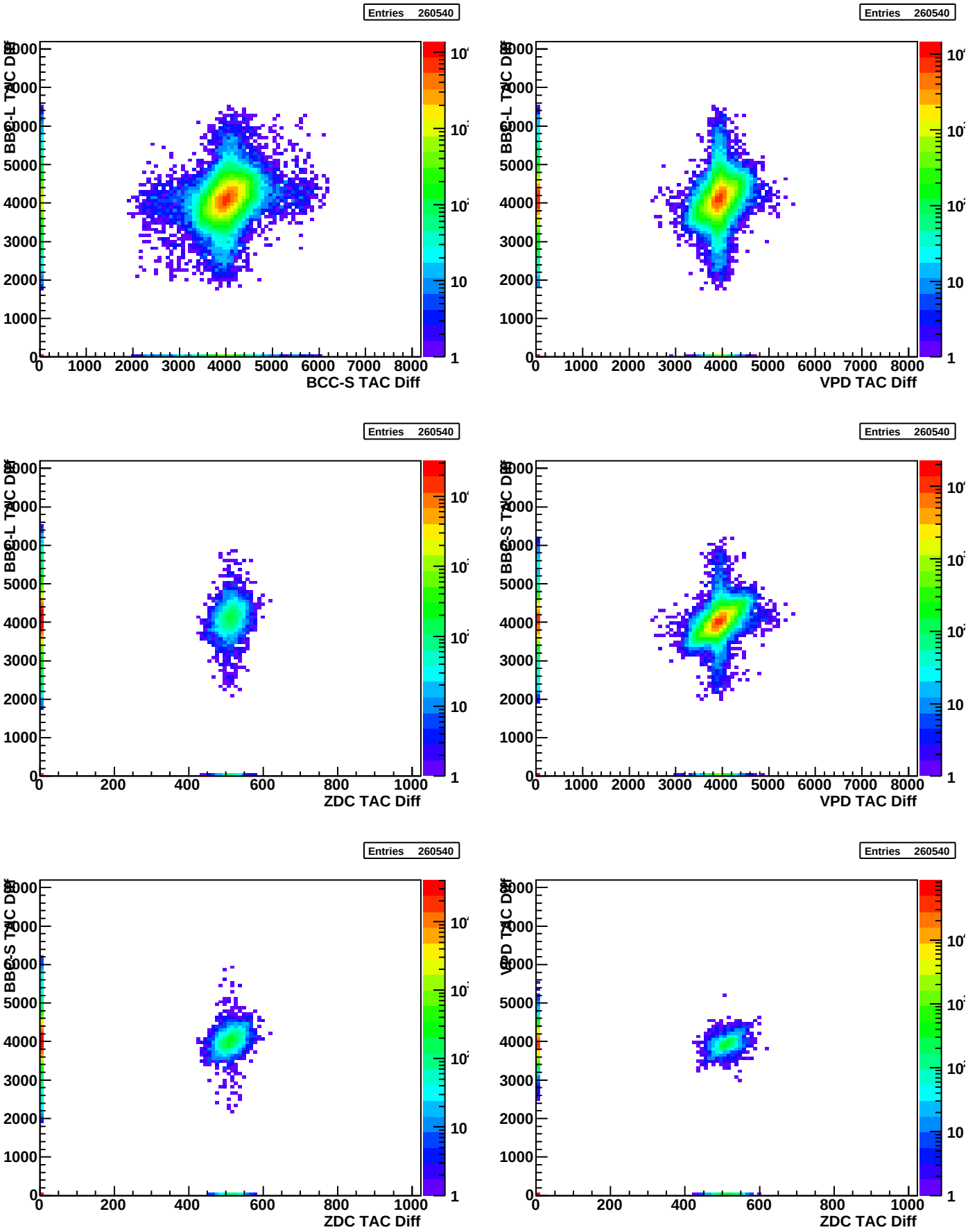

Entries 260540
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

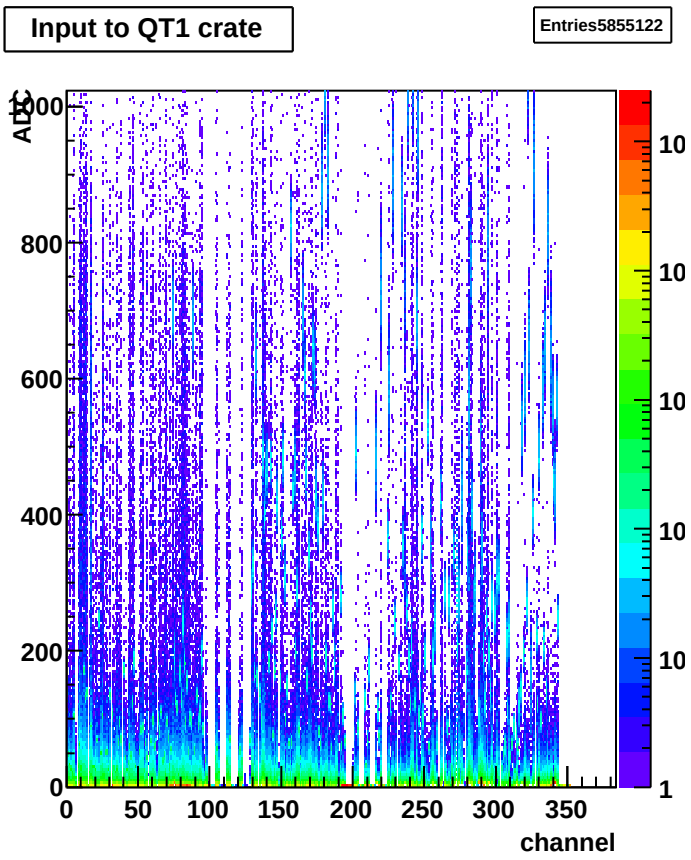

Entries 260540
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

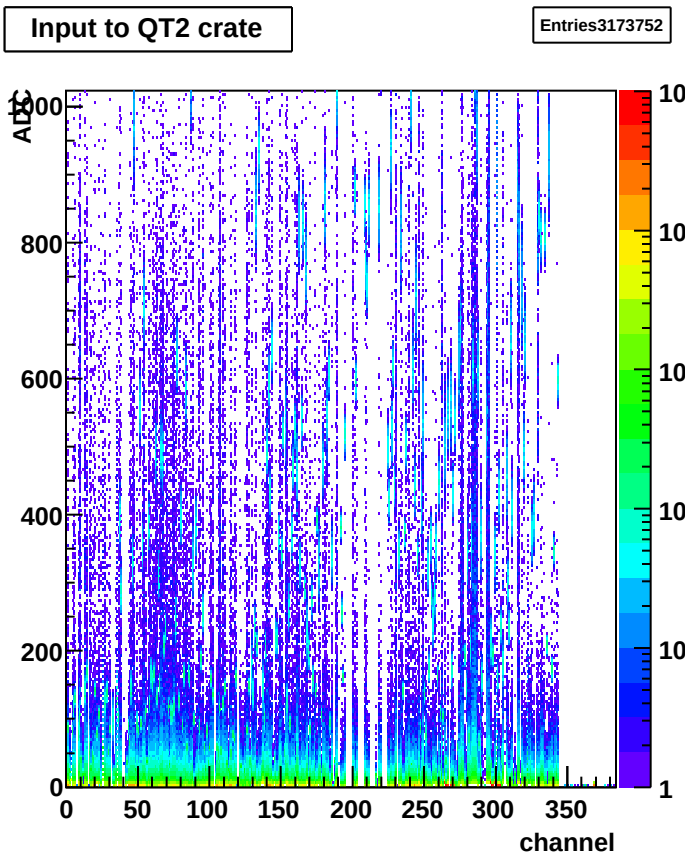

Entries 260540
BBC-L-East ADC Sum
BBC-L-West ADC Sum

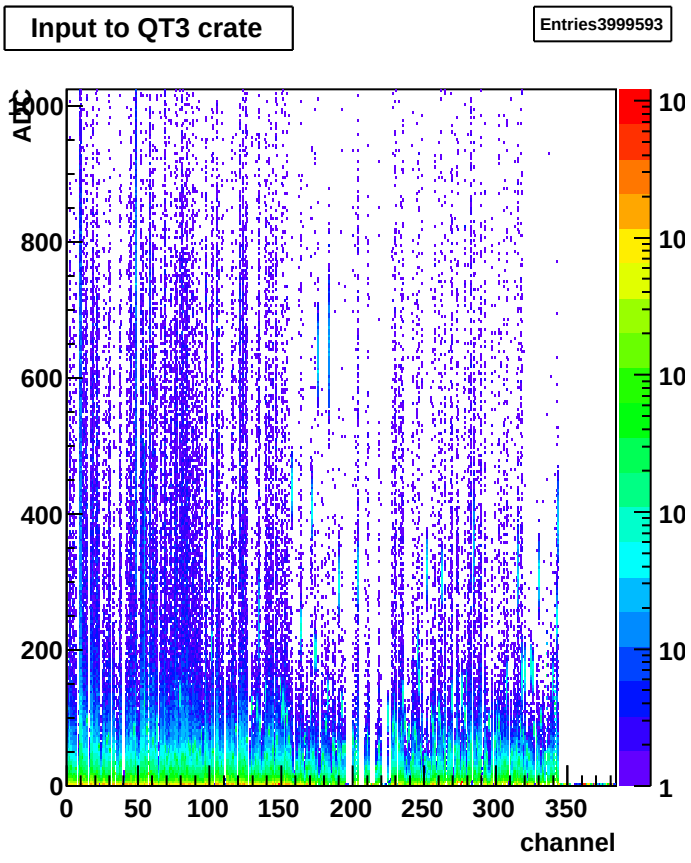

Entries 260540
BBC-S-East ADC Sum
BBC-S-West ADC Sum

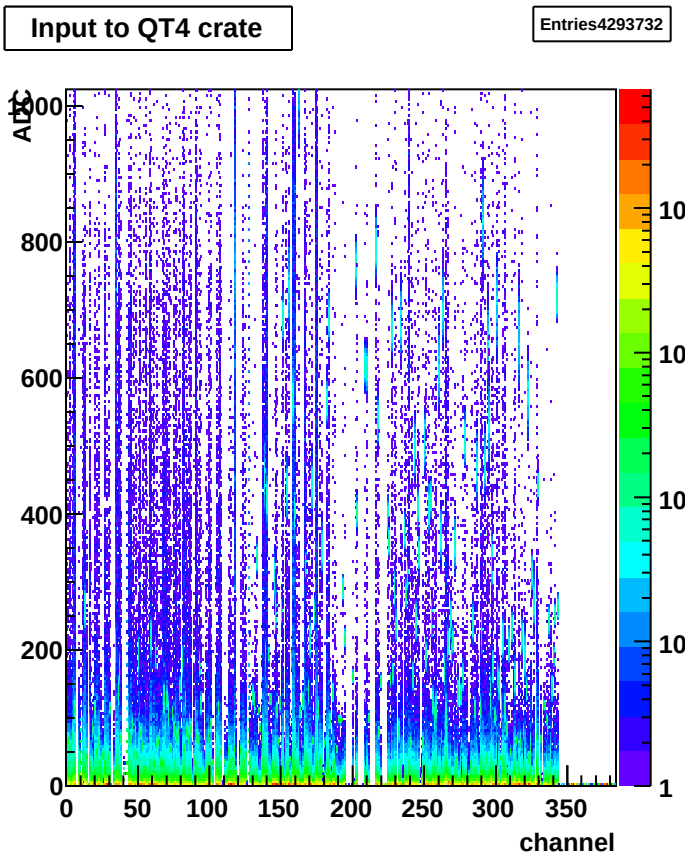

Entries 260540
ZDC East ADC Sum Att
ZDC West ADC Sum Att

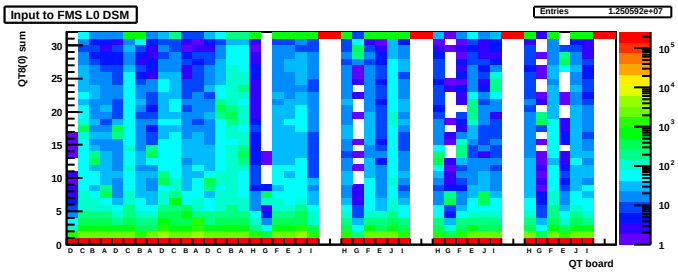

Entries 260540
BBC-L TAC Diff
BCC-S TAC Diff
Entries 260540
BBC-L TAC Diff
VPD TAC Diff
Entries 260540
BBC-L TAC Diff
ZDC TAC Diff
Entries 260540
BBC-S TAC Diff
VPD TAC Diff
Entries 260540
BBC-S TAC Diff
ZDC TAC Diff
Entries 260540
VPD TAC Diff
ZDC TAC Diff

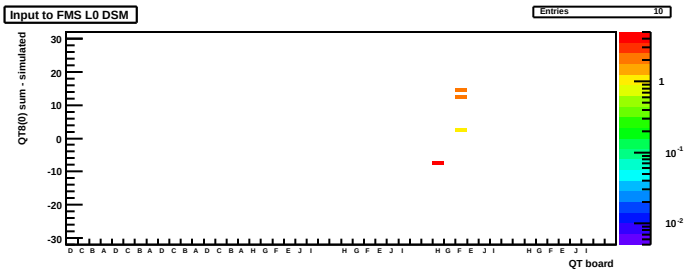

Input to QT1 crate
Entries5855122
ADC
channel

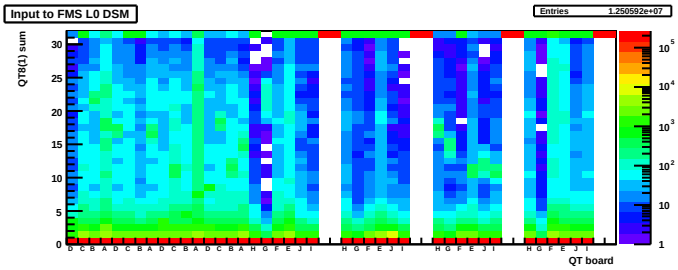

Input to QT2 crate
Entries3173752
ADC
channel

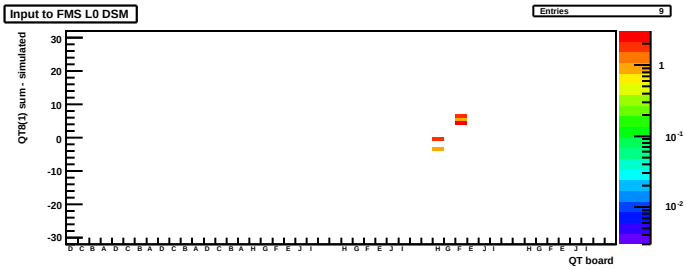

Input to QT3 crate
Entries3999593
ADC
channel

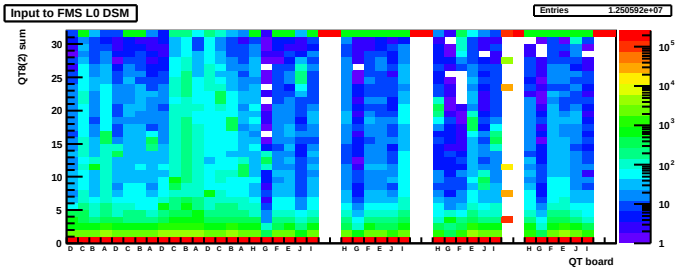

Input to QT4 crate
Entries4293732
ADC
channel

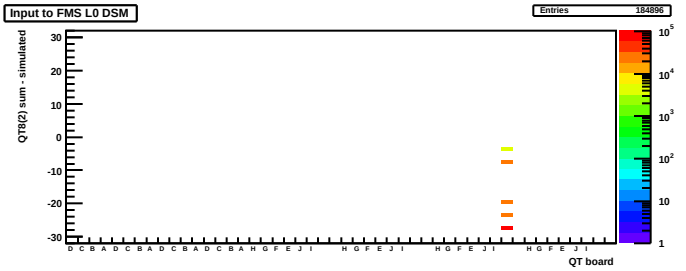

Input to FMS L0 DSM
Entries 1.250592e+07
QT8(0) sum
QT board

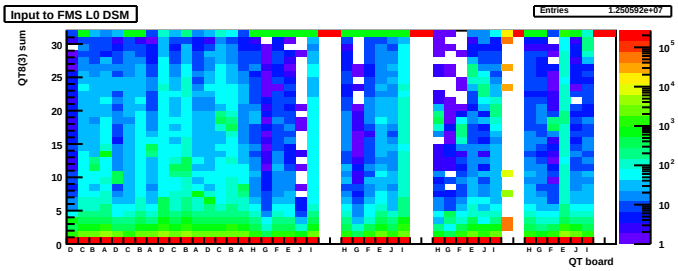

Input to FMS L0 DSM
Entries 10
QT8(0) sum - simulated
QT board

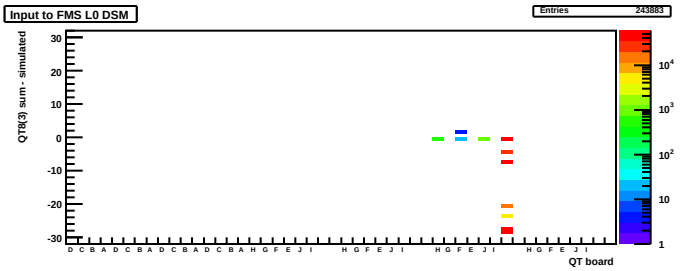

Input to FMS L0 DSM
Entries 1.250592e+07
QT8(1) sum
QT board

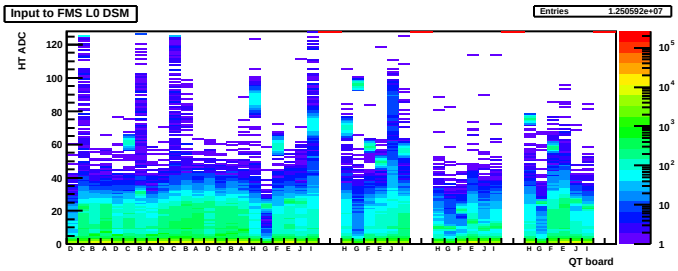

Input to FMS L0 DSM
Entries 9
QT8(1) sum - simulated
QT board

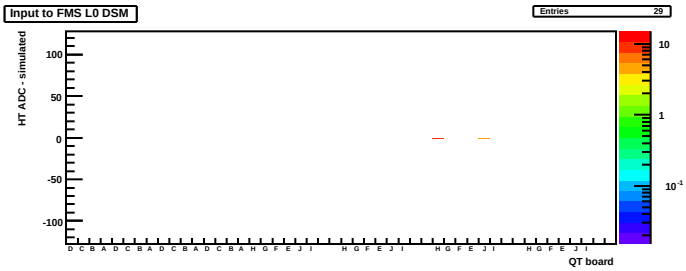

Input to FMS L0 DSM
Entries 1.250592e+07
QT8(2) sum
QT board

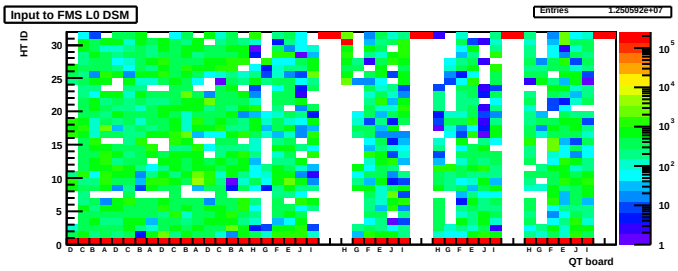

Input to FMS L0 DSM
Entries 184896
QT8(2) sum - simulated
QT board

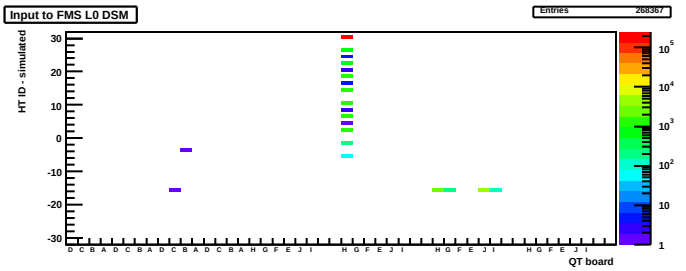

Input to FMS L0 DSM
Entries 1.250592e+07
QT8(3) sum
QT board

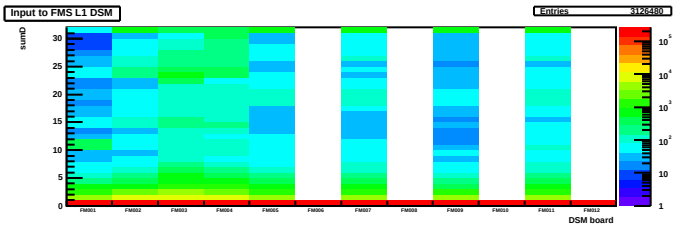

Input to FMS L0 DSM
Entries 243883
QT8(3) sum - simulated
QT board

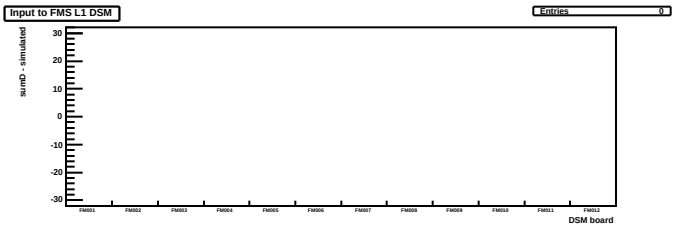

Input to FMS L0 DSM
Entries 1.250592e+07
HT ADC
QT board

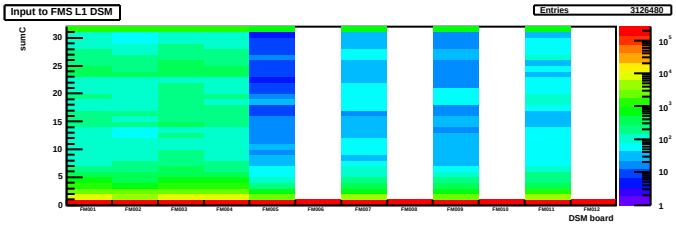

Input to FMS L0 DSM
Entries 29
HT ADC - simulated
QT board

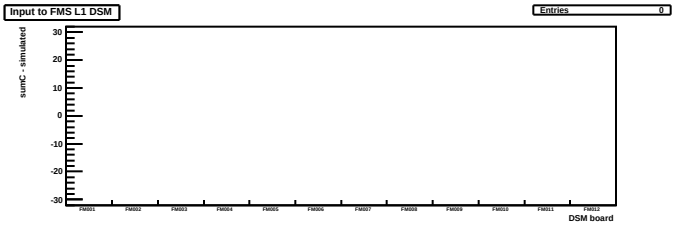

Input to FMS L0 DSM
Entries 1.250592e+07
HT ID
QT board

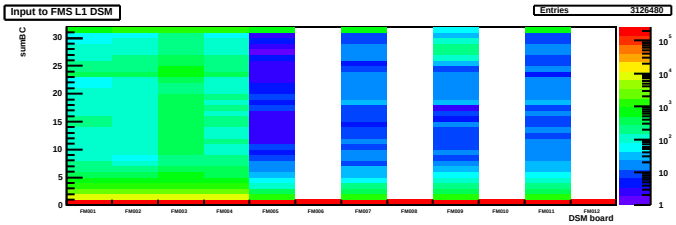

Input to FMS L0 DSM
Entries 268367
HT ID - simulated
QT board

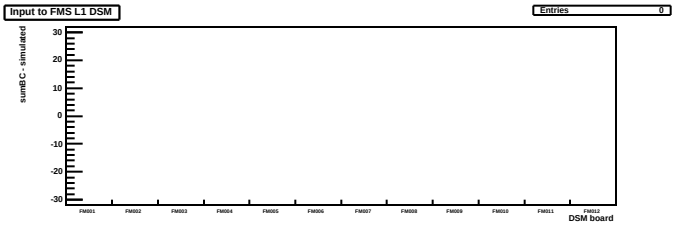

Input to FMS L1 DSM
Entries 3126480
sumD
DSM board

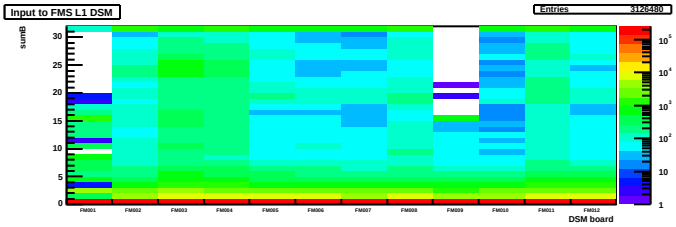

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

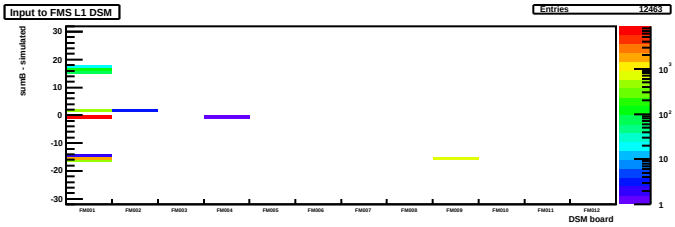

Input to FMS L1 DSM
Entries 3126480
sumC
DSM board

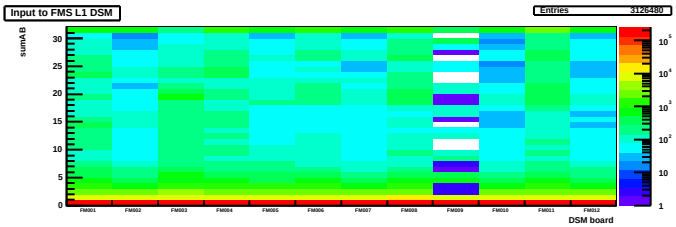

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

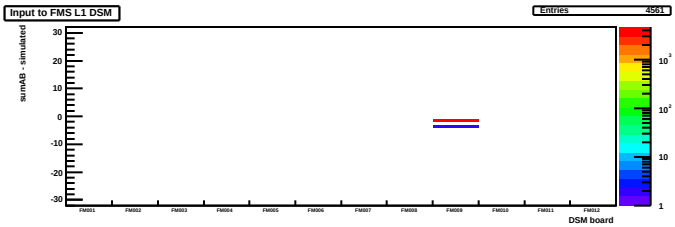

Input to FMS L1 DSM
Entries 3126480
sumBC
DSM board

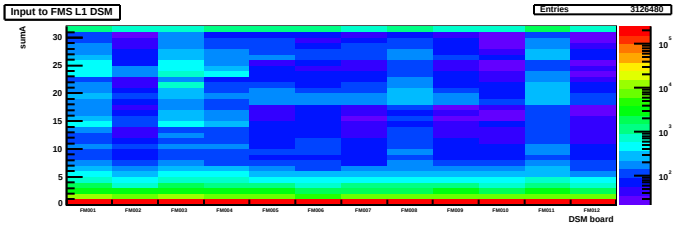

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

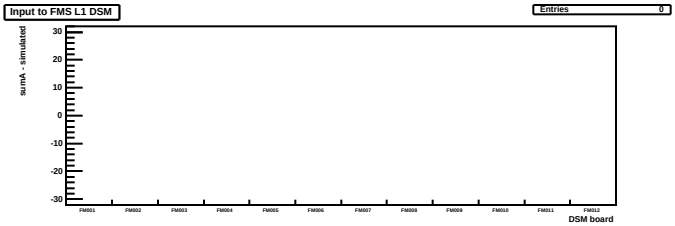

Input to FMS L1 DSM
Entries 3126480
sumB
DSM board

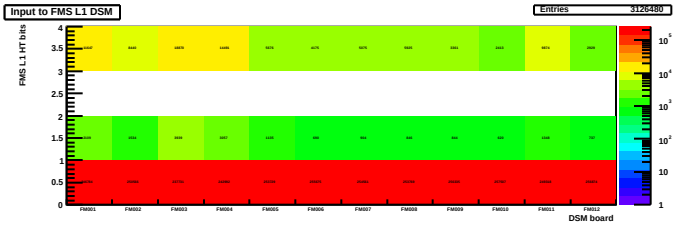

Input to FMS L1 DSM
Entries 12463
sumB - simulated
DSM board

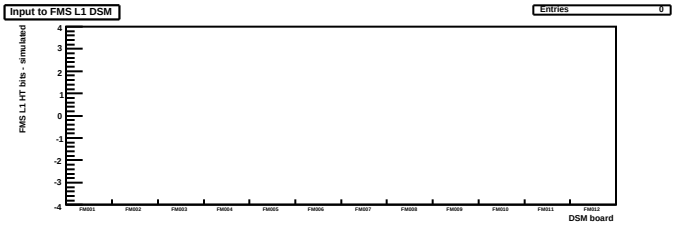

Input to FMS L1 DSM
Entries 3126480
sumAB
DSM board

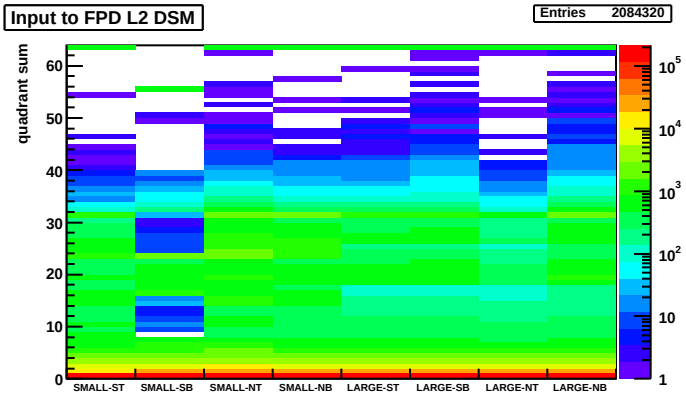

Input to FMS L1 DSM
Entries 4561
sumAB - simulated
DSM board

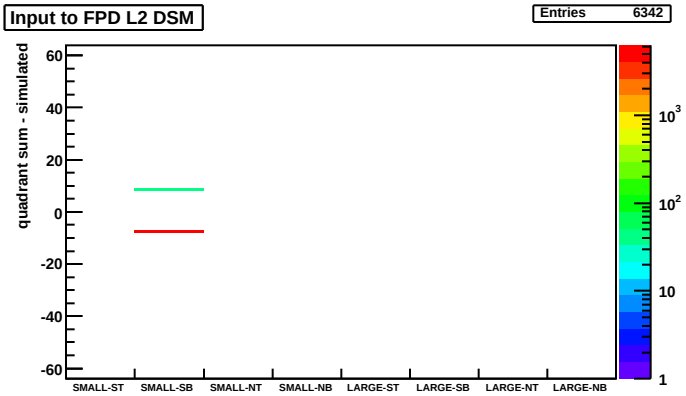

Input to FMS L1 DSM
Entries 3126480
sumA
DSM board

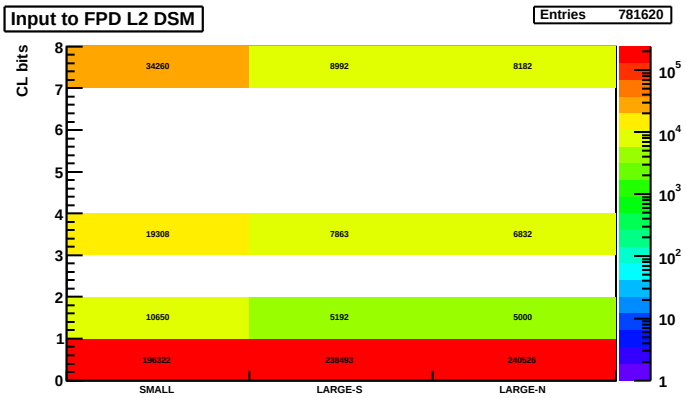

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

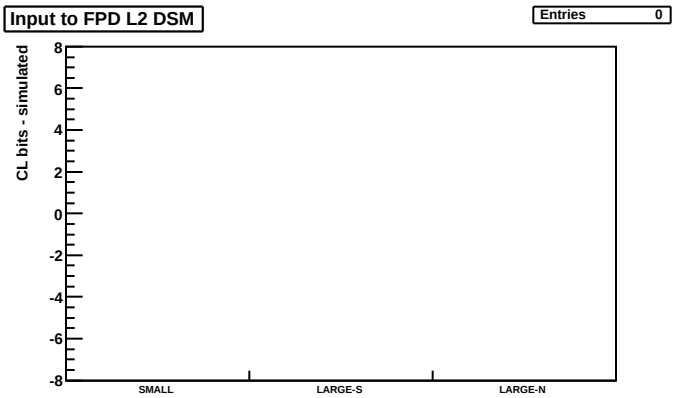

Input to FMS L1 DSM
Entries 3126480
FMS L1 HT bits
DSM board

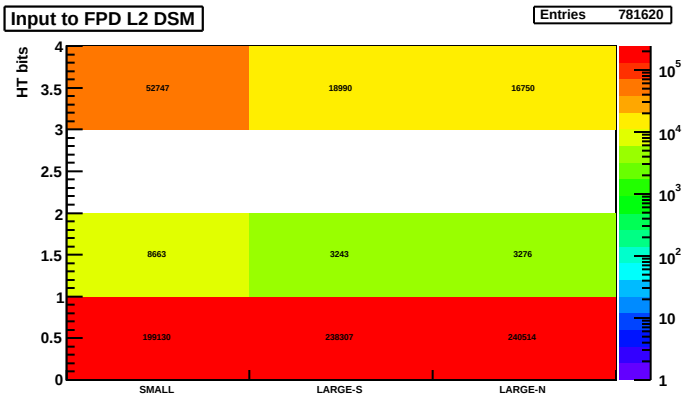

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

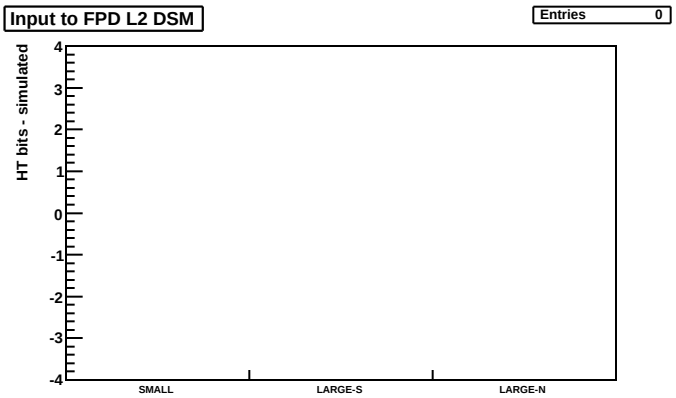

Input to FPD L2 DSM
Entries 2084320
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

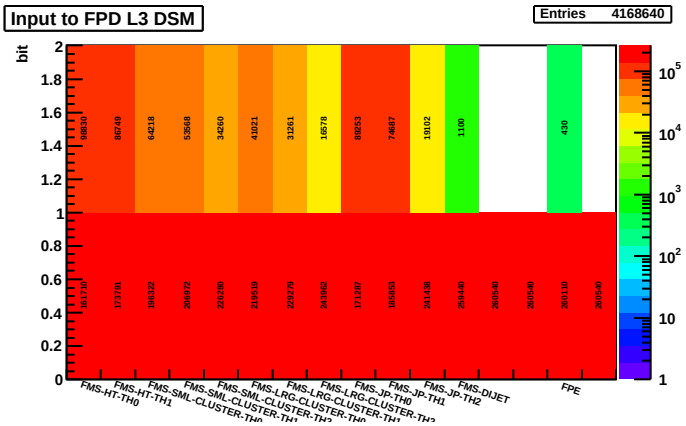

Input to FPD L2 DSM
Entries 6342
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

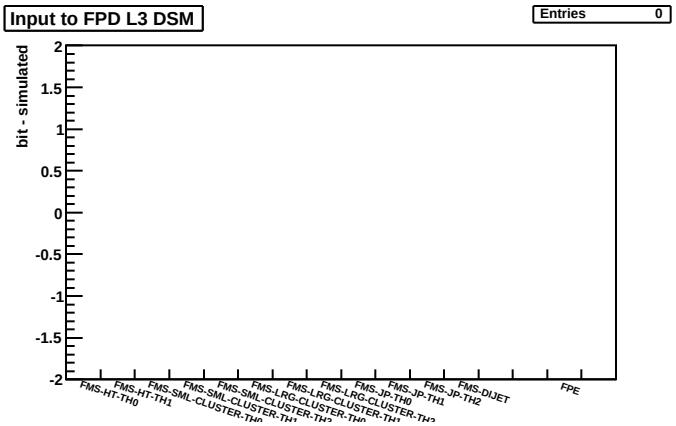

Input to FPD L2 DSM
Entries 781620
CL bits
34260
8992
8182
19308
7863
6832
10650
5192
5000
196322
238493
240526
SMALL
LARGE-S
LARGE-N

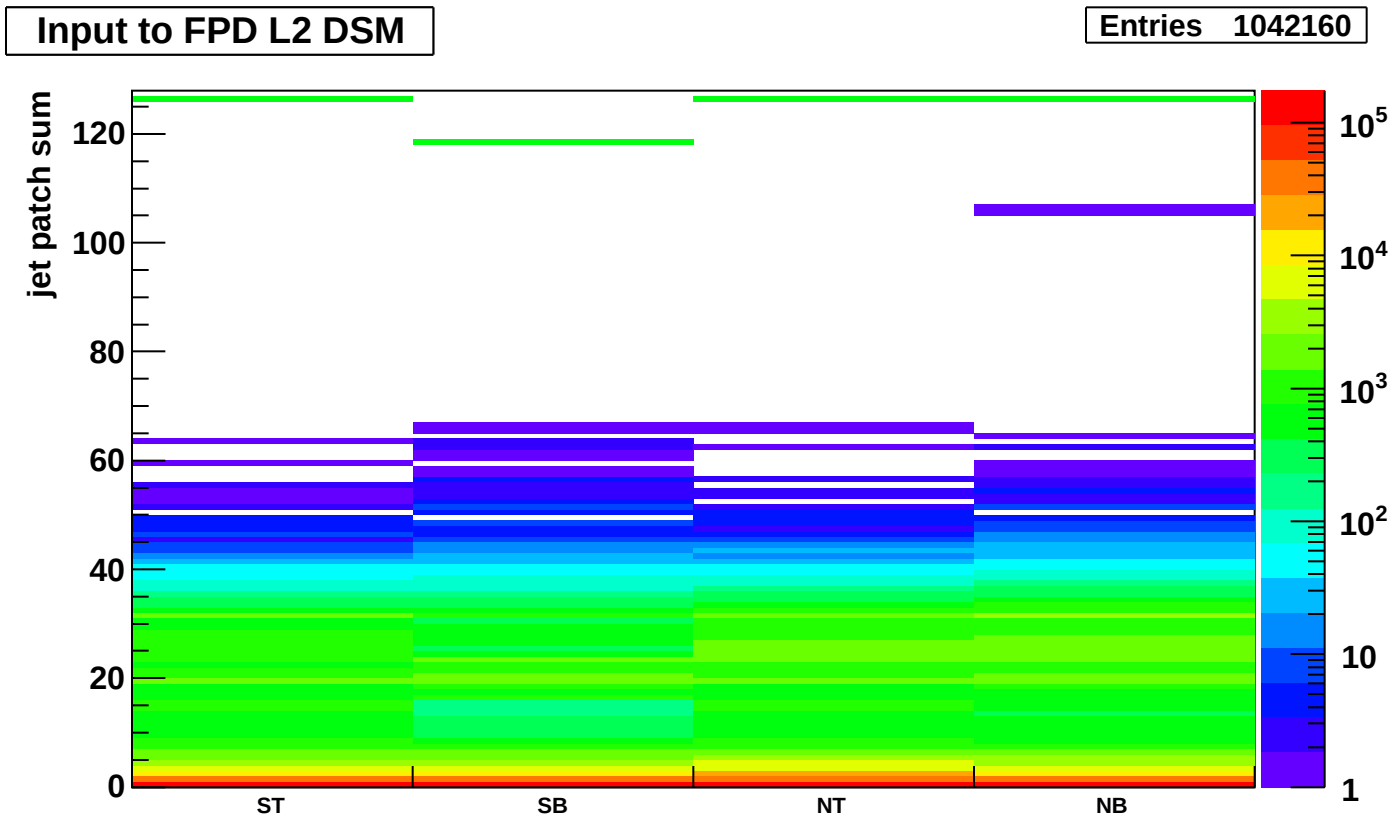

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

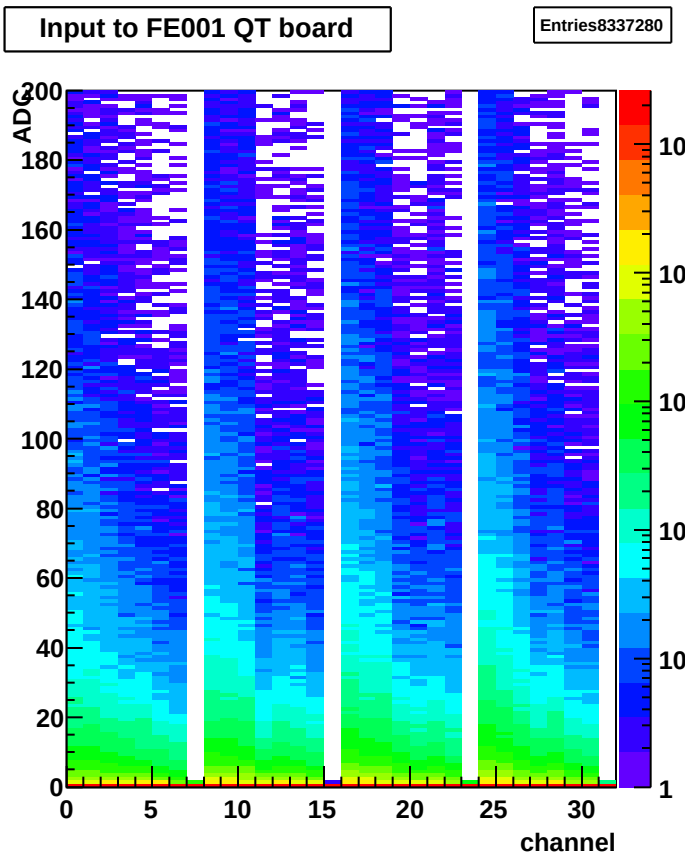

Input to FPD L2 DSM
Entries 781620
HT bits
52747
18990
16750
8663
3243
3276
199130
238307
240514
SMALL
LARGE-S
LARGE-N

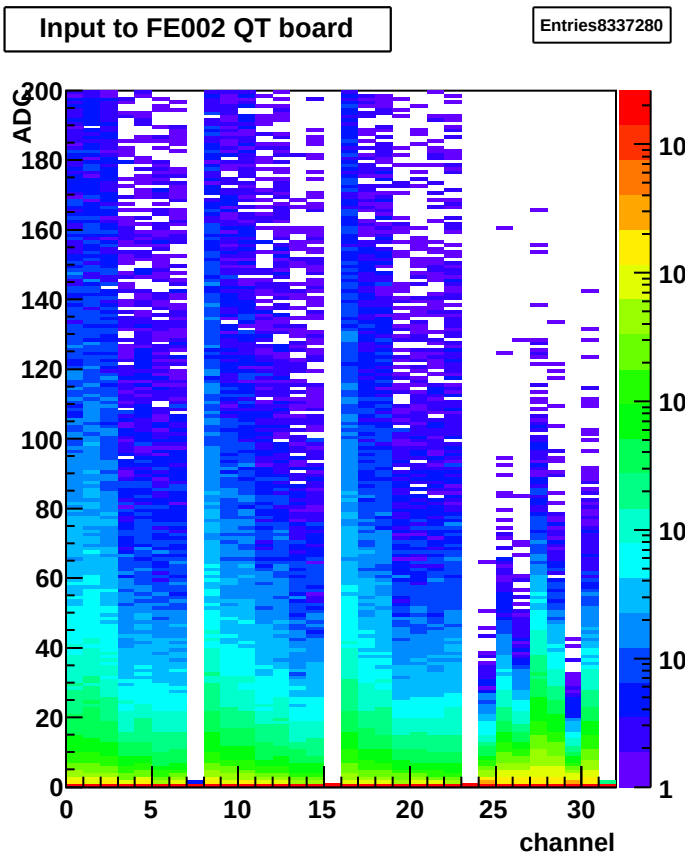

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

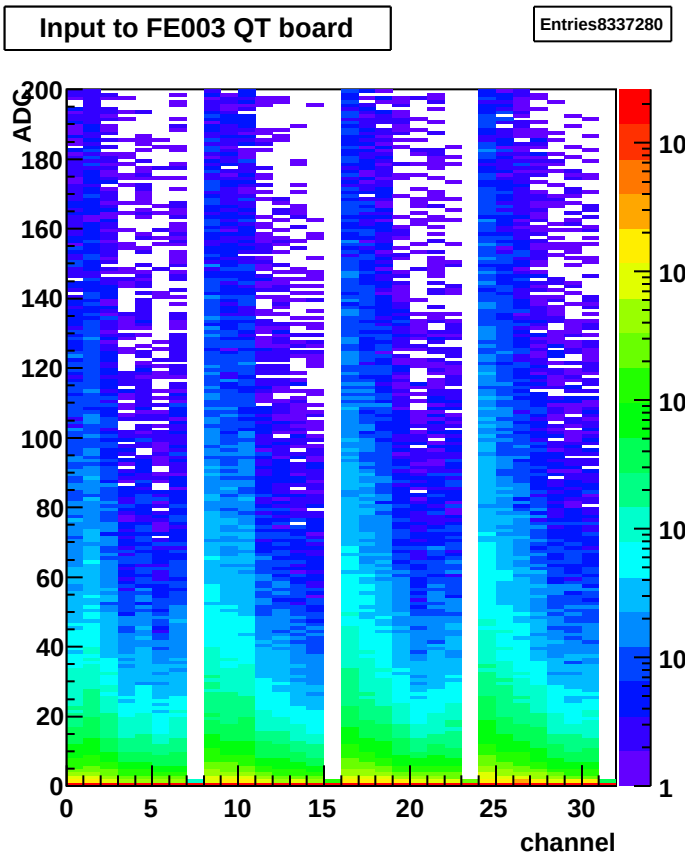

Input to FPD L3 DSM
Entries 4168640
bit
68830
85749
64218
53568
34260
41021
31261
16578
89253
74687
18102
1180
430
161730
173791
195322
206872
226230
219519
229279
243982
171287
185653
241438
259440
260540
260540
260540
260110
260540
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

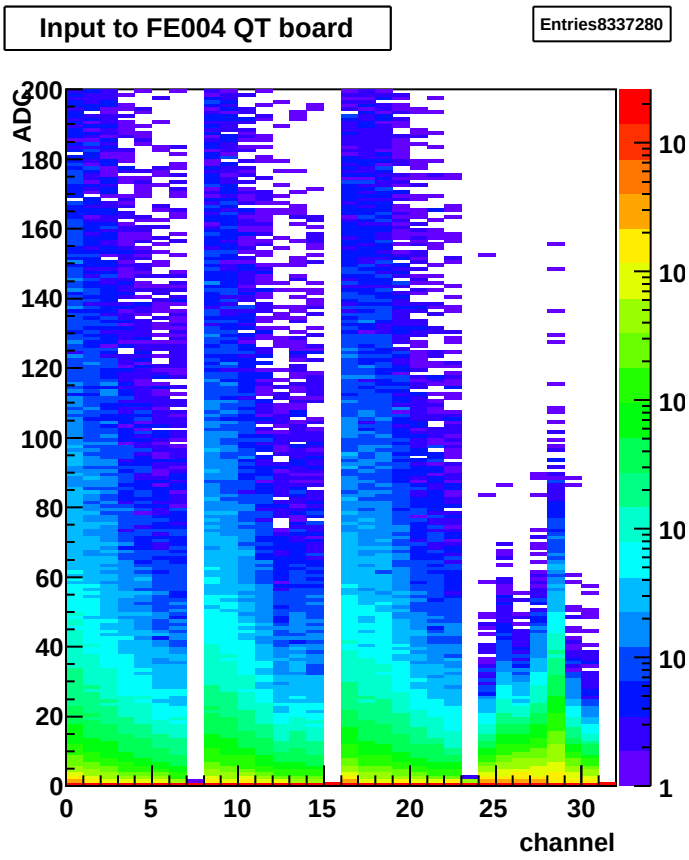

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

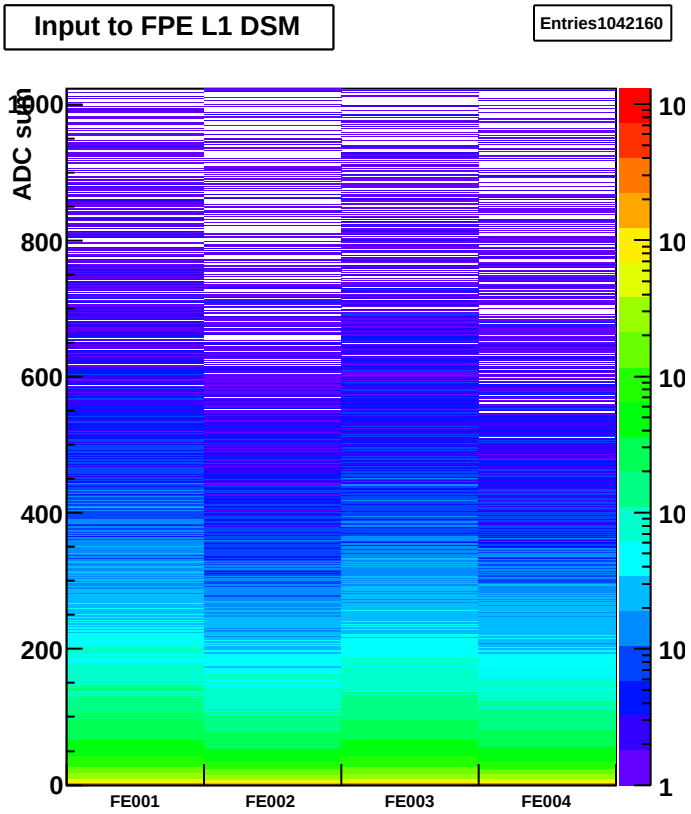

Input to FPD L2 DSM
Entries 1042160
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

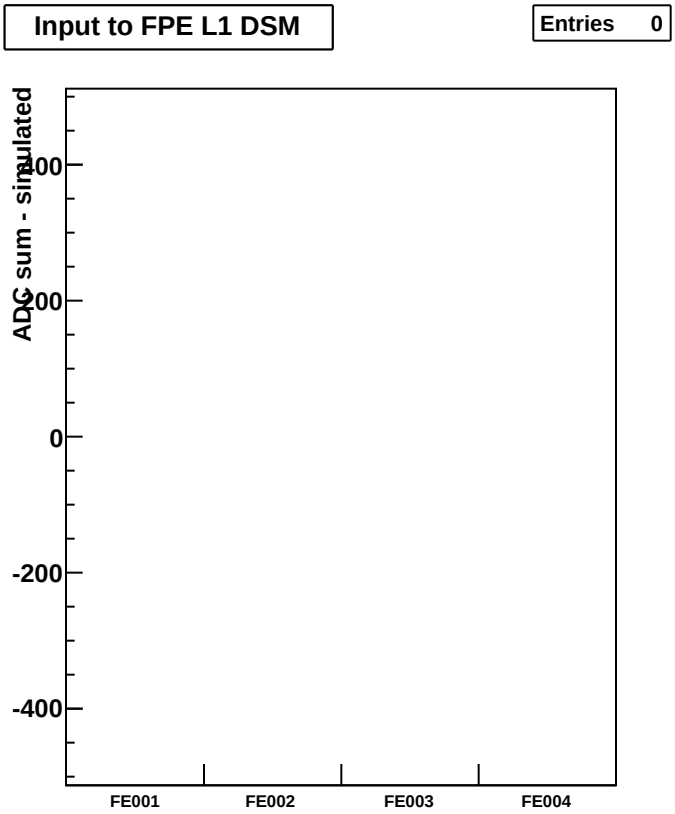

Input to FE001 QT board
Entries8337280
ADC
channel

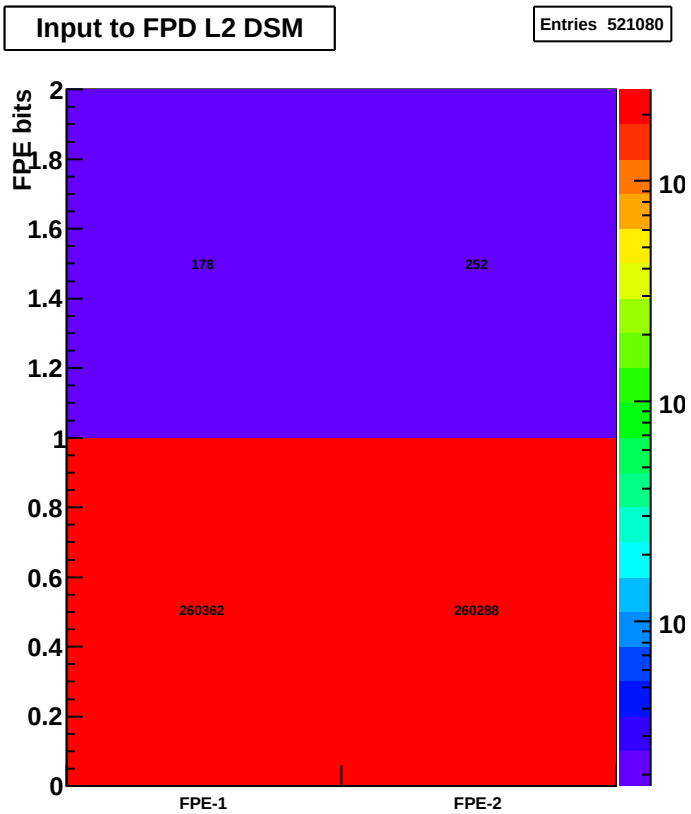

Input to FE002 QT board
Entries8337280
ADC
channel

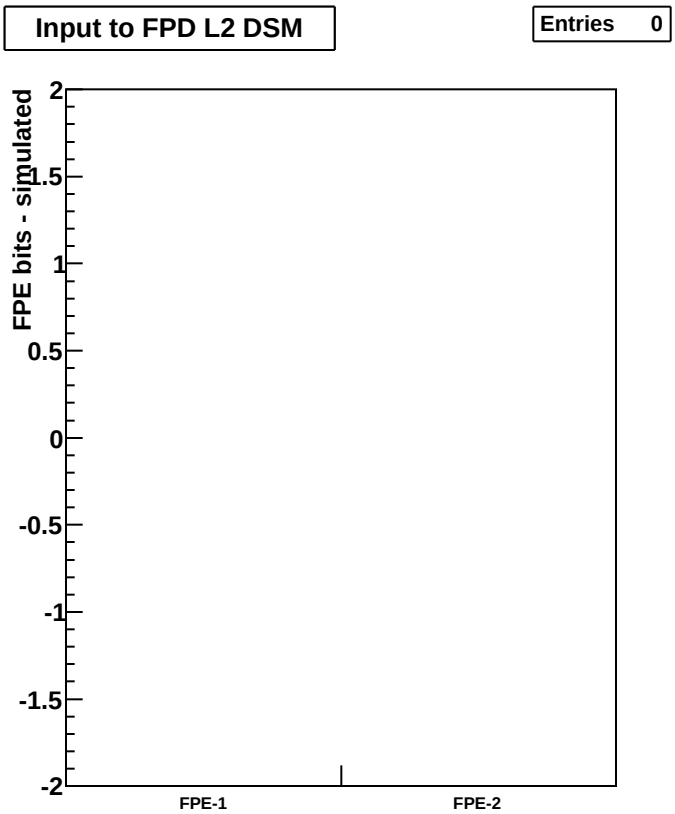

Input to FE003 QT board
Entries8337280
ADC
channel


Input to FE004 QT board
Entries8337280
ADC
channel


Input to FPE L1 DSM
Entries1042160
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 521080
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
178
252
260362
260288
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2