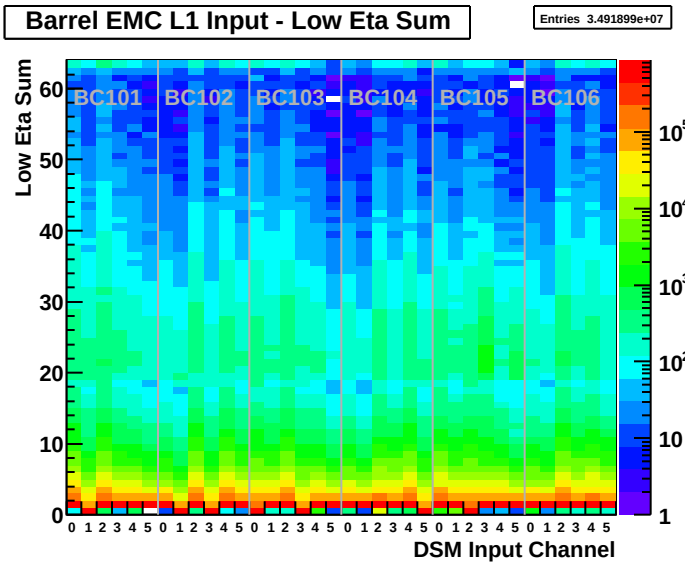
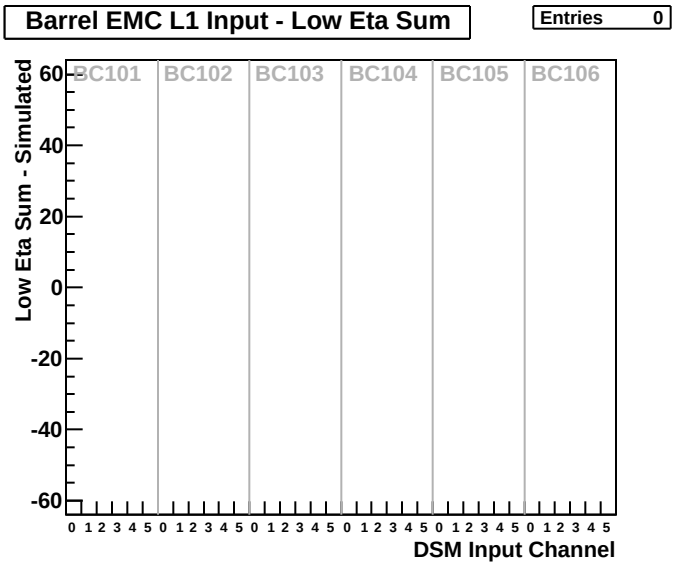
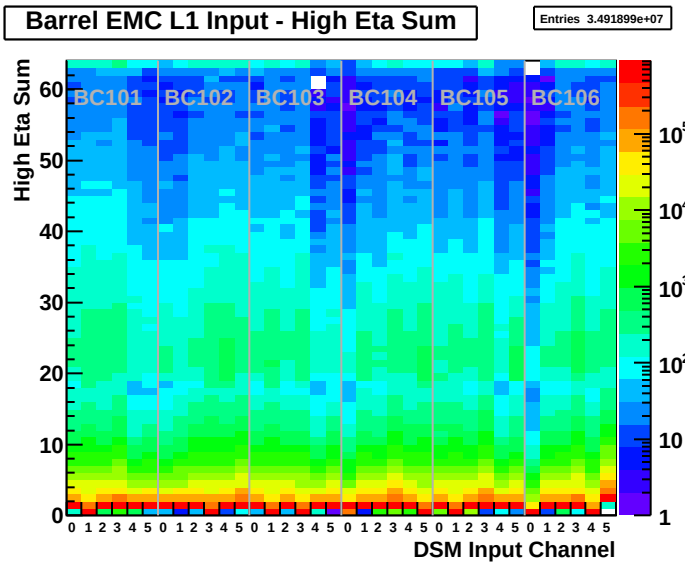
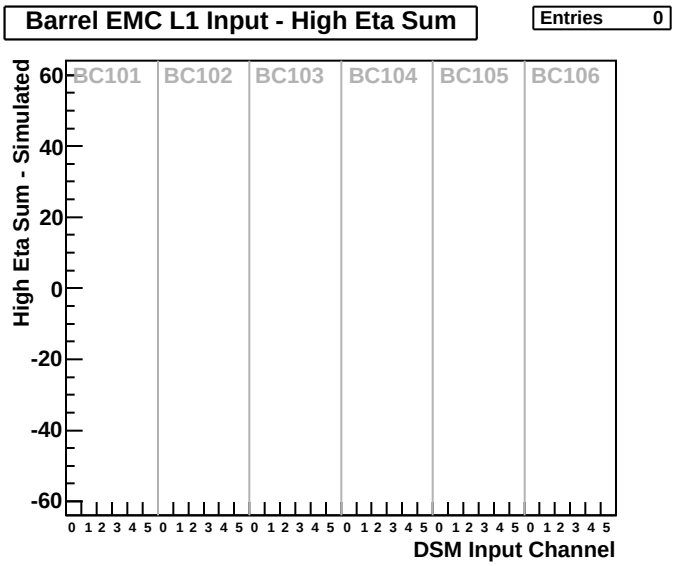
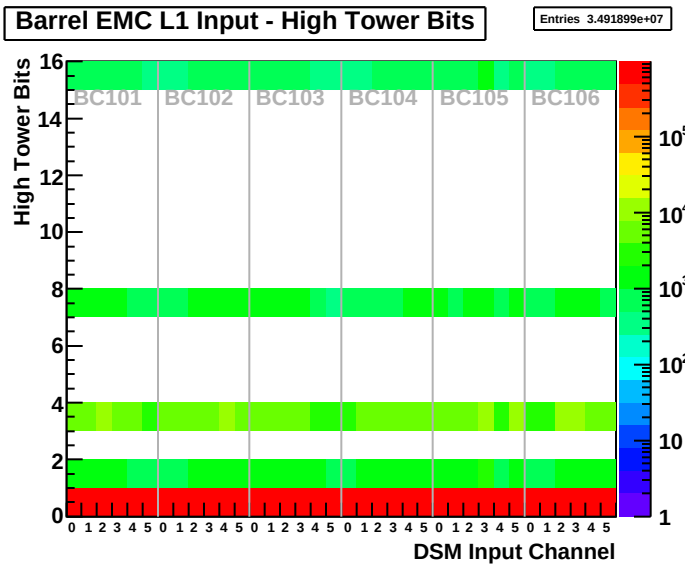
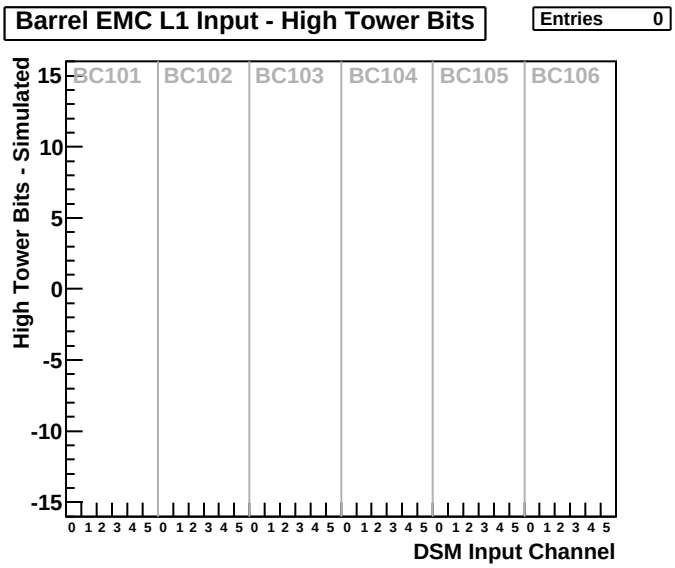

Barrel EMC L1 Input - Low Eta Sum
Entries 3.491899e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

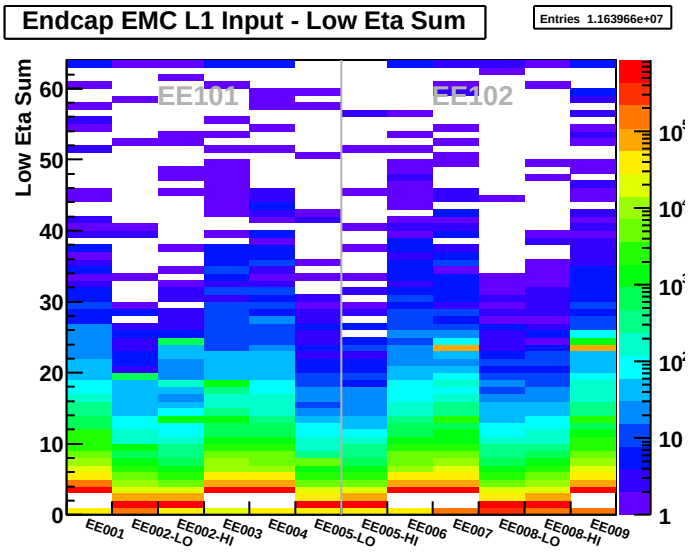

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

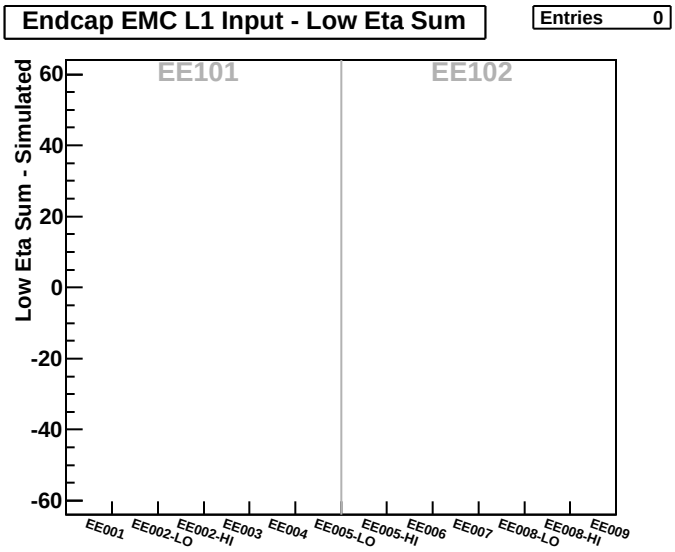

Barrel EMC L1 Input - High Eta Sum
Entries 3.491899e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

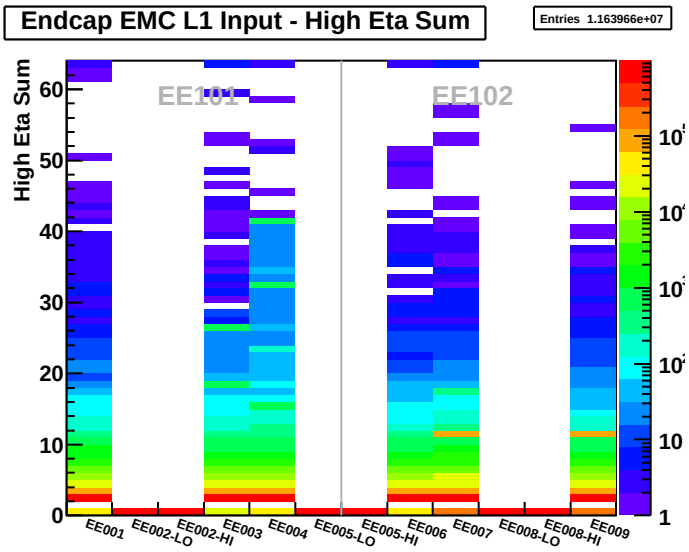

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

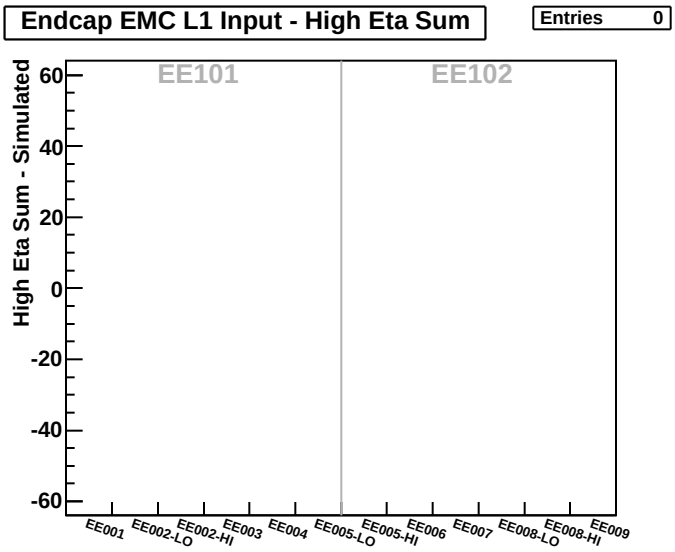

Barrel EMC L1 Input - High Tower Bits
Entries 3.491899e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

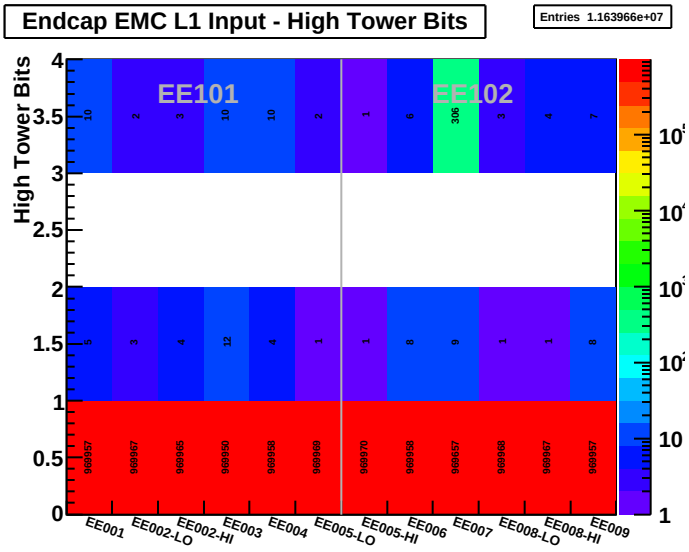

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

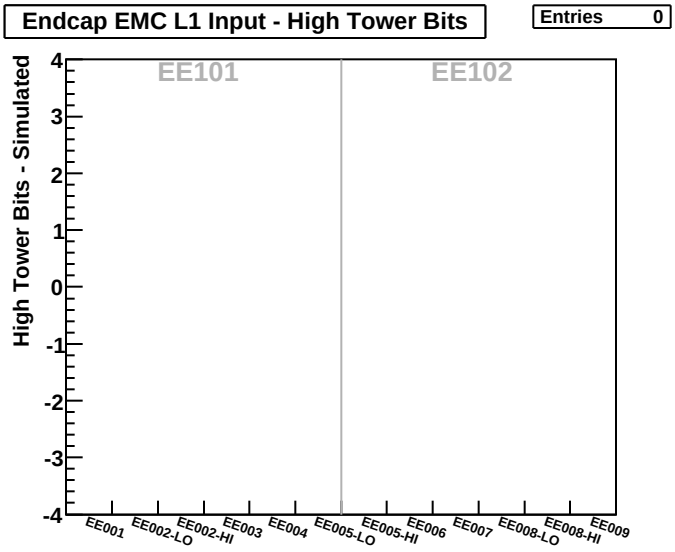

Endcap EMC L1 Input - Low Eta Sum
Entries 1.163966e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

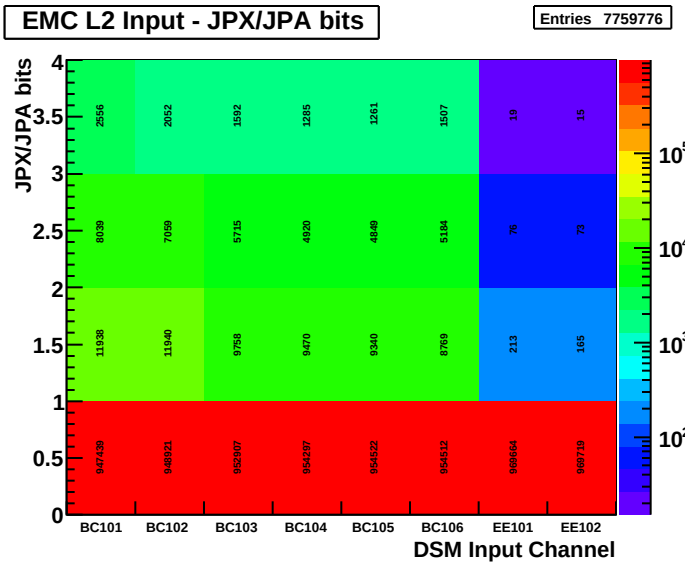

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

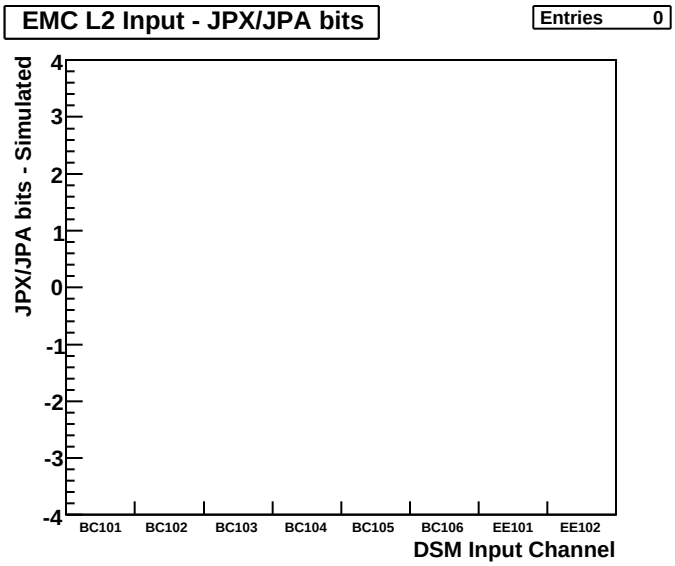

Endcap EMC L1 Input - High Eta Sum
Entries 1.163966e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

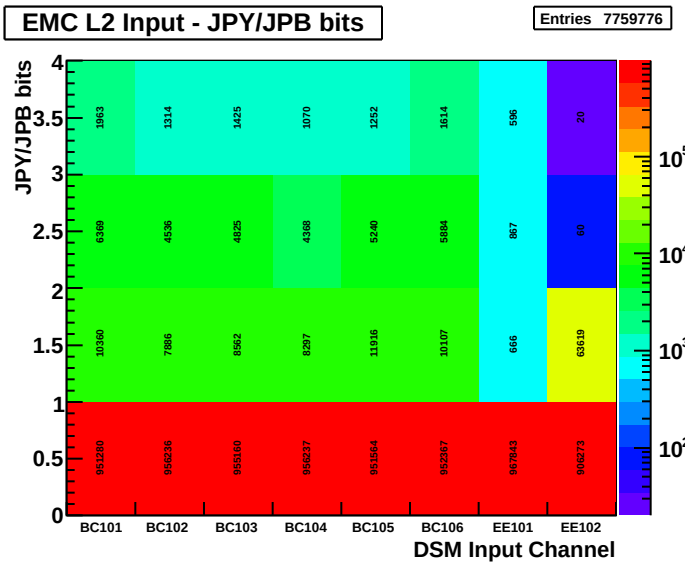

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

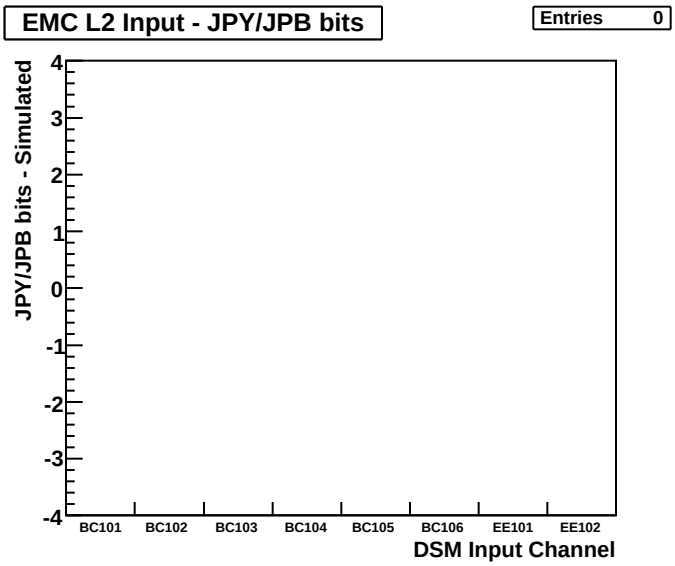

Endcap EMC L1 Input - High Tower Bits
Entries 1.163966e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

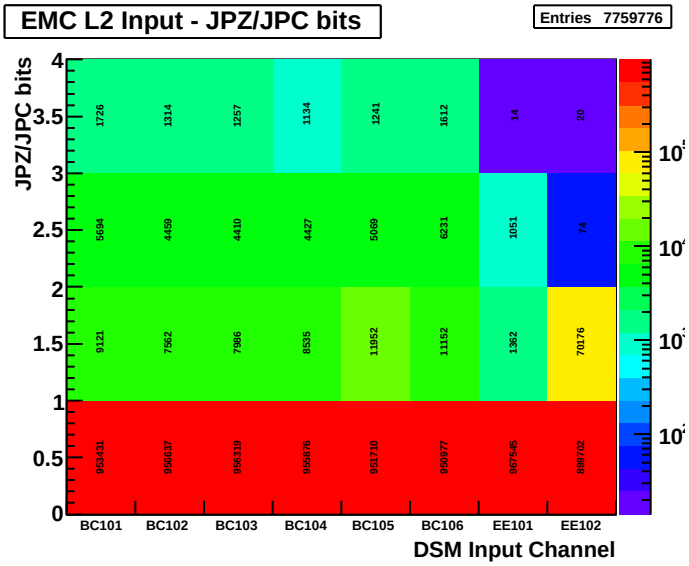

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

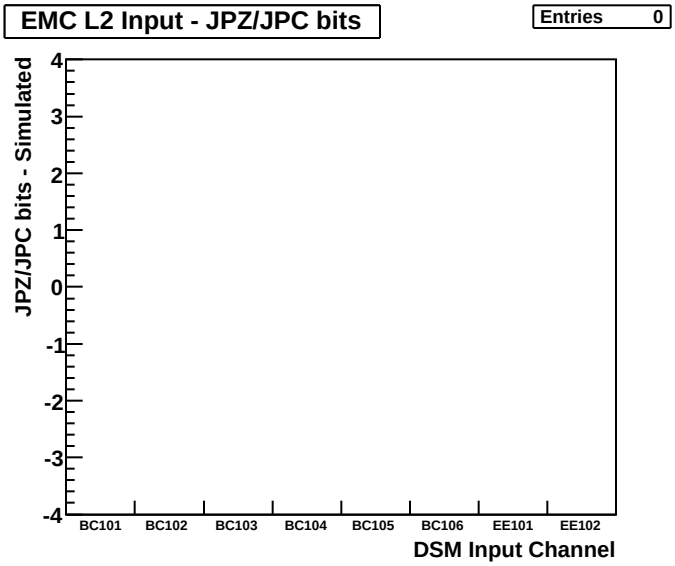

EMC L2 Input - JPX/JPA bits
Entries 7759776
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2556 2052 1592 1285 1261 1507 19 15
8039 7059 5715 4920 4849 5184 76 73
11938 11940 9758 9470 9340 8769 213 165
947439 948921 952907 954297 954522 954512 969664 969719

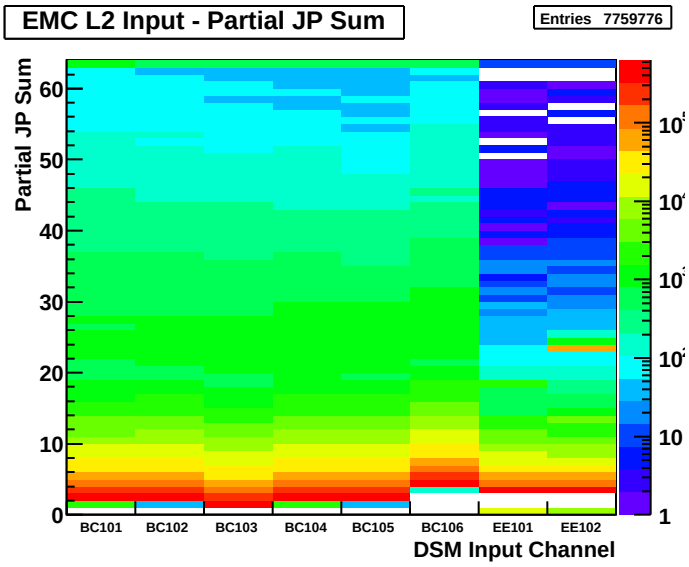

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

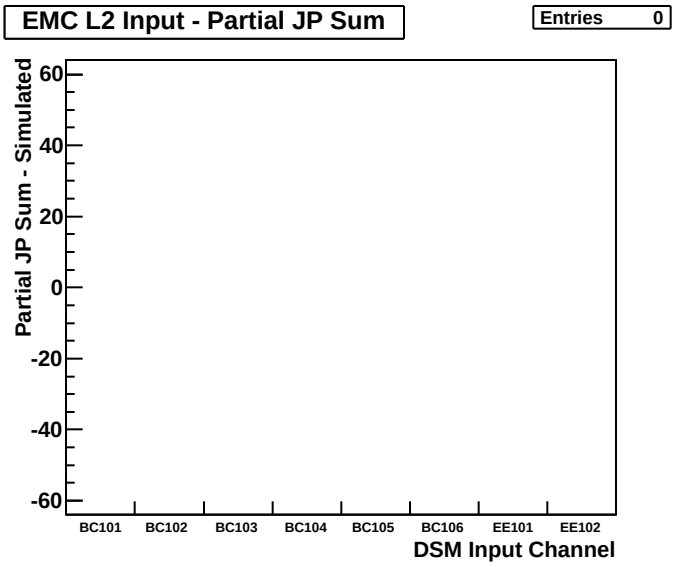

EMC L2 Input - JPY/JPB bits
Entries 7759776
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1963 1314 1425 1070 1252 1614 596 20
6389 4536 4825 4368 5240 5894 867 60
10360 7986 8562 8297 11916 10107 666 63619
951280 956236 955160 956237 951564 952367 967843 906273

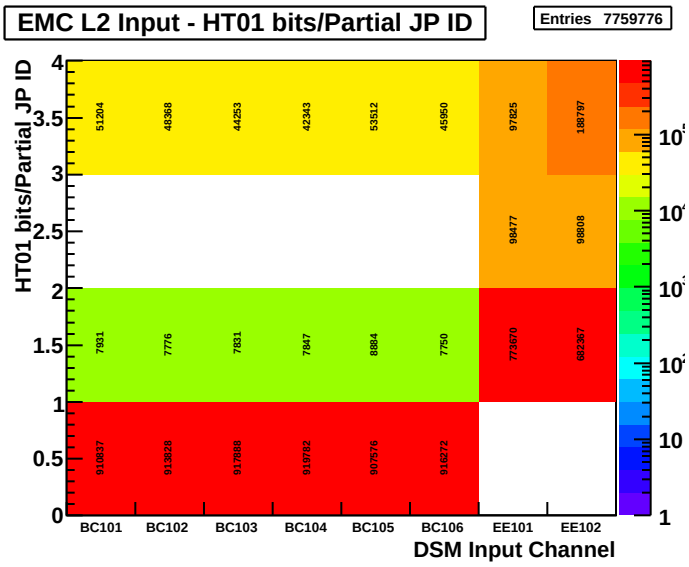

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

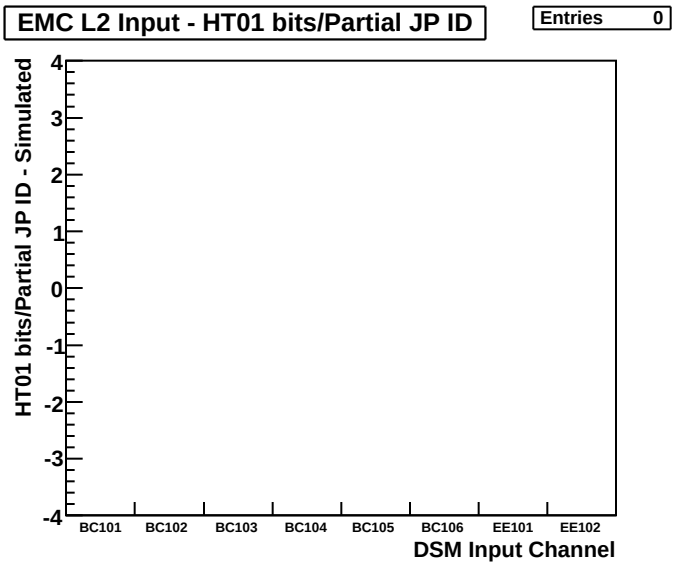

EMC L2 Input - JPZ/JPC bits
Entries 7759776
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1726 1314 1257 1134 1241 1612 14 20
5694 4459 4410 4427 5069 6231 1051 74
9121 7562 7986 8535 11952 11152 1362 70176
953431 956637 956319 955876 951710 950977 967545 899702

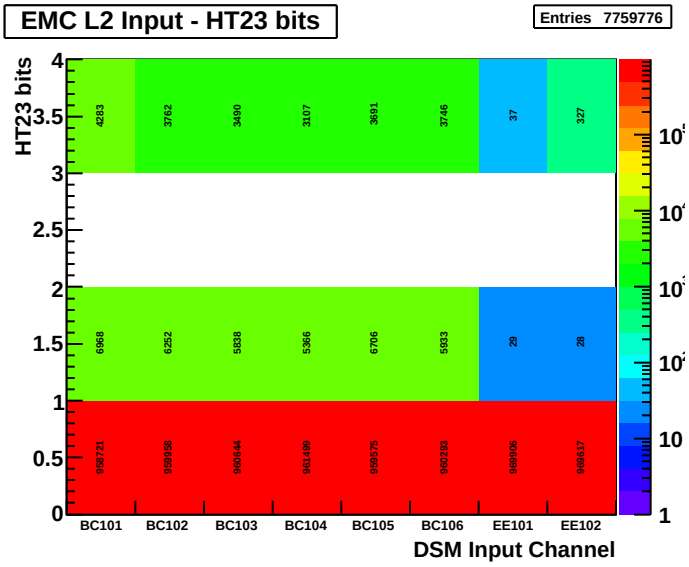

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

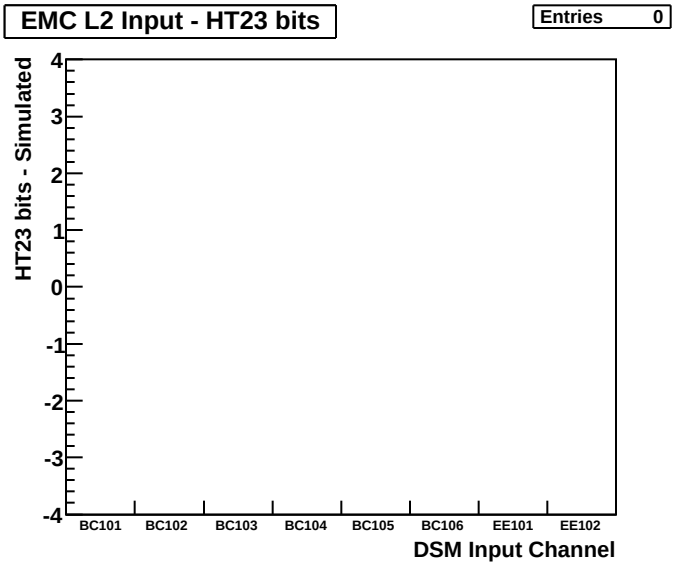

EMC L2 Input - Partial JP Sum
Entries 7759776
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

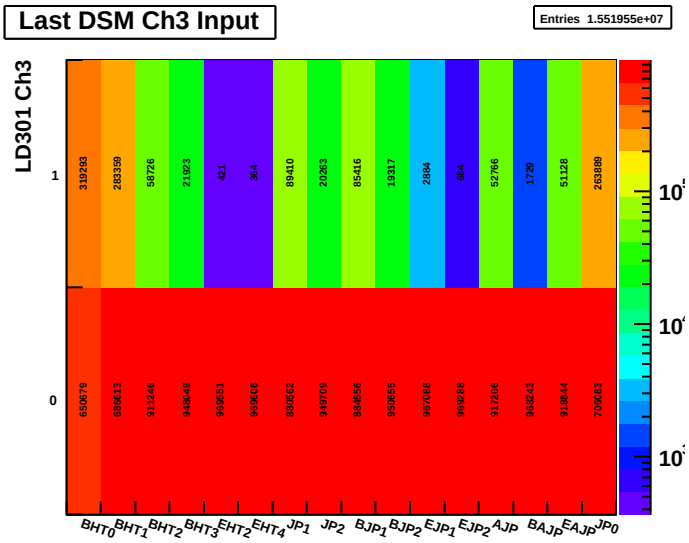

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

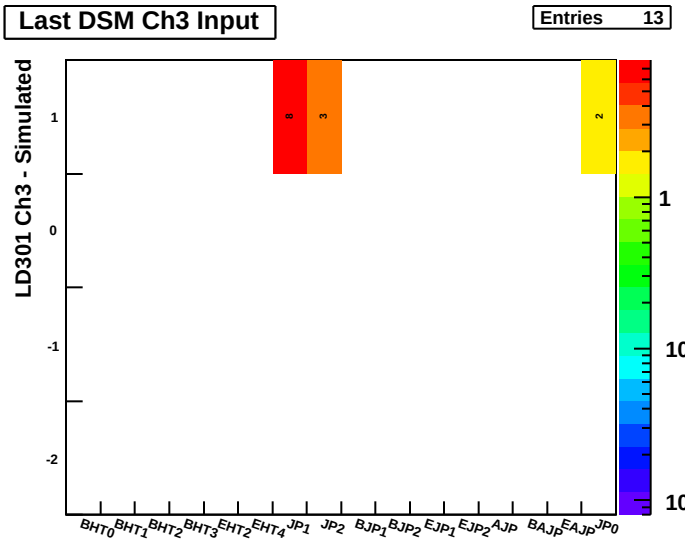

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7759776
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

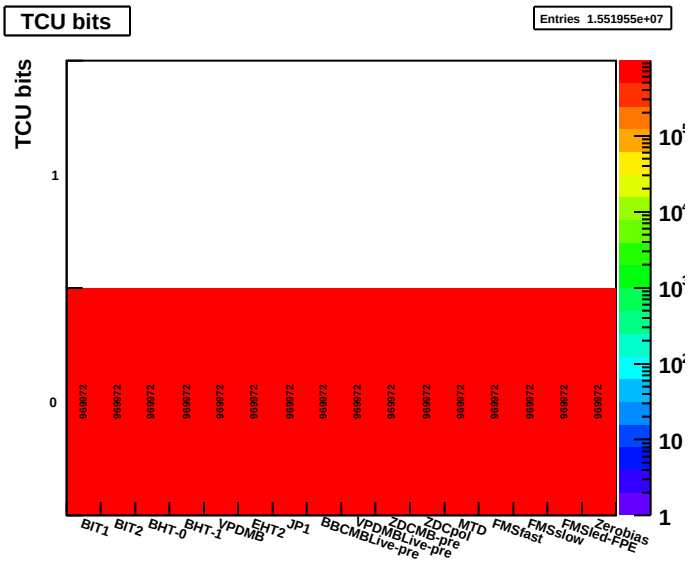

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

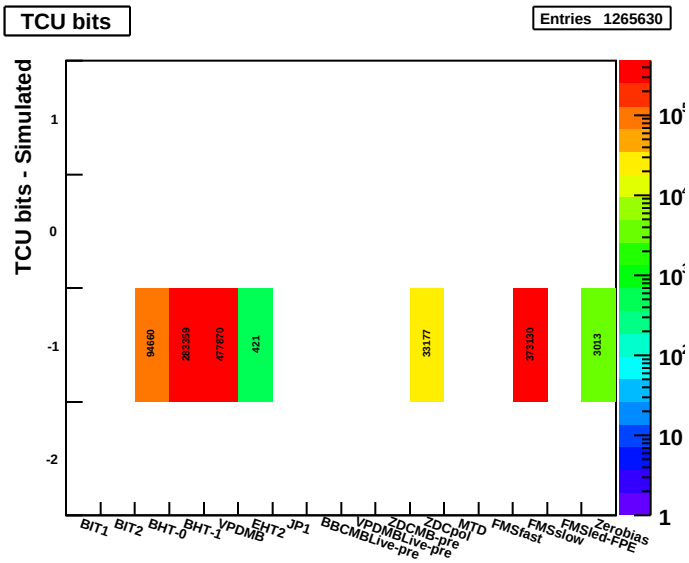

EMC L2 Input - HT23 bits
Entries 7759776
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

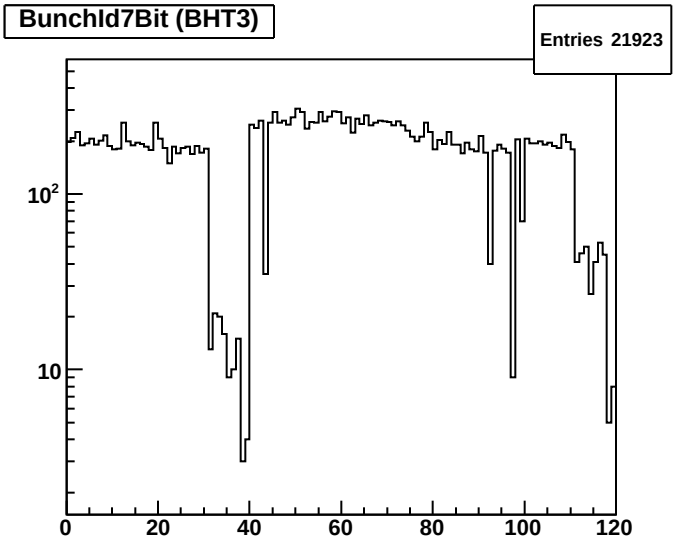

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

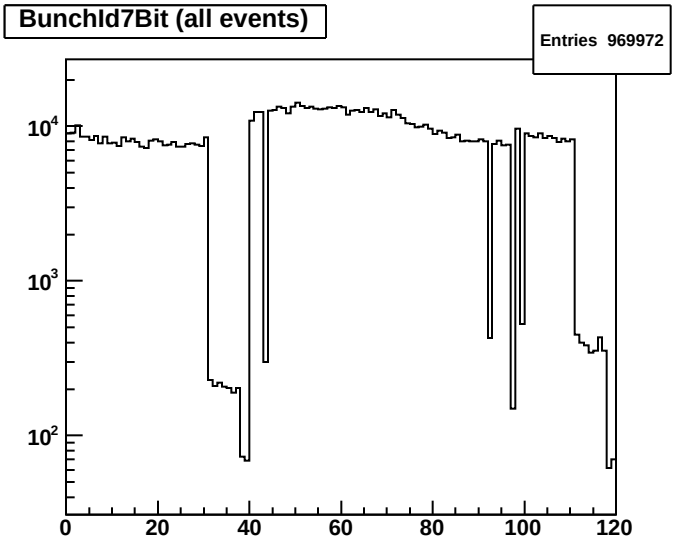

Last DSM Ch3 Input
Entries 1.551955e+07
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

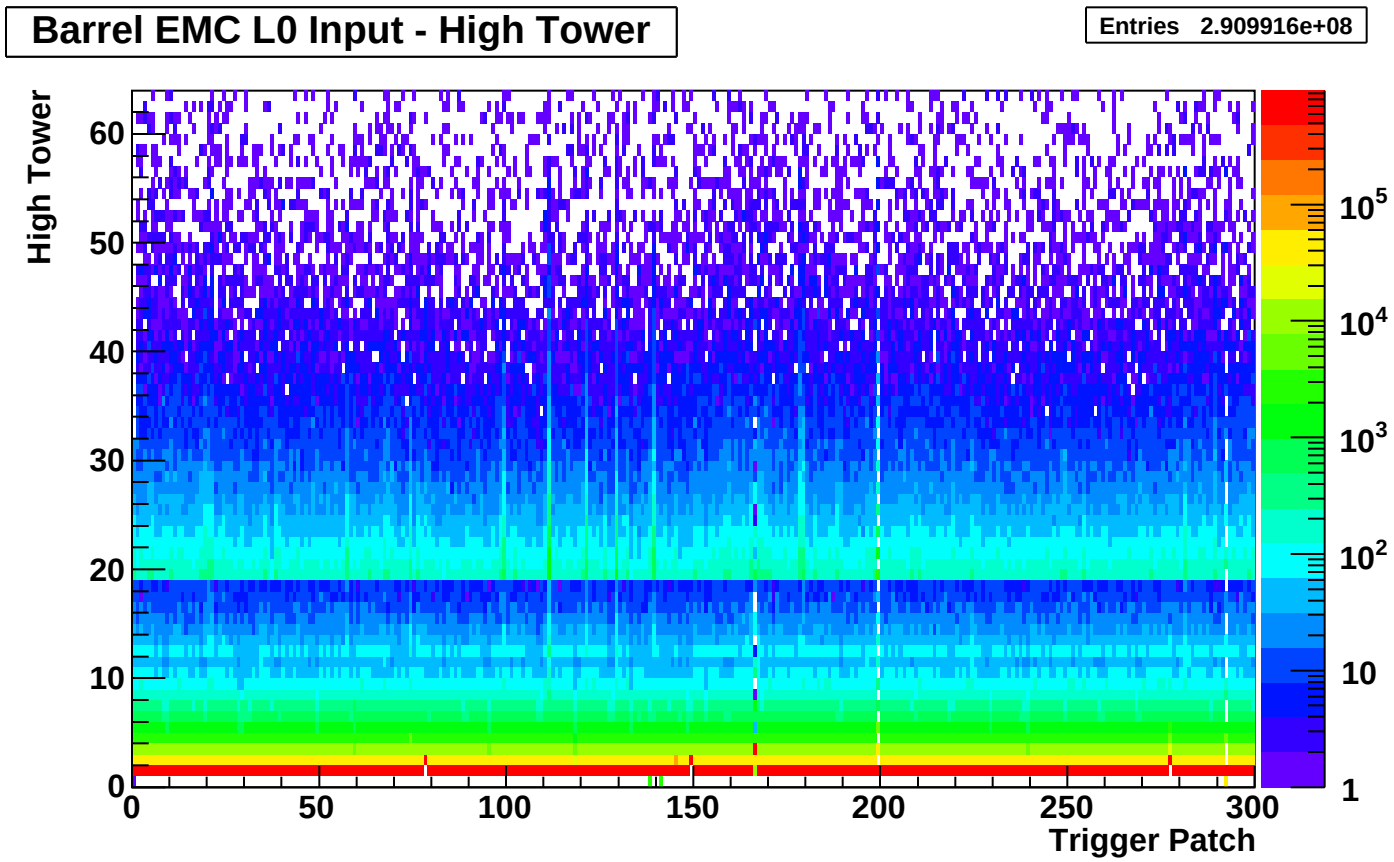

Last DSM Ch3 Input
Entries 13
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

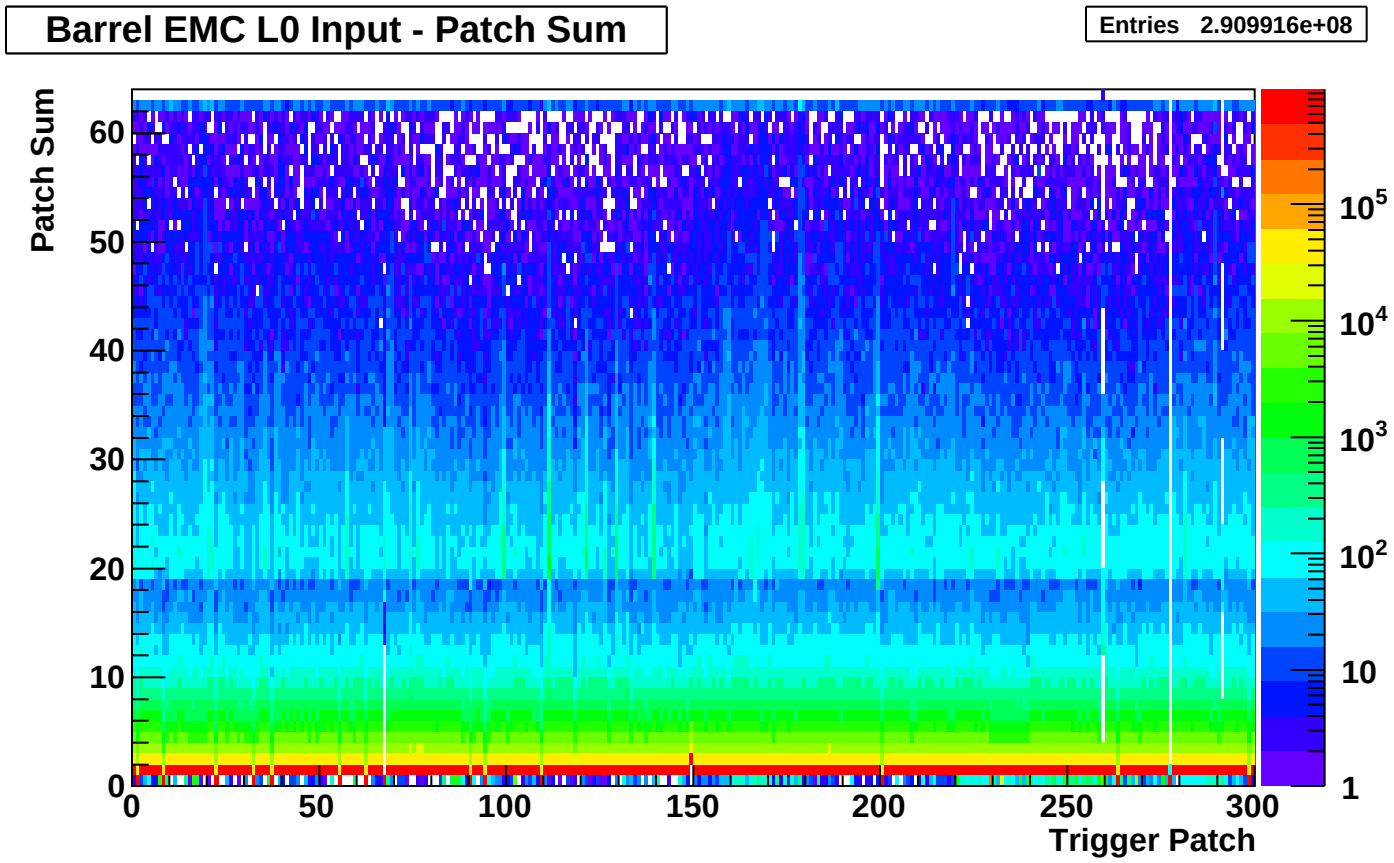

TCU bits
Entries 1.551955e+07
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

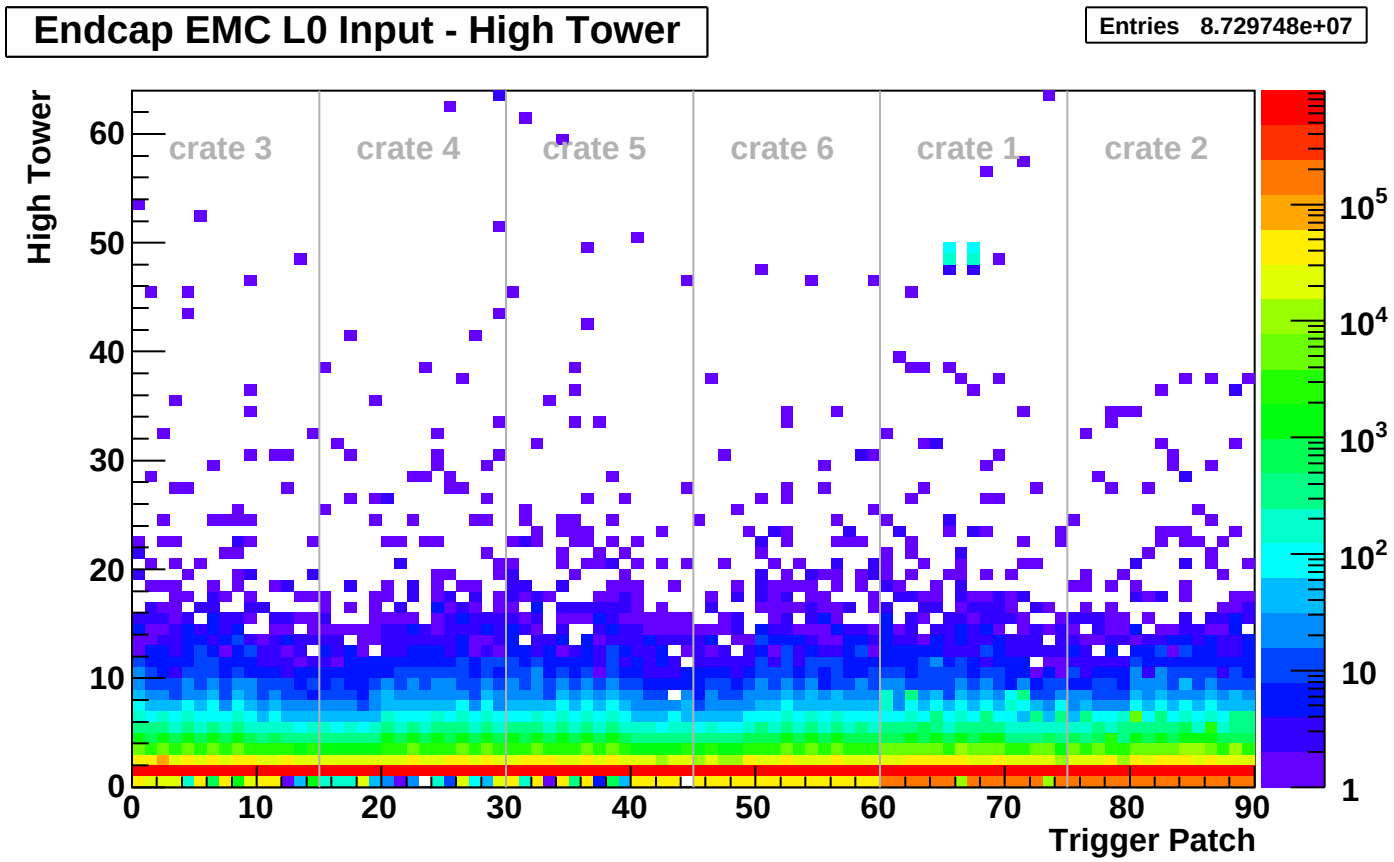

TCU bits
Entries 1265630
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

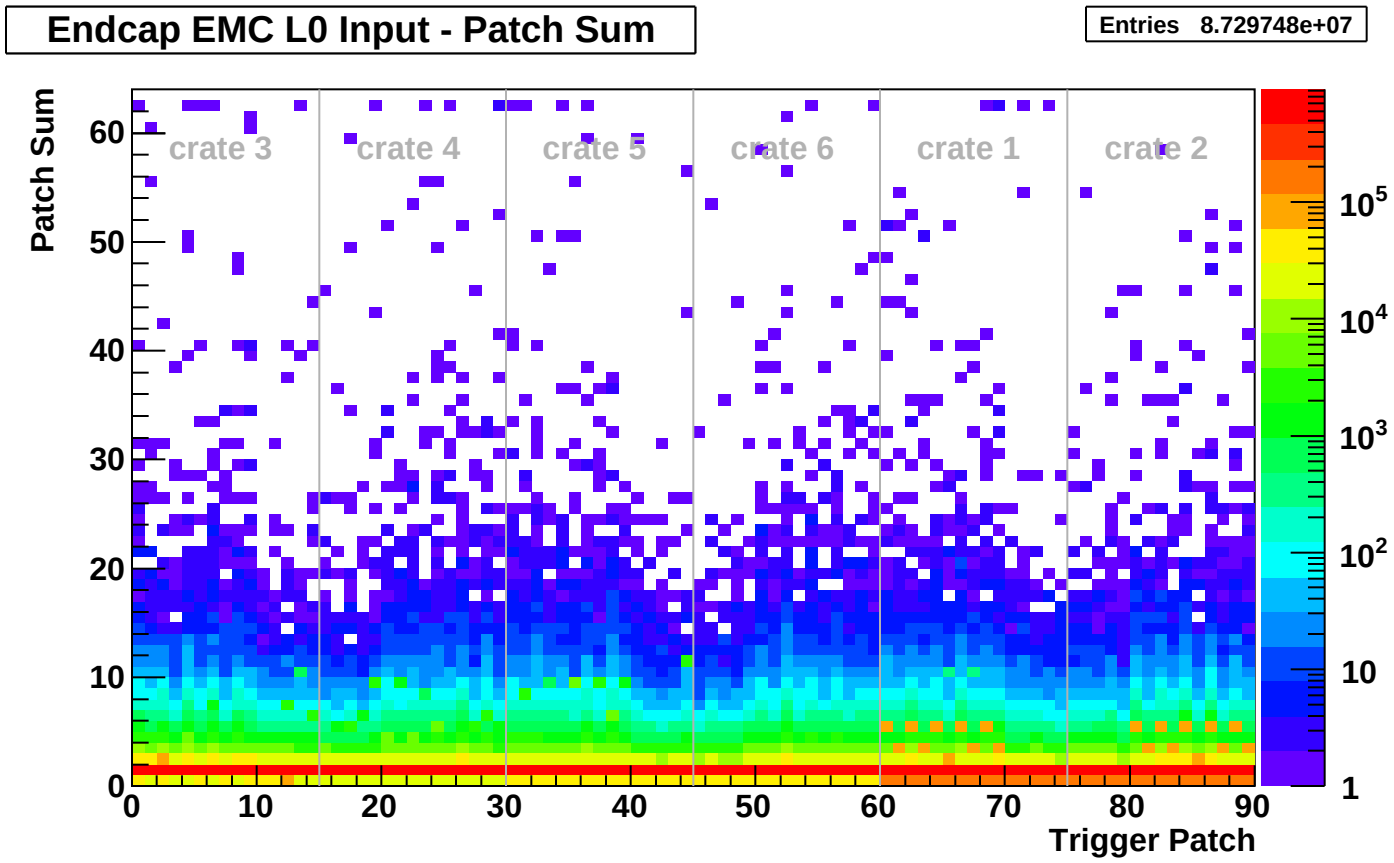

BunchId7Bit (BHT3)
Entries 21923

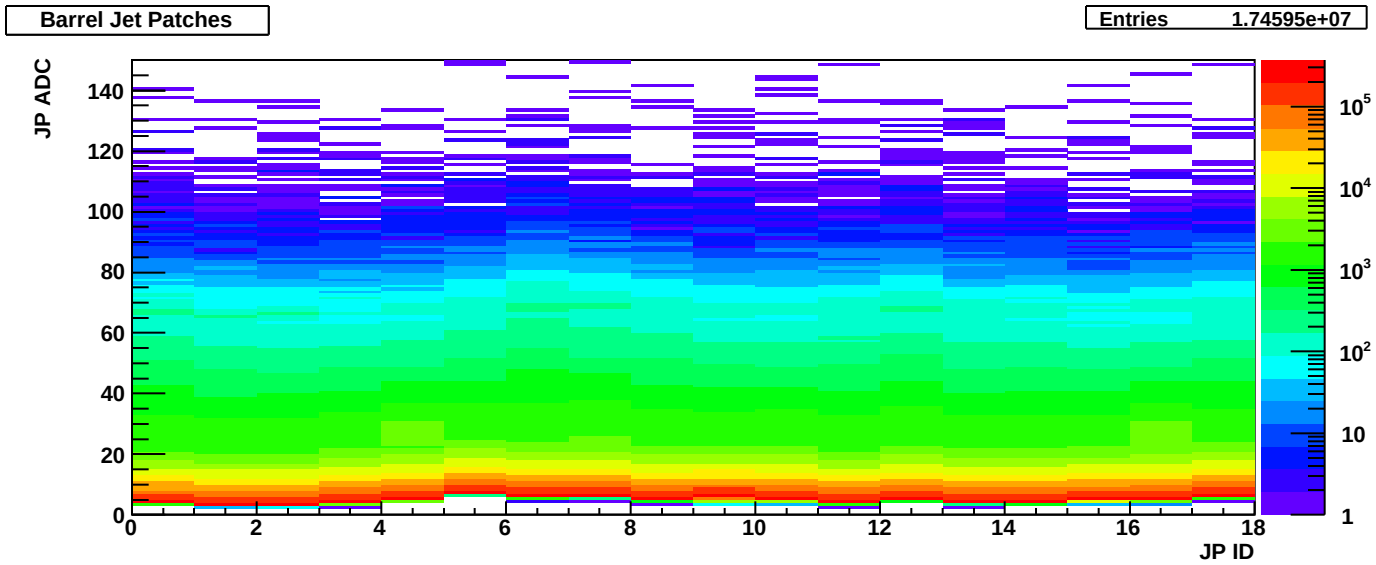

BunchId7Bit (all events)
Entries 969972

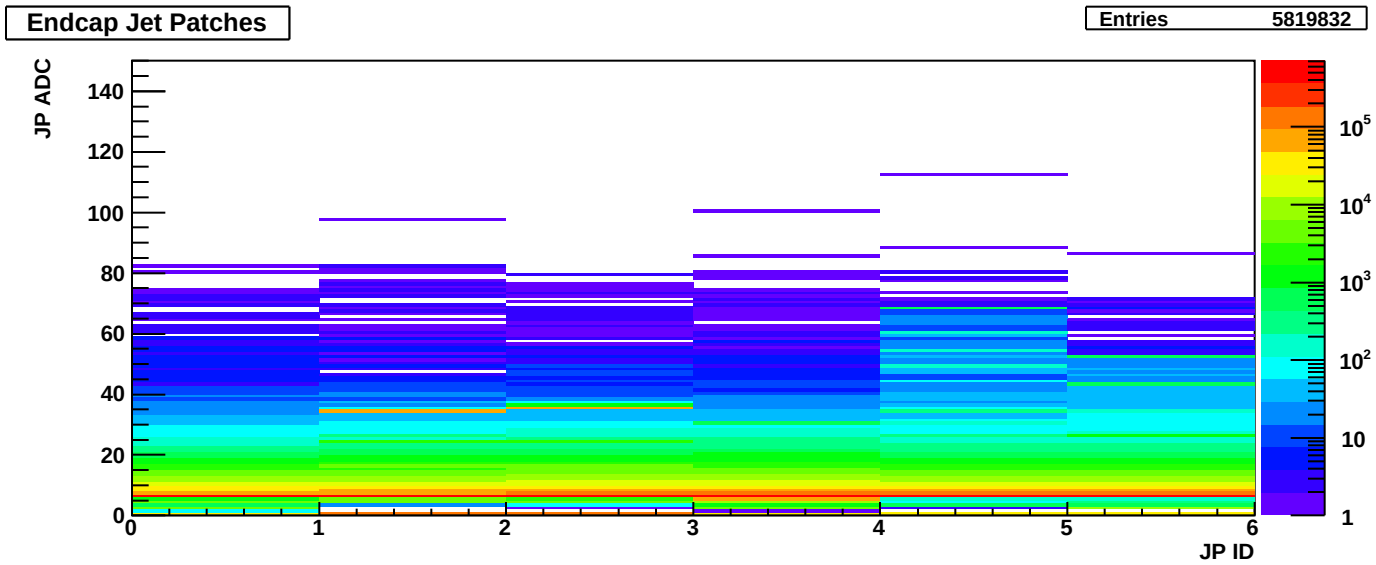

Barrel EMC L0 Input - High Tower
Entries 2.909916e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

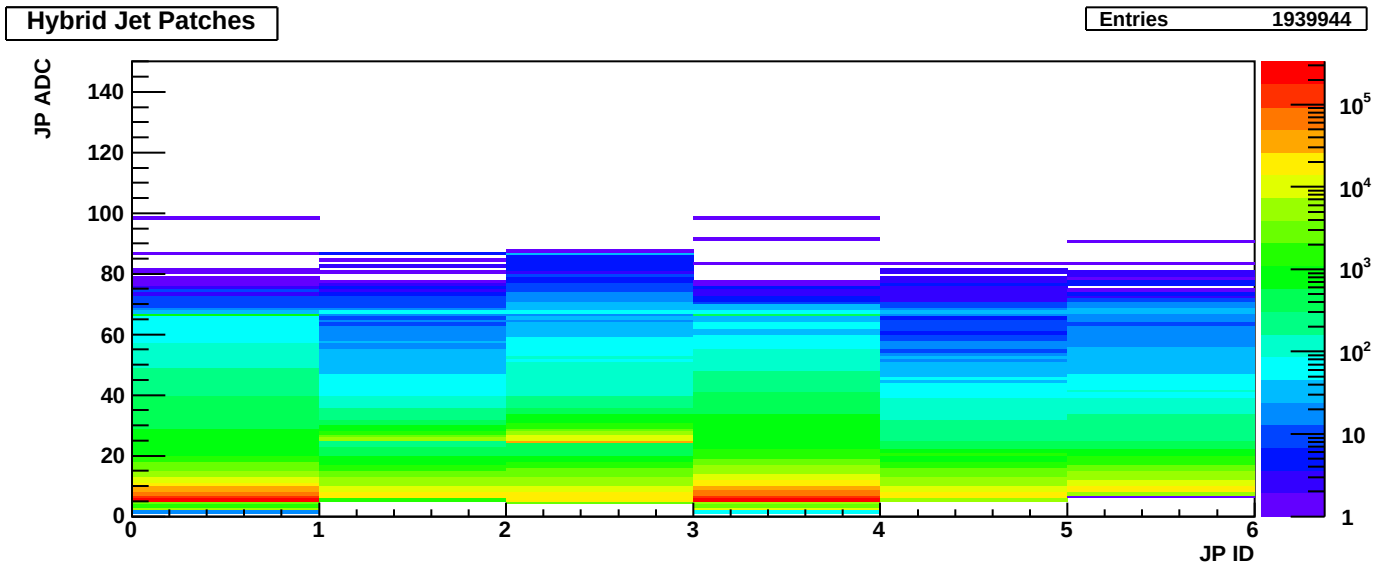

Barrel EMC L0 Input - Patch Sum
Entries 2.909916e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 8.729748e+07
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch


Endcap EMC L0 Input - Patch Sum
Entries 8.729748e+07
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

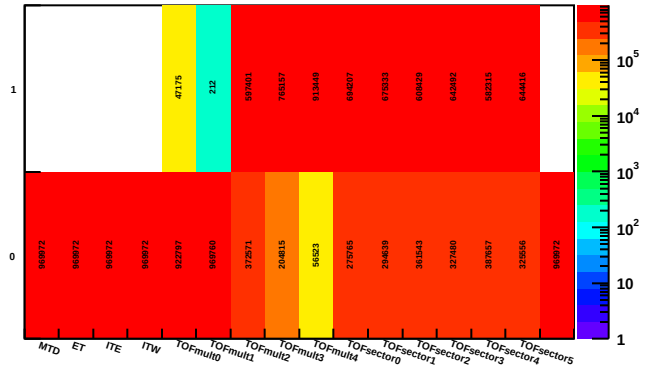

Barrel Jet Patches
Entries 1.74595e+07
JP ADC
JP ID


Endcap Jet Patches
Entries 5819832
JP ADC
JP ID


Hybrid Jet Patches
Entries 1939944
JP ADC
JP ID

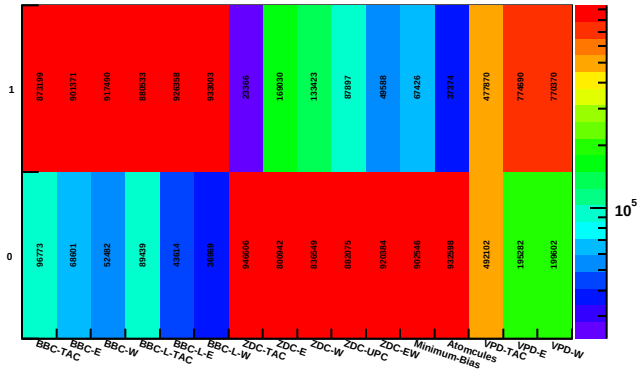
TF201 0-15 (ch0)

Entries 1.551955e+07



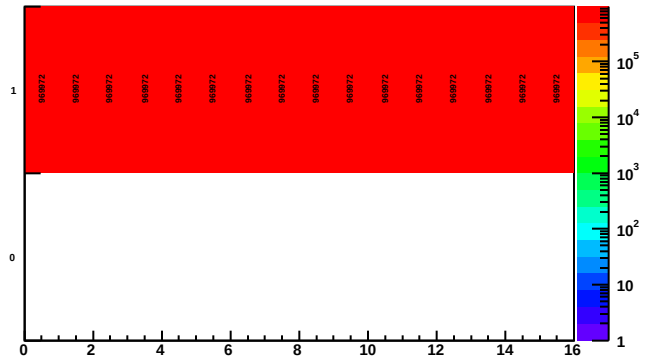
VT201 0-15 (ch1)

Entries 1.551955e+07



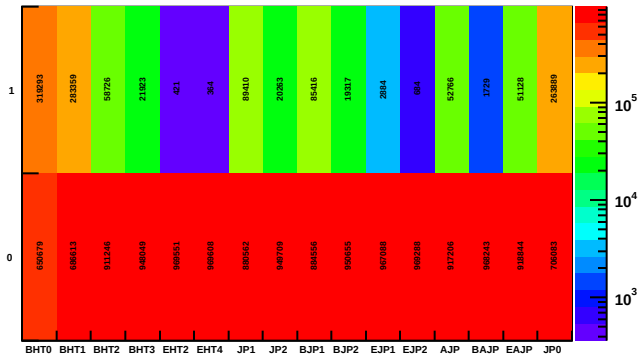
Unused (ch2)

Entries 1.551955e+07



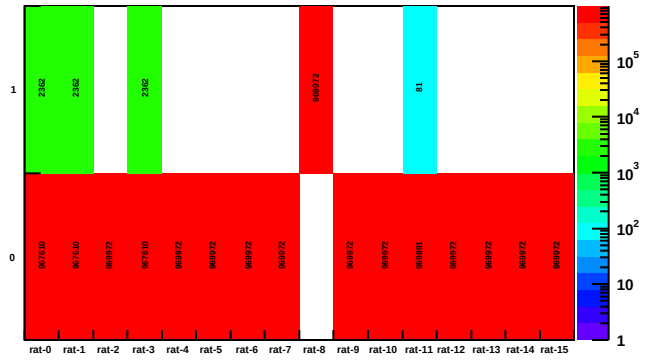
EM201 0-15 (ch3)

Entries 1.551955e+07



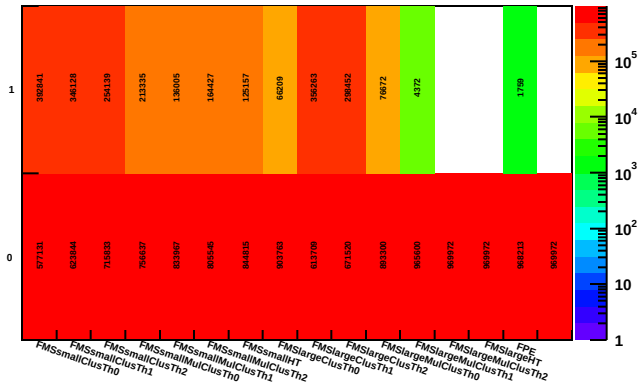
RAT board (ch4)

Entries 1.551955e+07



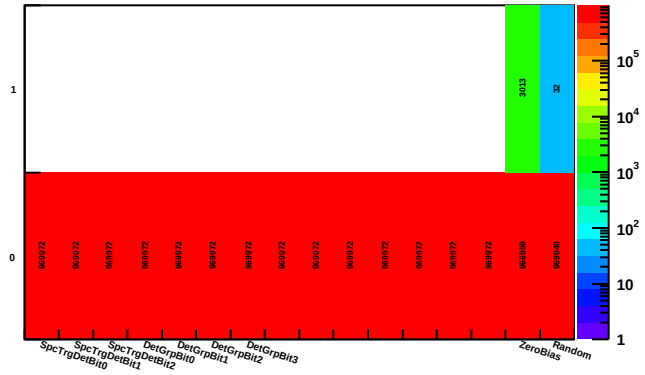
FP201 0-15 (ch5)

Entries 1.551955e+07



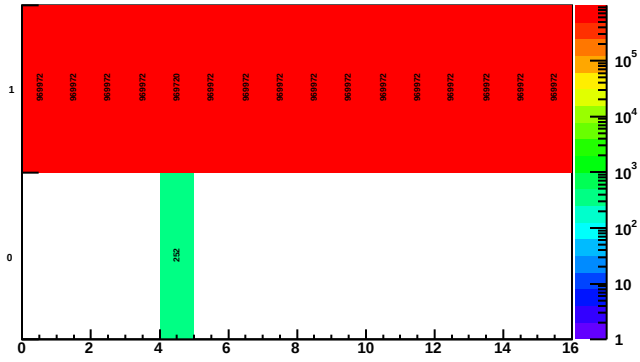
ST201 0-15 (ch6)

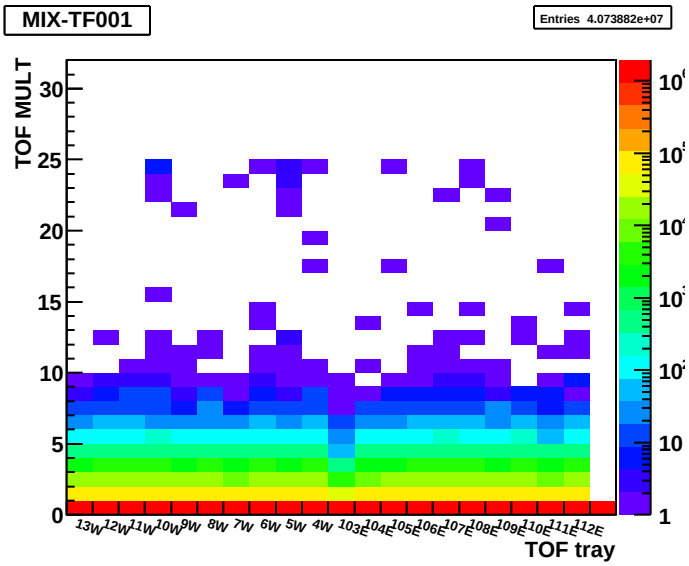
Entries 1.551955e+07

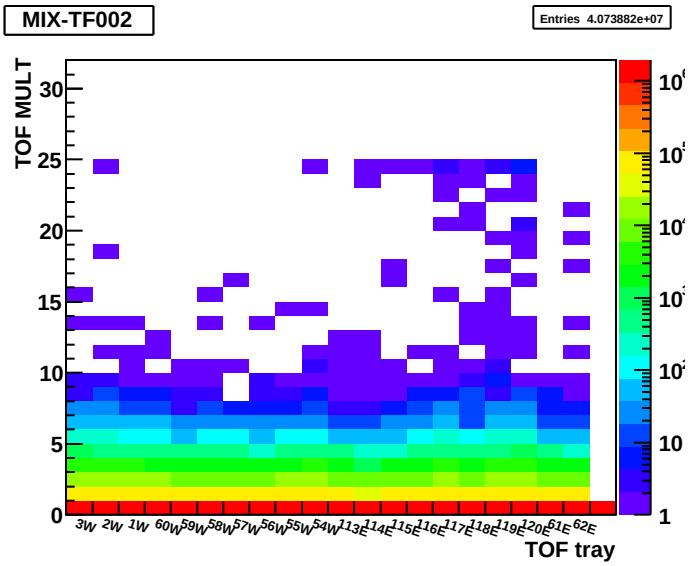


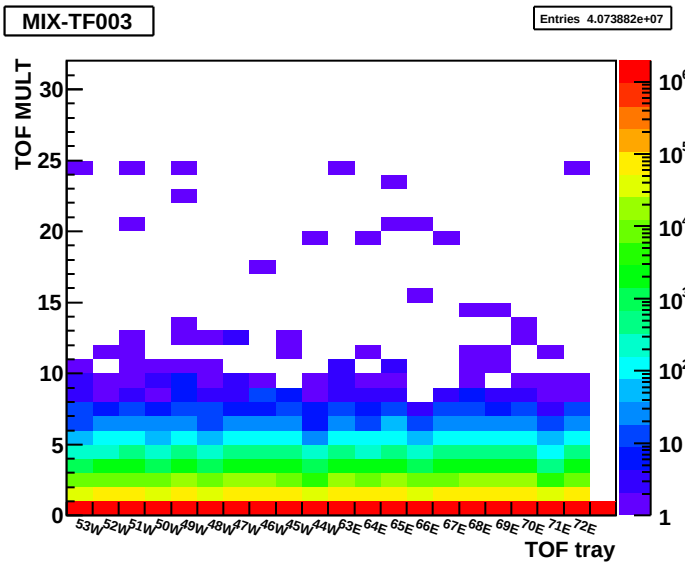
Unused (ch7)

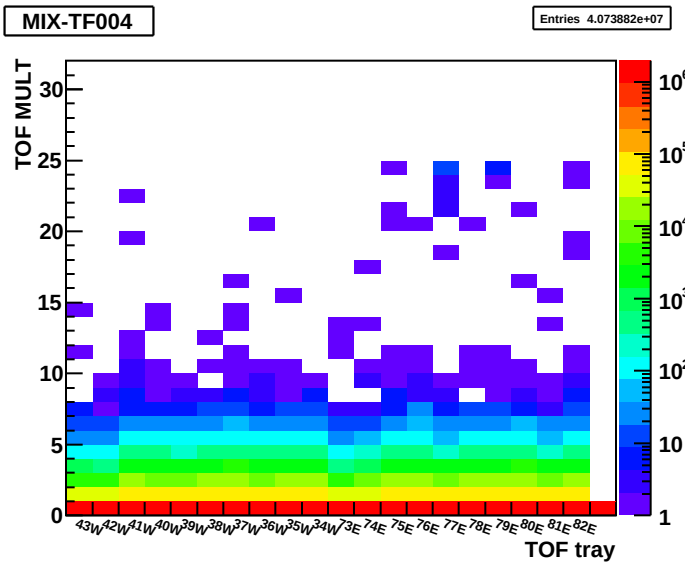
Entries 1.551955e+07

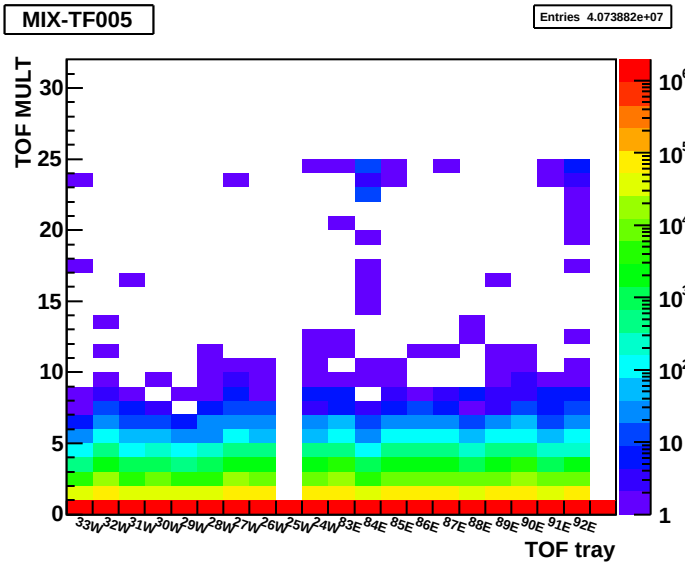


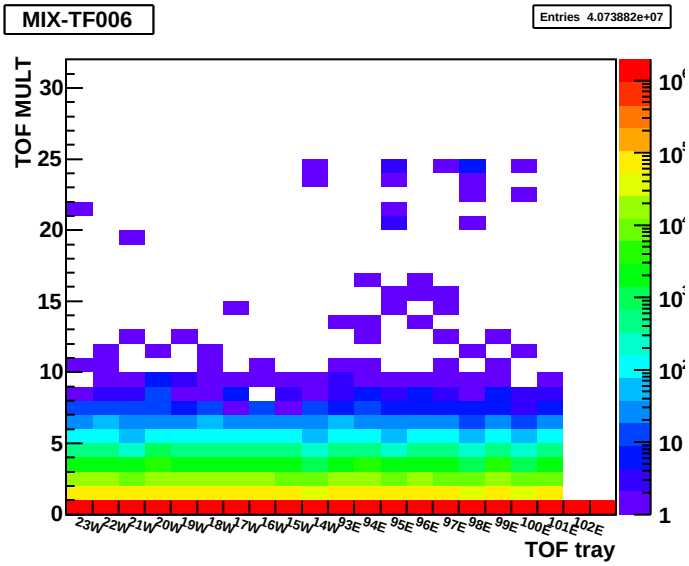

MIX-TF001
Entries 4.073882e+07
TOF MULT
TOF tray

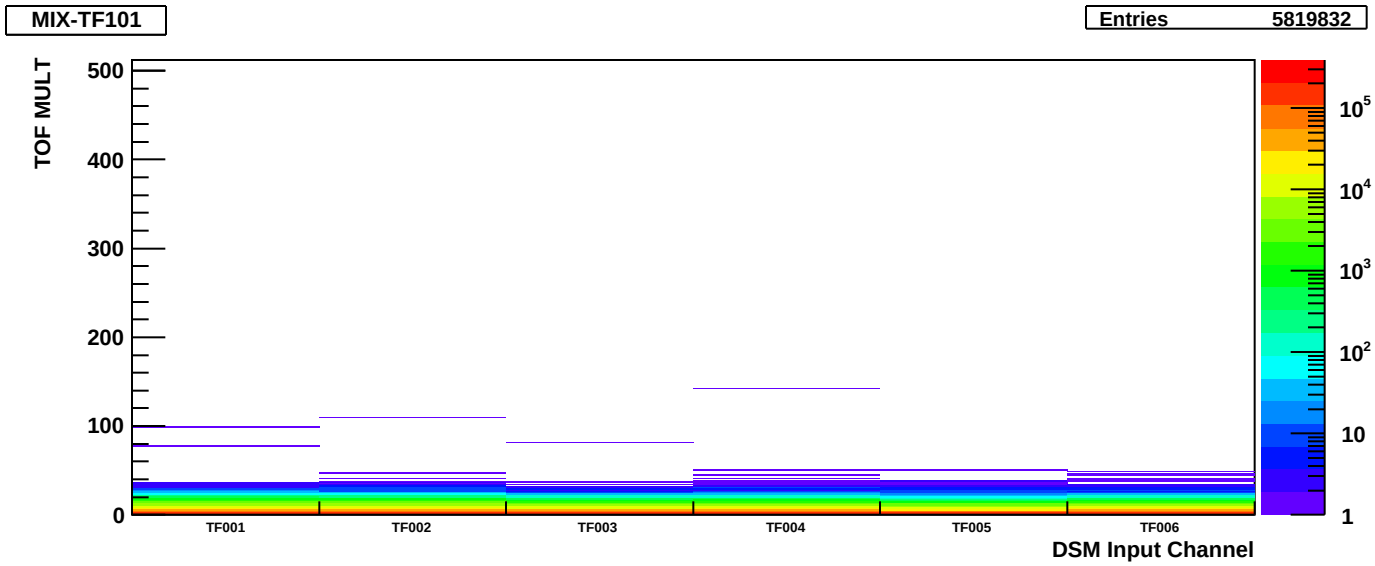

MIX-TF002
Entries 4.073882e+07
TOF MULT
TOF tray

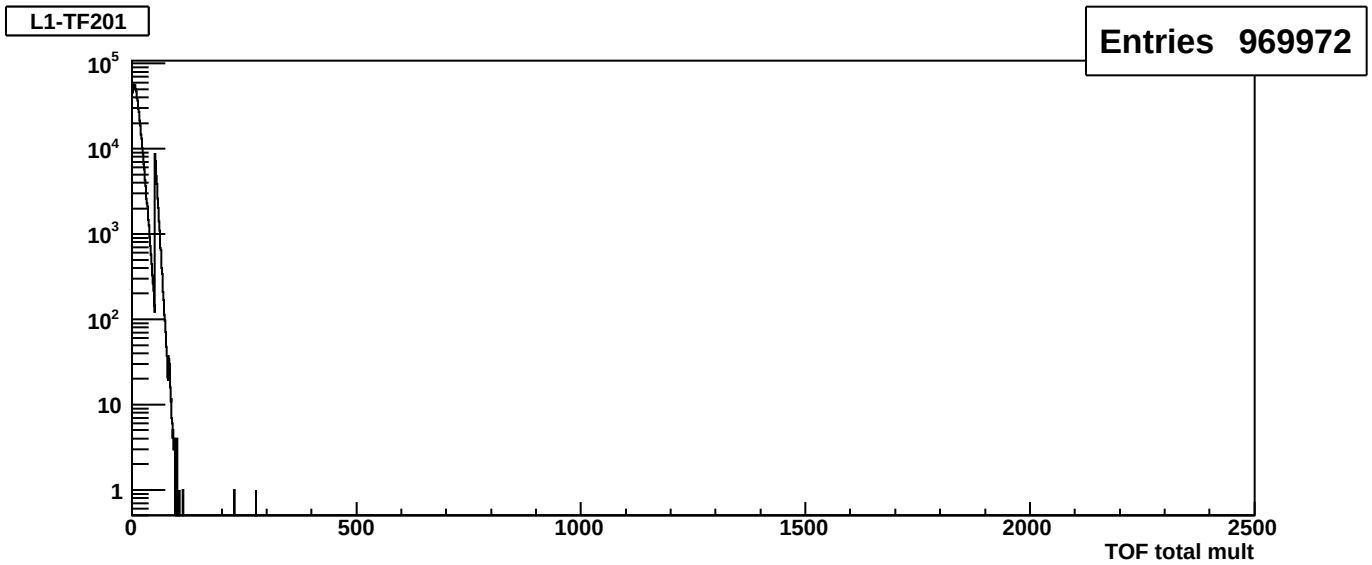

MIX-TF003
Entries 4.073882e+07
TOF MULT
TOF tray

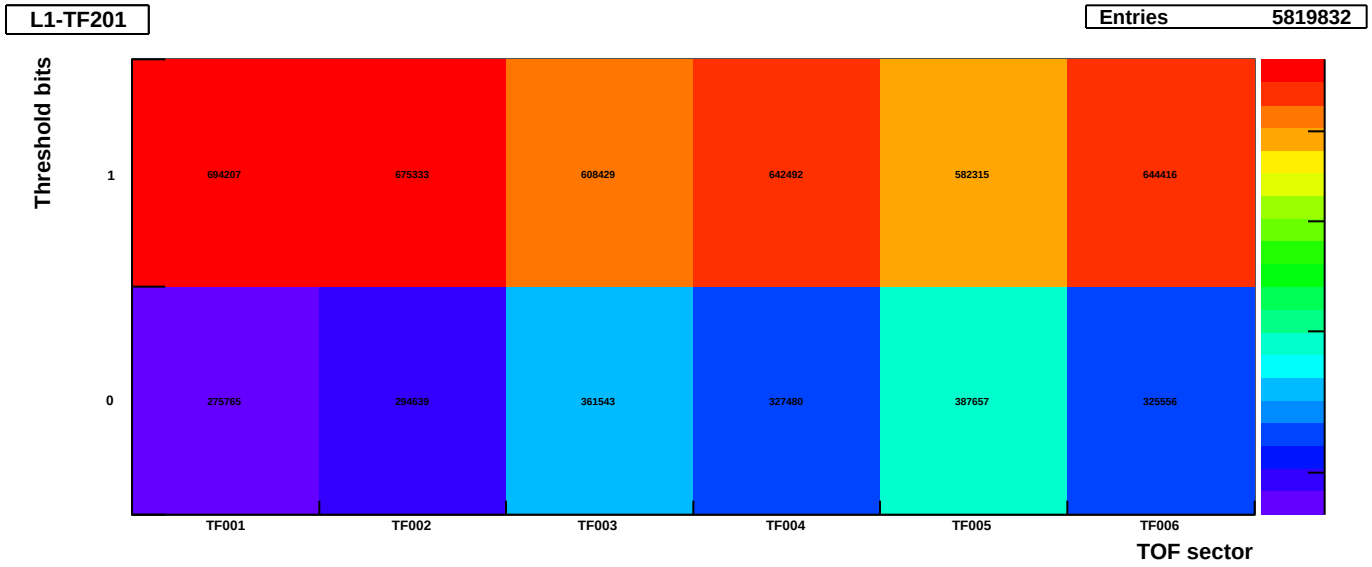

MIX-TF004
Entries 4.073882e+07
TOF MULT
TOF tray

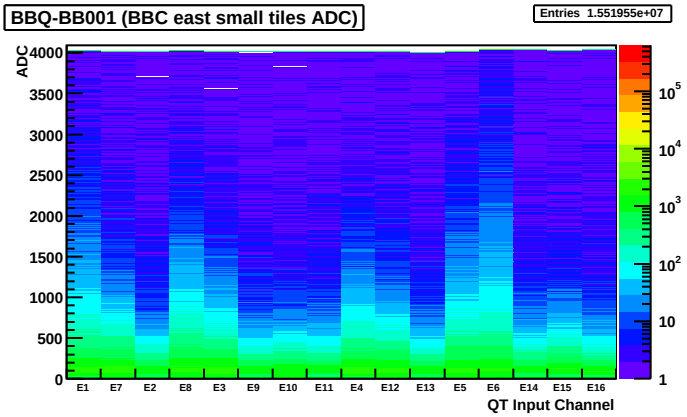

MIX-TF005
Entries 4.073882e+07
TOF MULT
TOF tray

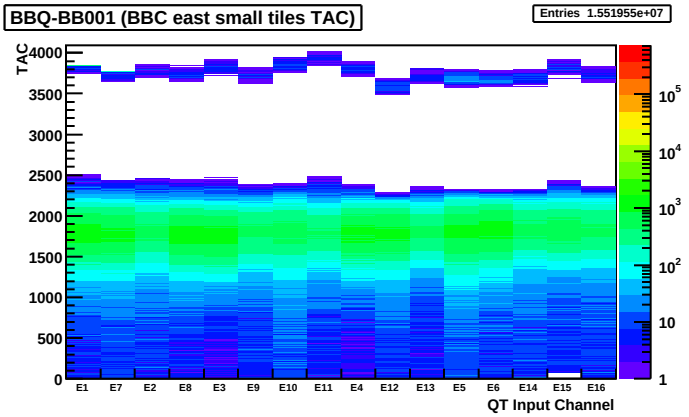

MIX-TF006
Entries 4.073882e+07
TOF MULT
TOF tray

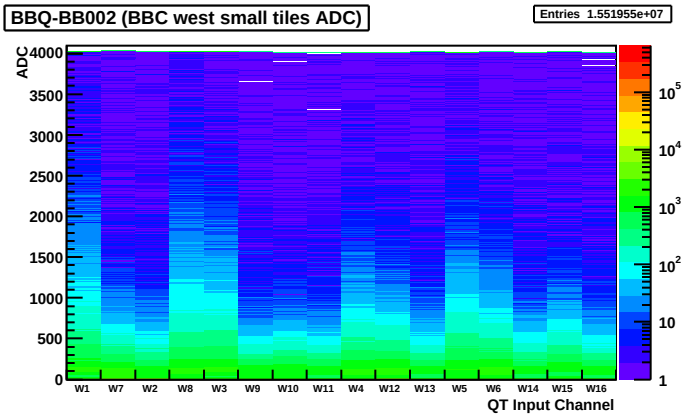

MIX-TF101
Entries 5819832
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

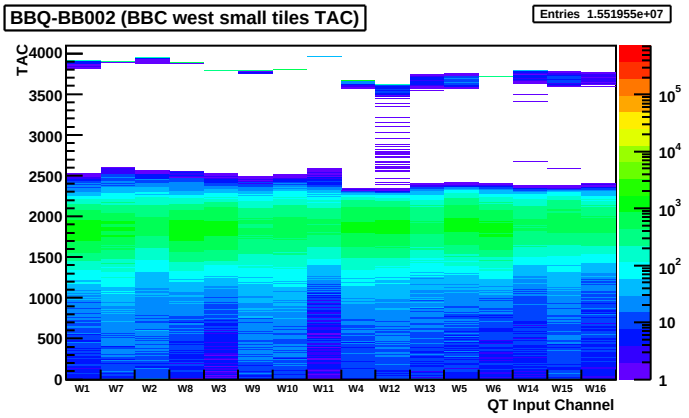

L1-TF201
Entries 969972
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

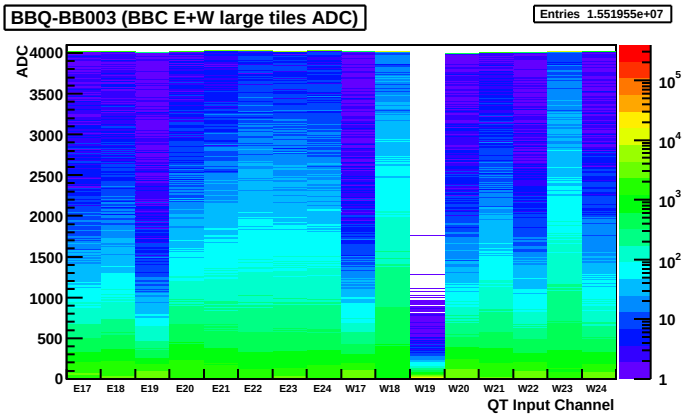

L1-TF201
Entries 5819832
Threshold bits
1
0
694207
675333
608429
642492
582315
644416
275765
294639
361543
327480
387657
325556
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

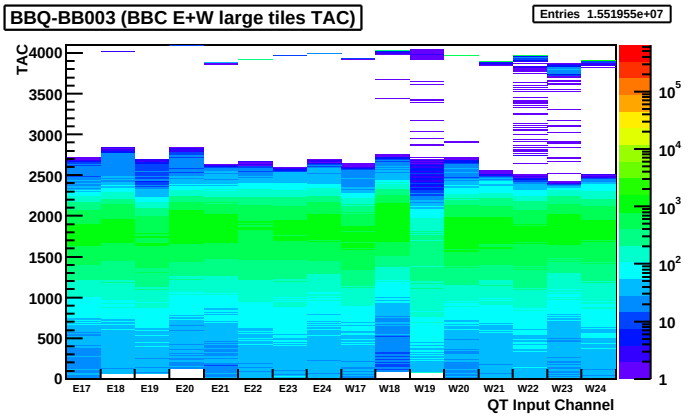

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.551955e+07
ADC
QT Input Channel

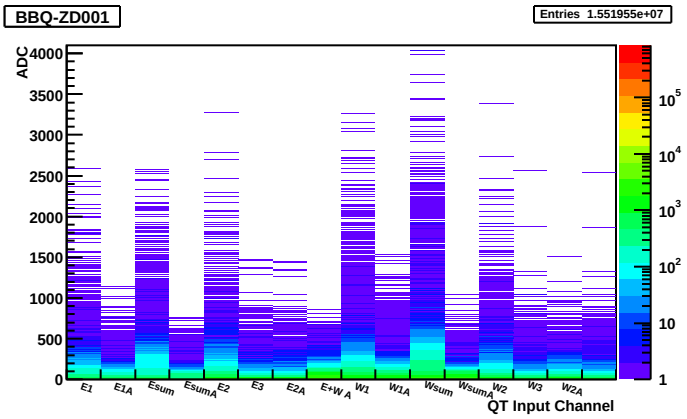

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.551955e+07
TAC
QT Input Channel

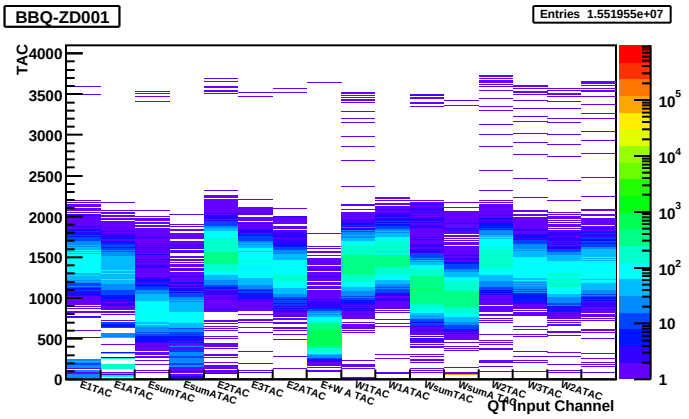

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.551955e+07
ADC
QT Input Channel

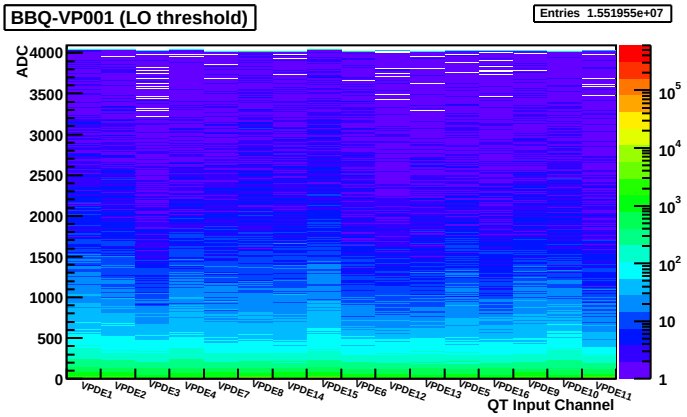

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.551955e+07
TAC
QT Input Channel

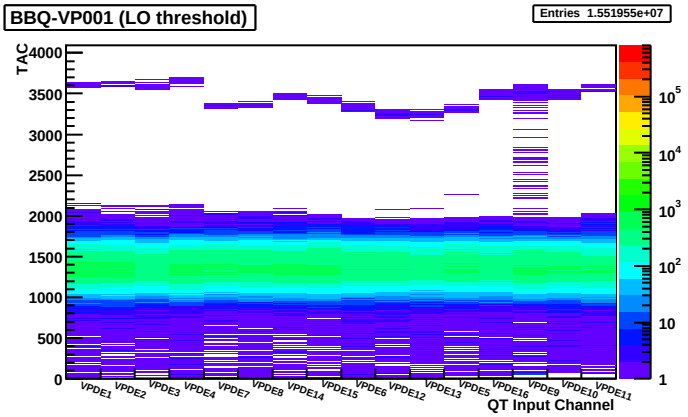

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.551955e+07
ADC
QT Input Channel

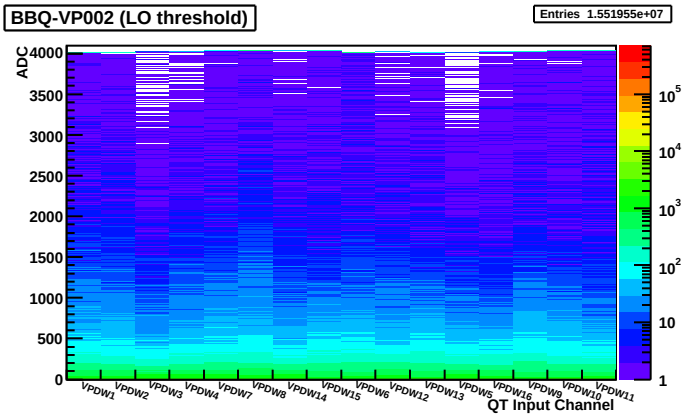

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.551955e+07
TAC
QT Input Channel

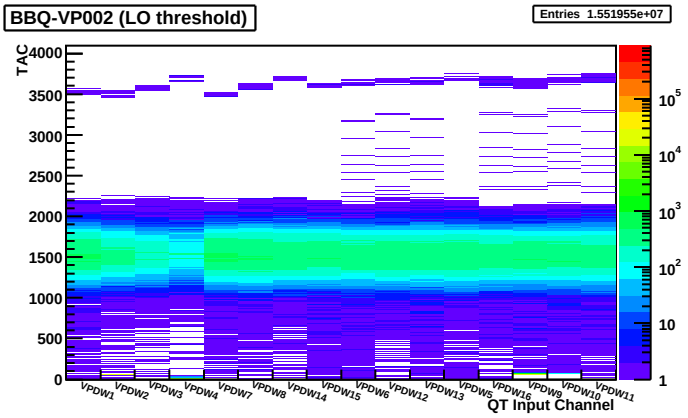

BBQ-ZD001
Entries 1.551955e+07
ADC
QT Input Channel

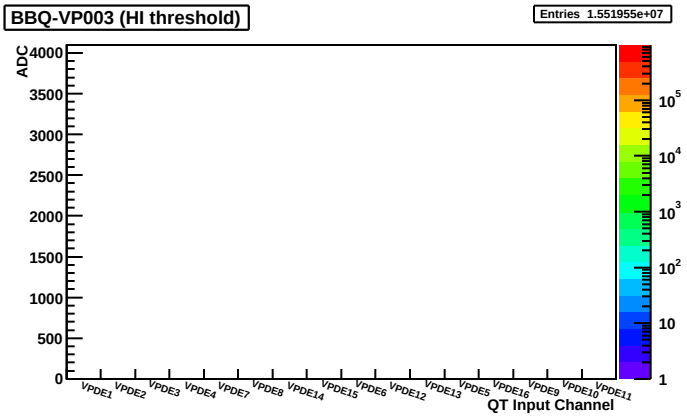

BBQ-ZD001
Entries 1.551955e+07
TAC
QT Input Channel

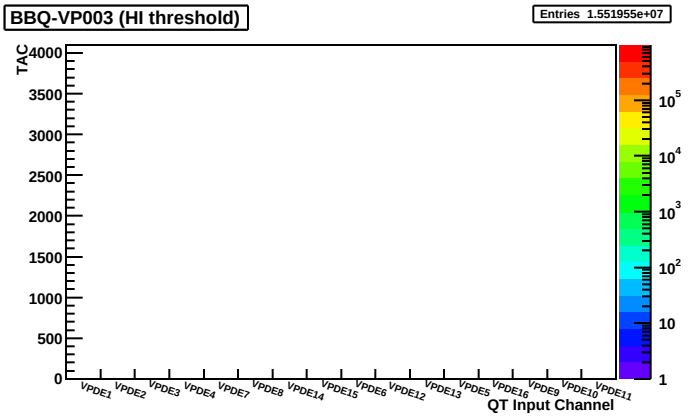

BBQ-VP001 (LO threshold)
Entries 1.551955e+07
ADC
QT Input Channel

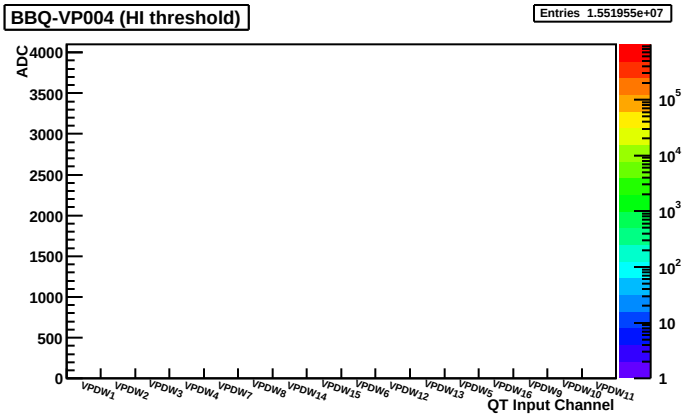

BBQ-VP001 (LO threshold)
Entries 1.551955e+07
TAC
QT Input Channel

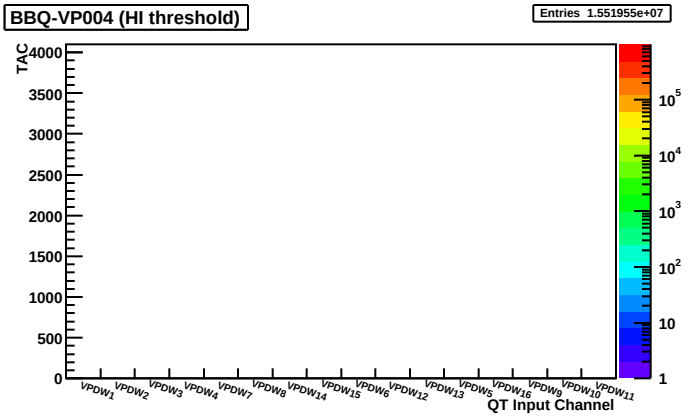

BBQ-VP002 (LO threshold)
Entries 1.551955e+07
ADC
QT Input Channel

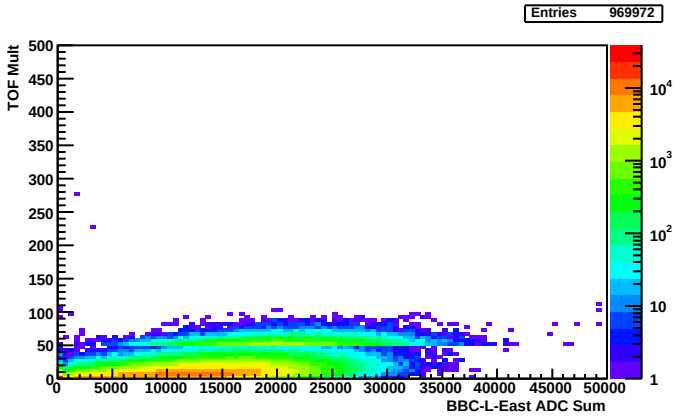

BBQ-VP002 (LO threshold)
Entries 1.551955e+07
TAC
QT Input Channel

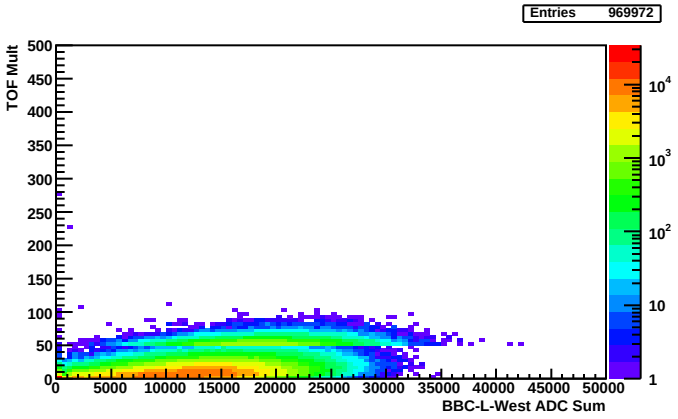

BBQ-VP003 (HI threshold)
Entries 1.551955e+07
ADC
QT Input Channel

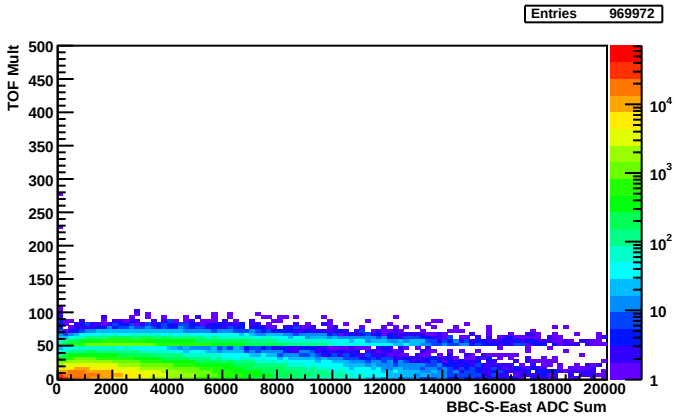

BBQ-VP003 (HI threshold)
Entries 1.551955e+07
TAC
QT Input Channel

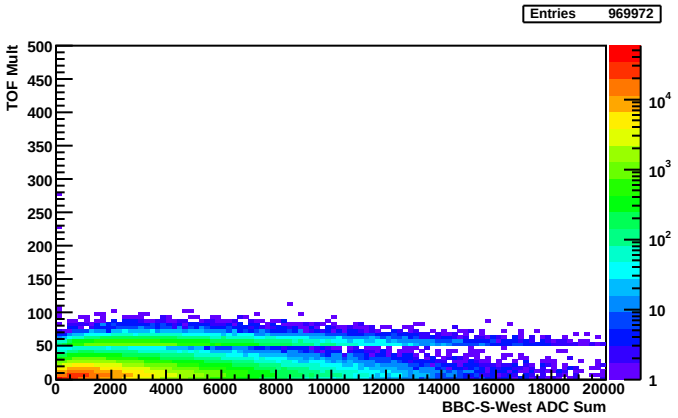

BBQ-VP004 (HI threshold)
Entries 1.551955e+07
ADC
QT Input Channel

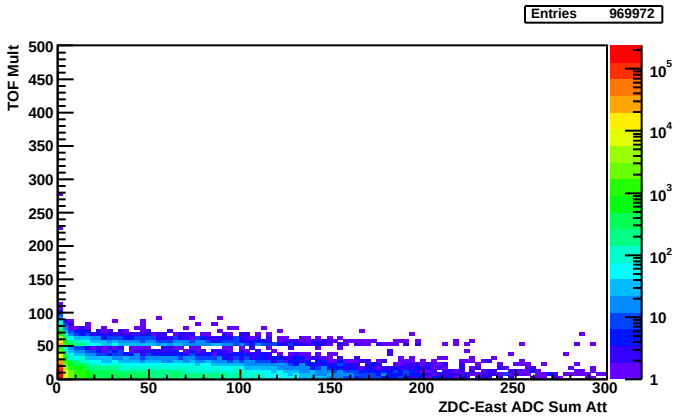

BBQ-VP004 (HI threshold)
Entries 1.551955e+07
TAC
QT Input Channel

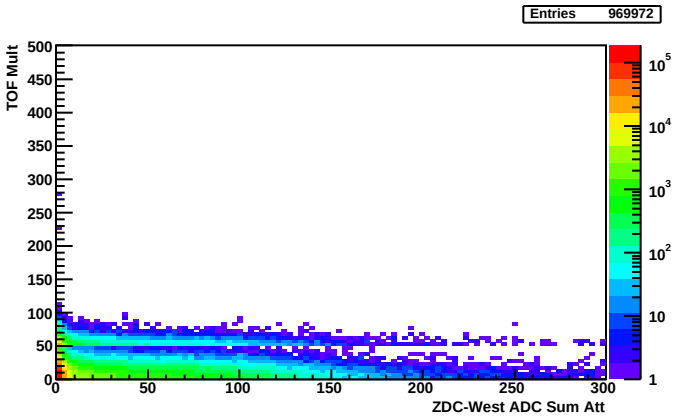

Entries 969972
TOF Mult
BBC-L-East ADC Sum

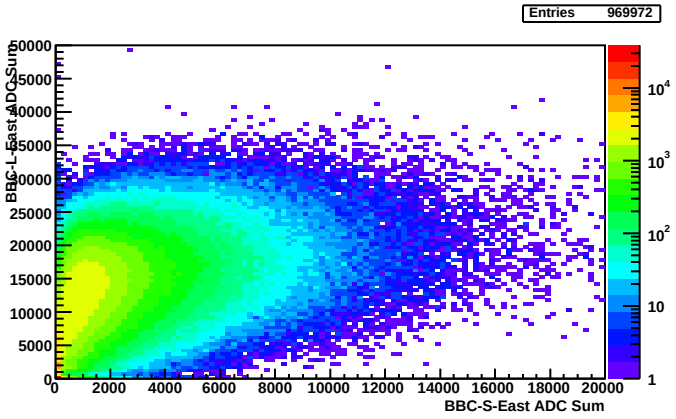

Entries 969972
TOF Mult
BBC-L-West ADC Sum

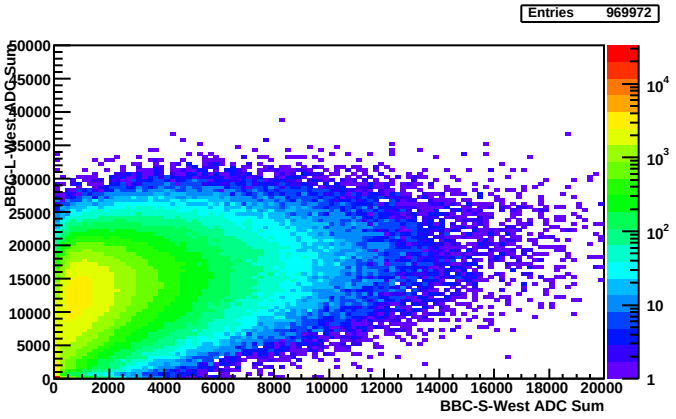

Entries 969972
TOF Mult
BBC-S-East ADC Sum

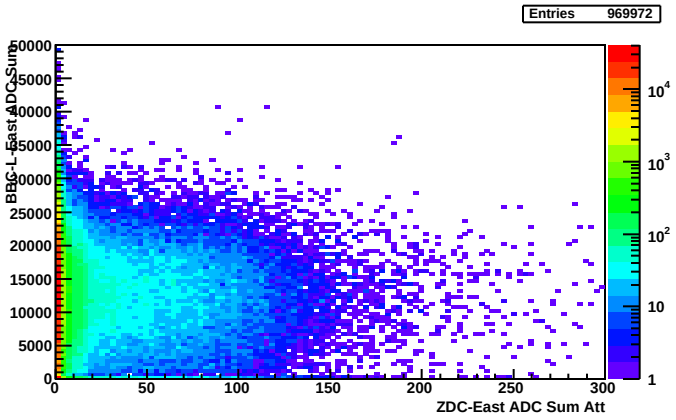

Entries 969972
TOF Mult
BBC-S-West ADC Sum

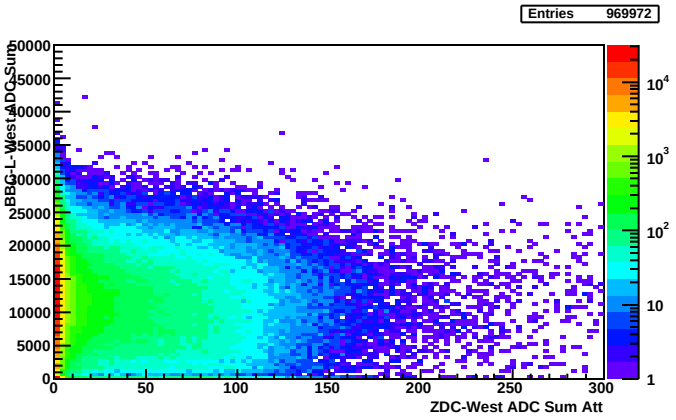

Entries 969972
TOF Mult
ZDC-East ADC Sum Att

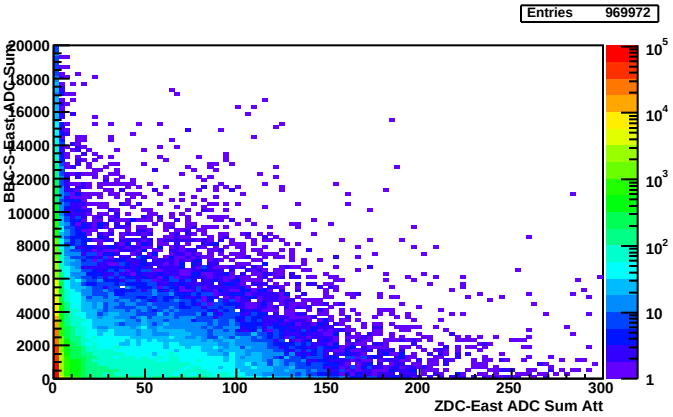

Entries 969972
TOF Mult
ZDC-West ADC Sum Att

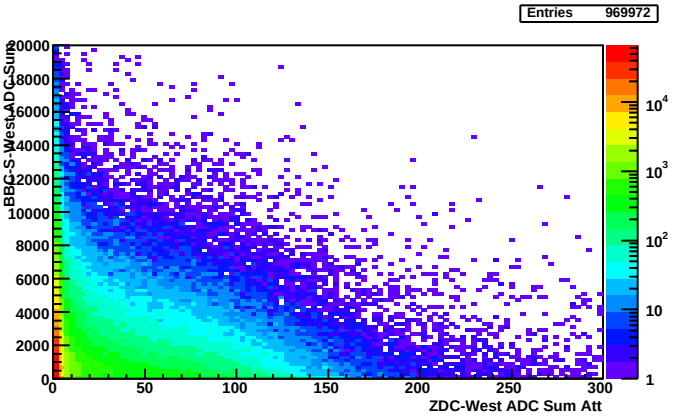

Entries 969972
BBC-L-East ADC Sum
BBC-S-East ADC Sum

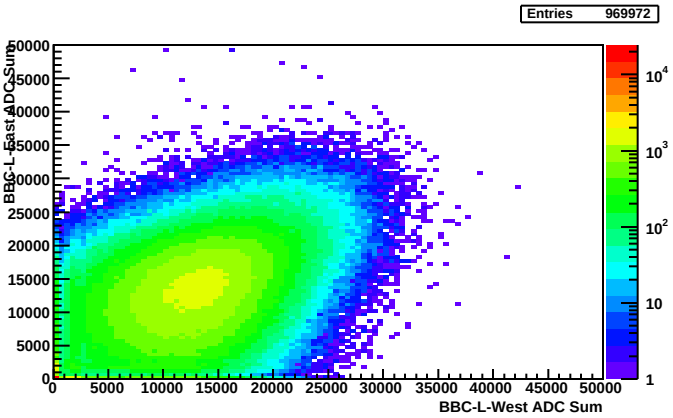

Entries 969972
BBC-L-West ADC Sum
BBC-S-West ADC Sum

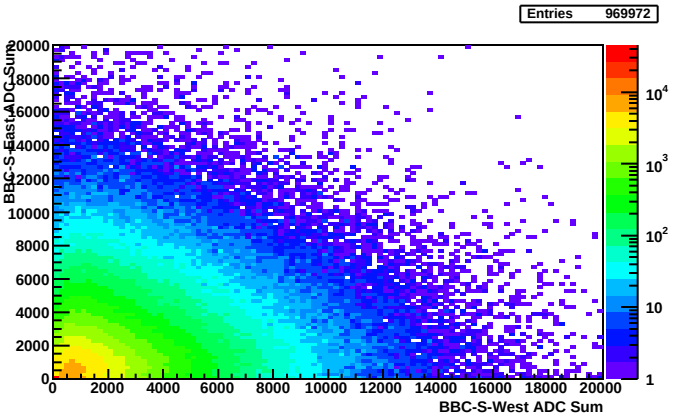

Entries 969972
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

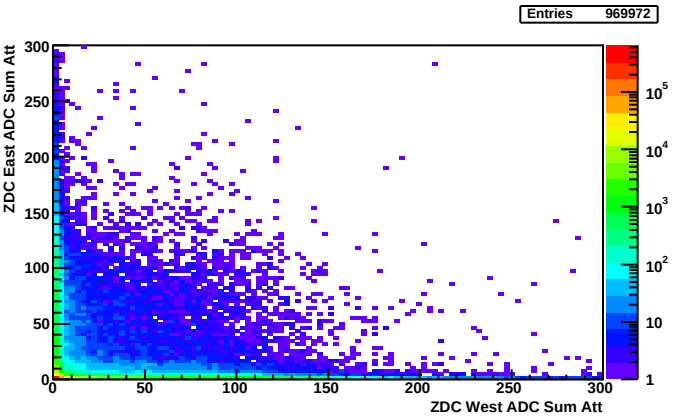

Entries 969972
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

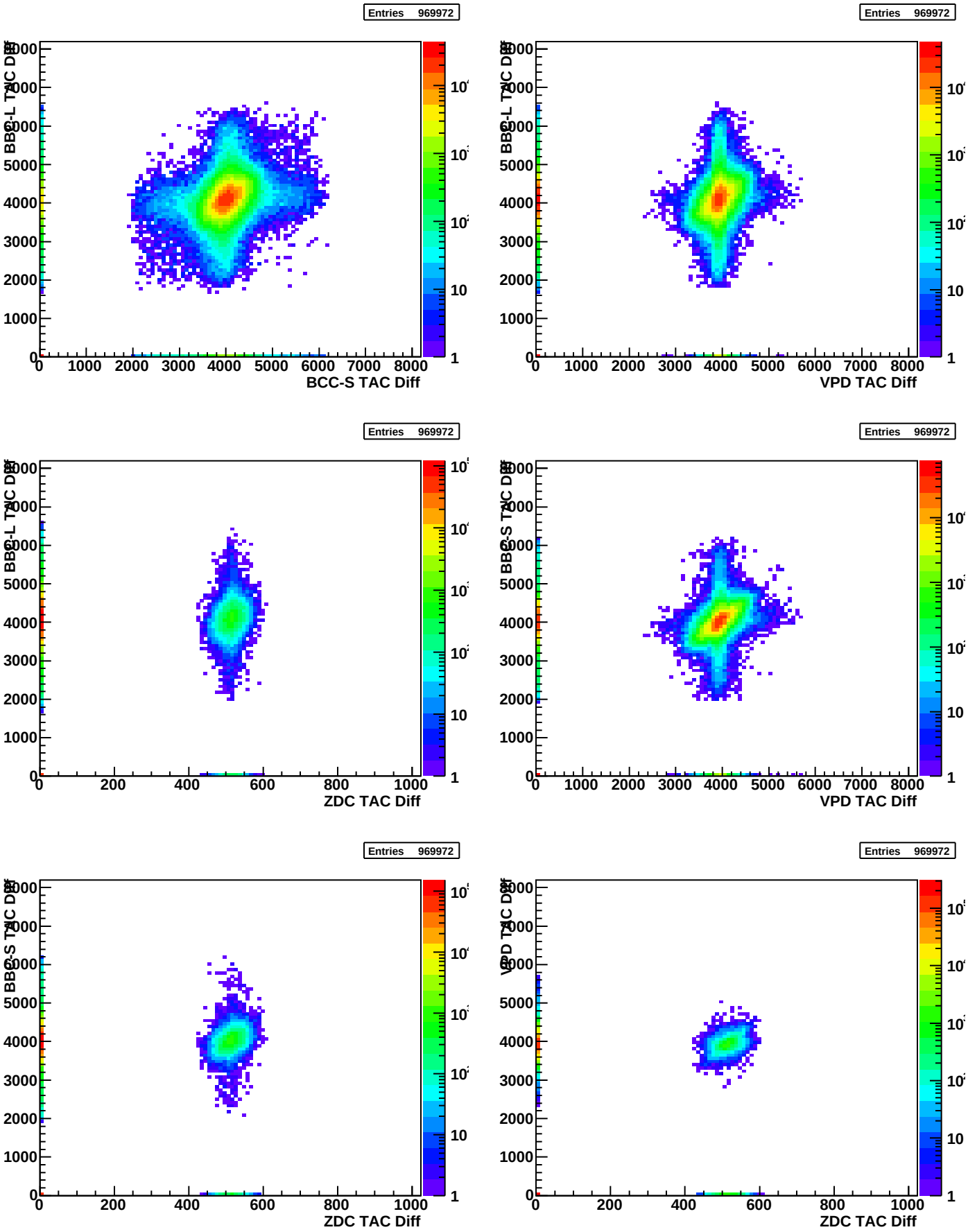

Entries 969972
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

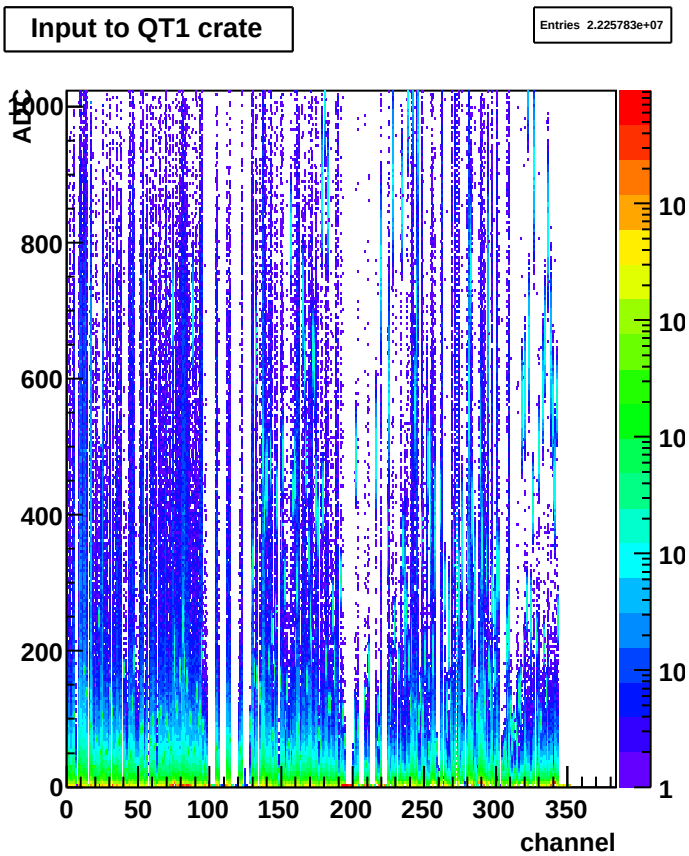

Entries 969972
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

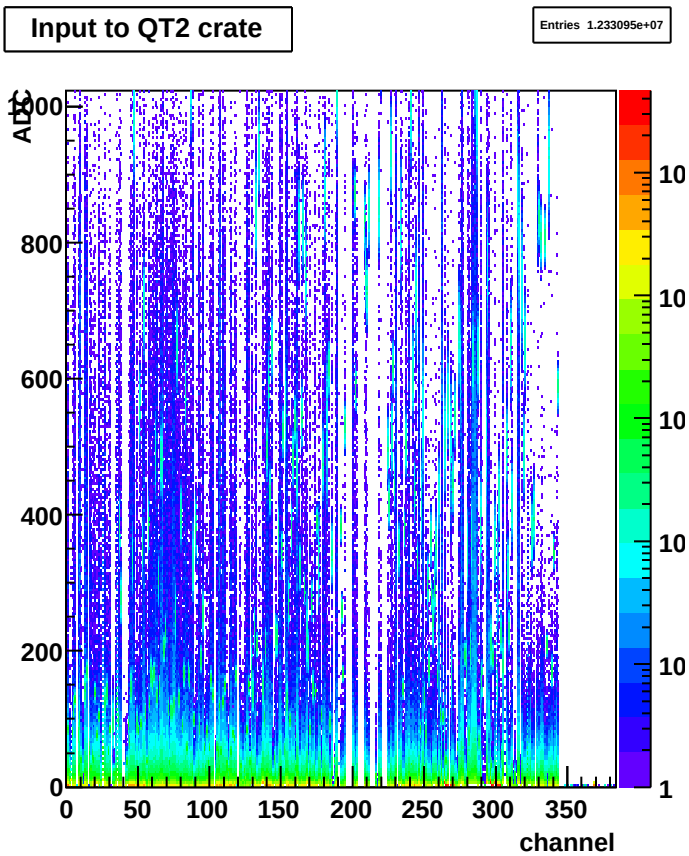

Entries 969972
BBC-L-East ADC Sum
BBC-L-West ADC Sum

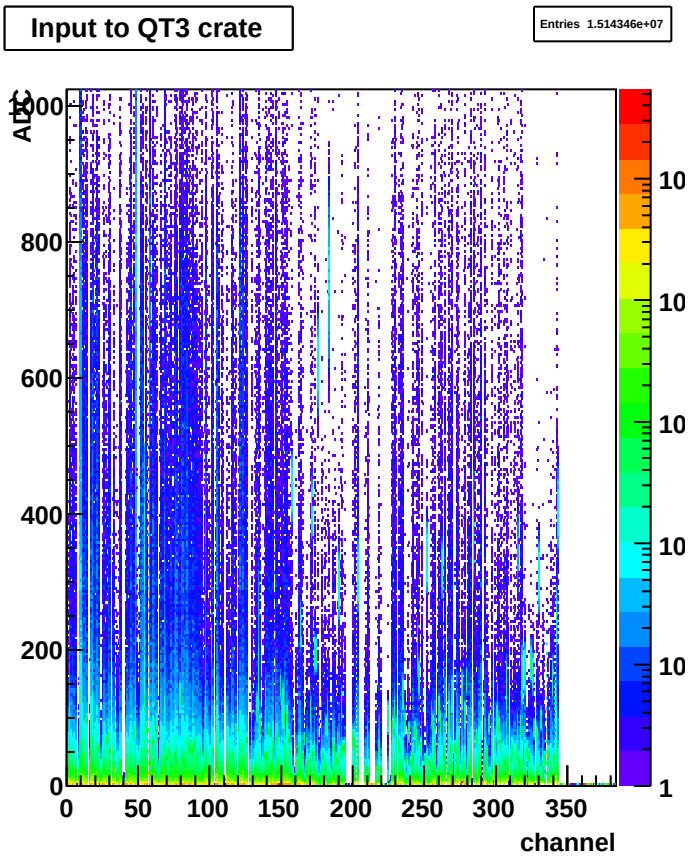

Entries 969972
BBC-S-East ADC Sum
BBC-S-West ADC Sum

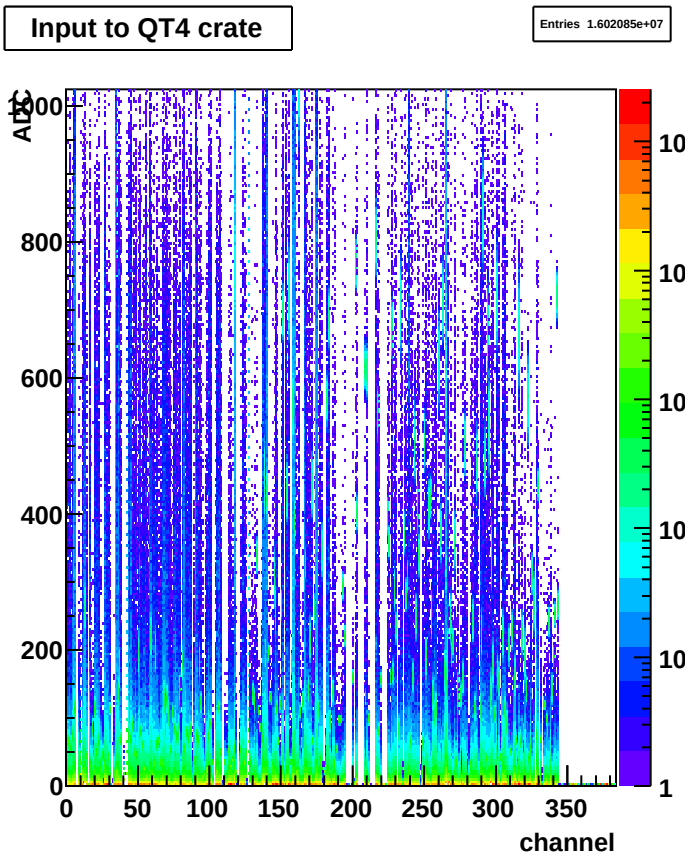

Entries 969972
ZDC East ADC Sum Att
ZDC West ADC Sum Att

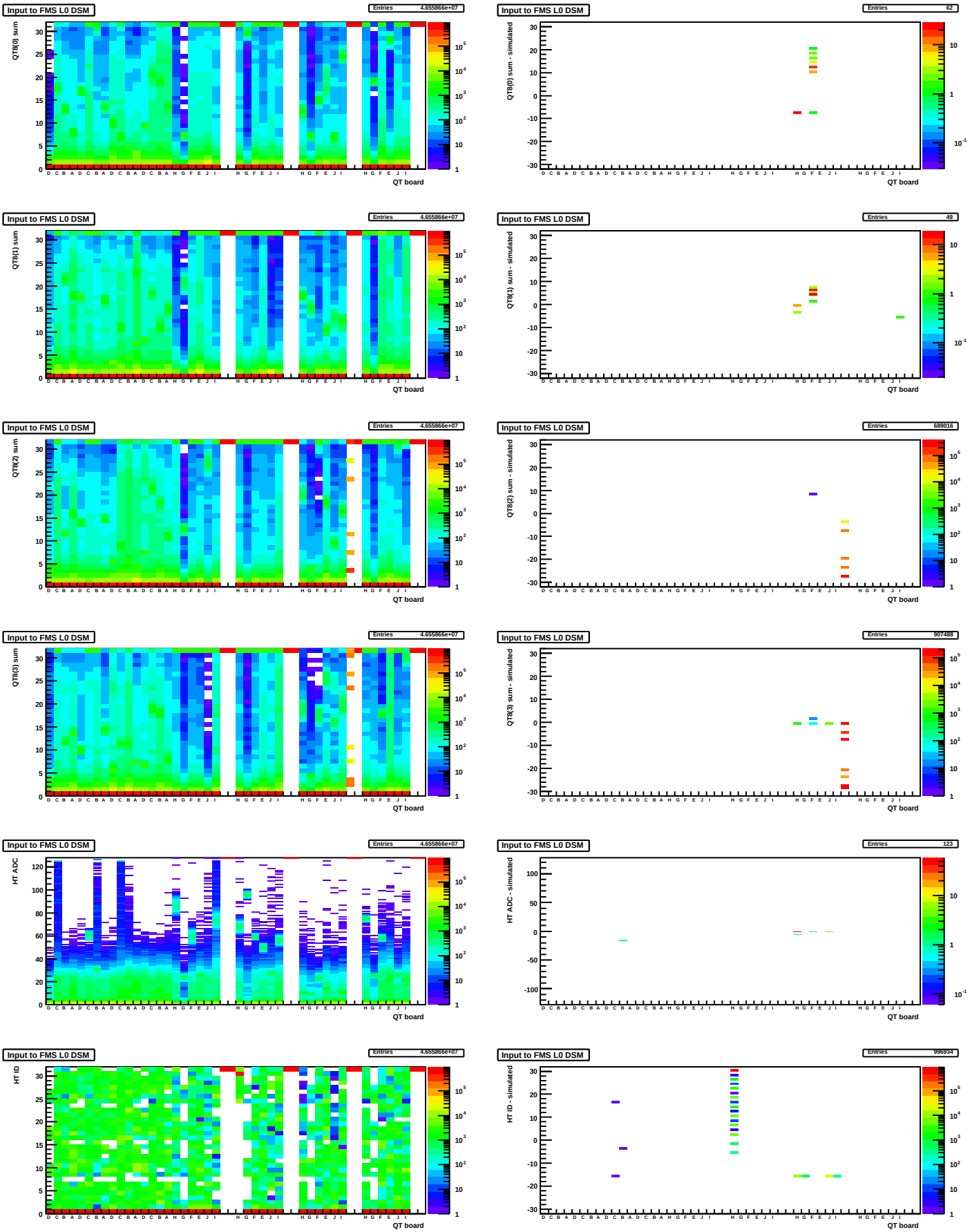

Entries 969972
BBC-L TAC Diff
BCC-S TAC Diff
Entries 969972
BBC-L TAC Diff
VPD TAC Diff
Entries 969972
BBC-L TAC Diff
ZDC TAC Diff
Entries 969972
BBC-S TAC Diff
VPD TAC Diff
Entries 969972
BBC-S TAC Diff
ZDC TAC Diff
Entries 969972
VPD TAC Diff
ZDC TAC Diff

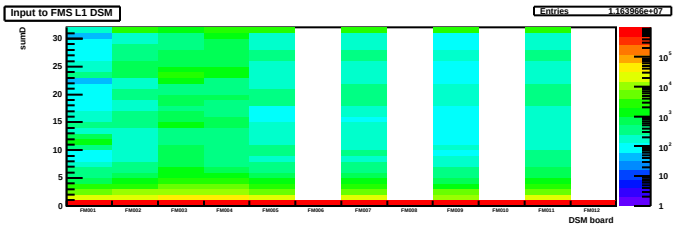

Input to QT1 crate
Entries 2.225783e+07
ADC
channel

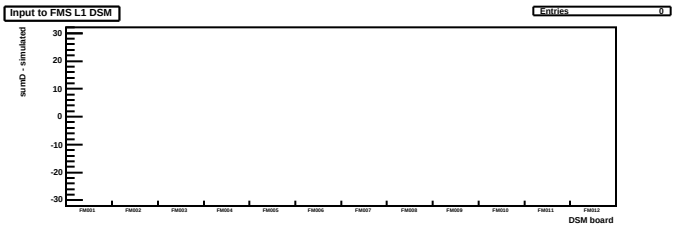

Input to QT2 crate
Entries 1.233095e+07
ADC
channel

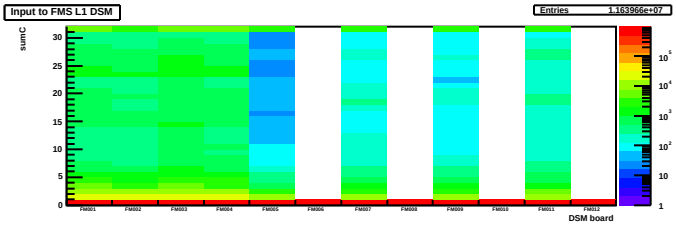

Input to QT3 crate
Entries 1.514346e+07
ADC
channel

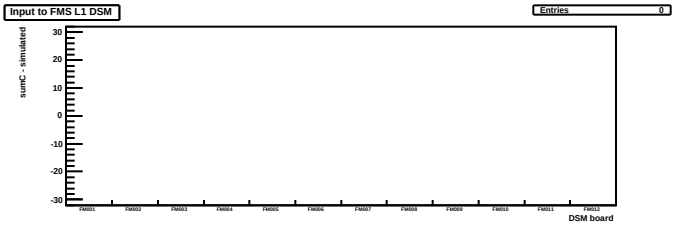

Input to QT4 crate
Entries 1.602085e+07
ADC
channel

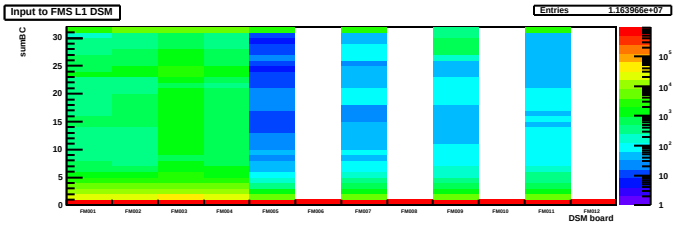

Input to FMS L0 DSM
Entries 4.655866e+07
QT8(0) sum
QT8(1) sum
QT8(2) sum
QT8(3) sum
HT ADC
HT ID
Entries 62
Entries 49
Entries 689016
Entries 907488
Entries 123
Entries 996934
QT8(0) sum - simulated
QT8(1) sum - simulated
QT8(2) sum - simulated
QT8(3) sum - simulated
HT ADC - simulated
HT ID - simulated
QT board

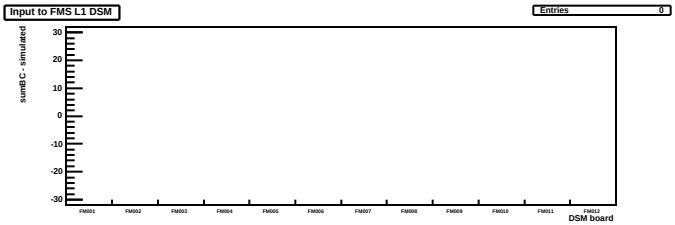

Input to FMS L1 DSM
Entries 1.163966e+07
sumD
DSM board

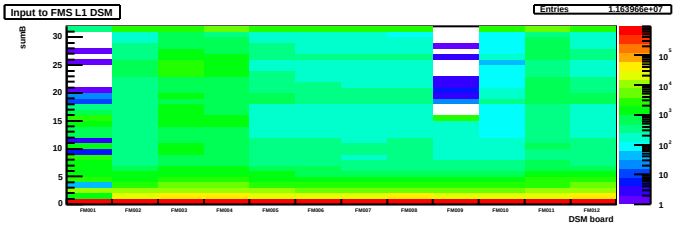

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

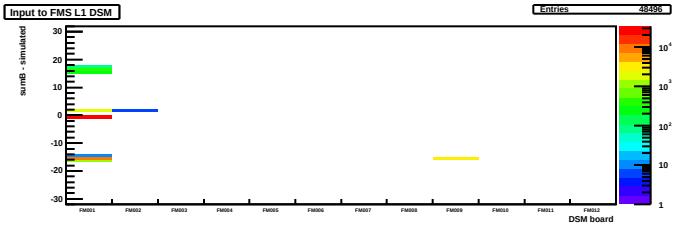

Input to FMS L1 DSM
Entries 1.163966e+07
sumC
DSM board

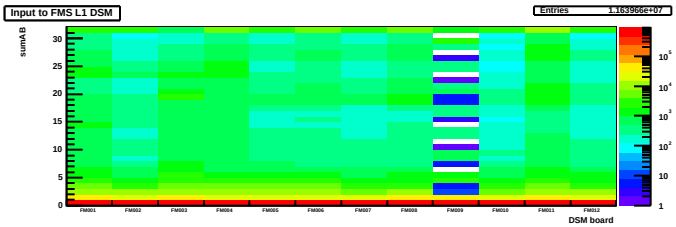

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

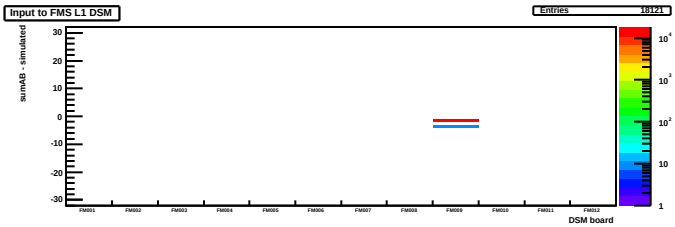

Input to FMS L1 DSM
Entries 1.163966e+07
sumBC
DSM board

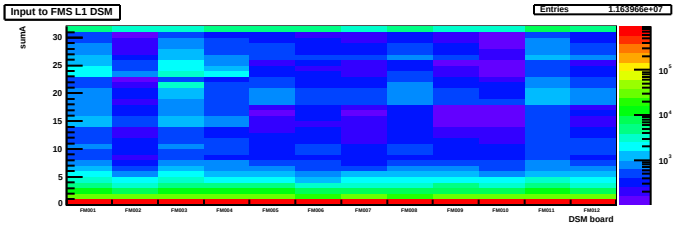

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

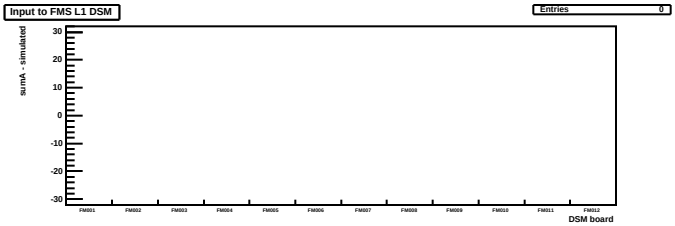

Input to FMS L1 DSM
Entries 1.163966e+07
sumB
DSM board

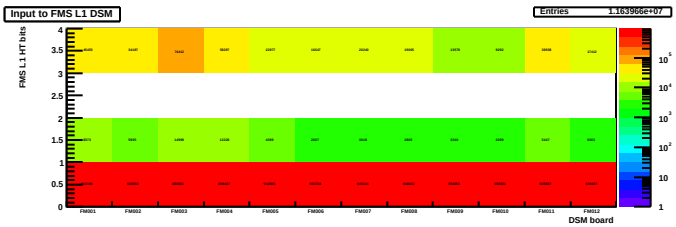

Input to FMS L1 DSM
Entries 48496
sumB - simulated
DSM board

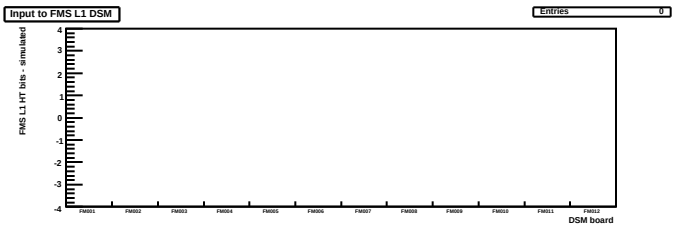

Input to FMS L1 DSM
Entries 1.163966e+07
sumAB
DSM board

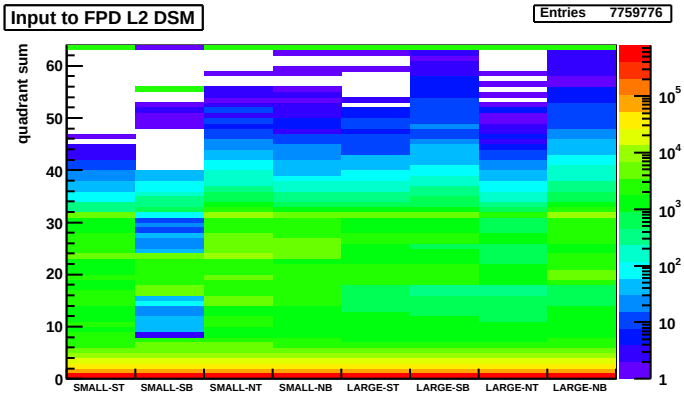

Input to FMS L1 DSM
Entries 18121
sumAB - simulated
DSM board

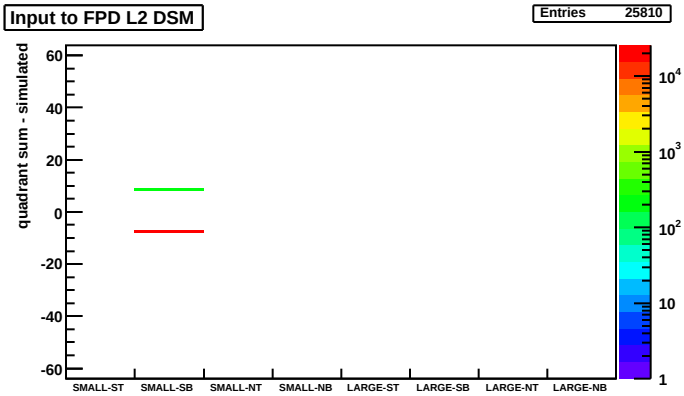

Input to FMS L1 DSM
Entries 1.163966e+07
sumA
DSM board

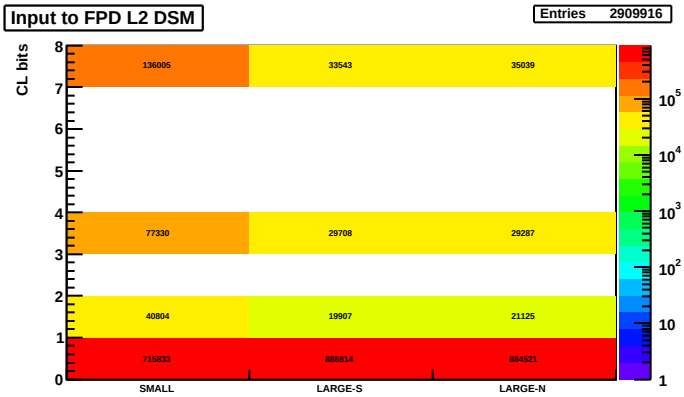

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

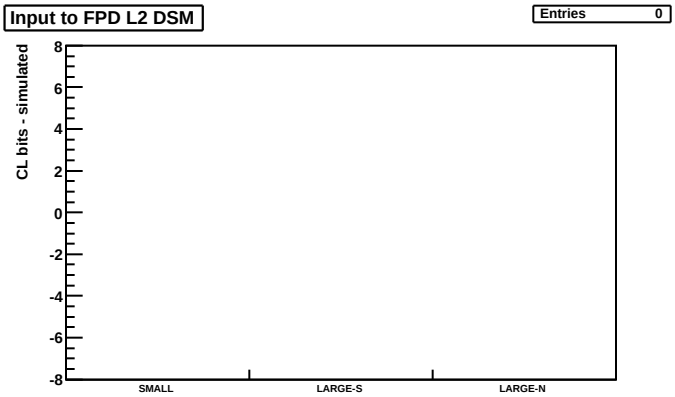

Input to FMS L1 DSM
Entries 1.163966e+07
FMS L1 HT bits
DSM board

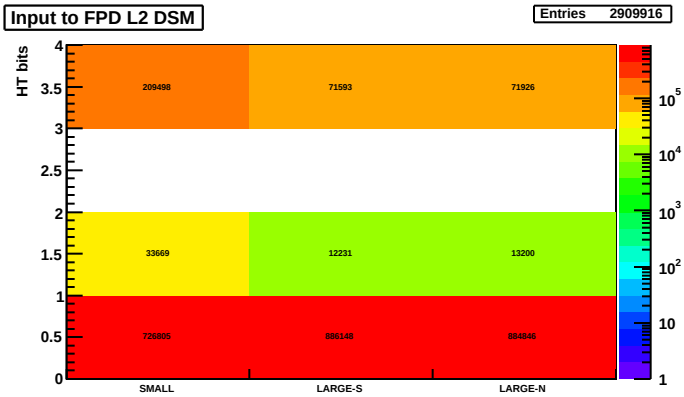

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

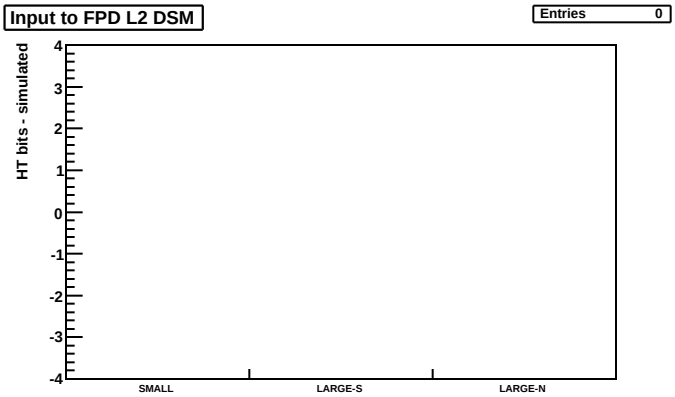

Input to FPD L2 DSM
Entries 7759776
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

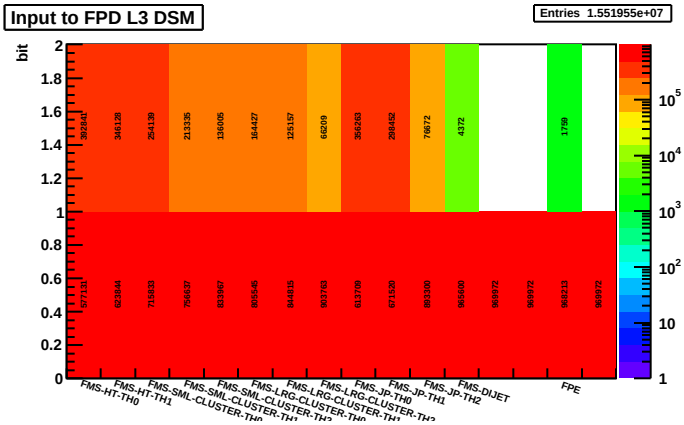

Input to FPD L2 DSM
Entries 25810
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

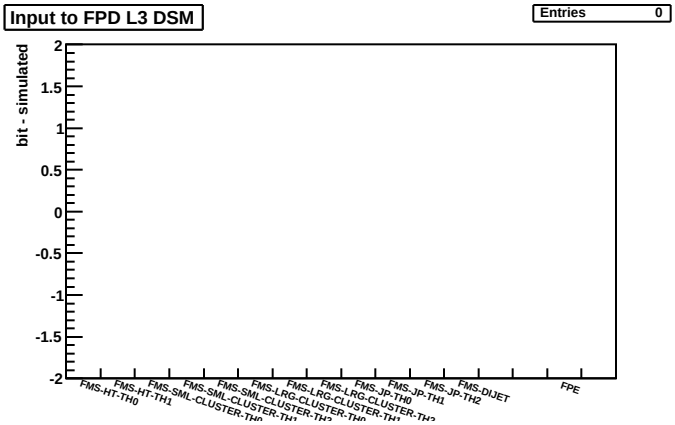

Input to FPD L2 DSM
Entries 2909916
CL bits
136005
33543
35039
77330
29708
29287
40804
19907
21125
715833
886814
884521
SMALL
LARGE-S
LARGE-N

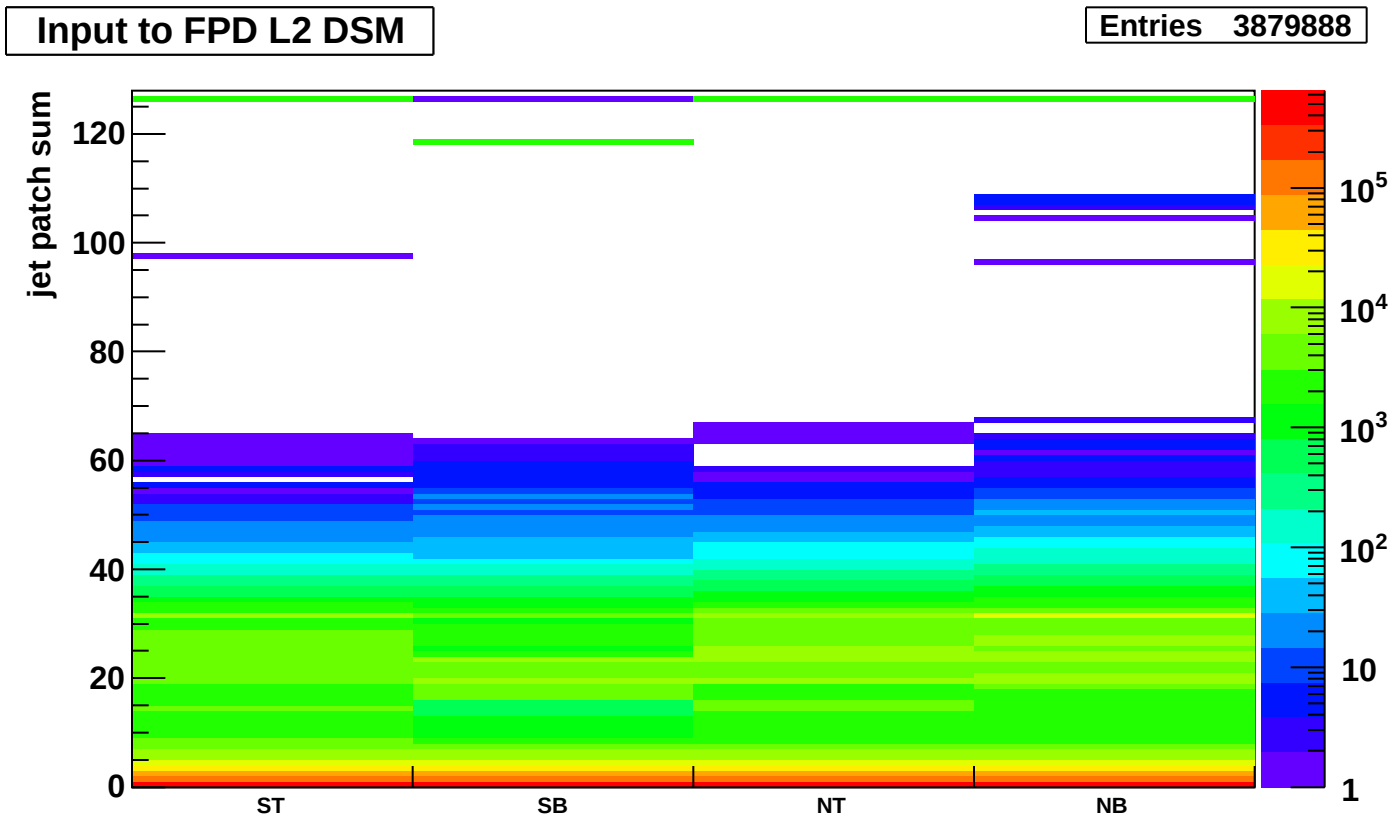

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

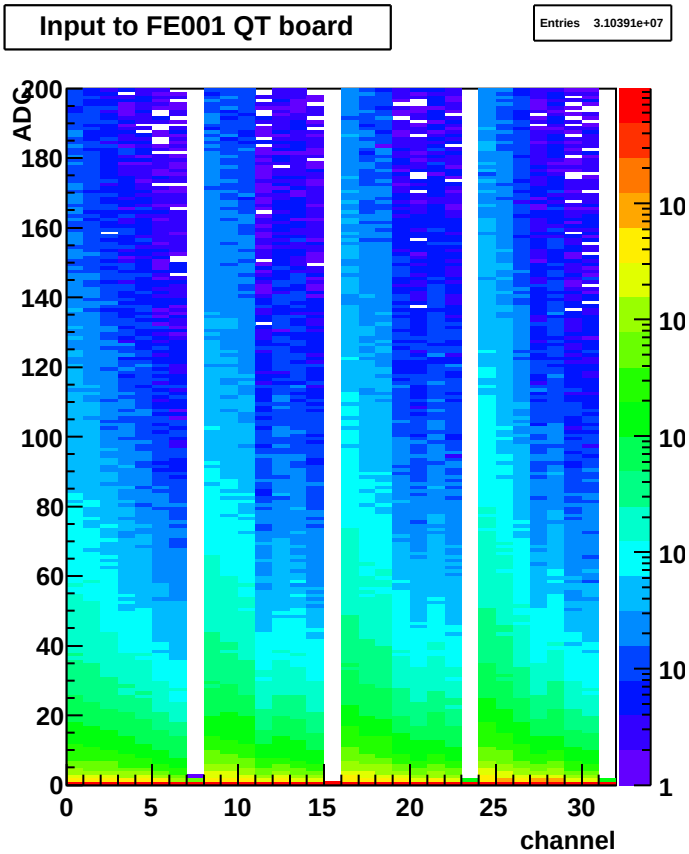

Input to FPD L2 DSM
Entries 2909916
HT bits
209498
71593
71926
33669
12231
13200
726805
886148
884846
SMALL
LARGE-S
LARGE-N

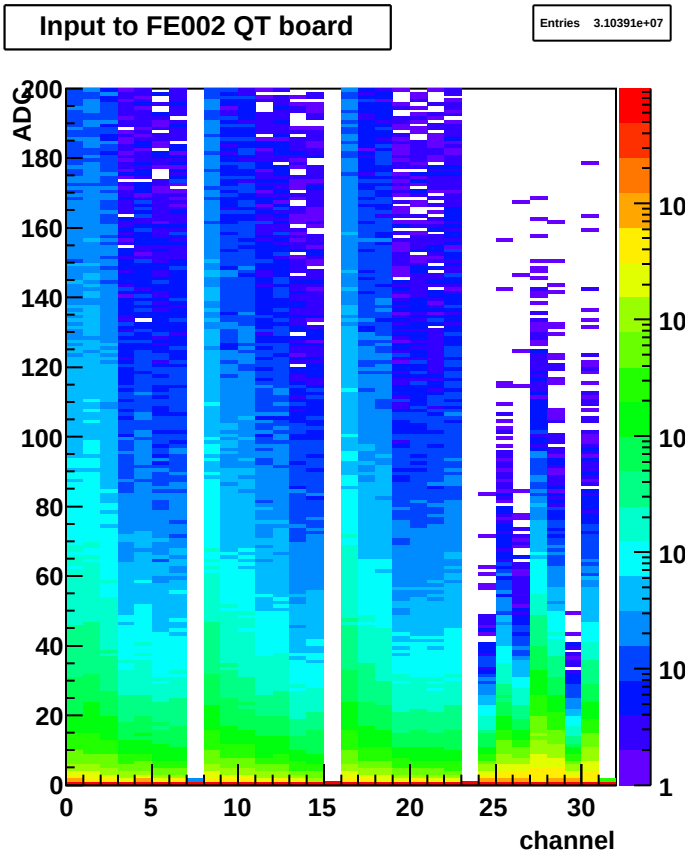

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

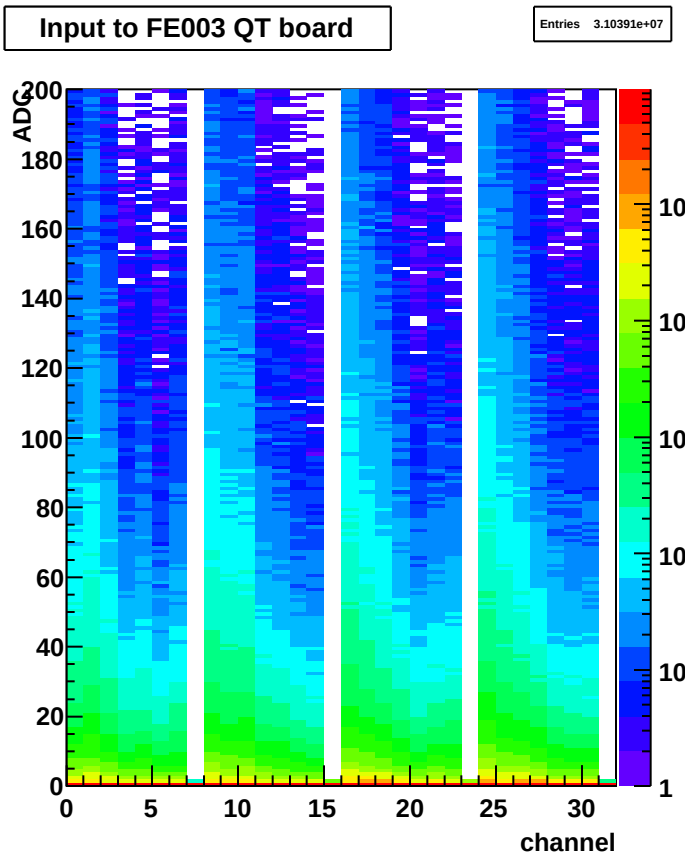

Input to FPD L3 DSM
Entries 1.551955e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

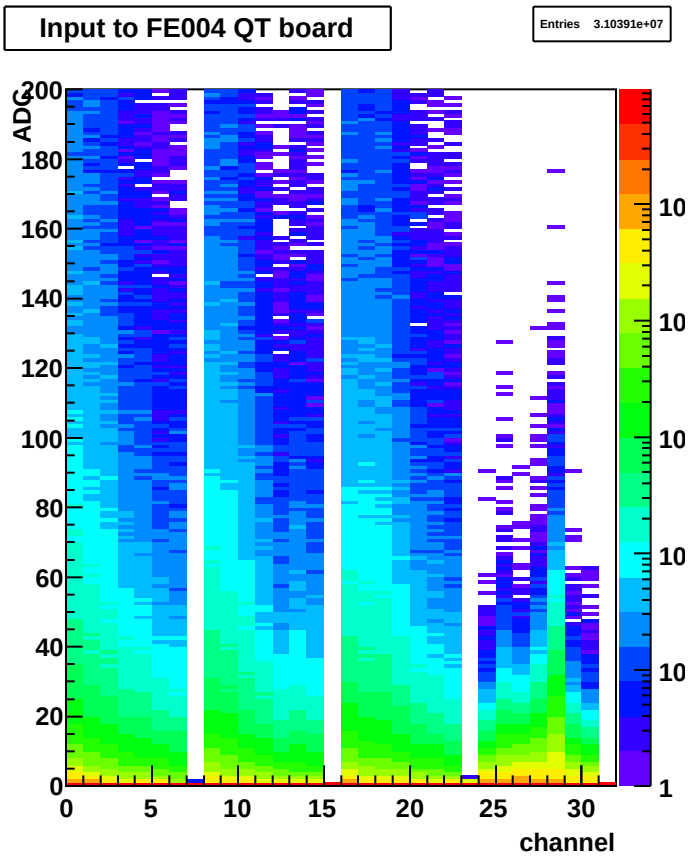

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

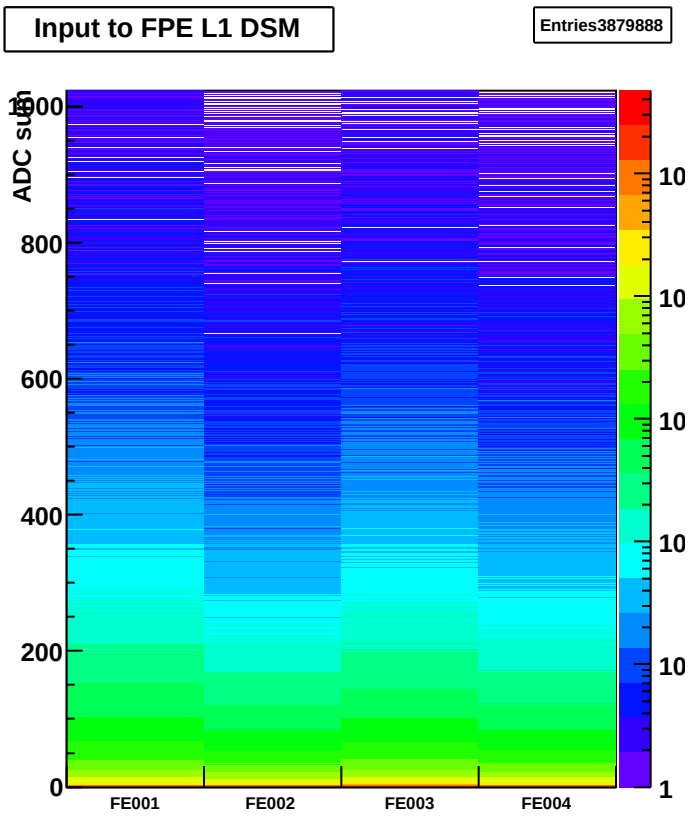

Input to FPD L2 DSM
Entries 3879888
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

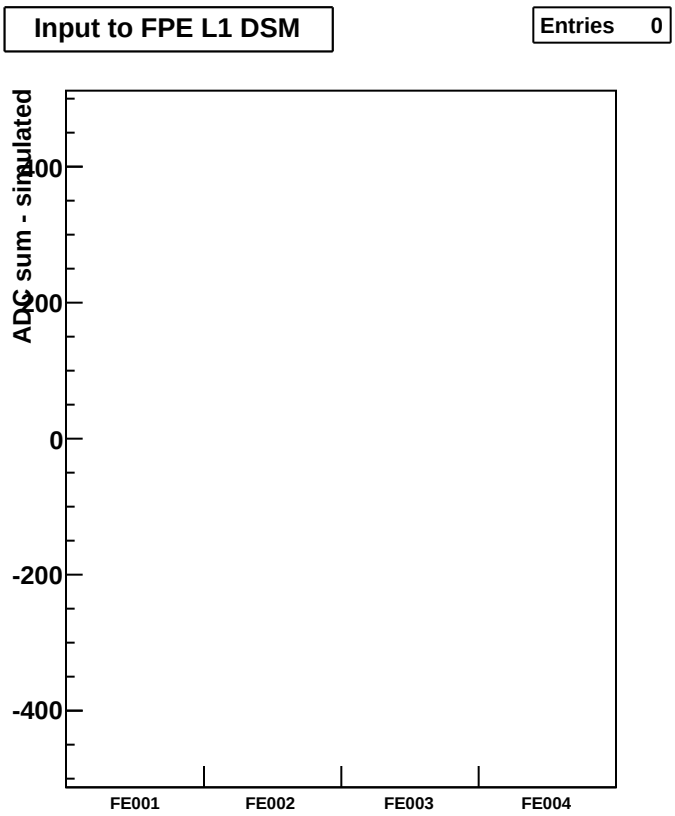

Input to FE001 QT board
Entries 3.10391e+07
ADC
channel

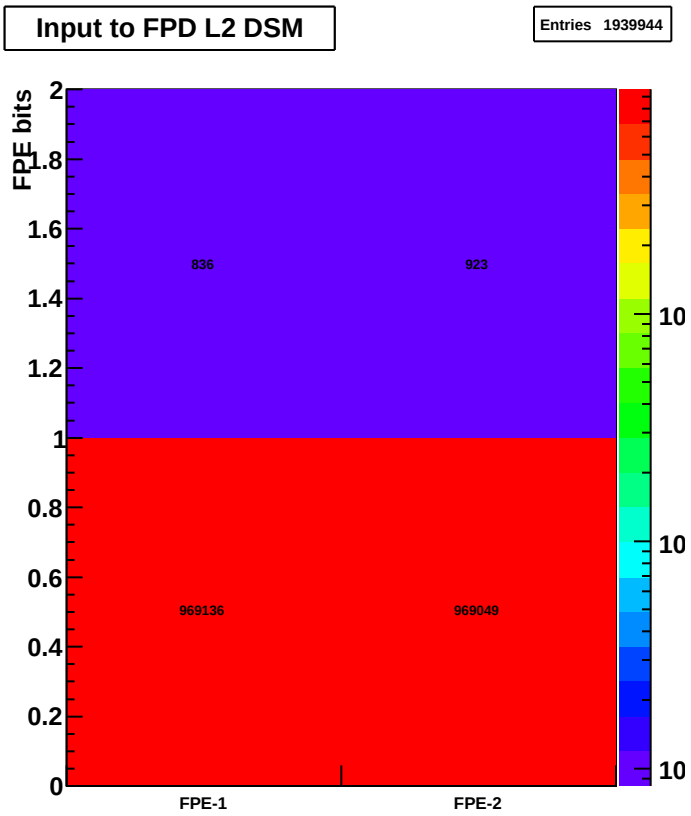

Input to FE002 QT board
Entries 3.10391e+07
ADC
channel

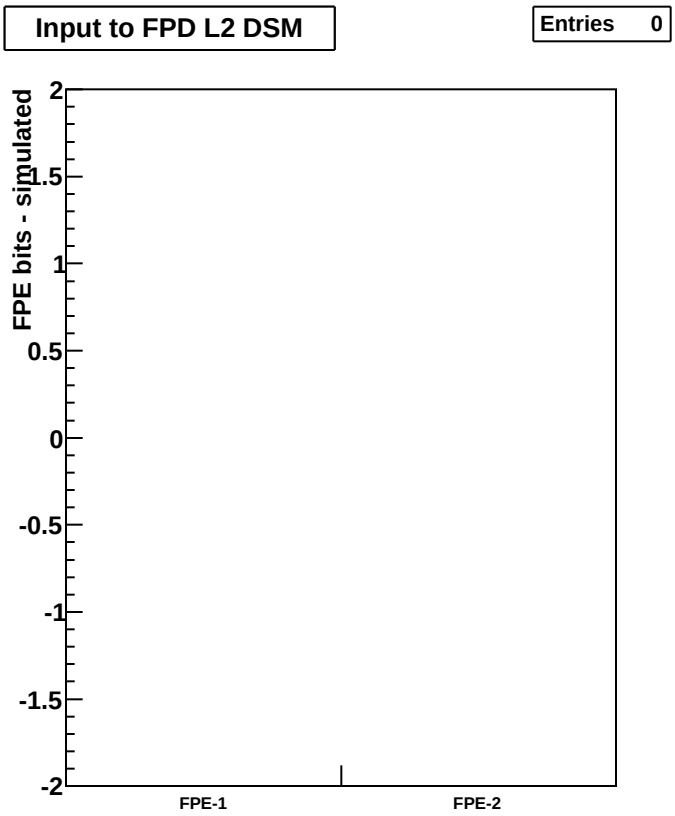

Input to FE003 QT board
Entries 3.10391e+07
ADC
channel


Input to FE004 QT board
Entries 3.10391e+07
ADC
channel


Input to FPE L1 DSM
Entries3879888
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1939944
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
836
923
969136
969049
FPE-1
FPE-2
10
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2