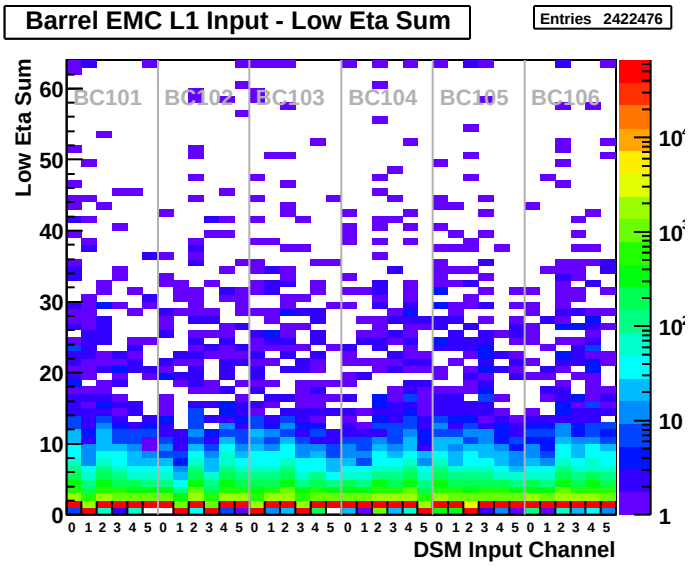
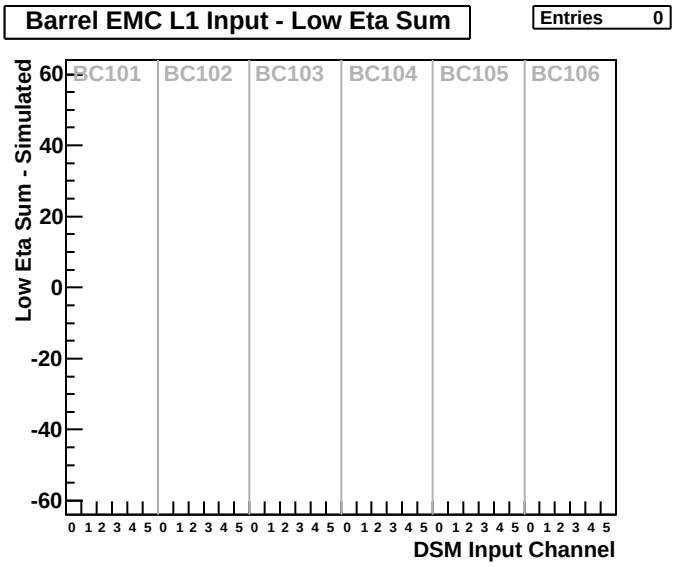
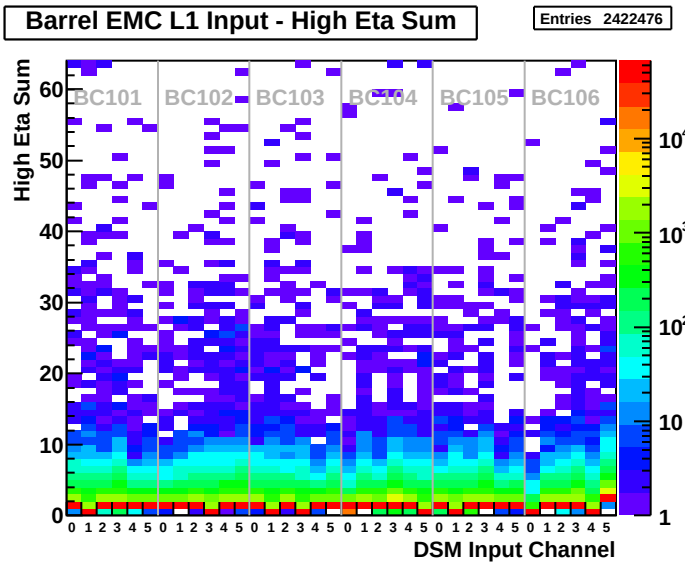
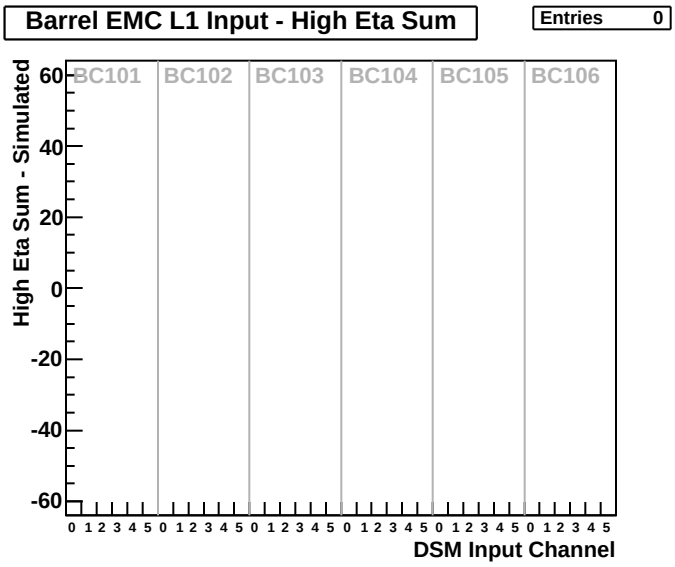
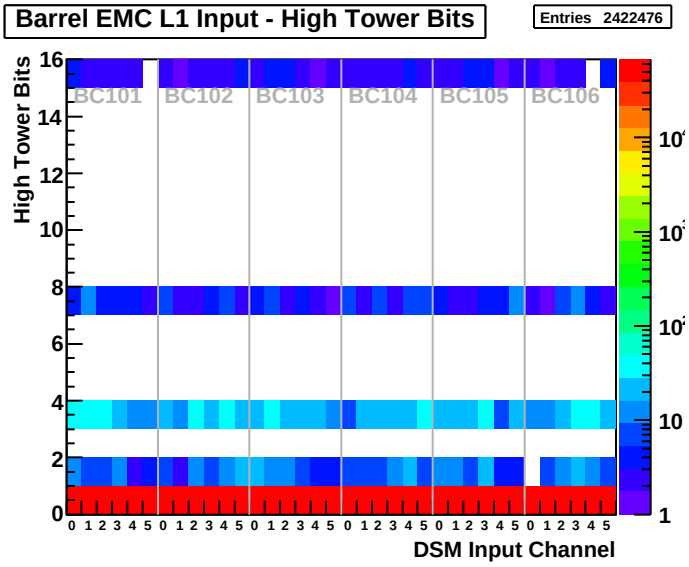
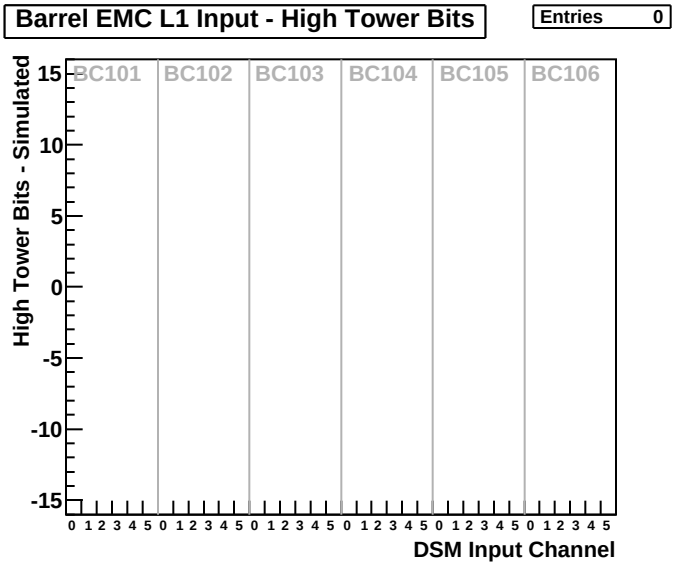

Barrel EMC L1 Input - Low Eta Sum
Entries 2422476
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

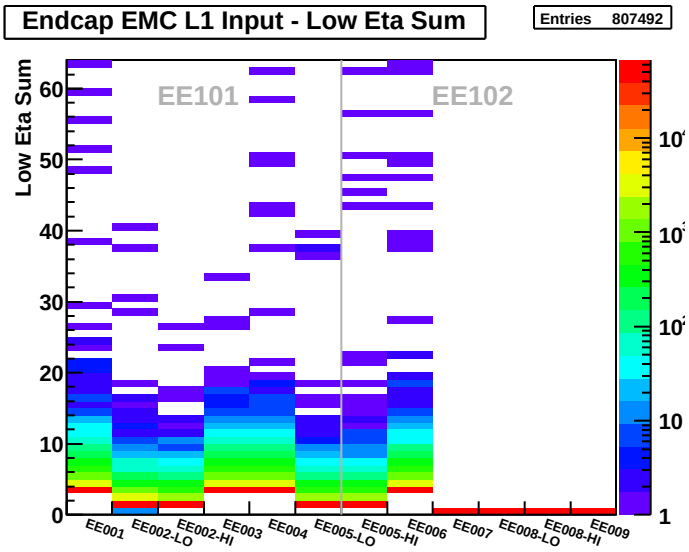

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

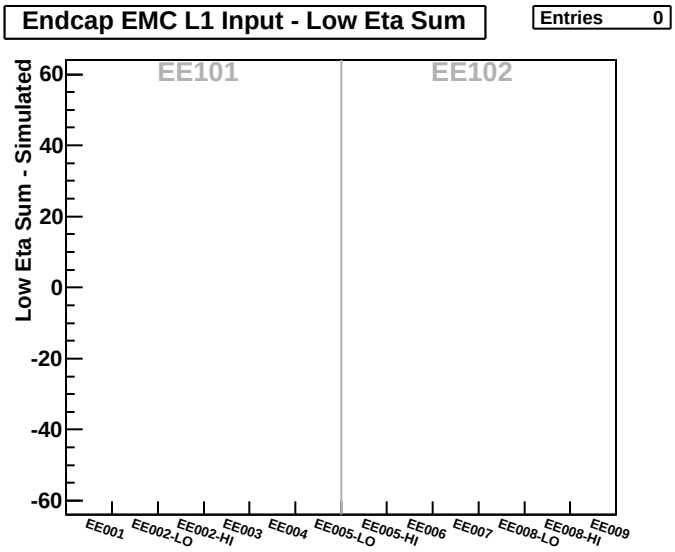

Barrel EMC L1 Input - High Eta Sum
Entries 2422476
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

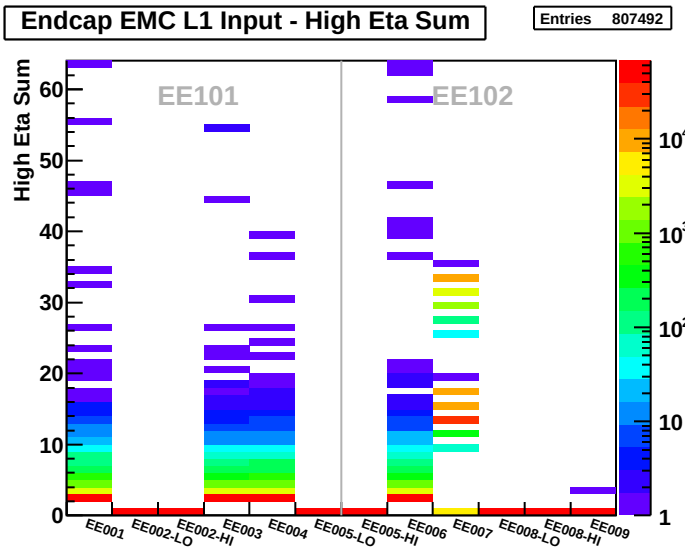

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

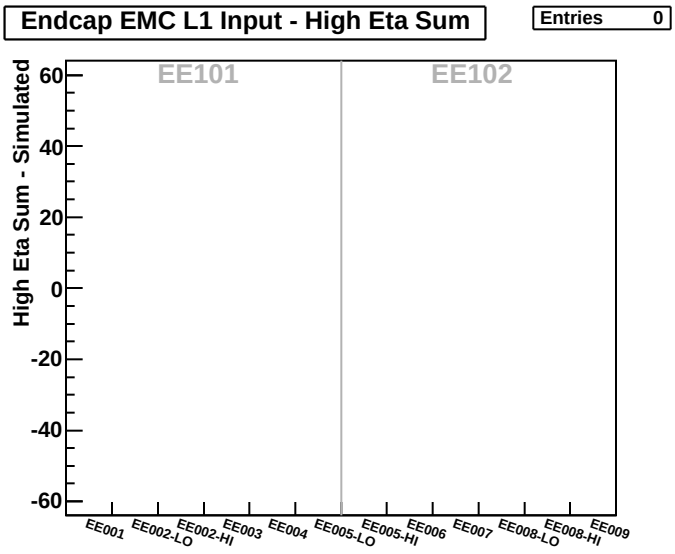

Barrel EMC L1 Input - High Tower Bits
Entries 2422476
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

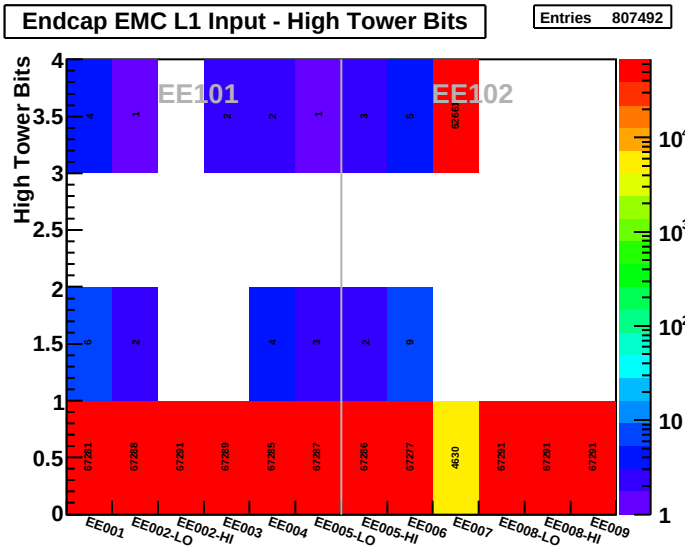

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

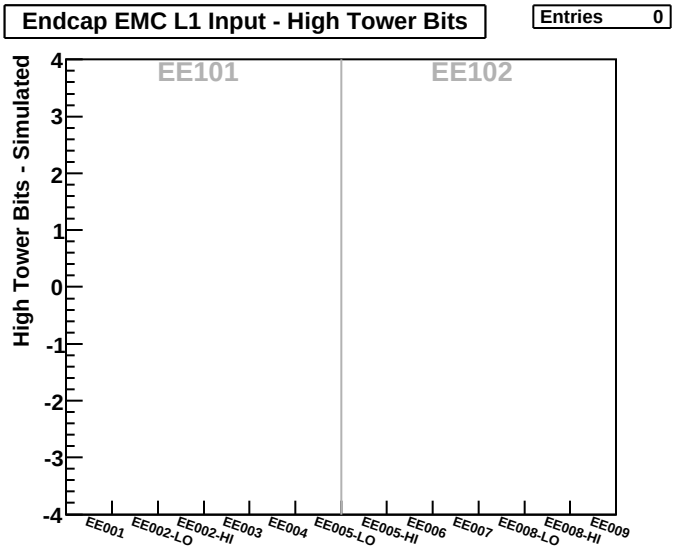

Endcap EMC L1 Input - Low Eta Sum
Entries 807492
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

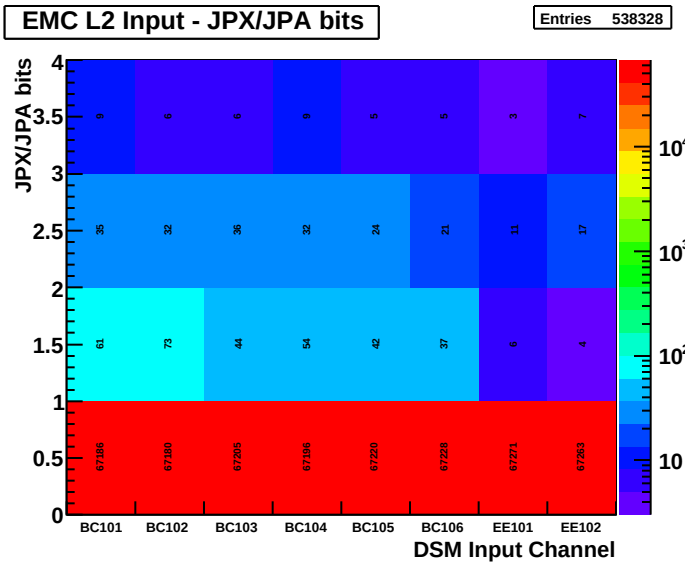

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

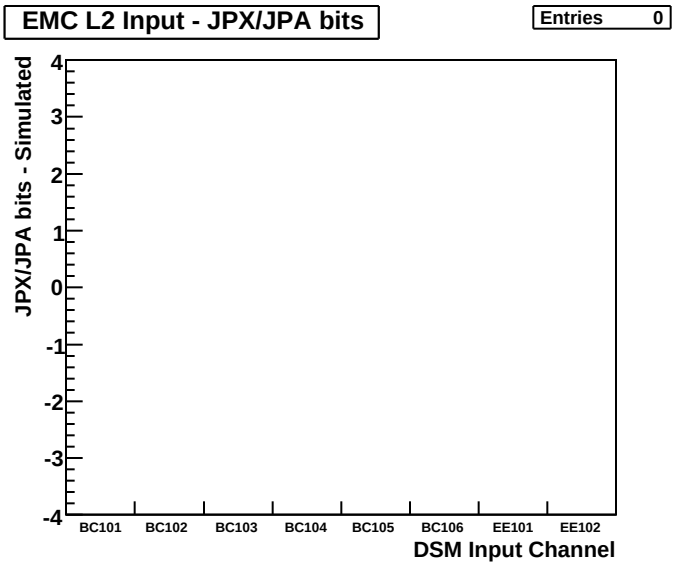

Endcap EMC L1 Input - High Eta Sum
Entries 807492
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

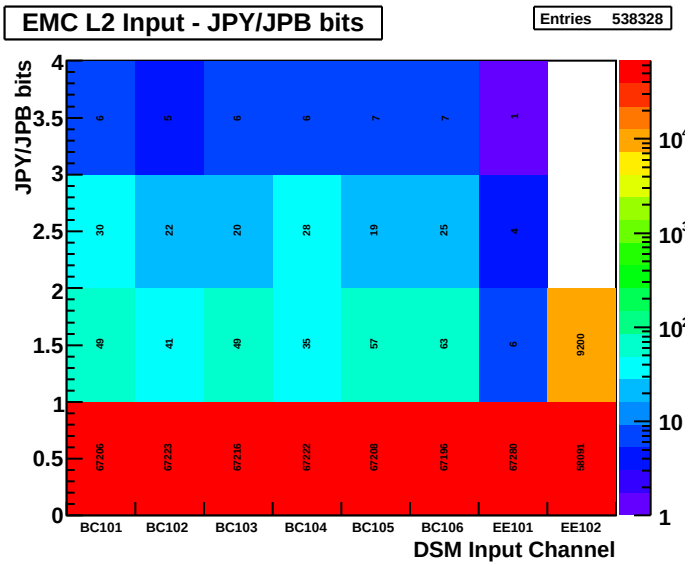

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

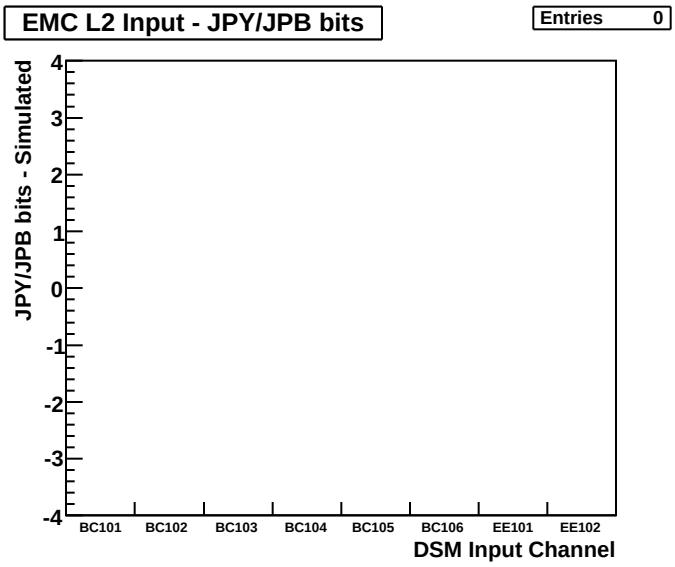

Endcap EMC L1 Input - High Tower Bits
Entries 807492
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

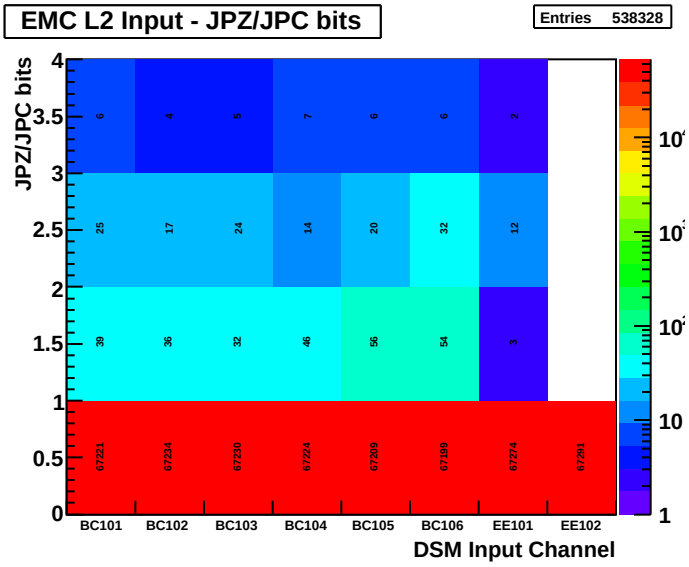

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

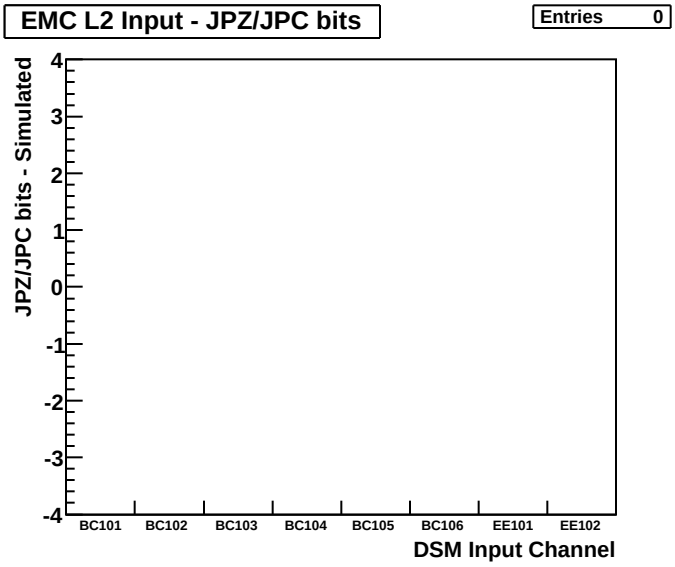

EMC L2 Input - JPX/JPA bits
Entries 538328
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
9 6 6 9 5 5 3 7
35 32 36 32 24 21 11 17
61 73 44 54 42 37 6 4
67186 67180 67205 67196 67220 67228 67271 67263

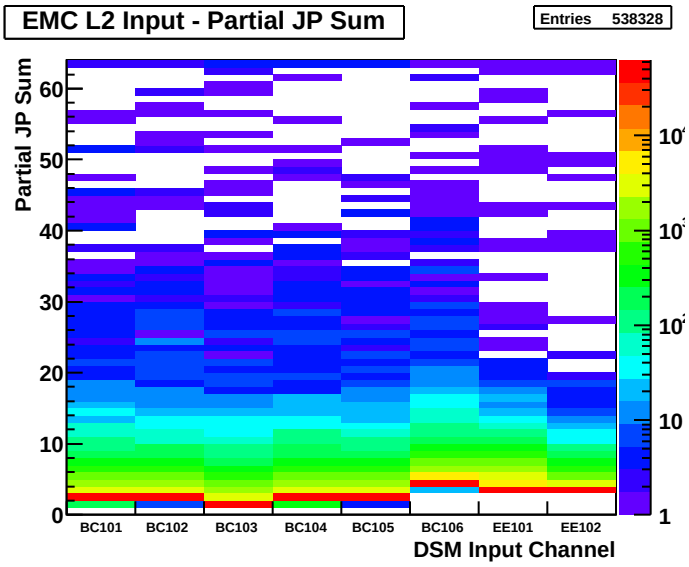

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

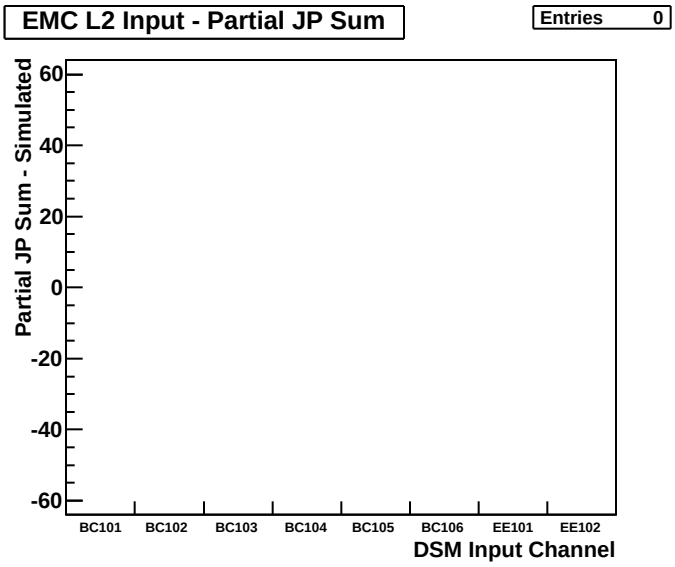

EMC L2 Input - JPY/JPB bits
Entries 538328
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6 5 6 6 7 7 1
30 22 20 28 19 25 4
49 41 49 35 57 63 6 9200
67206 67223 67216 67222 67208 67196 67280 58091

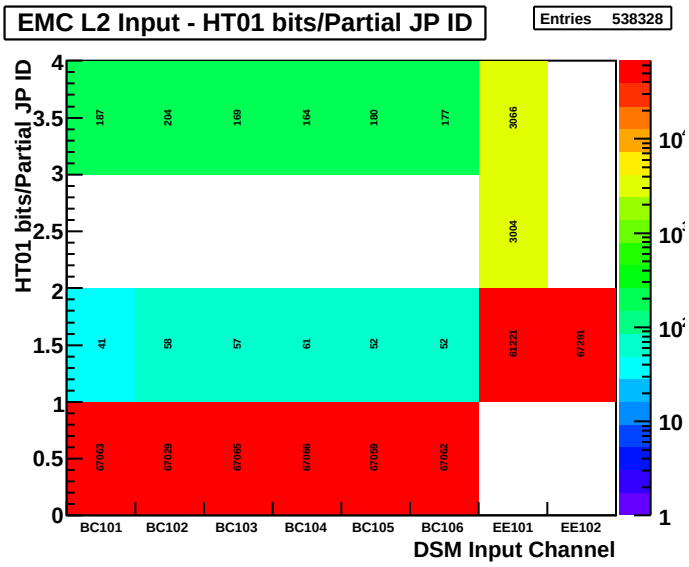

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

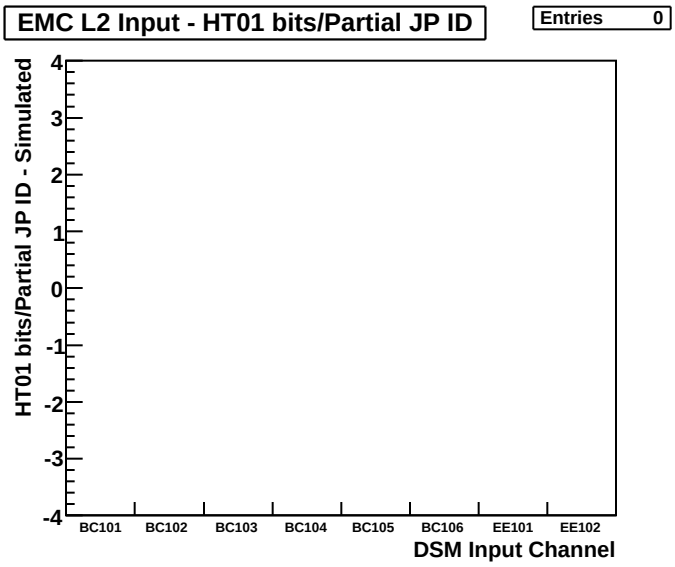

EMC L2 Input - JPZ/JPC bits
Entries 538328
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
6 4 5 7 6 6 2
25 17 24 14 20 32 12
39 36 32 46 56 54 3
67221 67234 67220 67224 67209 67199 67274 67291

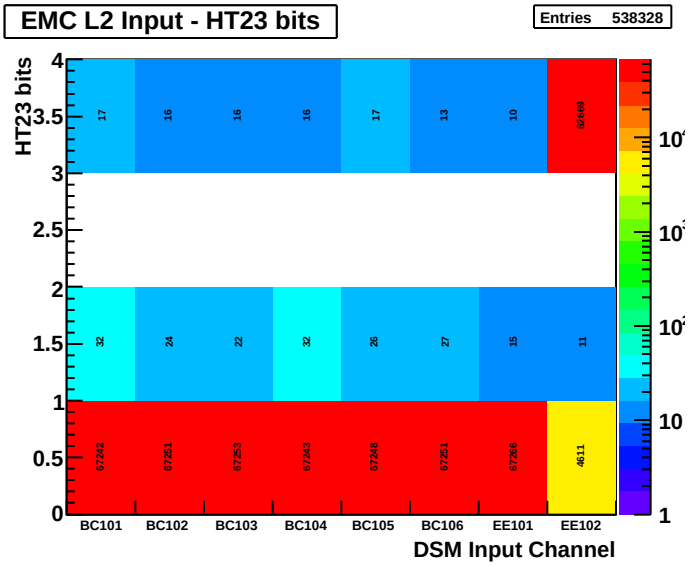

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

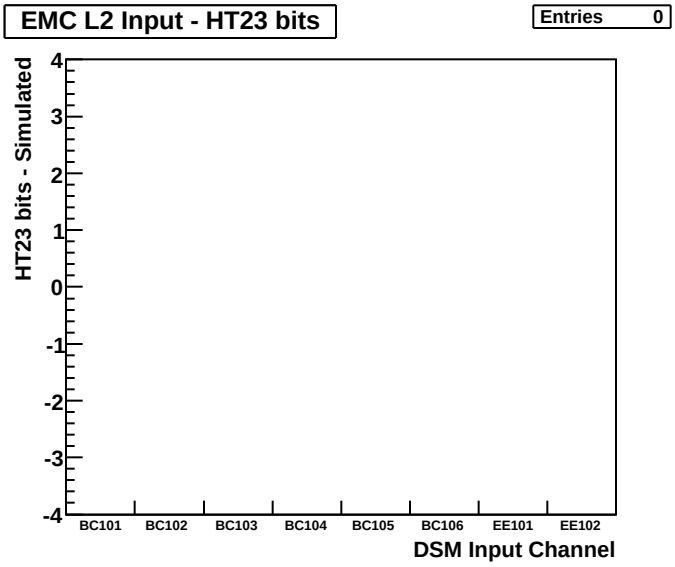

EMC L2 Input - Partial JP Sum
Entries 538328
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

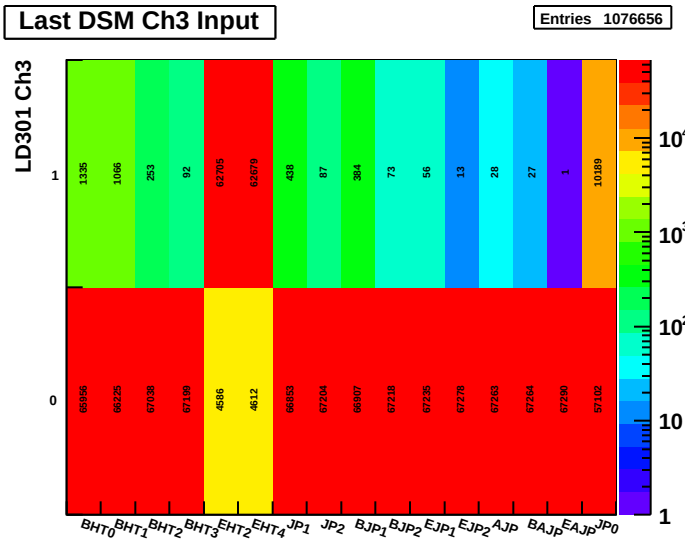

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

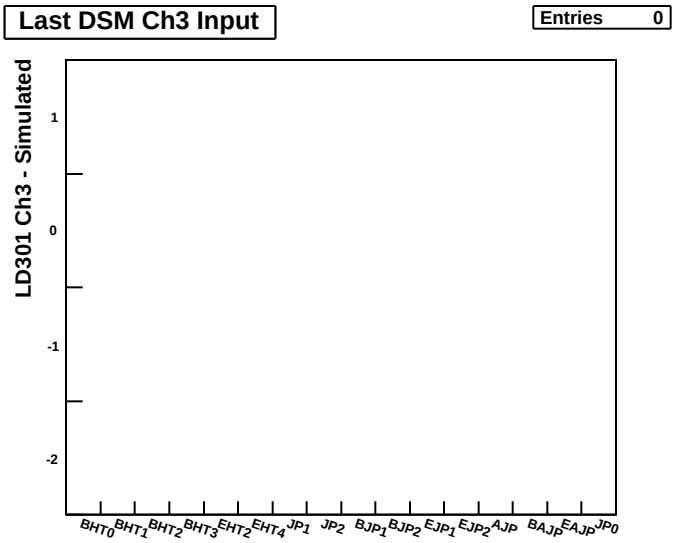

EMC L2 Input - HT01 bits/Partial JP ID
Entries 538328
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

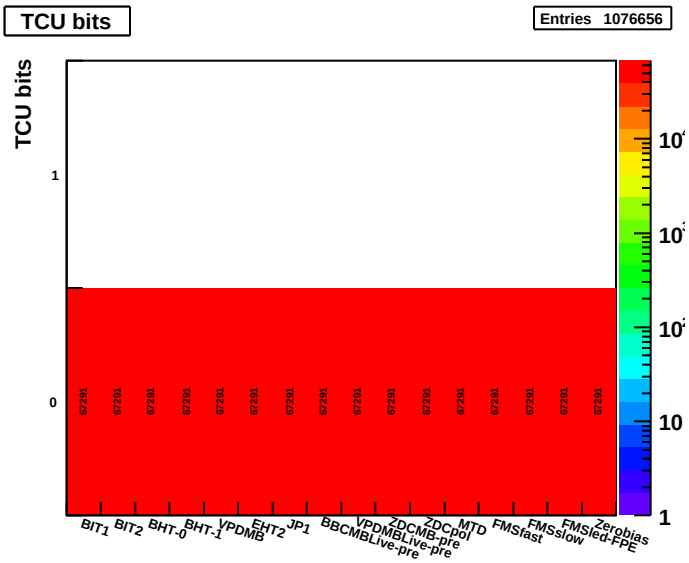

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

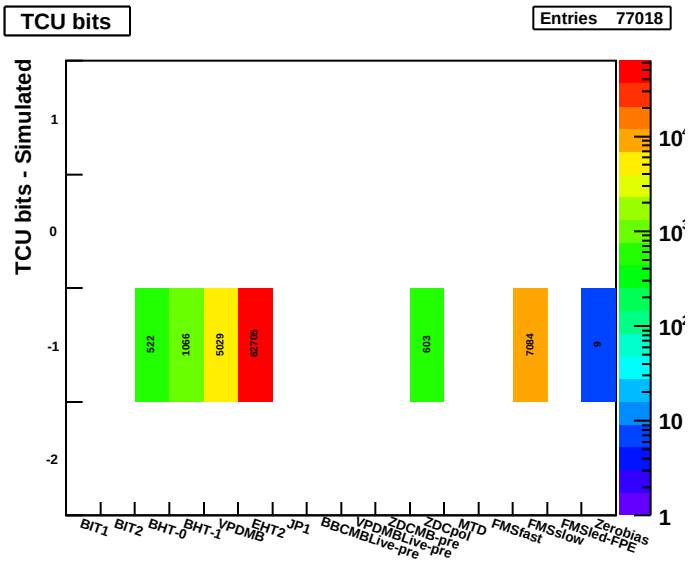

EMC L2 Input - HT23 bits
Entries 538328
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

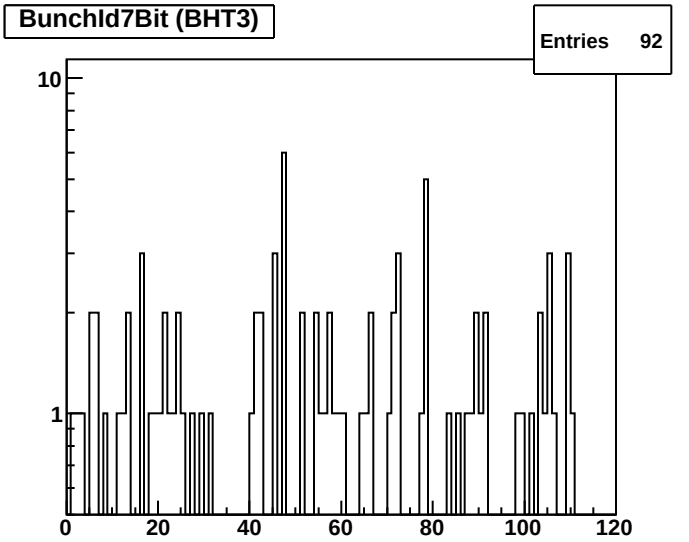

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

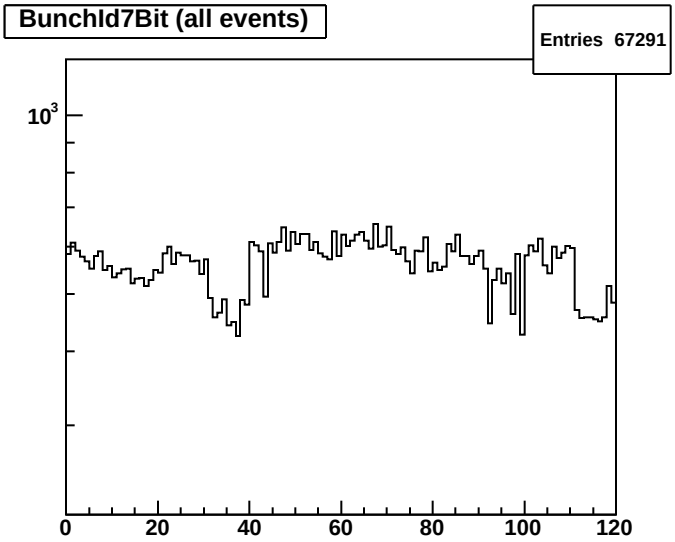

Last DSM Ch3 Input
Entries 1076656
LD301 Ch3
1335 1066 253 92 62705 62679 438 87 384 73 56 13 28 27 1 10189
65956 66225 67038 67199 4586 4612 66853 67204 66907 67218 67235 67278 67263 67264 67290 57102
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

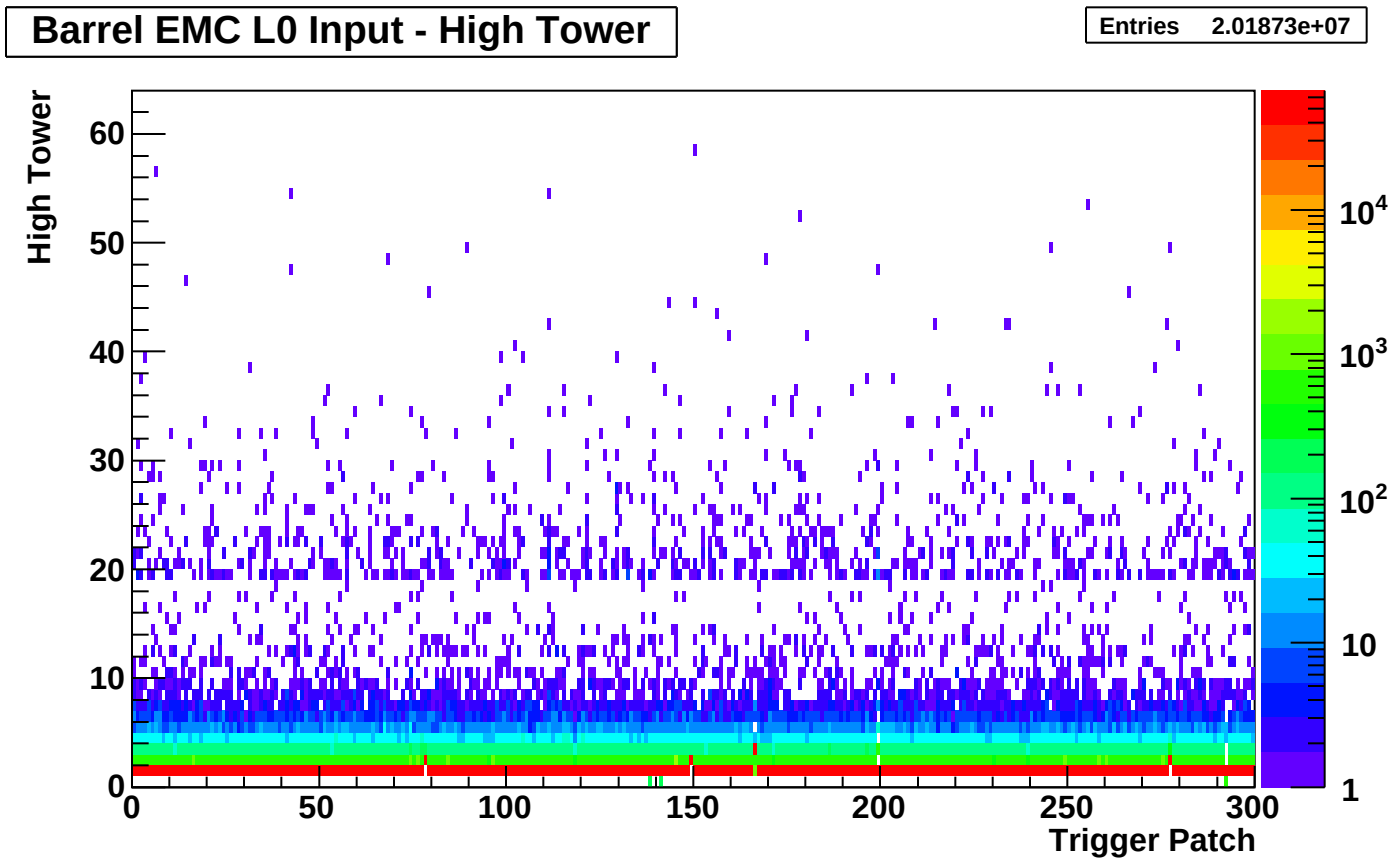

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

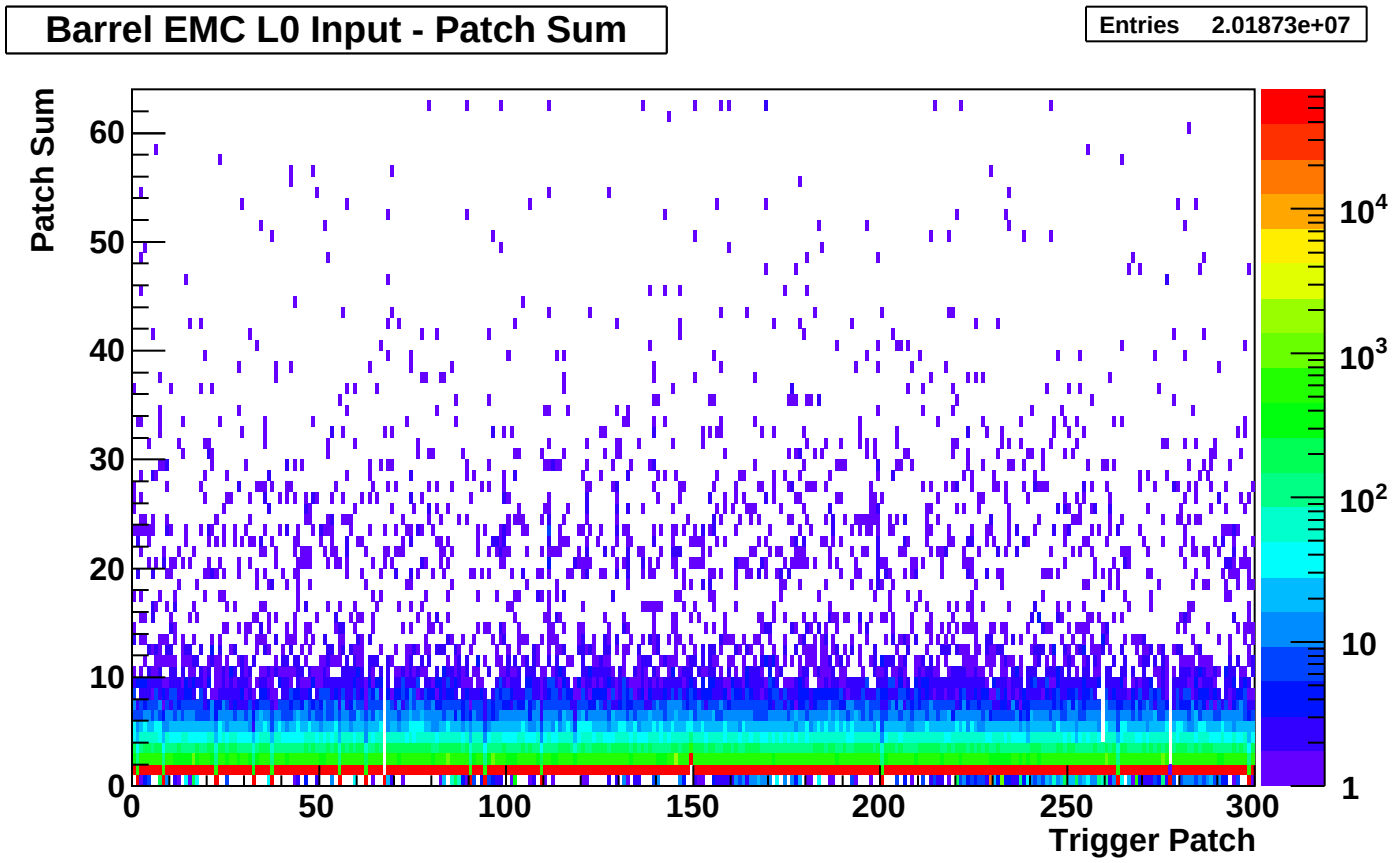

TCU bits
Entries 1076656
TCU bits
67291 67291 67291 67291 67291 67291 67291 67291 67291 67291 67291 67291 67291 67291 67291 67291
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

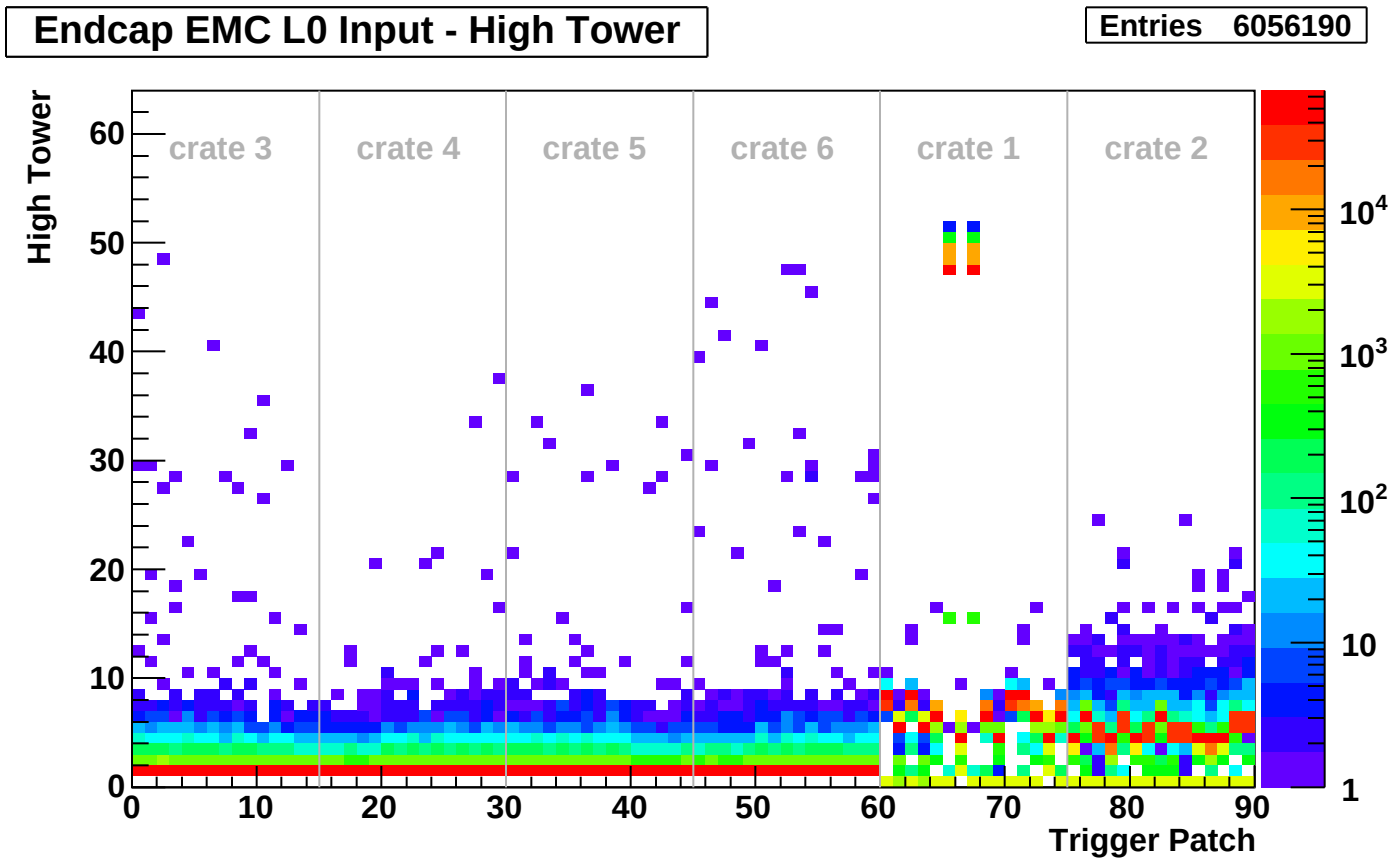

TCU bits
Entries 77018
TCU bits - Simulated
522 1066 5029 62705 603 7084 9
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

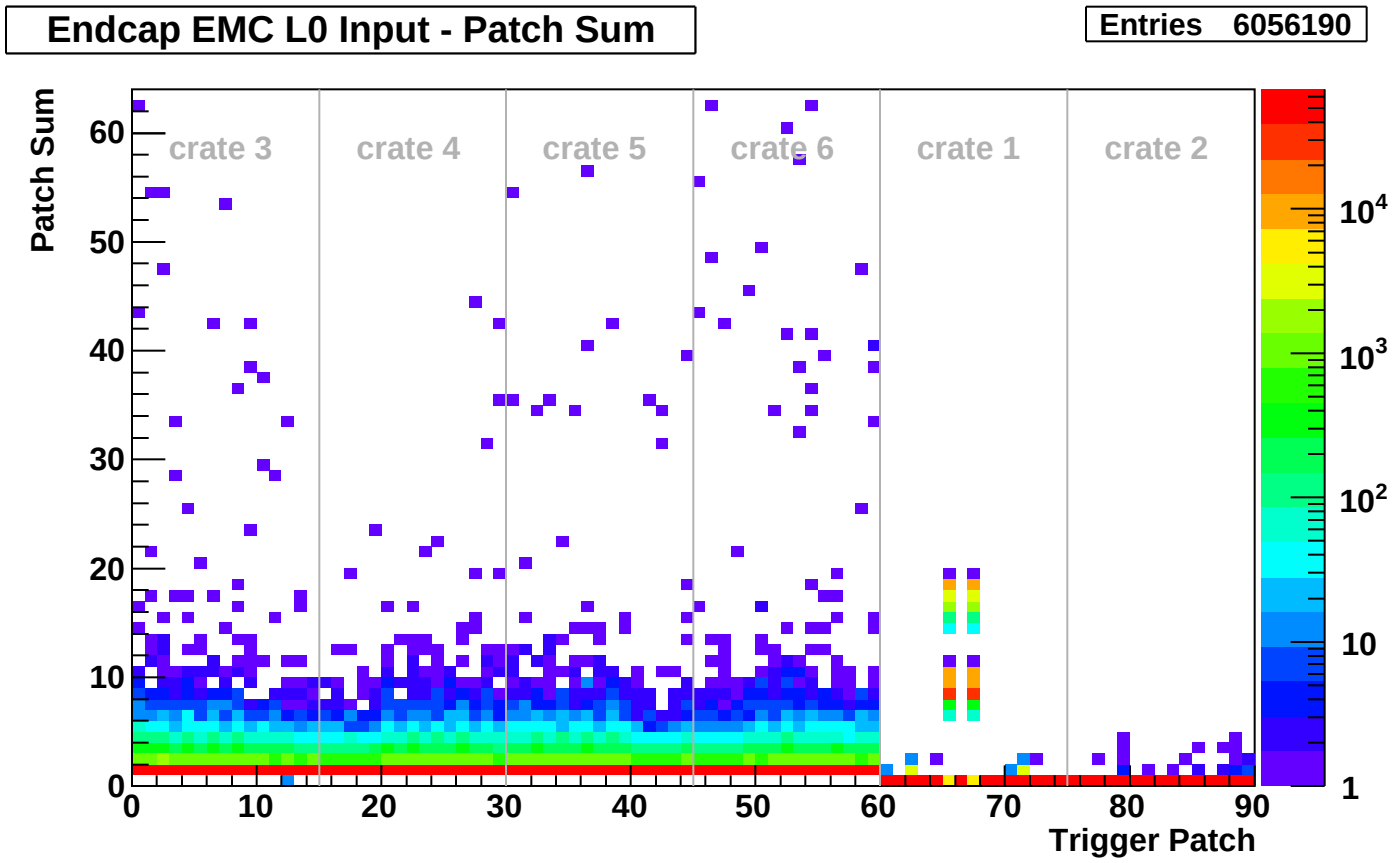

BunchId7Bit (BHT3)
Entries 92
0 20 40 60 80 100 120

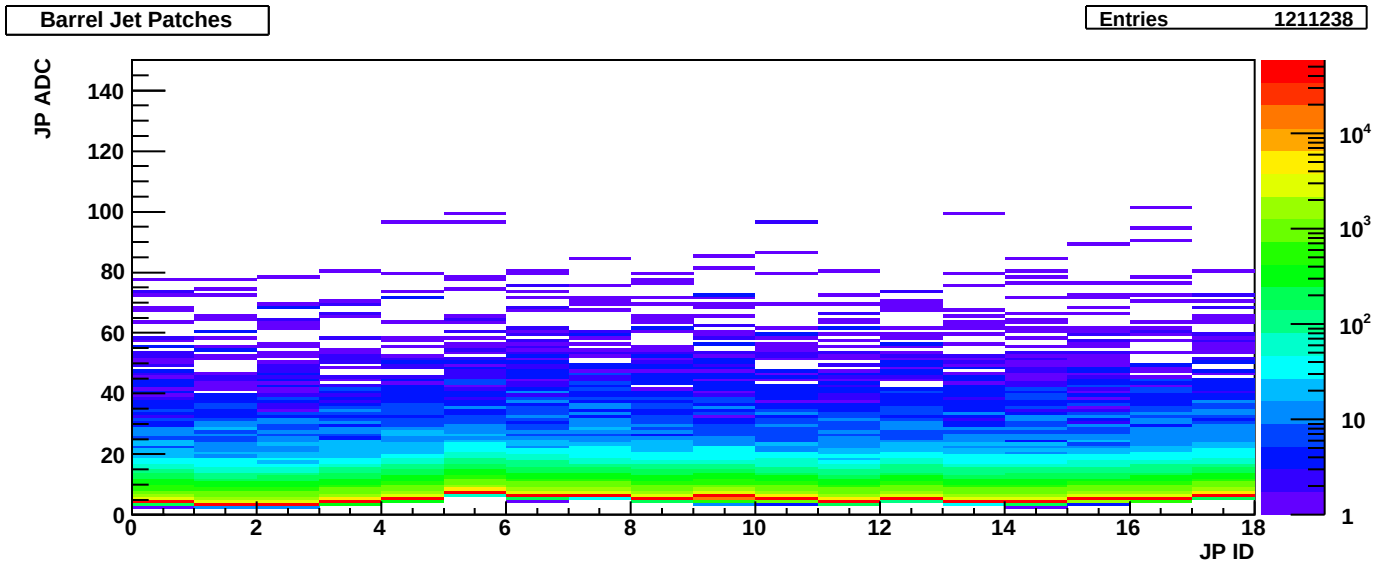

BunchId7Bit (all events)
Entries 67291
0 20 40 60 80 100 120

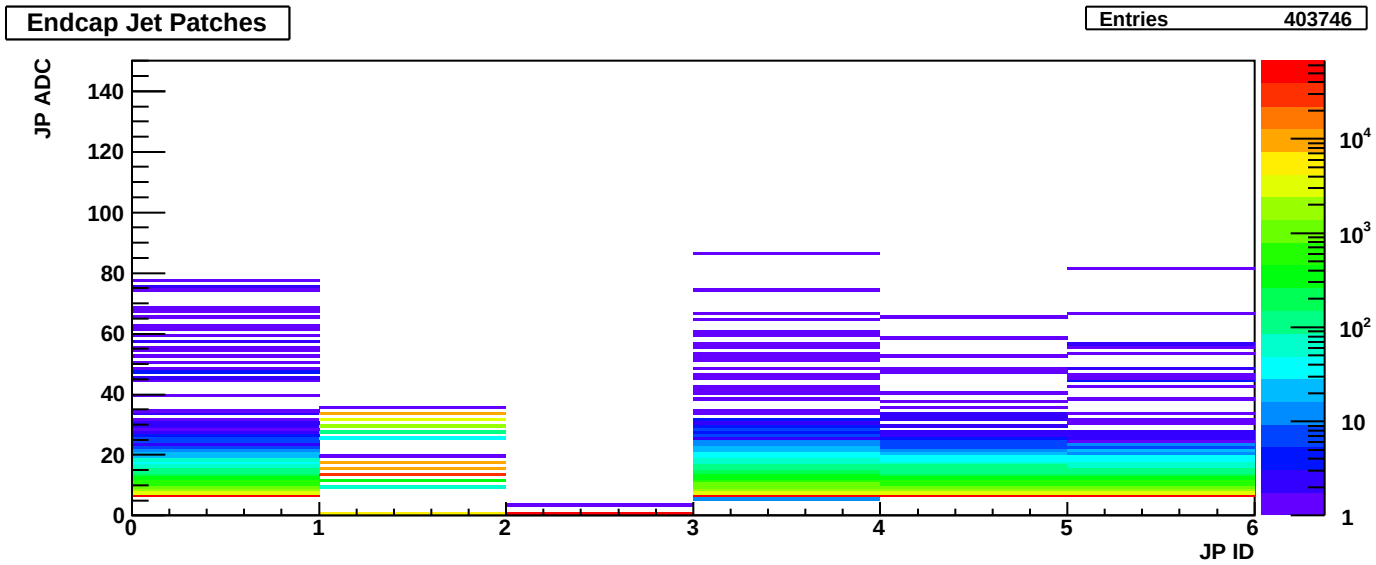

Barrel EMC L0 Input - High Tower
Entries 2.01873e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

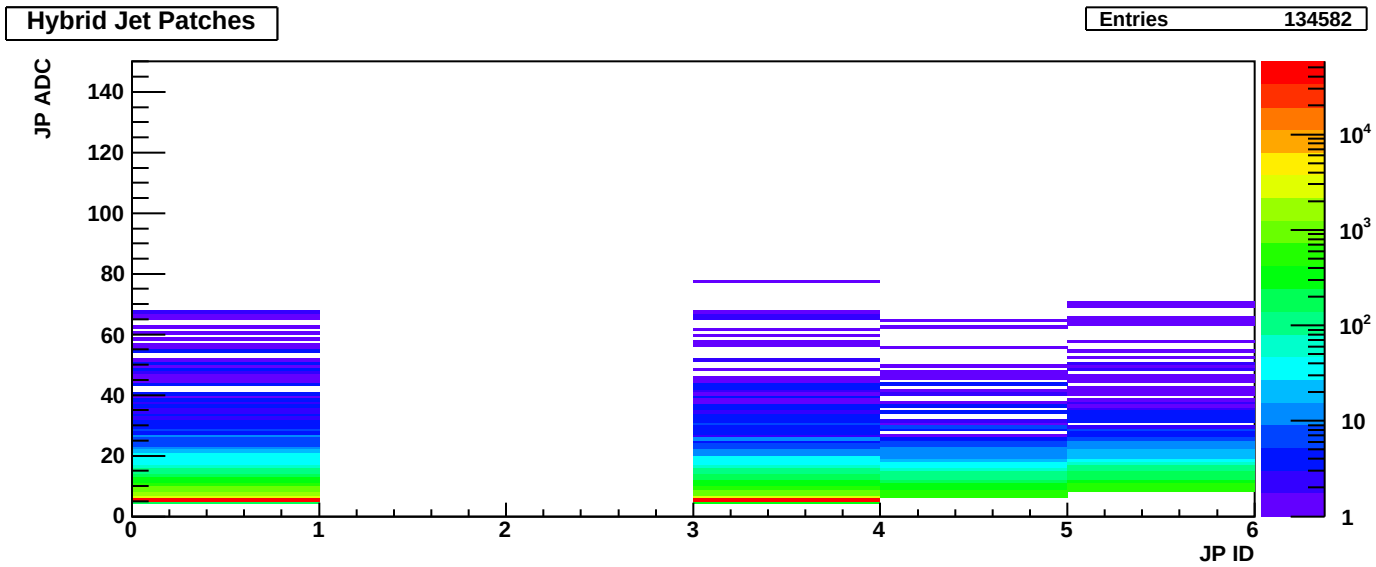

Barrel EMC L0 Input - Patch Sum
Entries 2.01873e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

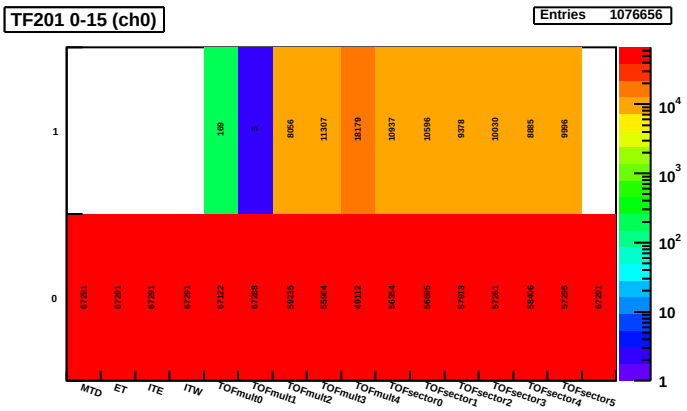

Endcap EMC L0 Input - High Tower
Entries 6056190
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

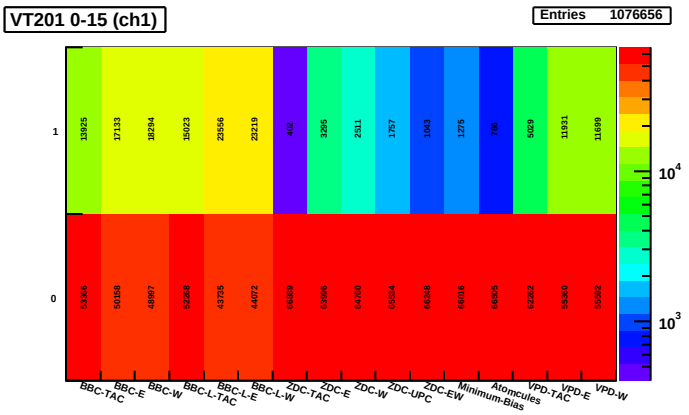

Endcap EMC L0 Input - Patch Sum
Entries 6056190
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

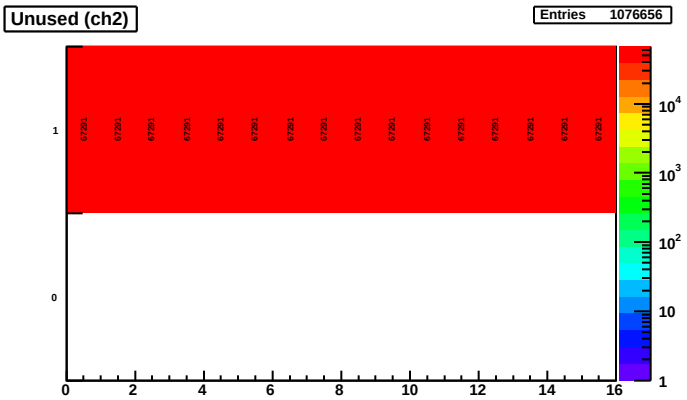

Barrel Jet Patches
Entries 1211238
JP ADC
JP ID

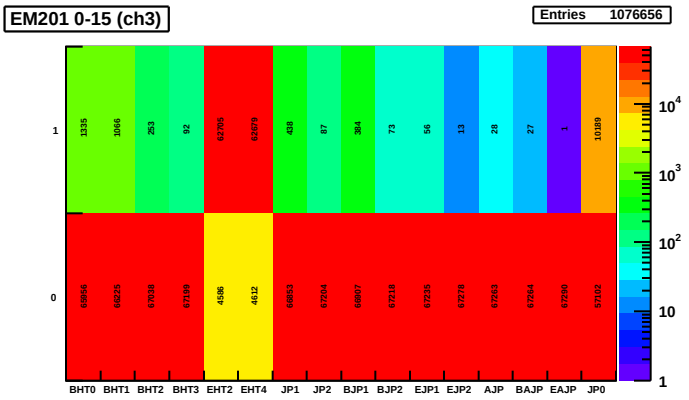

Endcap Jet Patches
Entries 403746
JP ADC
JP ID

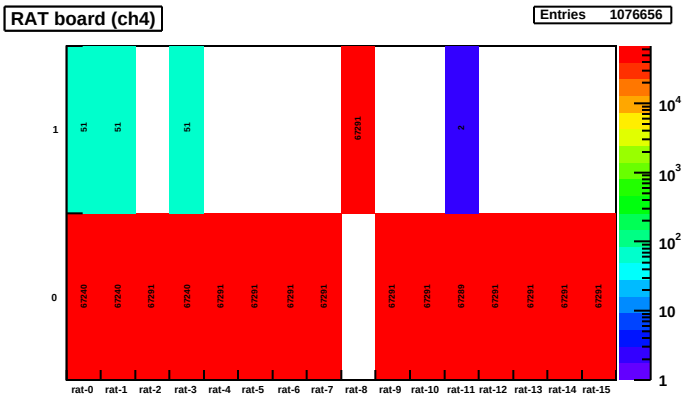

Hybrid Jet Patches
Entries 134582
JP ADC
JP ID

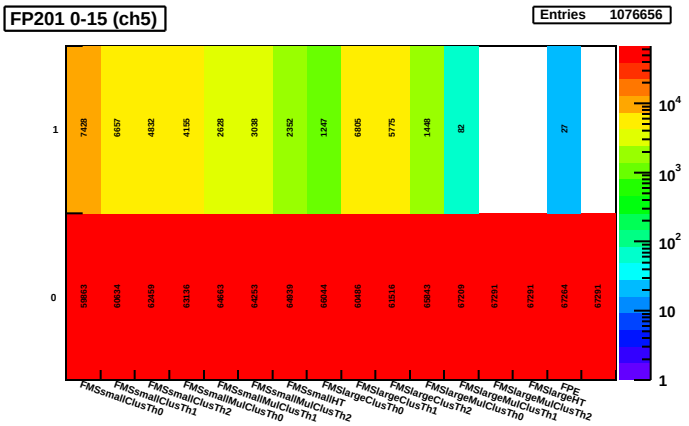

TF201 0-15 (ch0)
Entries 1076656
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

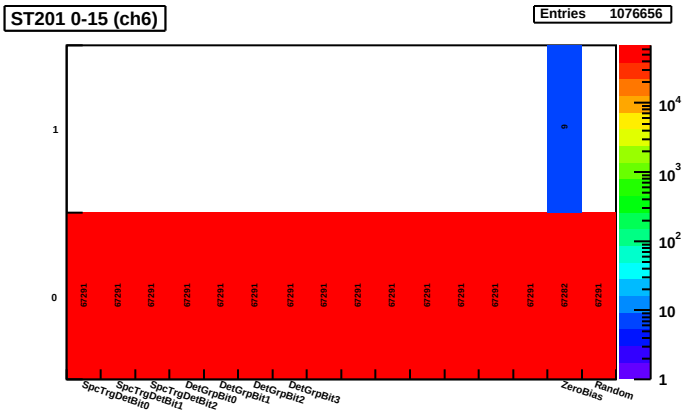

VT201 0-15 (ch1)
Entries 1076656
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

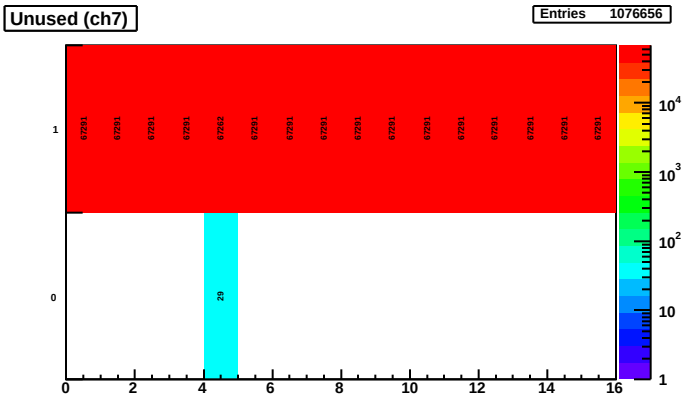

Unused (ch2)
Entries 1076656

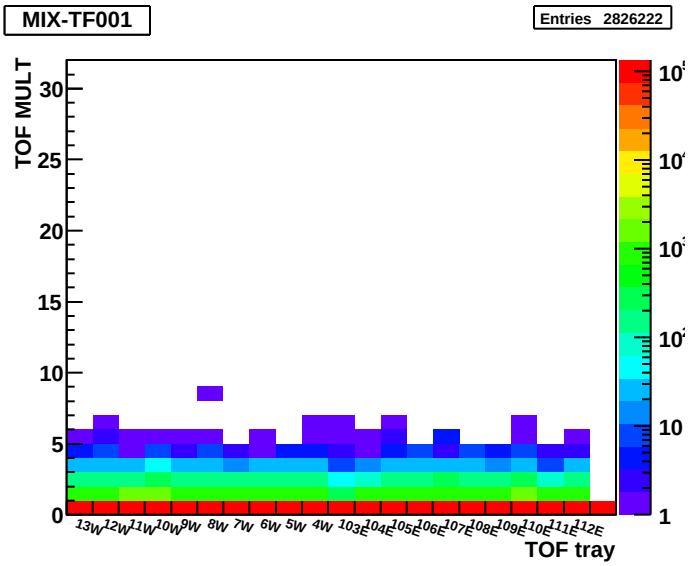

EM201 0-15 (ch3)
Entries 1076656
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

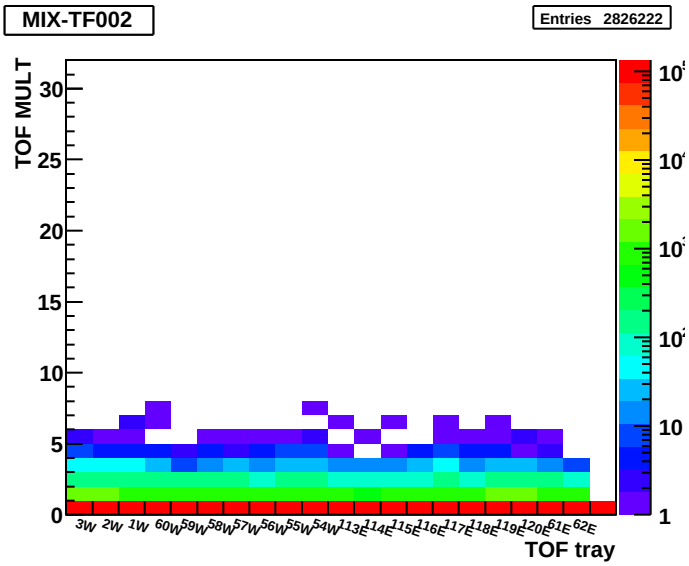

RAT board (ch4)
Entries 1076656
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

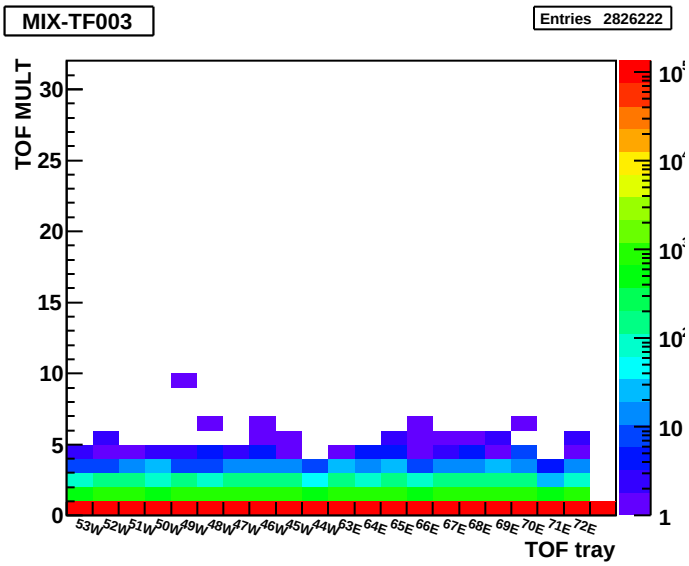

FP201 0-15 (ch5)
Entries 1076656
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

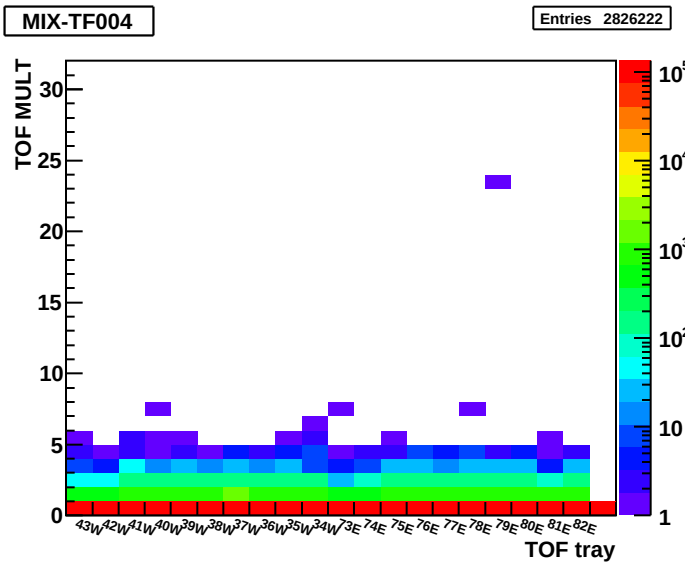

ST201 0-15 (ch6)
Entries 1076656
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

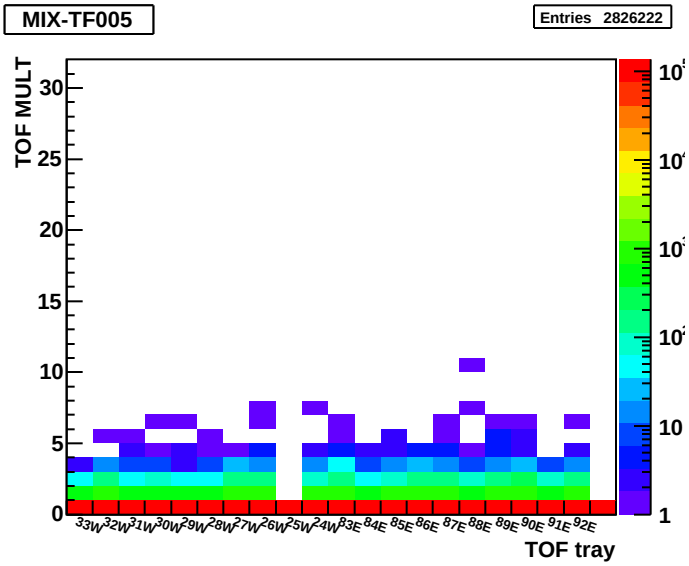

Unused (ch7)
Entries 1076656

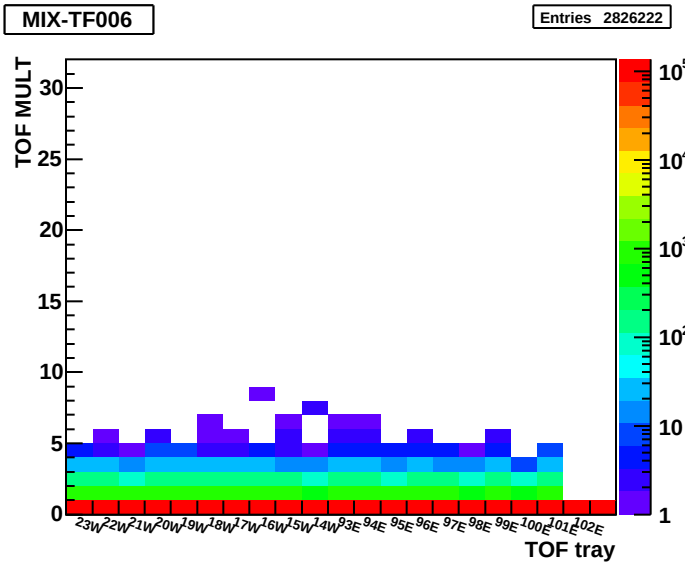

MIX-TF001
Entries 2826222
TOF MULT
TOF tray

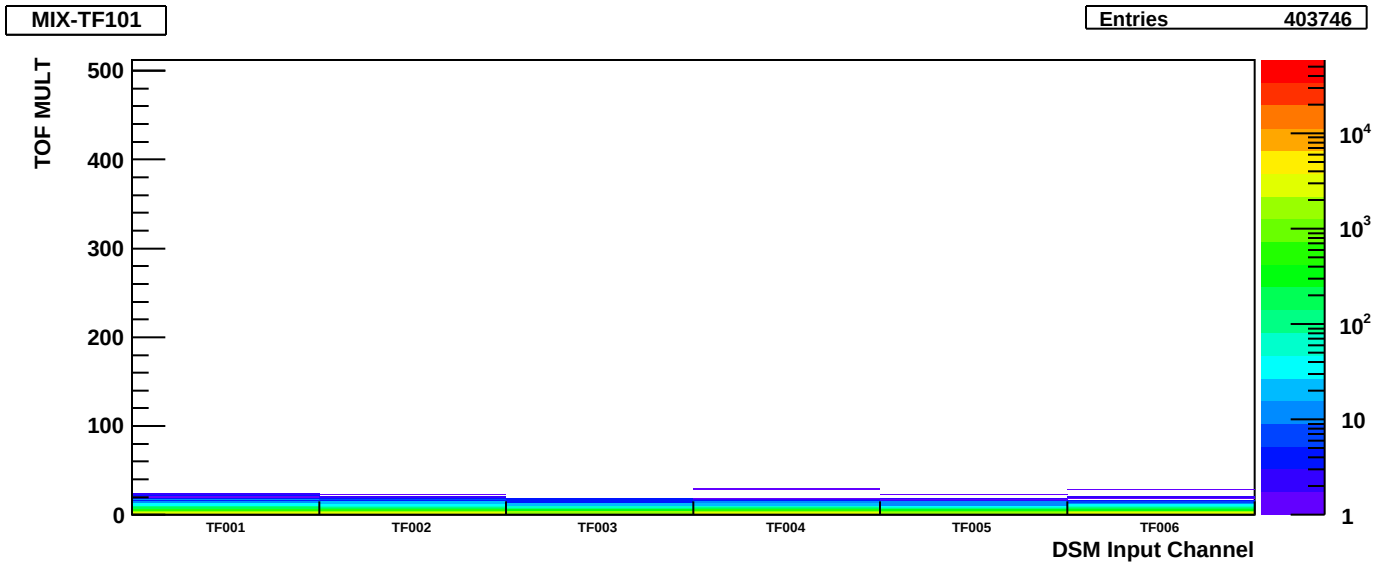

MIX-TF002
Entries 2826222
TOF MULT
TOF tray

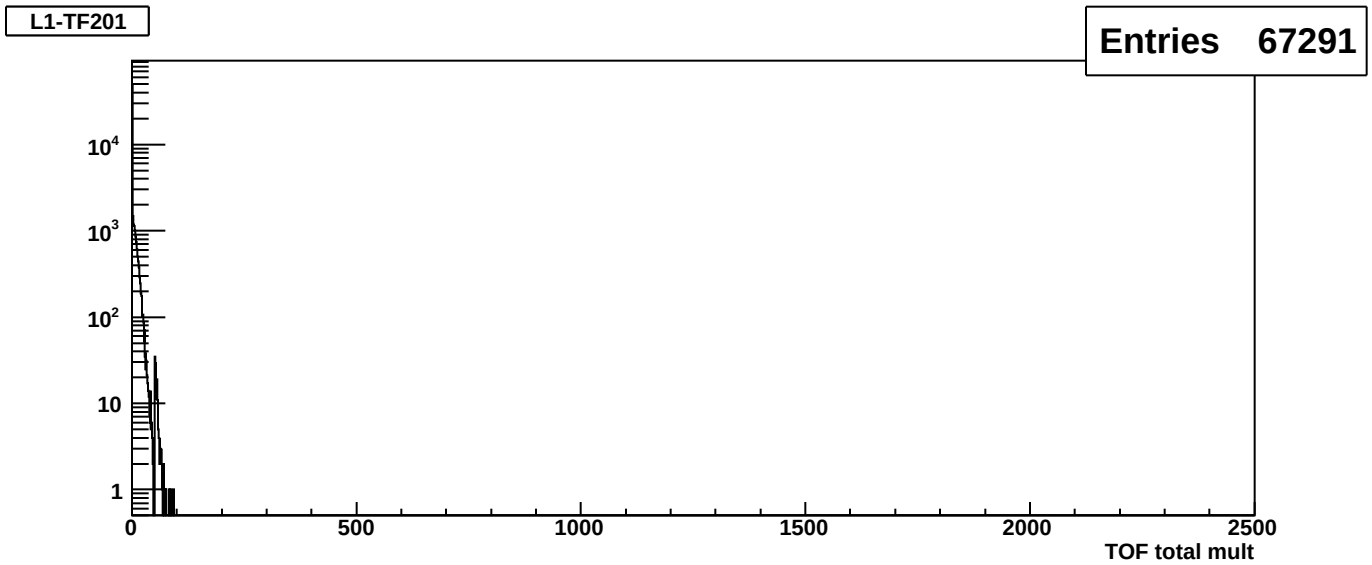

MIX-TF003
Entries 2826222
TOF MULT
TOF tray

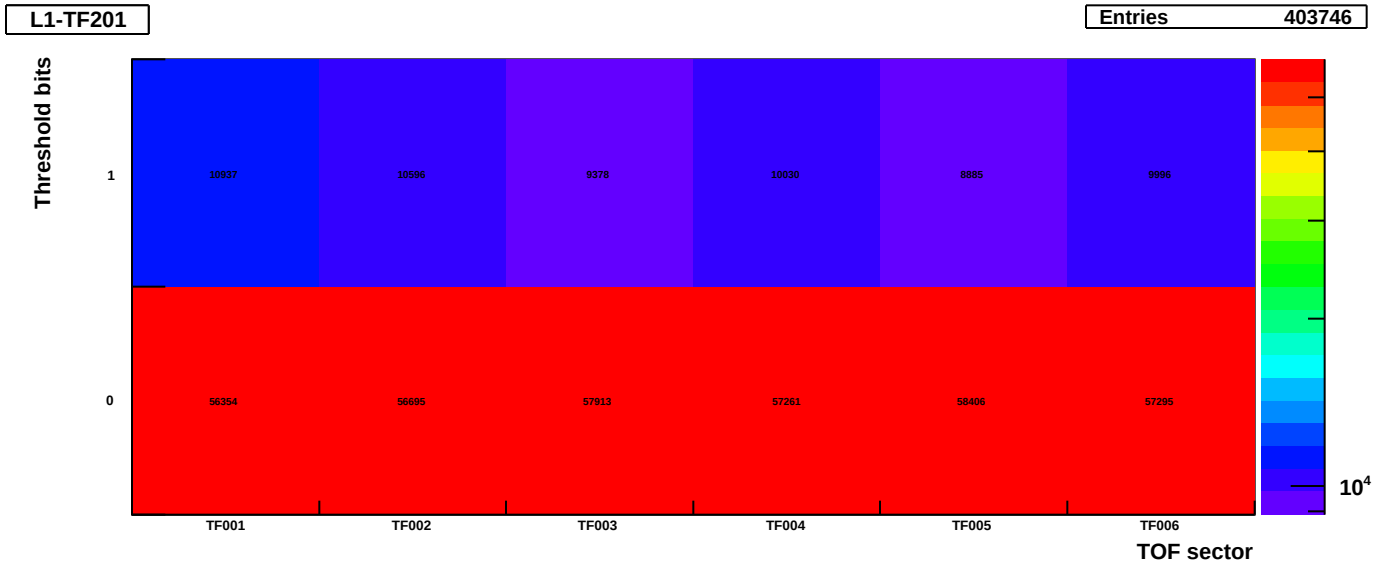

MIX-TF004
Entries 2826222
TOF MULT
TOF tray

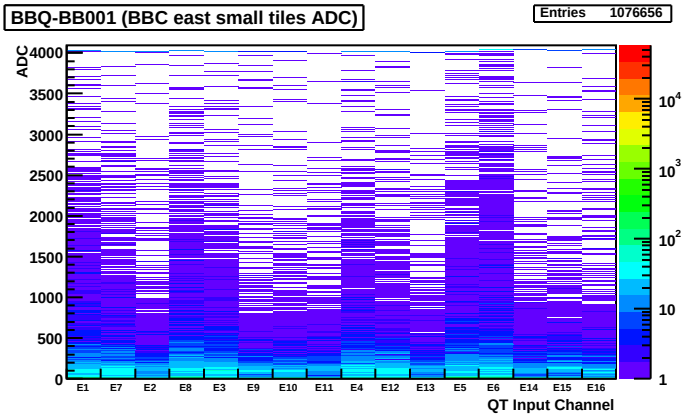

MIX-TF005
Entries 2826222
TOF MULT
TOF tray

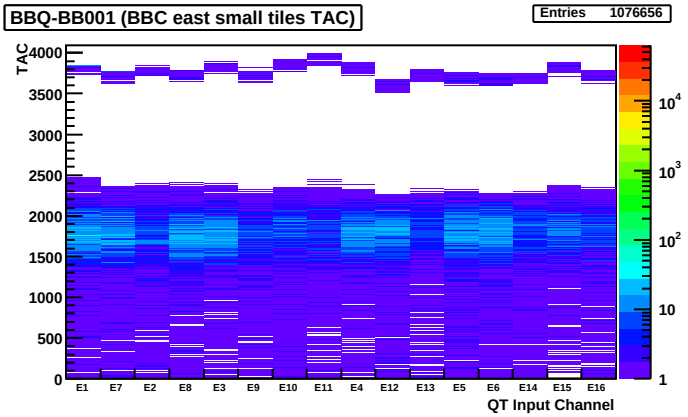

MIX-TF006
Entries 2826222
TOF MULT
TOF tray

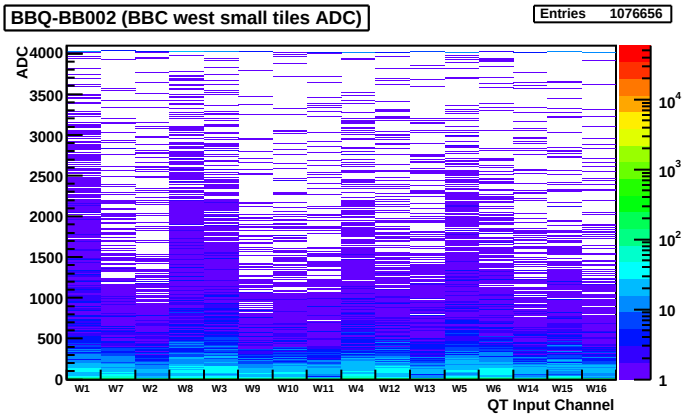

MIX-TF101
Entries 403746
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

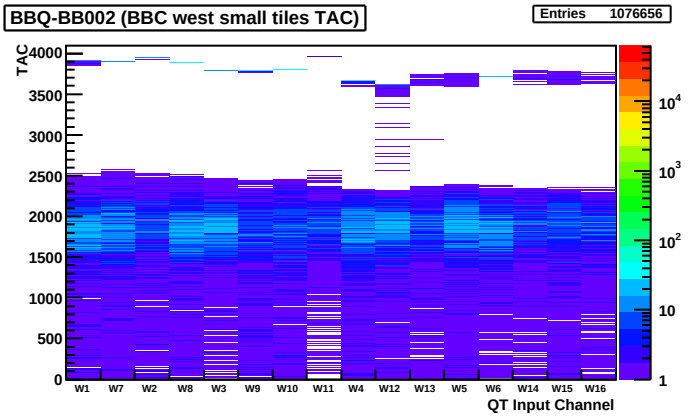

L1-TF201
Entries 67291
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

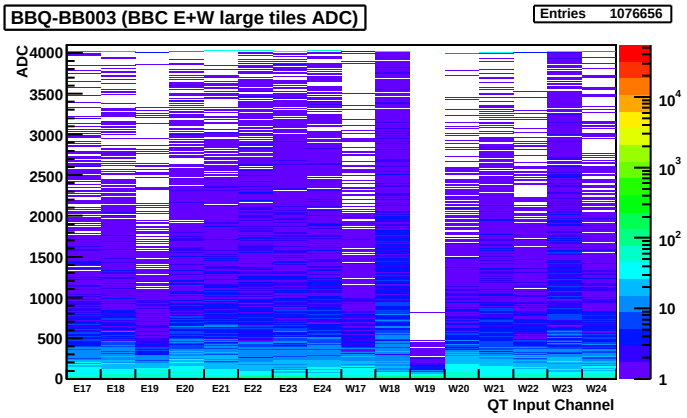

L1-TF201
Entries 403746
Threshold bits
1
0
10937
10596
9378
10030
8885
9996
56354
56695
57913
57261
58406
57295
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

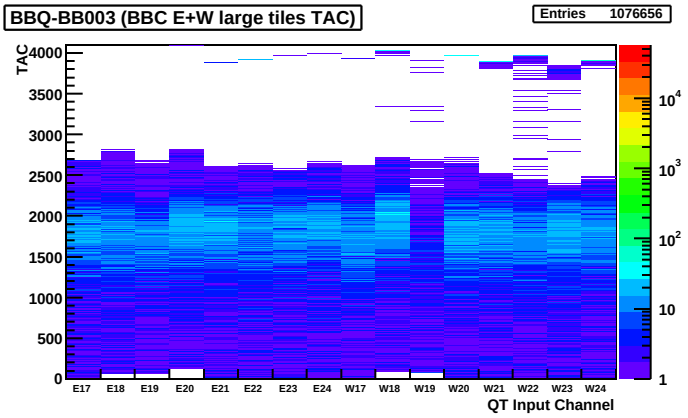

BBQ-BB001 (BBC east small tiles ADC)
Entries 1076656
ADC
QT Input Channel

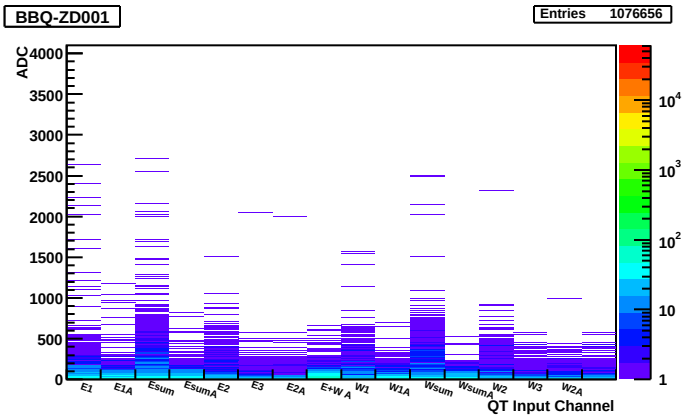

BBQ-BB001 (BBC east small tiles TAC)
Entries 1076656
TAC
QT Input Channel

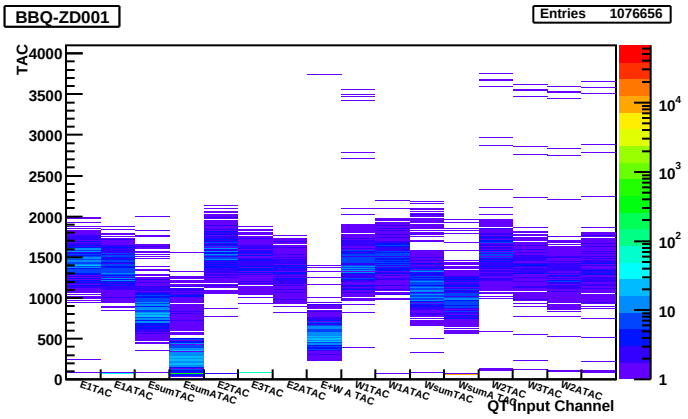

BBQ-BB002 (BBC west small tiles ADC)
Entries 1076656
ADC
QT Input Channel

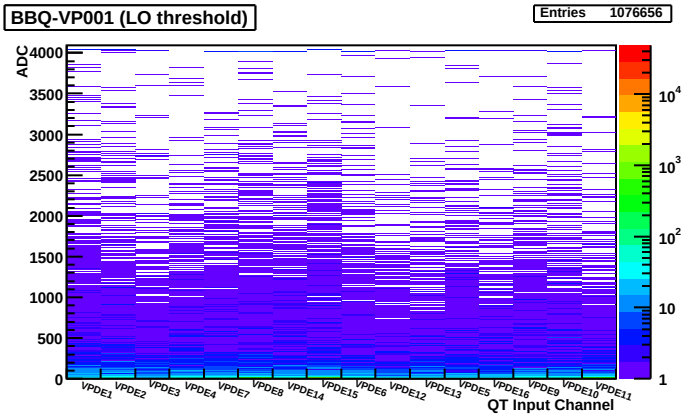

BBQ-BB002 (BBC west small tiles TAC)
Entries 1076656
TAC
QT Input Channel

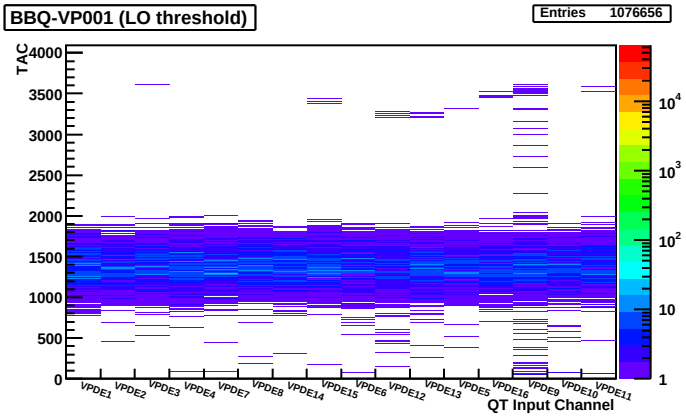

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1076656
ADC
QT Input Channel

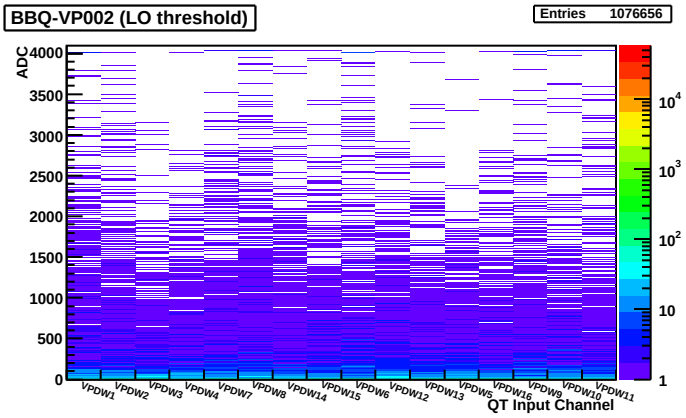

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1076656
TAC
QT Input Channel

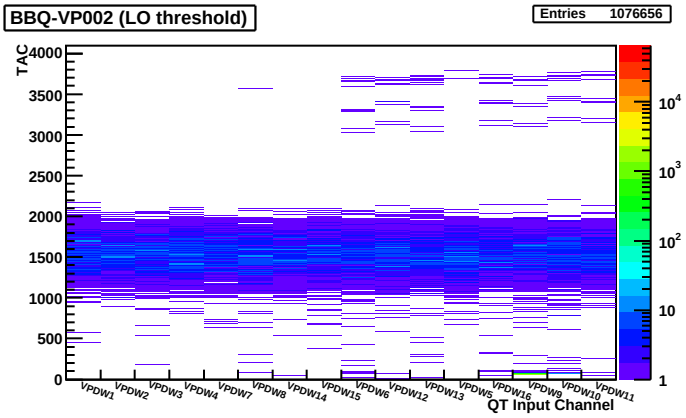

BBQ-ZD001
Entries 1076656
ADC
QT Input Channel

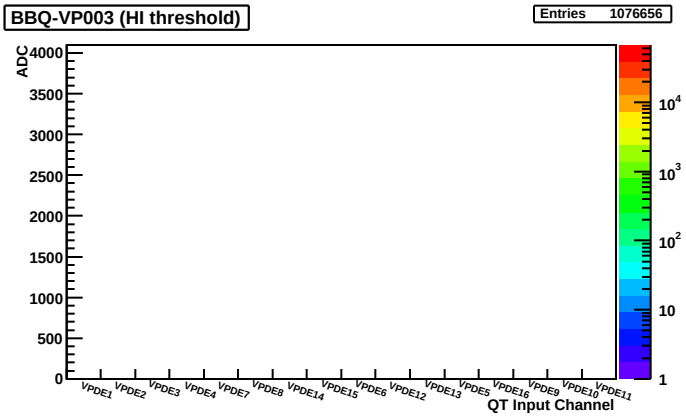

BBQ-ZD001
Entries 1076656
TAC
QT Input Channel

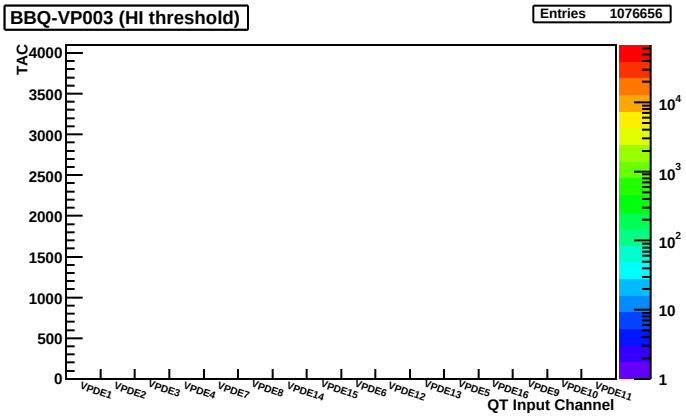

BBQ-VP001 (LO threshold)
Entries 1076656
ADC
QT Input Channel

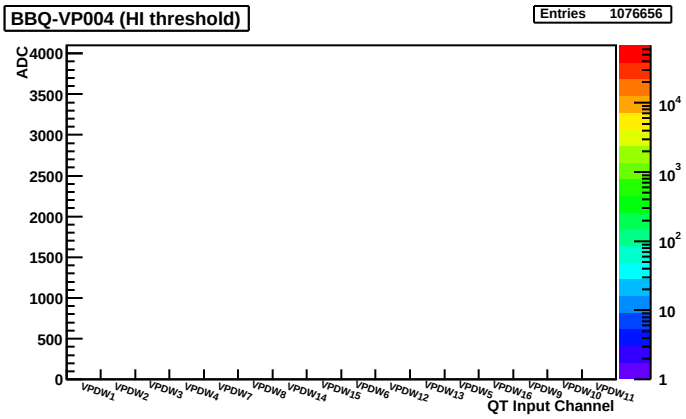

BBQ-VP001 (LO threshold)
Entries 1076656
TAC
QT Input Channel

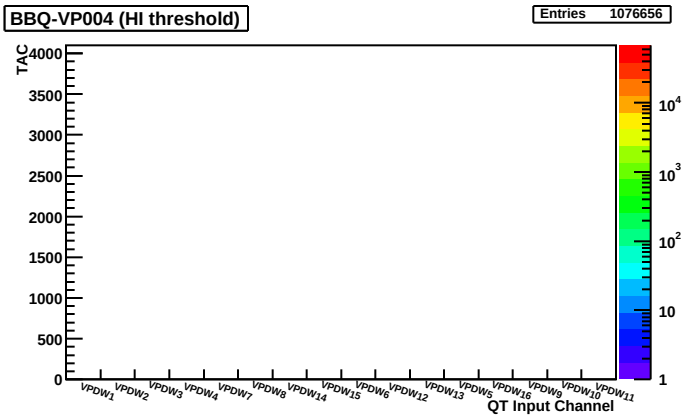

BBQ-VP002 (LO threshold)
Entries 1076656
ADC
QT Input Channel

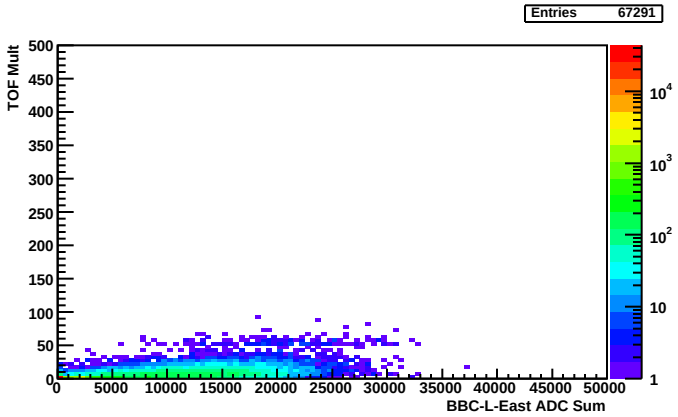

BBQ-VP002 (LO threshold)
Entries 1076656
TAC
QT Input Channel

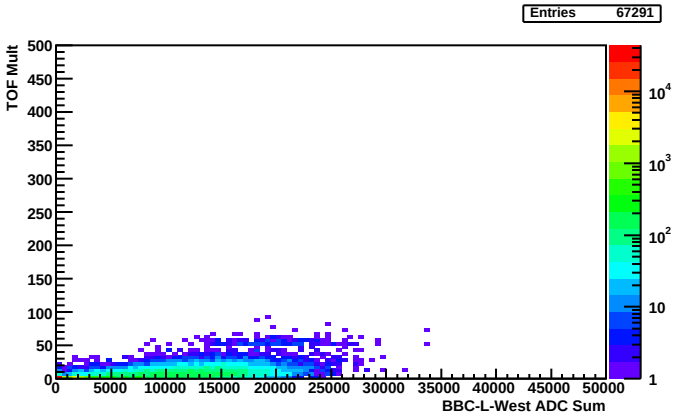

BBQ-VP003 (HI threshold)
Entries 1076656
ADC
QT Input Channel

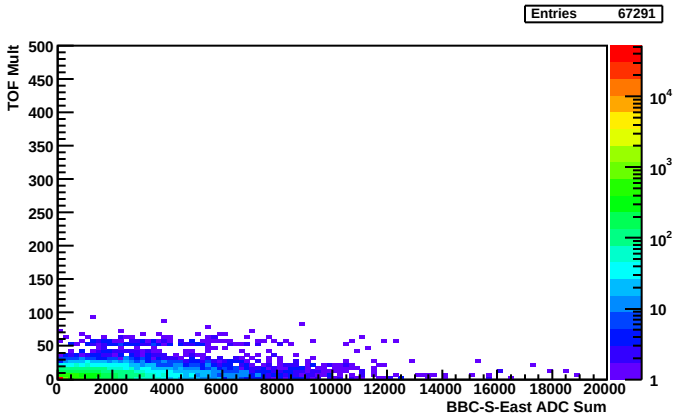

BBQ-VP003 (HI threshold)
Entries 1076656
TAC
QT Input Channel

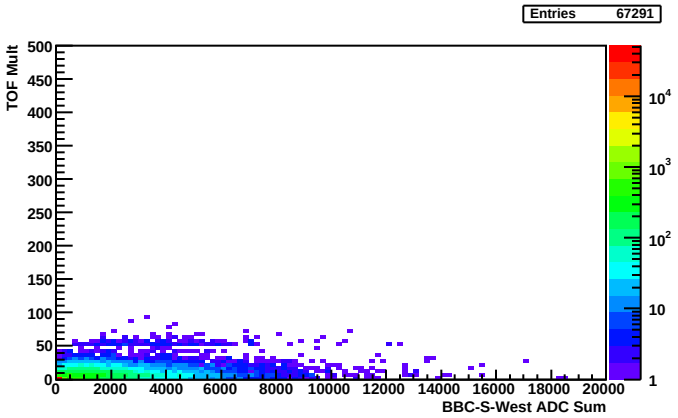

BBQ-VP004 (HI threshold)
Entries 1076656
ADC
QT Input Channel

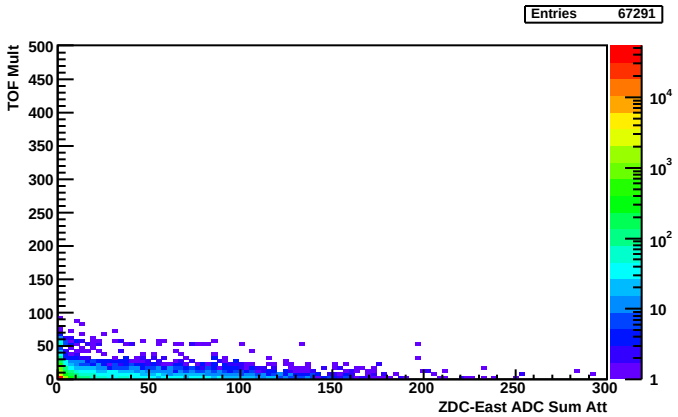

BBQ-VP004 (HI threshold)
Entries 1076656
TAC
QT Input Channel

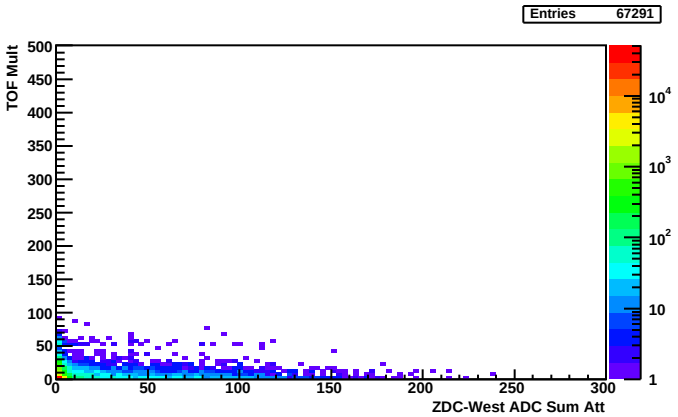

Entries 67291
TOF Mult
BBC-L-East ADC Sum

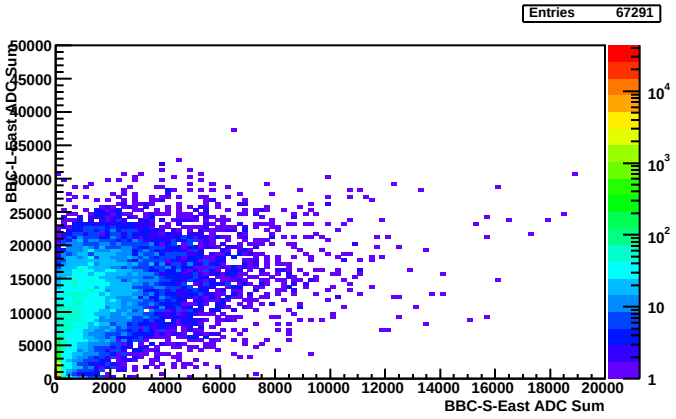

Entries 67291
TOF Mult
BBC-L-West ADC Sum

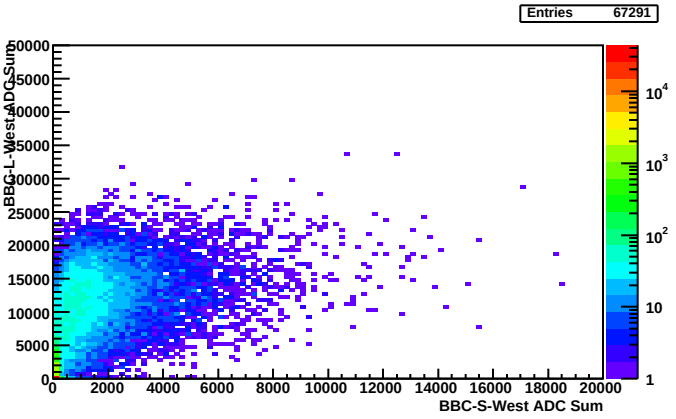

Entries 67291
TOF Mult
BBC-S-East ADC Sum

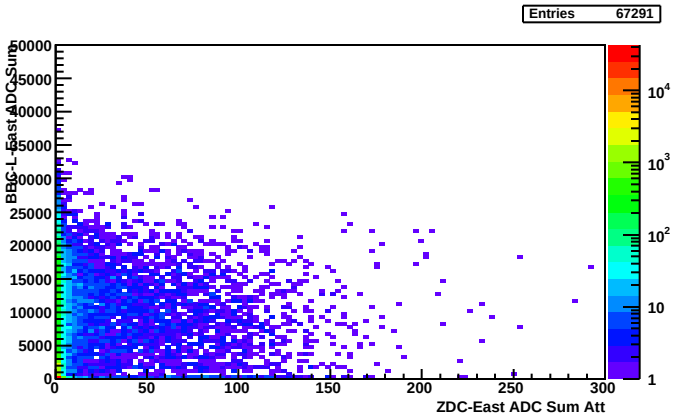

Entries 67291
TOF Mult
BBC-S-West ADC Sum

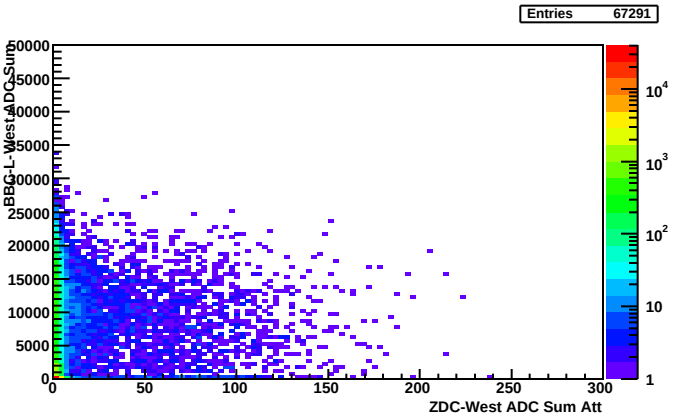

Entries 67291
TOF Mult
ZDC-East ADC Sum Att

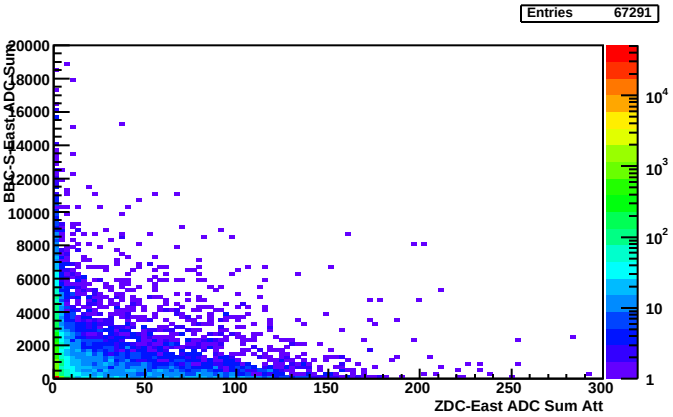

Entries 67291
TOF Mult
ZDC-West ADC Sum Att

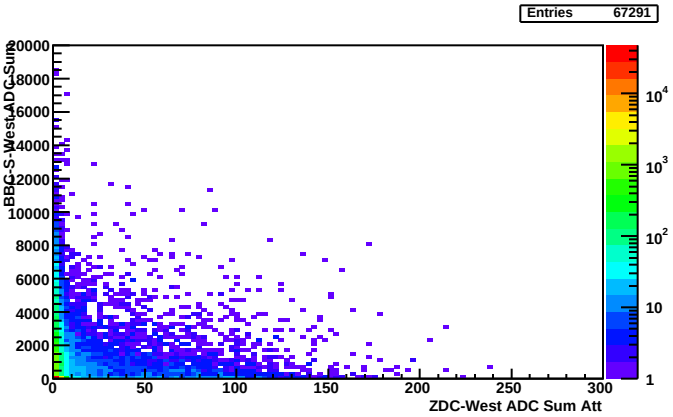

Entries 67291
BBC-L-East ADC Sum
BBC-S-East ADC Sum

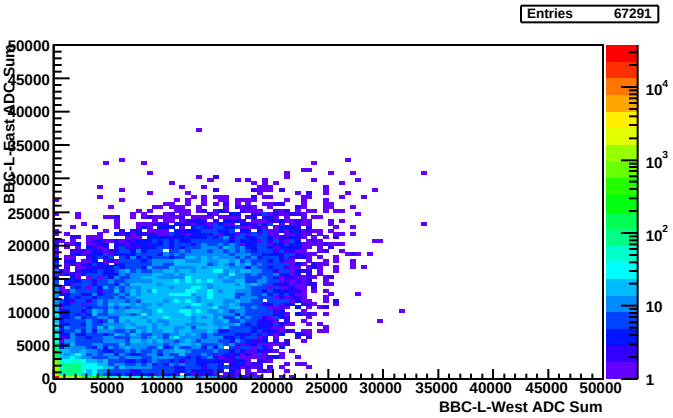

Entries 67291
BBC-L-West ADC Sum
BBC-S-West ADC Sum

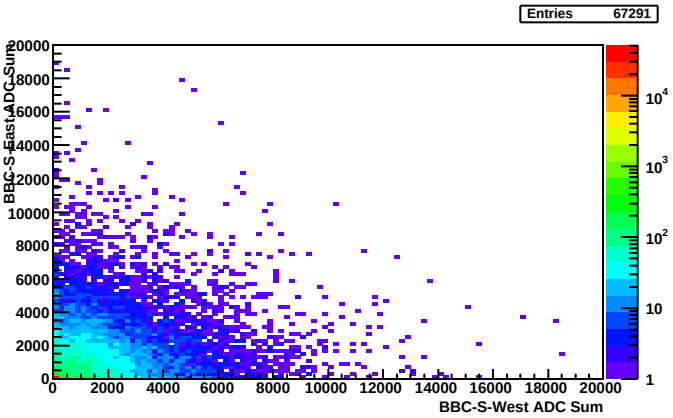

Entries 67291
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

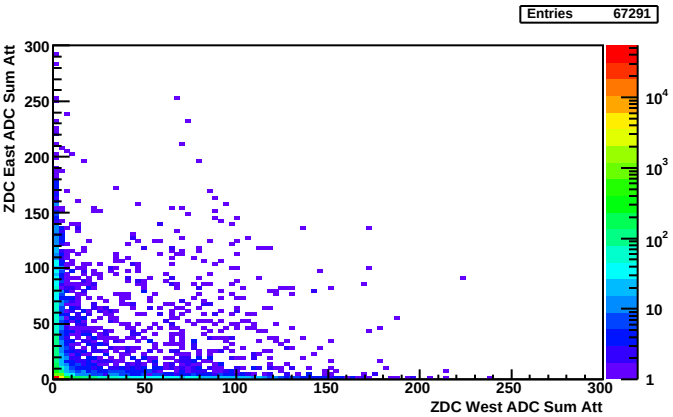

Entries 67291
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

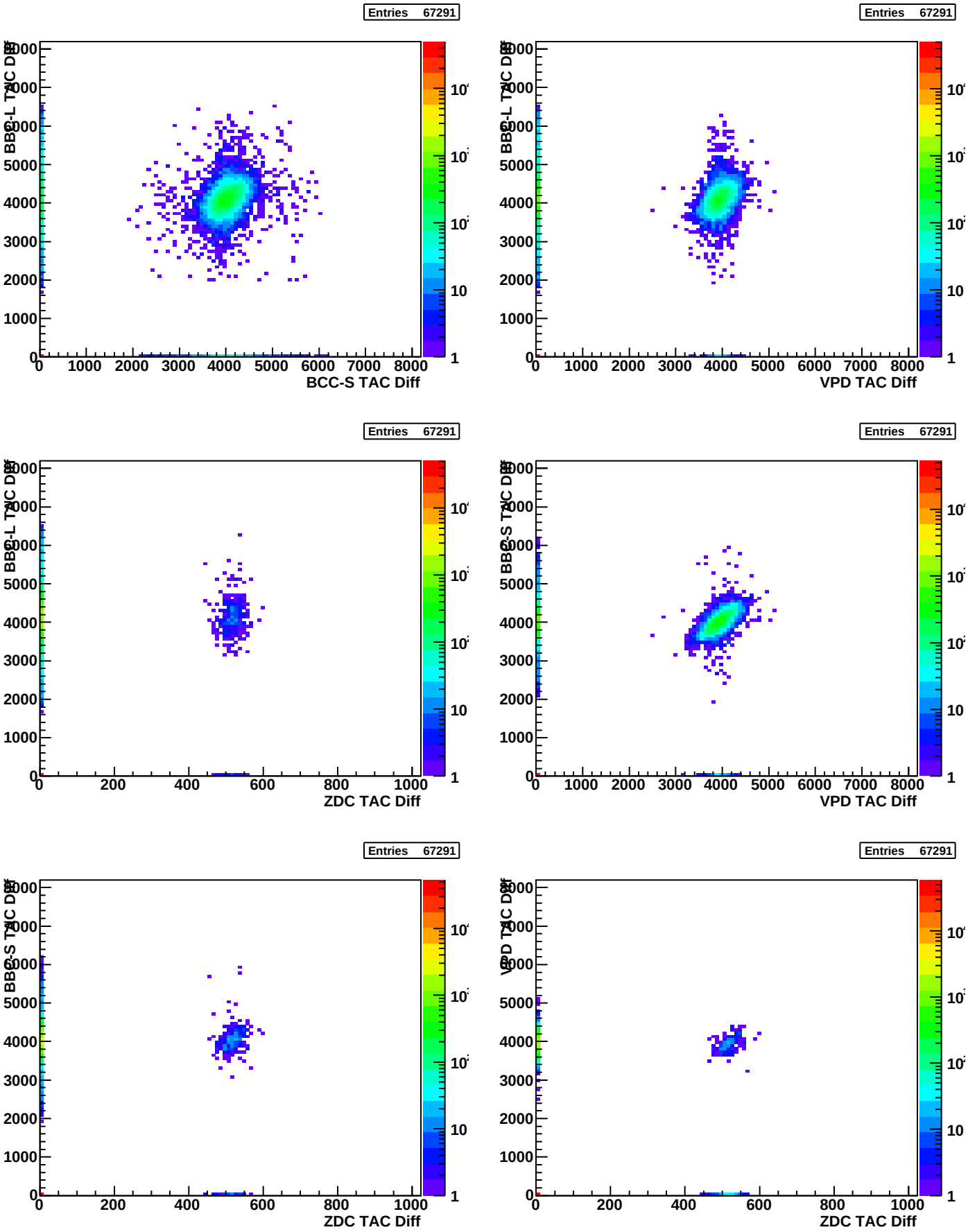

Entries 67291
BBC-S-East ADC Sum
ZDC-East ADC Sum Att


Entries 67291
BBC-S-West ADC Sum
ZDC-West ADC Sum Att


Entries 67291
BBC-L-East ADC Sum
BBC-L-West ADC Sum


Entries 67291
BBC-S-East ADC Sum
BBC-S-West ADC Sum


Entries 67291
ZDC East ADC Sum Att
ZDC West ADC Sum Att


Entries 67291
BBC-L TAC Diff
BCC-S TAC Diff
Entries 67291
BBC-L TAC Diff
VPD TAC Diff
Entries 67291
BBC-L TAC Diff
ZDC TAC Diff
Entries 67291
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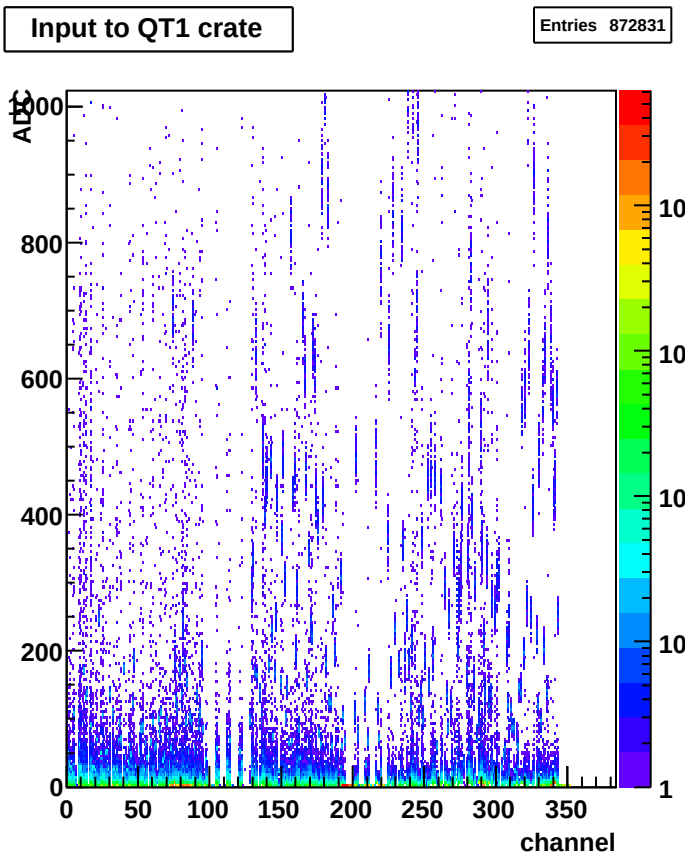
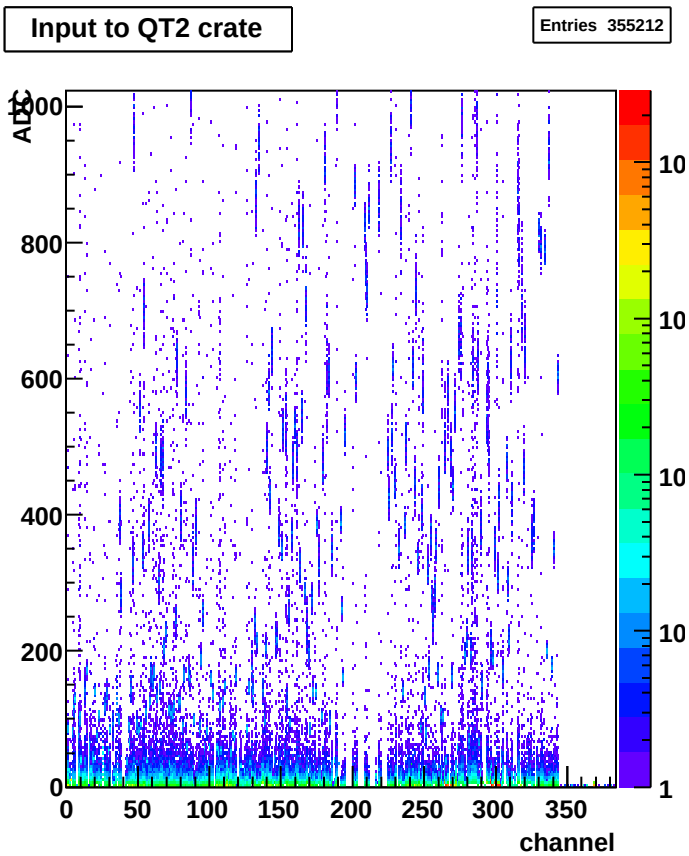
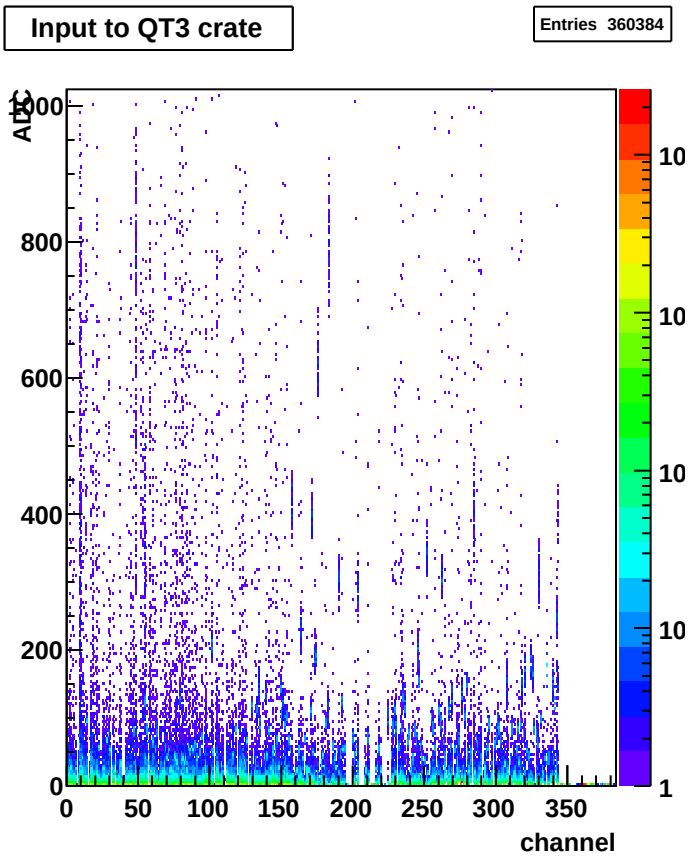
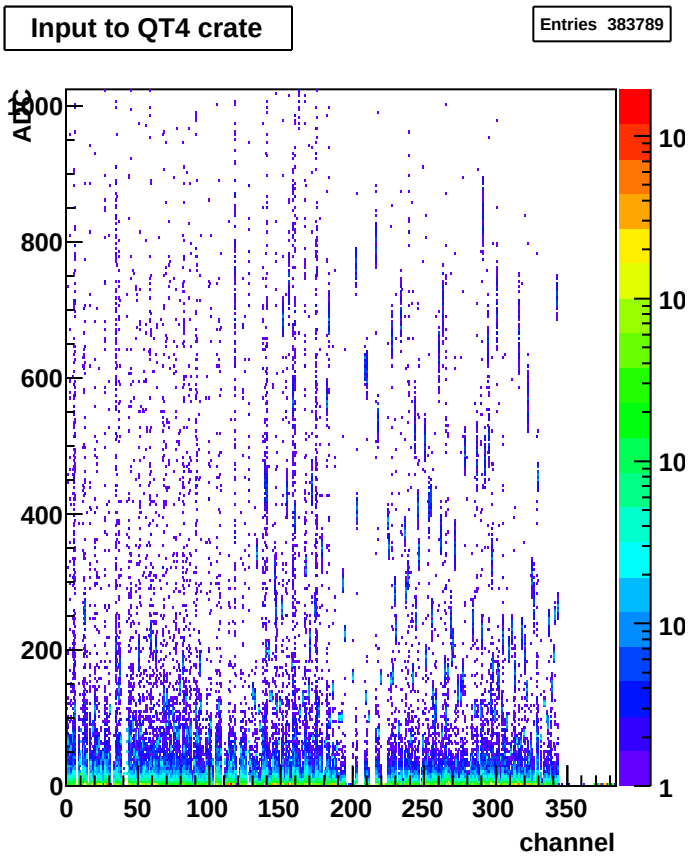
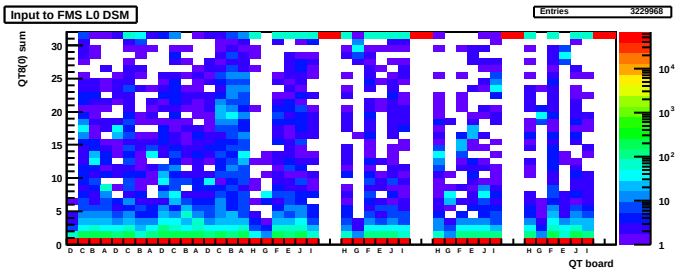
VPD TAC Diff
Entries 67291
BBC-S TAC Diff
ZDC TAC Diff
Entries 67291
VPD TAC Diff
ZDC TAC Diff

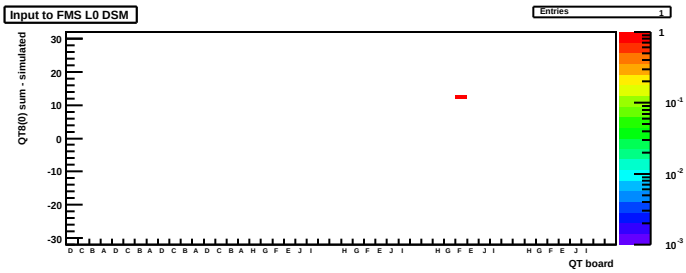

Input to QT1 crate
Entries 872831
ADC
channel

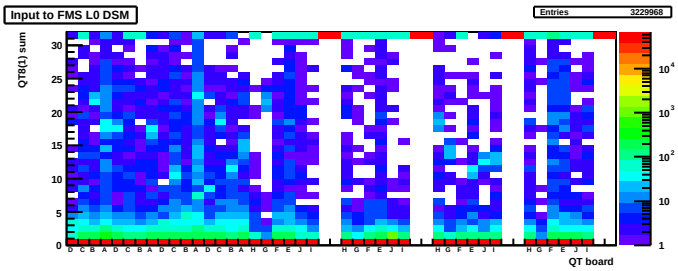

Input to QT2 crate
Entries 355212
ADC
channel

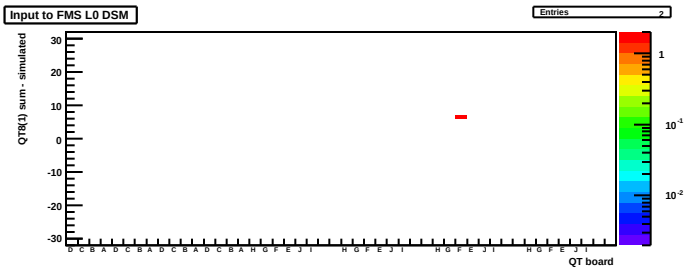

Input to QT3 crate
Entries 360384
ADC
channel

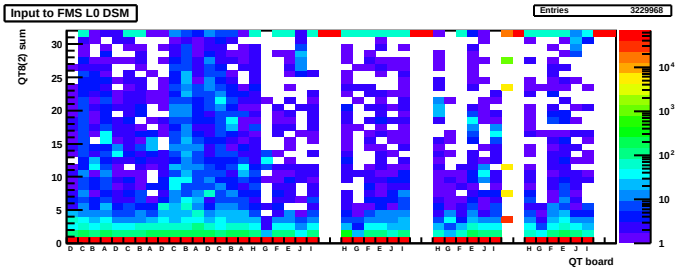

Input to QT4 crate
Entries 383789
ADC
channel

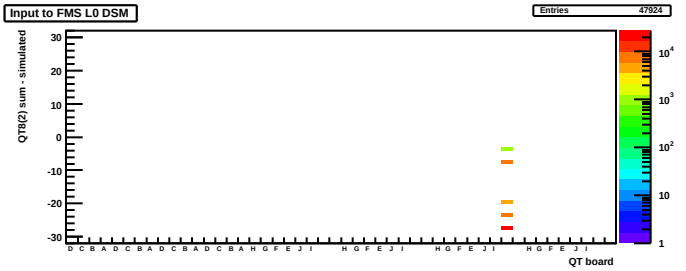

Input to FMS L0 DSM
Entries 3229968
QT8(0) sum
QT board

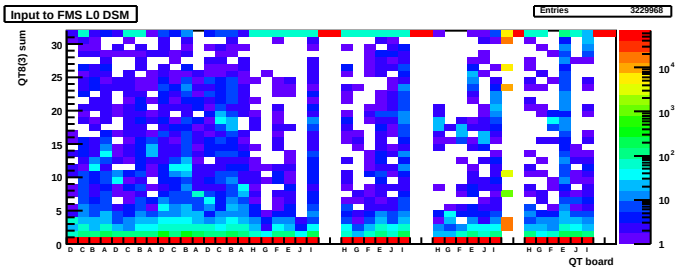

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

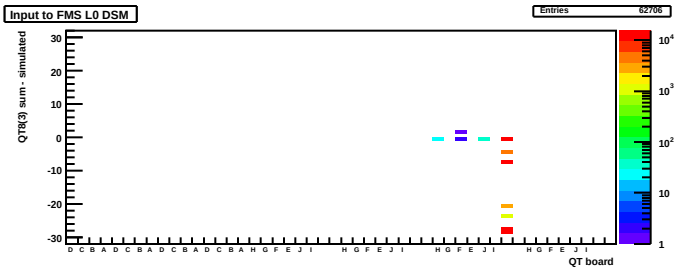

Input to FMS L0 DSM
Entries 3229968
QT8(1) sum
QT board

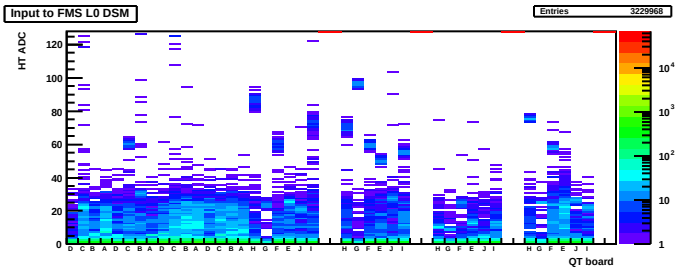

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

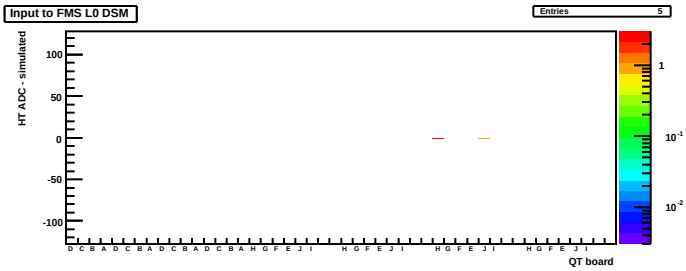

Input to FMS L0 DSM
Entries 3229968
QT8(2) sum
QT board

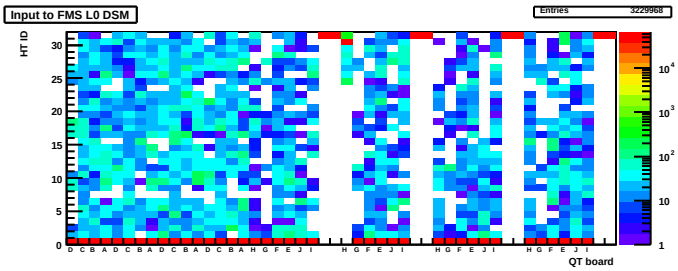

Input to FMS L0 DSM
Entries 47924
QT8(2) sum - simulated
QT board

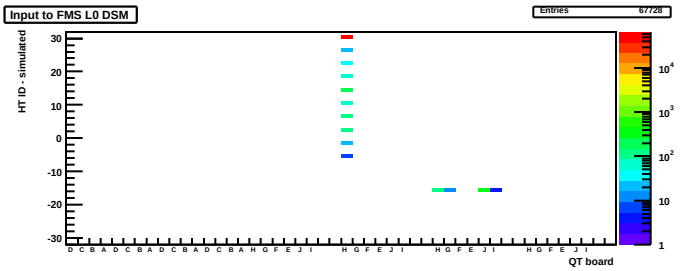

Input to FMS L0 DSM
Entries 3229968
QT8(3) sum
QT board

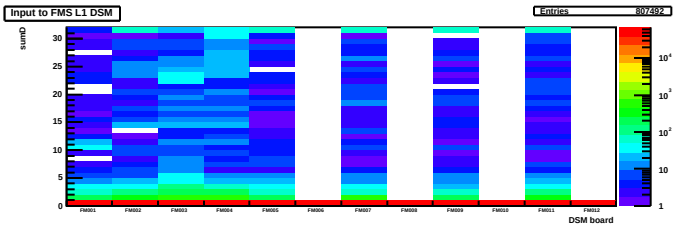

Input to FMS L0 DSM
Entries 62706
QT8(3) sum - simulated
QT board

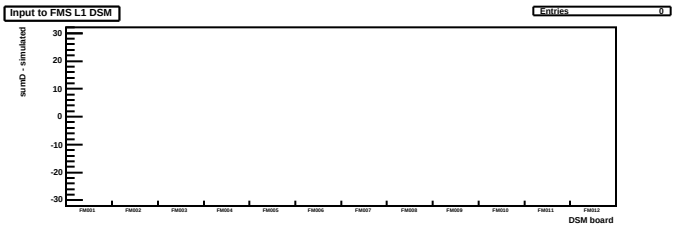

Input to FMS L0 DSM
Entries 3229968
HT ADC
QT board

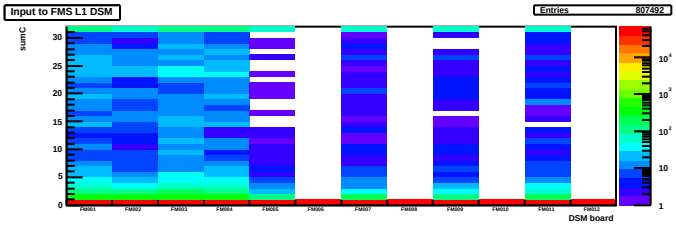

Input to FMS L0 DSM
Entries 5
HT ADC - simulated
QT board

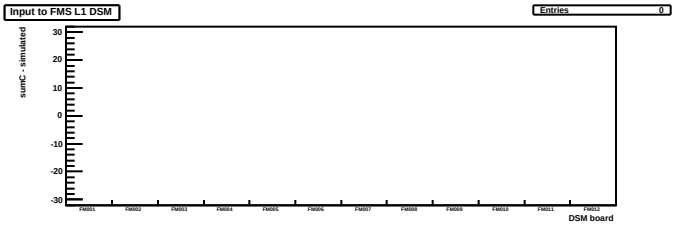

Input to FMS L0 DSM
Entries 3229968
HT ID
QT board

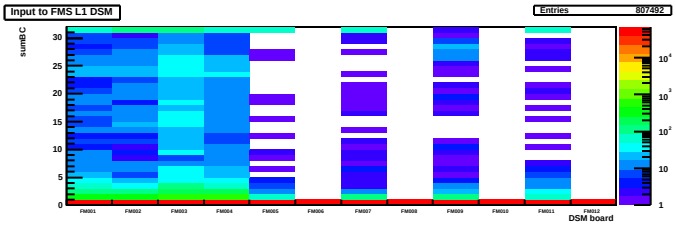

Input to FMS L0 DSM
Entries 67728
HT ID - simulated
QT board

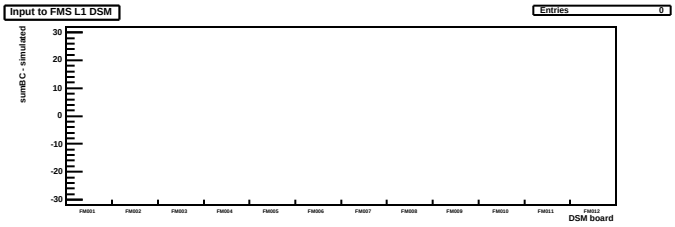

Input to FMS L1 DSM
Entries 807492
sumD
DSM board

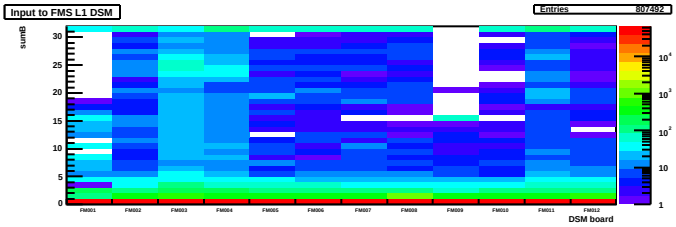

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

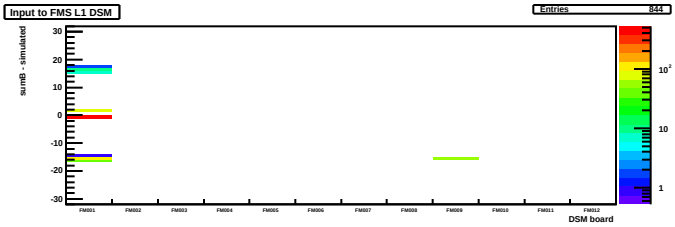

Input to FMS L1 DSM
Entries 807492
sumC
DSM board

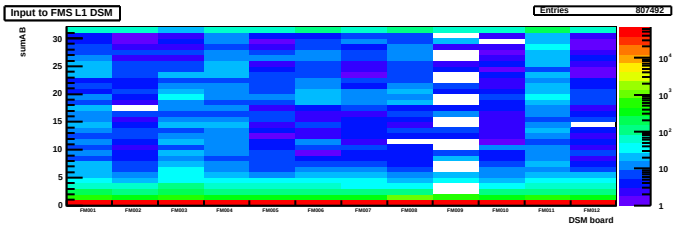

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

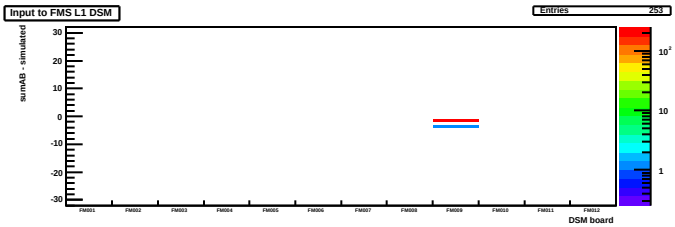

Input to FMS L1 DSM
Entries 807492
sumBC
DSM board

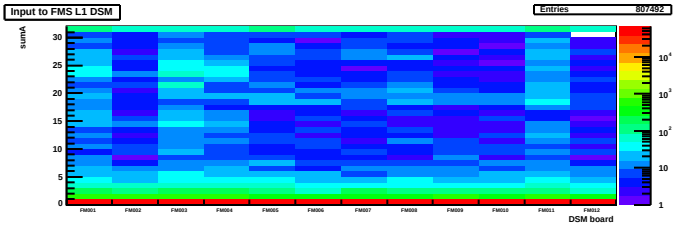

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

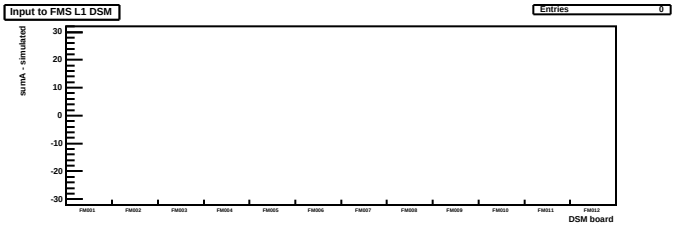

Input to FMS L1 DSM
Entries 807492
sumB
DSM board

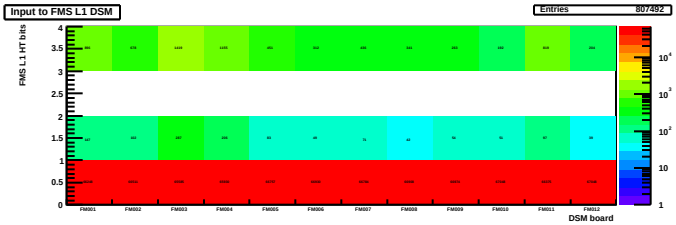

Input to FMS L1 DSM
Entries 844
sumB - simulated
DSM board

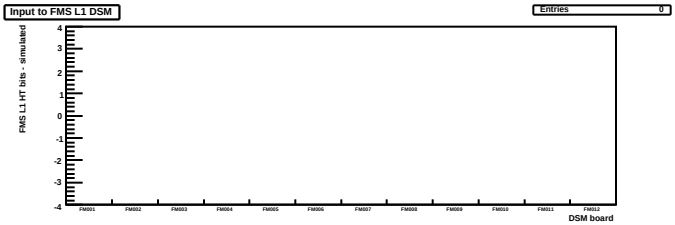

Input to FMS L1 DSM
Entries 807492
sumAB
DSM board

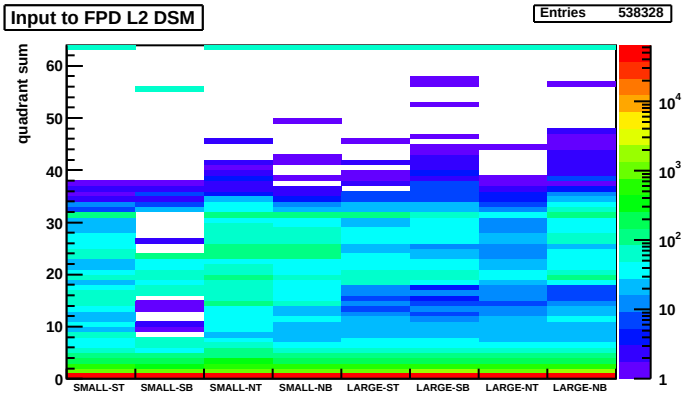

Input to FMS L1 DSM
Entries 253
sumAB - simulated
DSM board

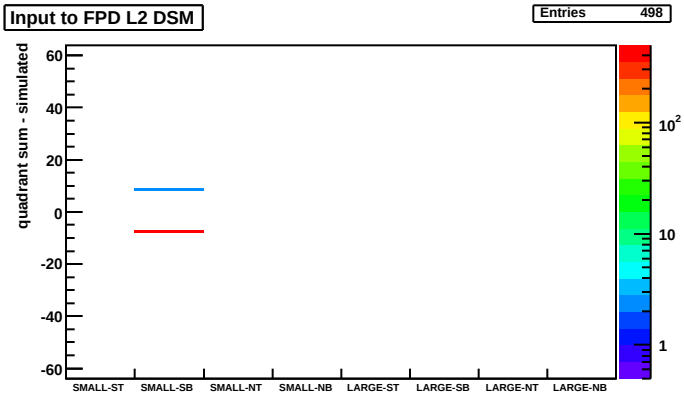

Input to FMS L1 DSM
Entries 807492
sumA
DSM board

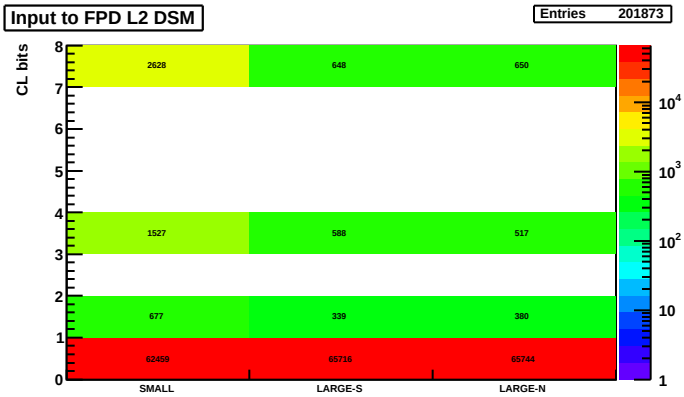

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

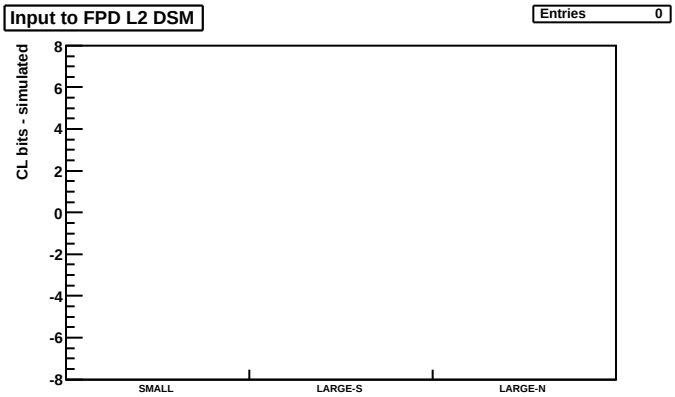

Input to FMS L1 DSM
Entries 807492
FMS L1 HT bits
DSM board

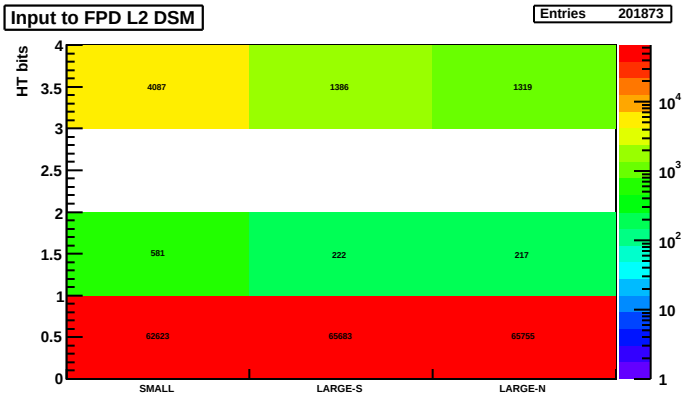

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

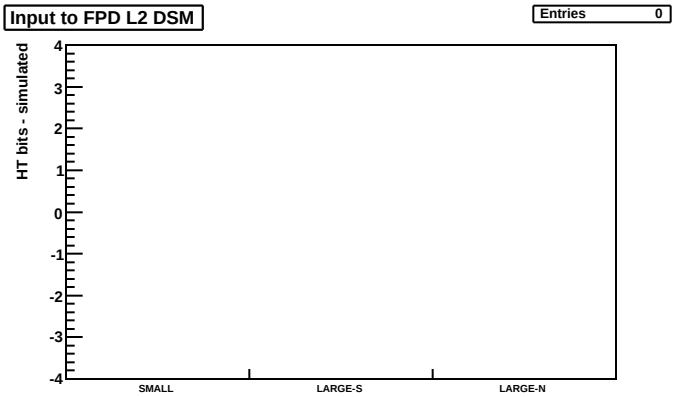

Input to FPD L2 DSM
Entries 538328
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

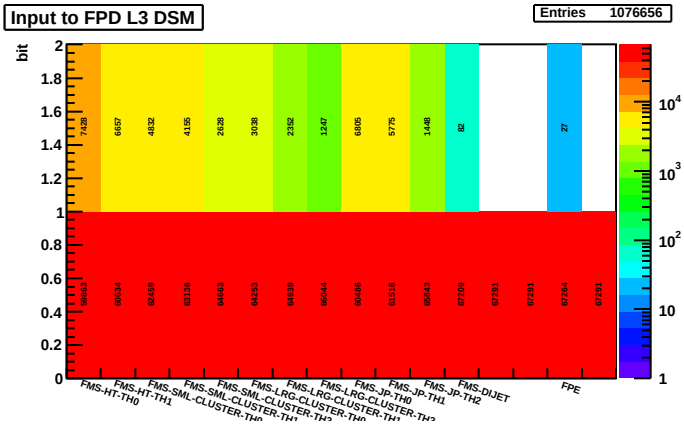

Input to FPD L2 DSM
Entries 498
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

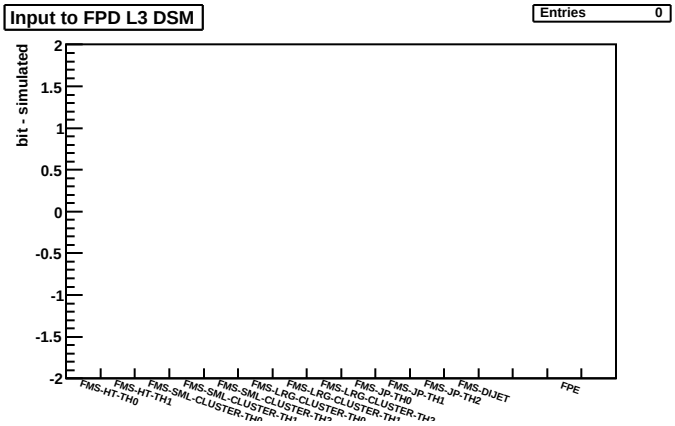

Input to FPD L2 DSM
Entries 201873
CL bits
2628
648
650
1527
588
517
677
339
380
62459
65716
65744
SMALL
LARGE-S
LARGE-N

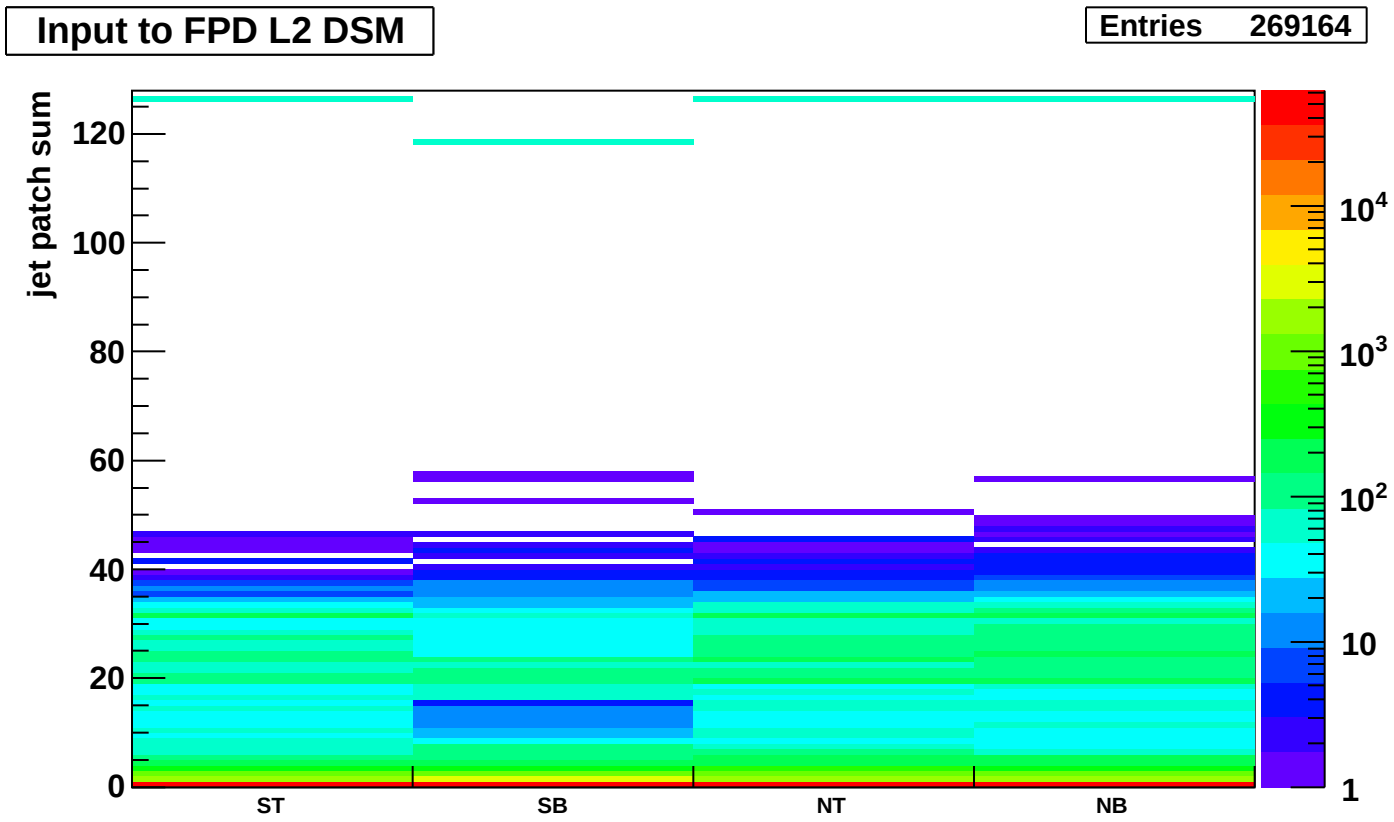

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

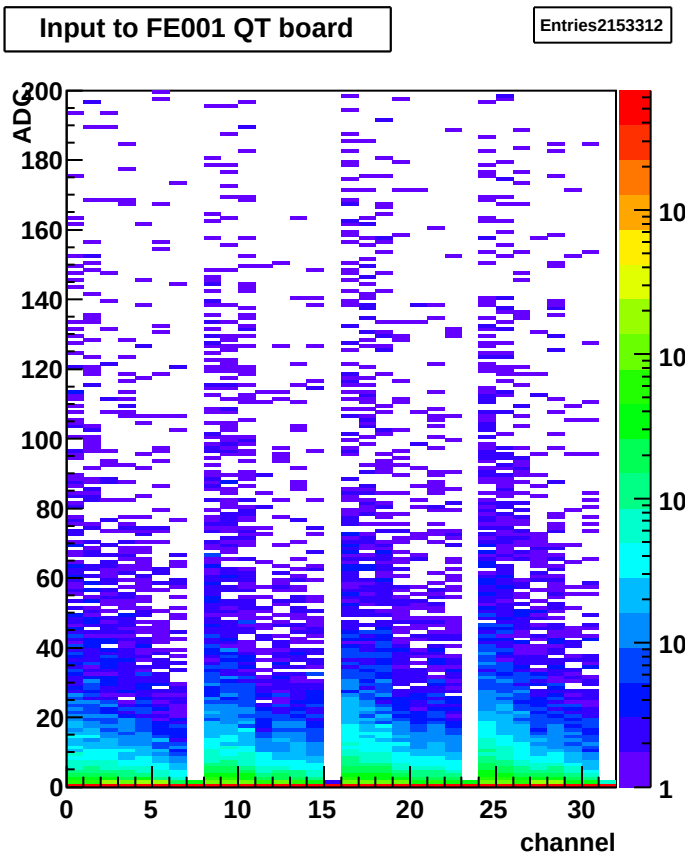

Input to FPD L2 DSM
Entries 201873
HT bits
4087
1386
1319
581
222
217
62623
65683
65755
SMALL
LARGE-S
LARGE-N

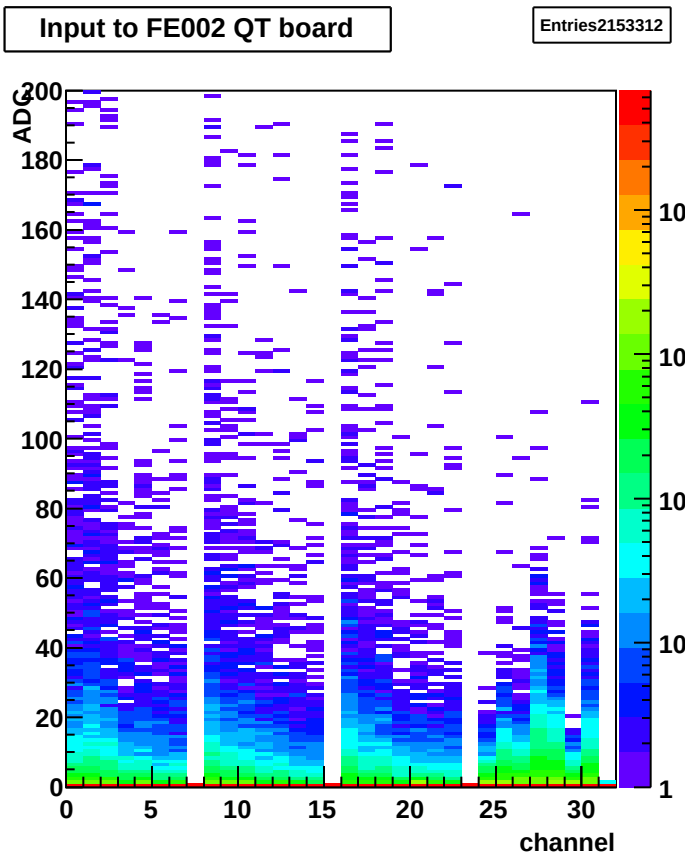

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

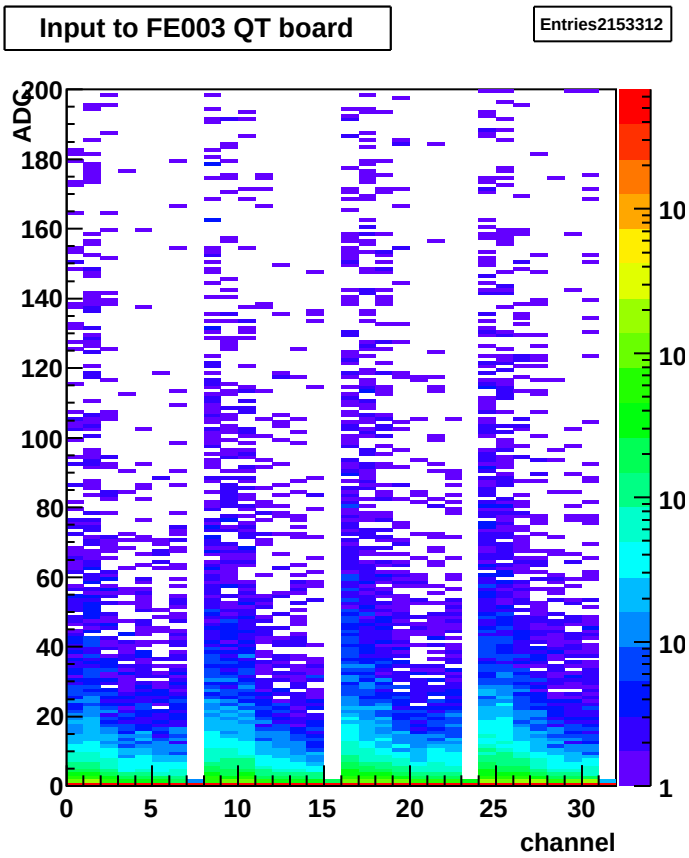

Input to FPD L3 DSM
Entries 1076656
bit
7428
6657
4832
4195
2628
3038
2352
1247
6805
5775
1448
92
27
58983
60634
62459
63136
64663
64253
64939
66044
60486
61516
65843
67209
67291
67291
67254
67291
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

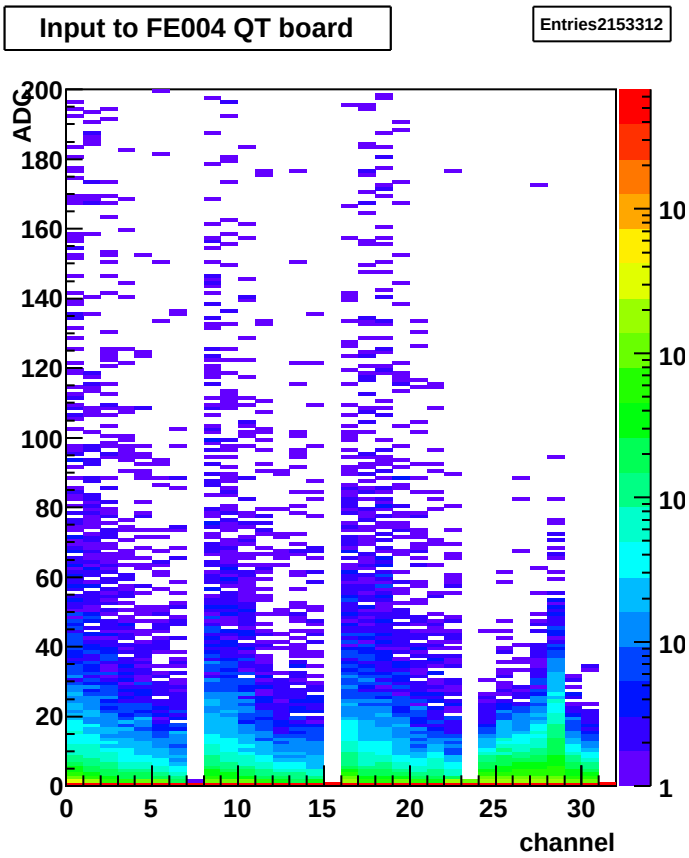

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

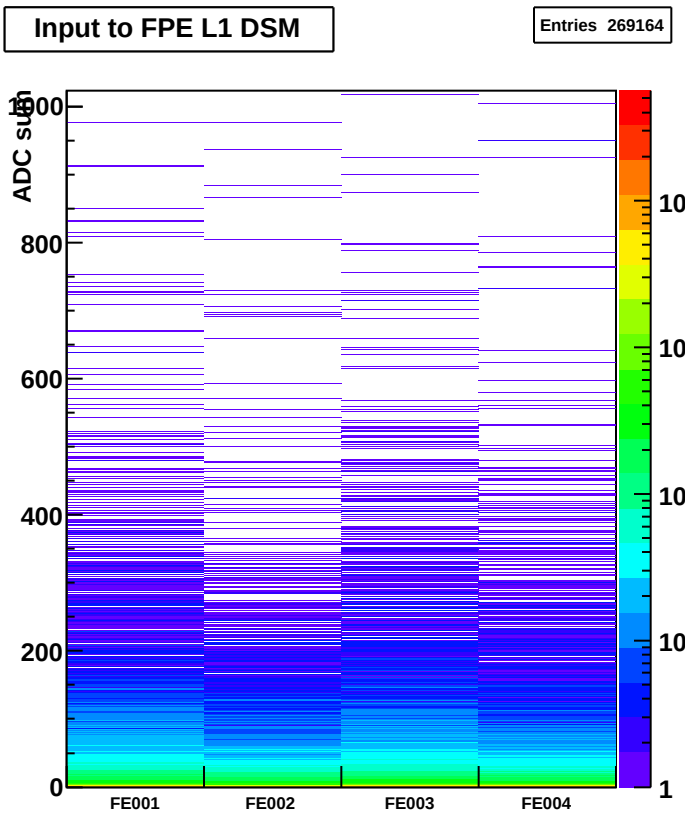

Input to FPD L2 DSM
Entries 269164
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

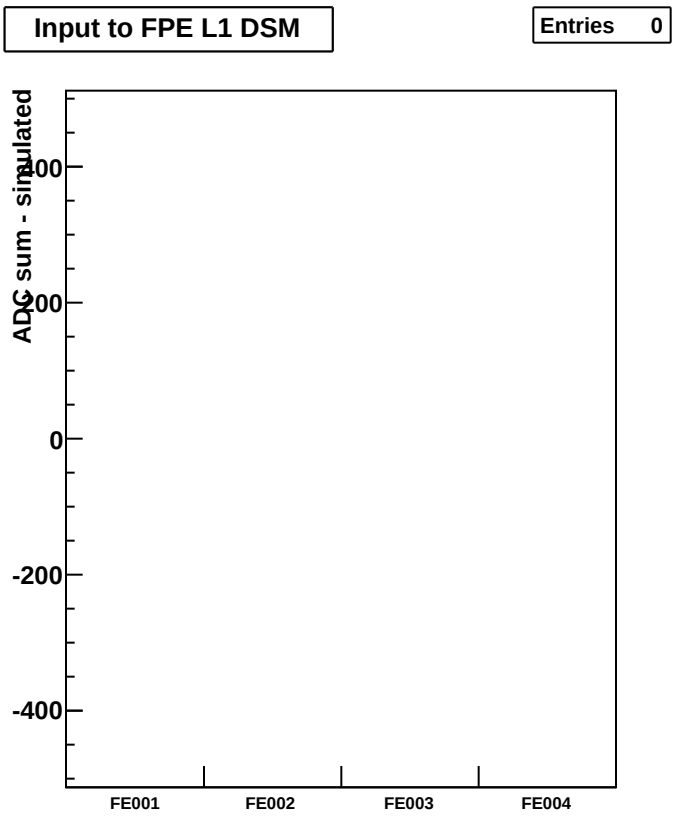

Input to FE001 QT board
Entries2153312
ADC
channel

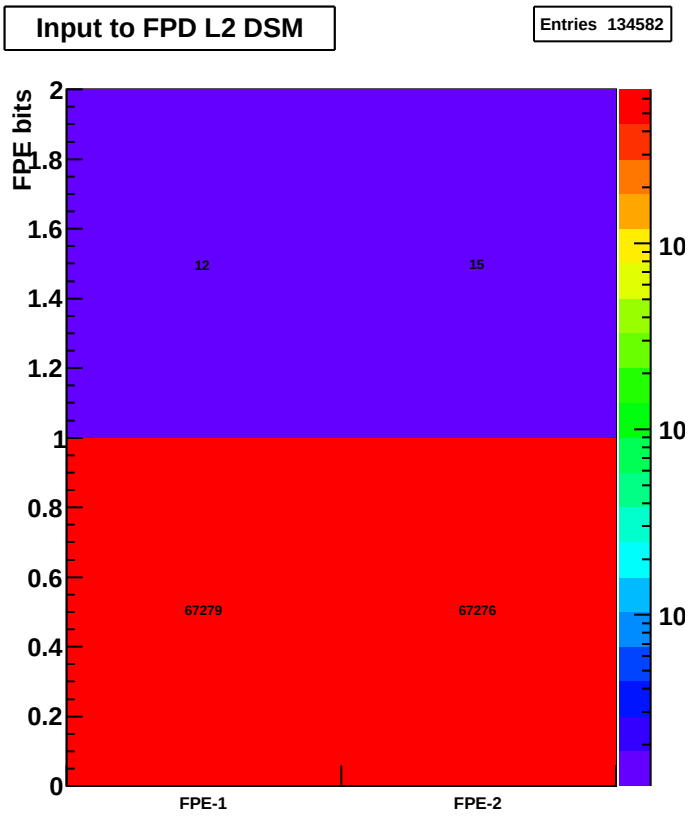

Input to FE002 QT board
Entries2153312
ADC
channel

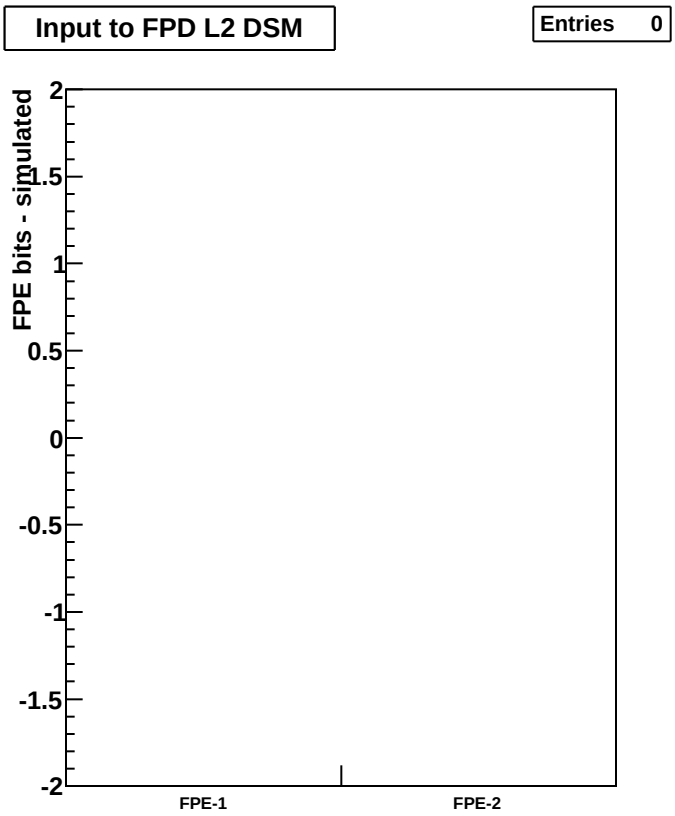

Input to FE003 QT board
Entries2153312
ADC
channel


Input to FE004 QT board
Entries2153312
ADC
channel


Input to FPE L1 DSM
Entries 269164
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 134582
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
12
15
67279
67276
FPE-1
FPE-2
10
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2