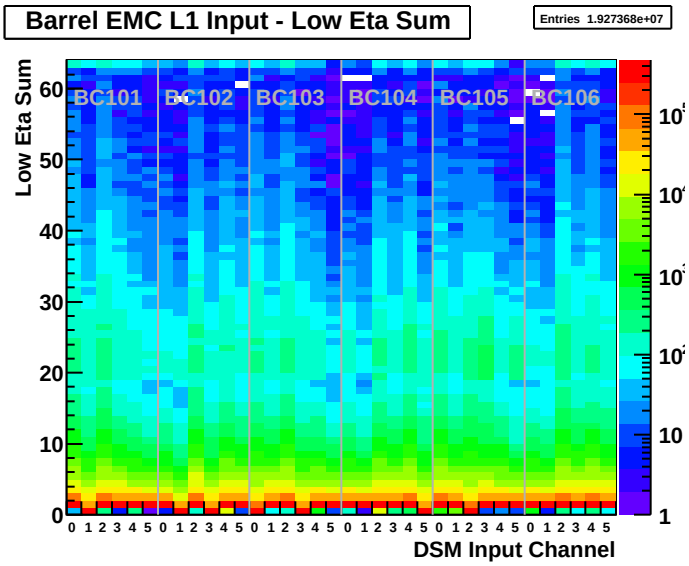
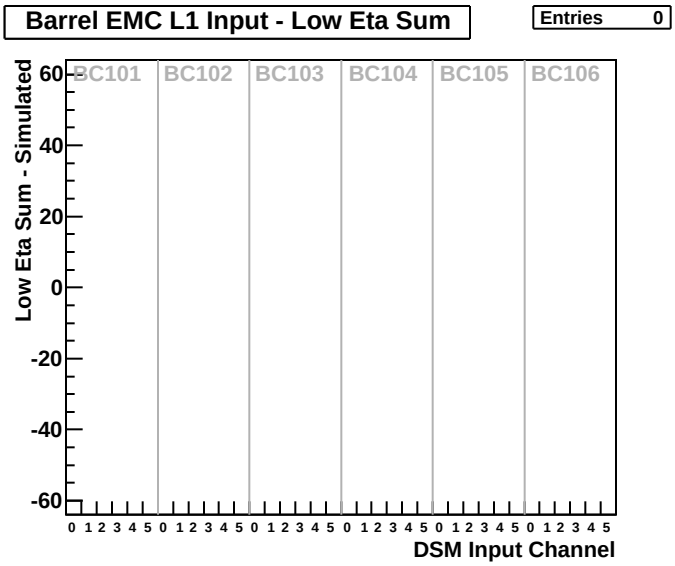
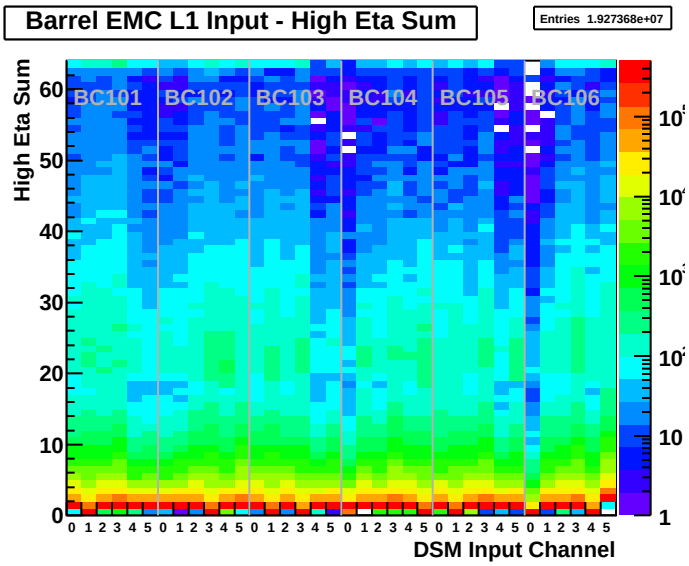
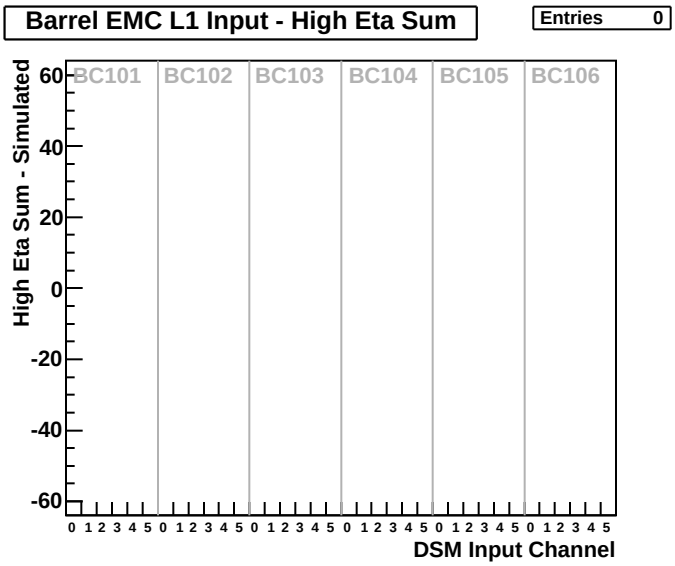
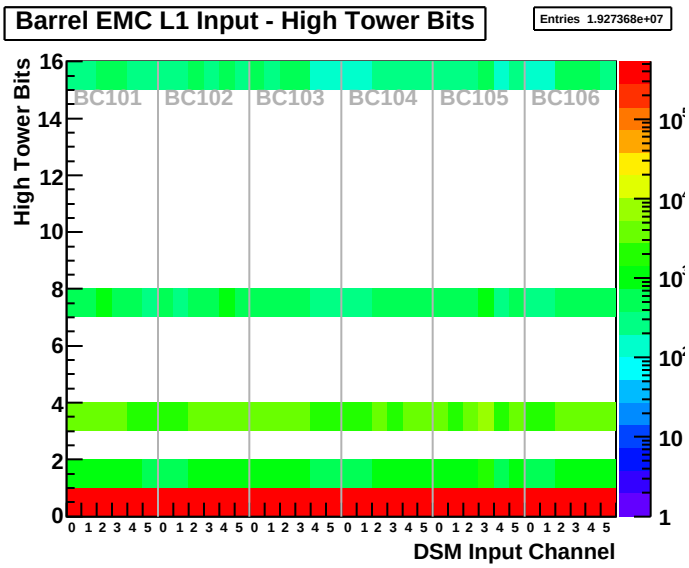
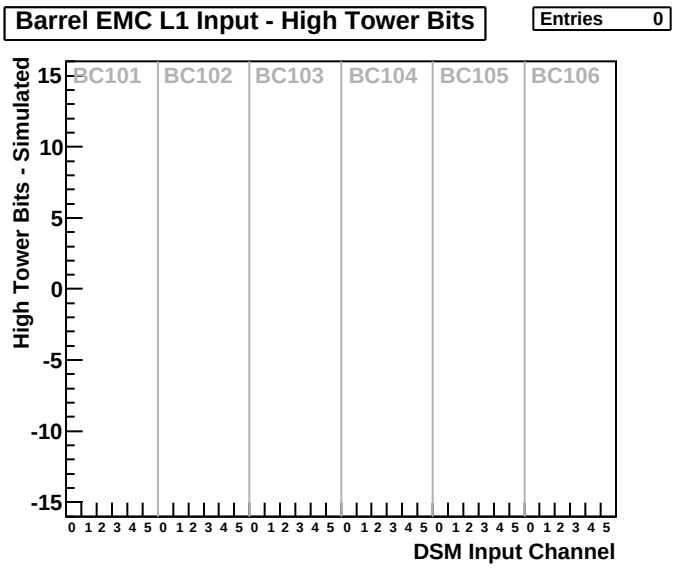

Barrel EMC L1 Input - Low Eta Sum
Entries 1.927368e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

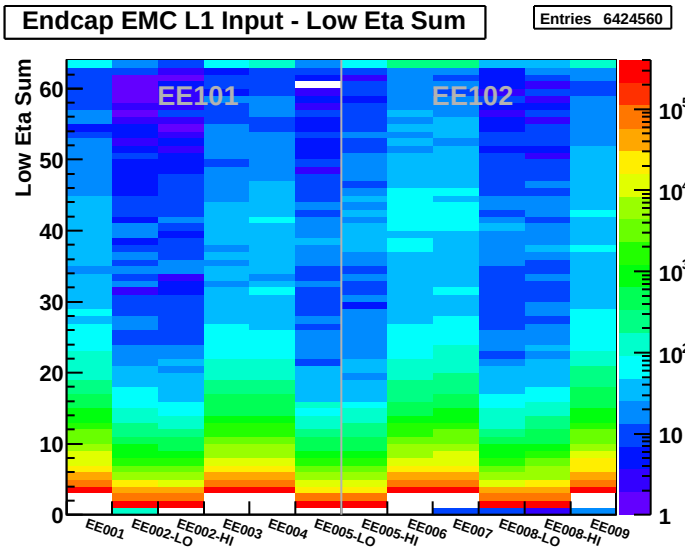

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

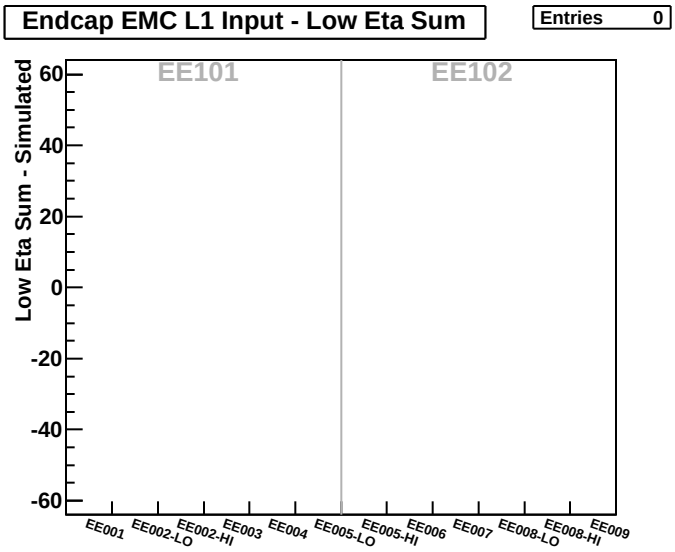

Barrel EMC L1 Input - High Eta Sum
Entries 1.927368e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

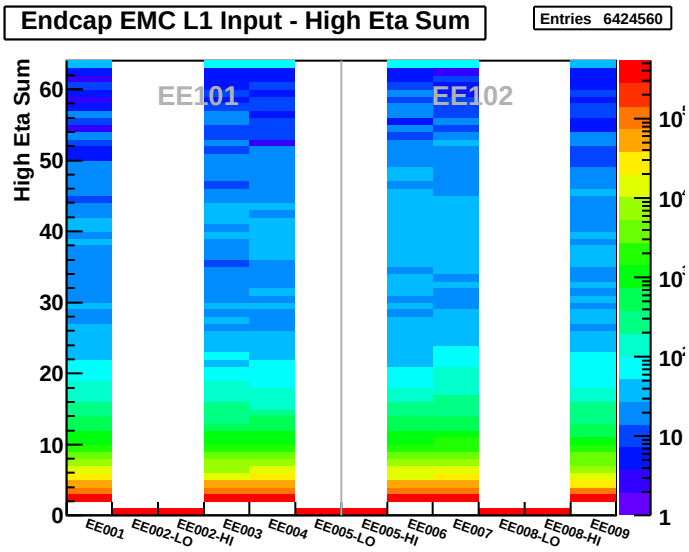

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

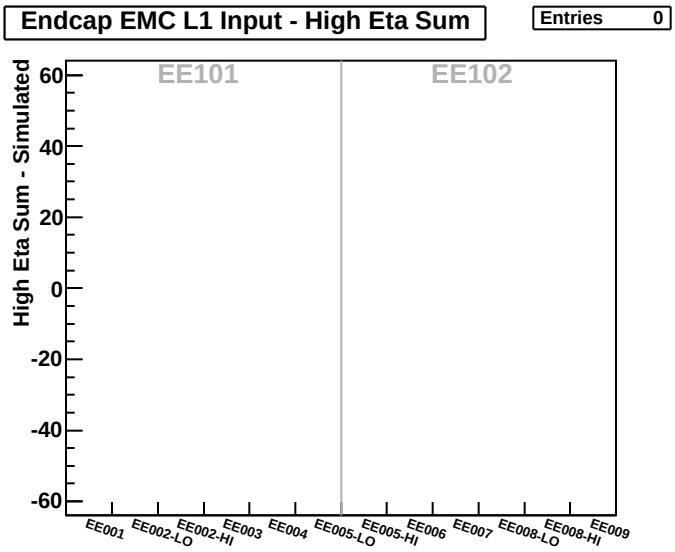

Barrel EMC L1 Input - High Tower Bits
Entries 1.927368e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

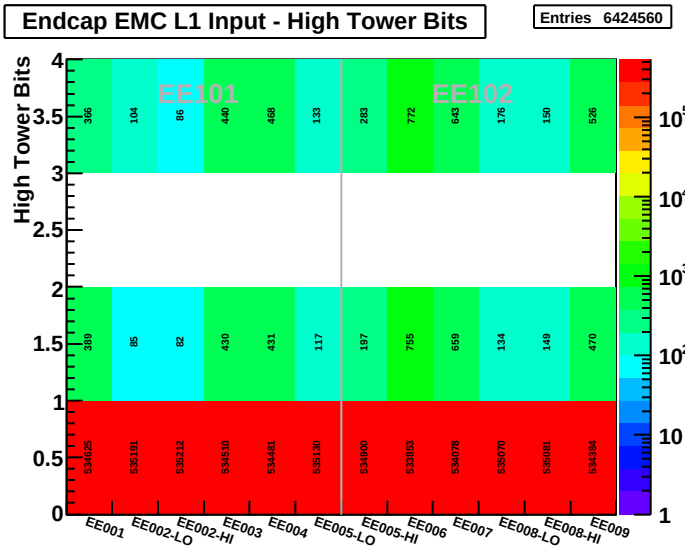

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

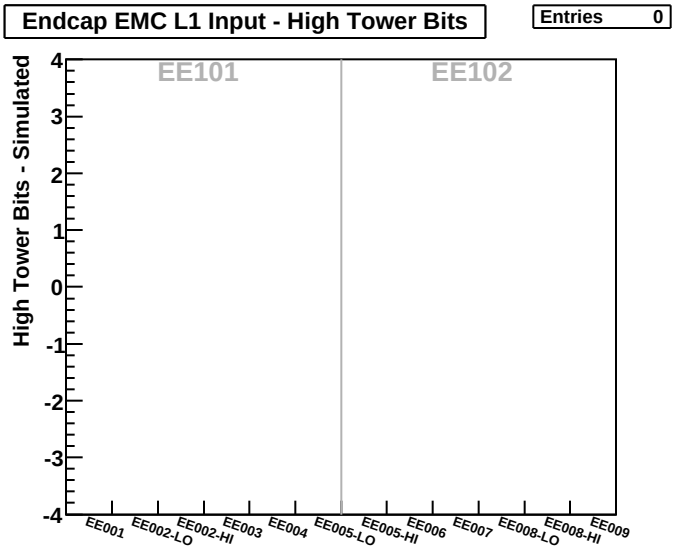

Endcap EMC L1 Input - Low Eta Sum
Entries 6424560
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

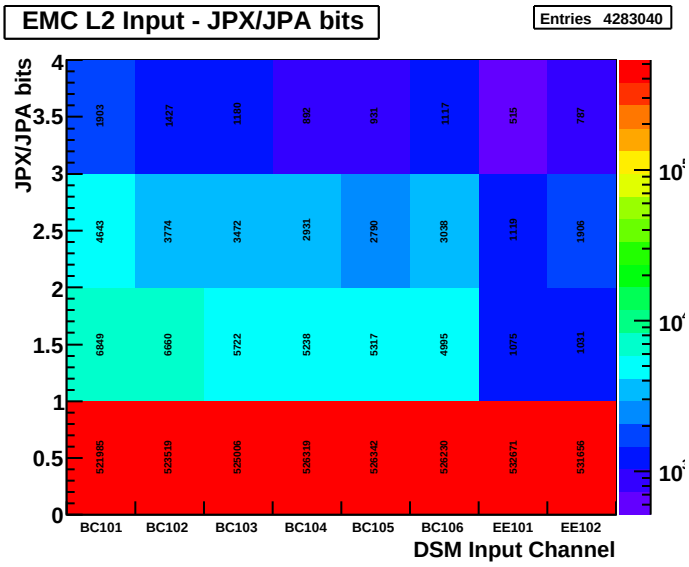

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

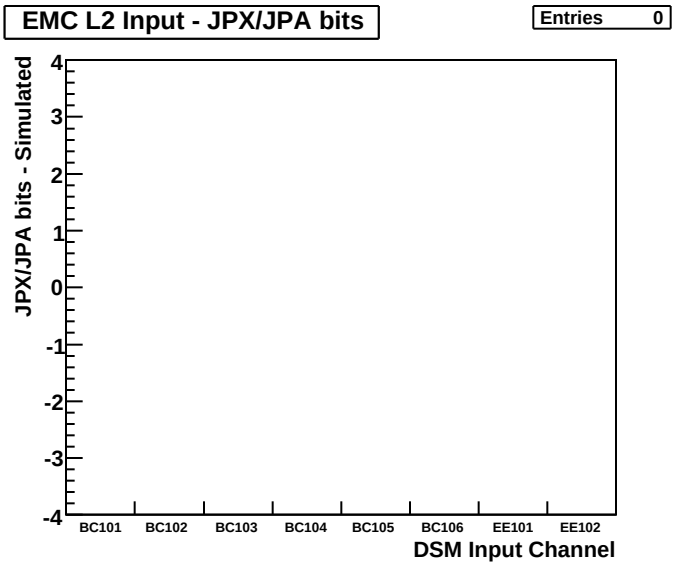

Endcap EMC L1 Input - High Eta Sum
Entries 6424560
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

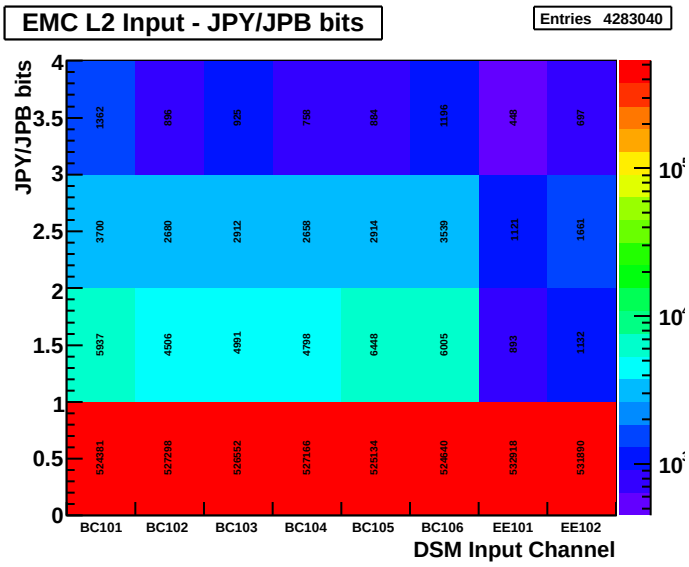

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

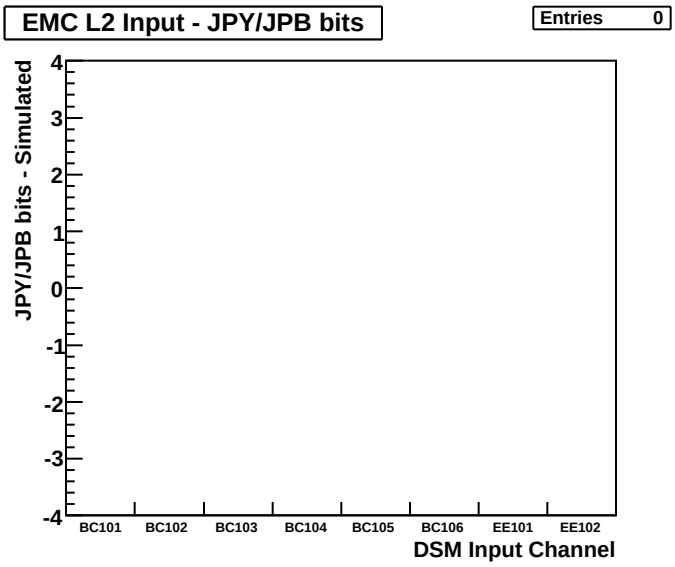

Endcap EMC L1 Input - High Tower Bits
Entries 6424560
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

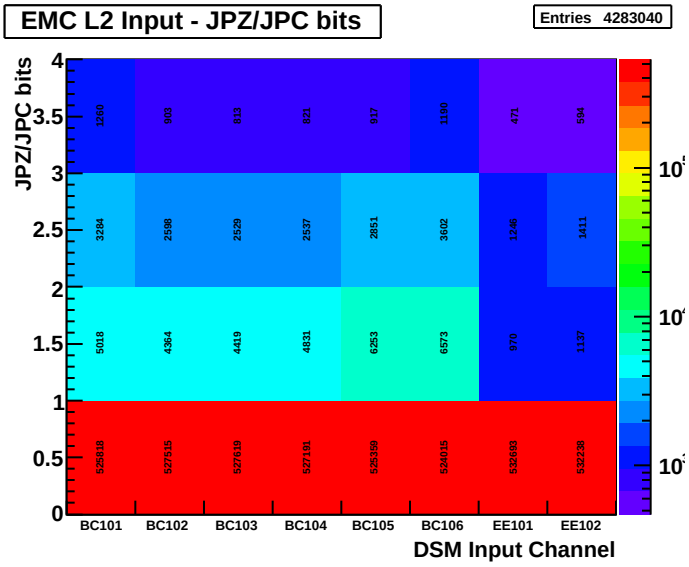

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

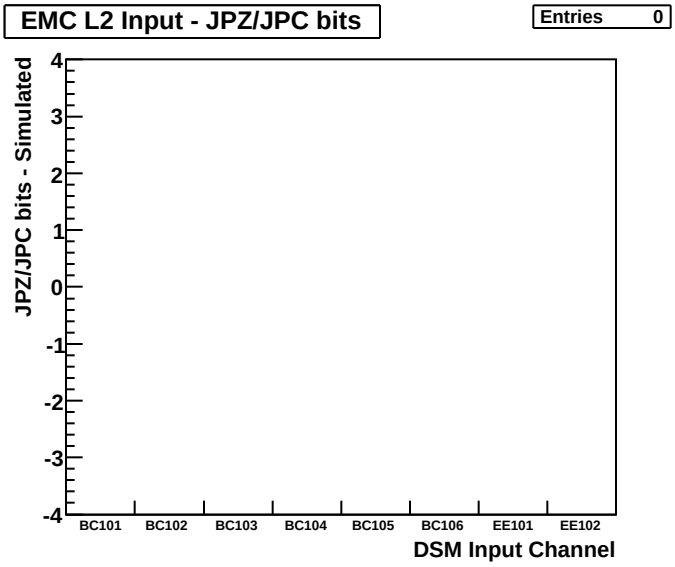

EMC L2 Input - JPX/JPA bits
Entries 4283040
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1903 1427 1180 892 931 1117 515 787
4643 3774 3472 2931 2790 3038 1119 1906
6849 6660 5722 5238 5317 4995 1075 1031
521985 523519 525006 528319 526342 526230 532671 531656

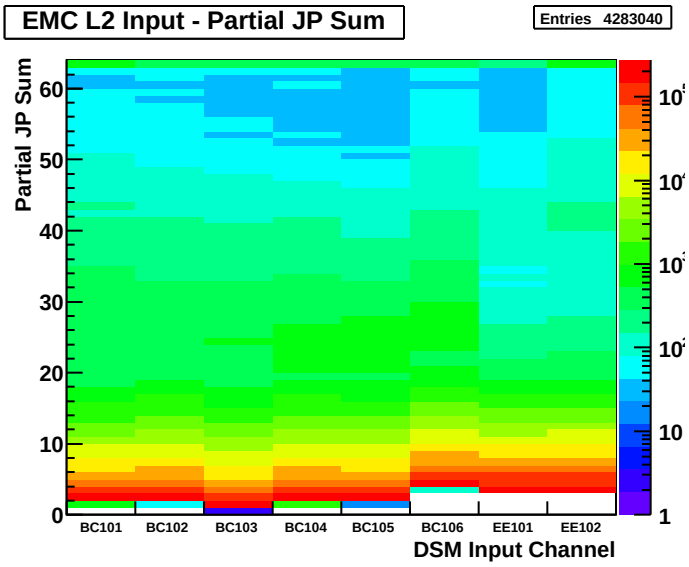

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

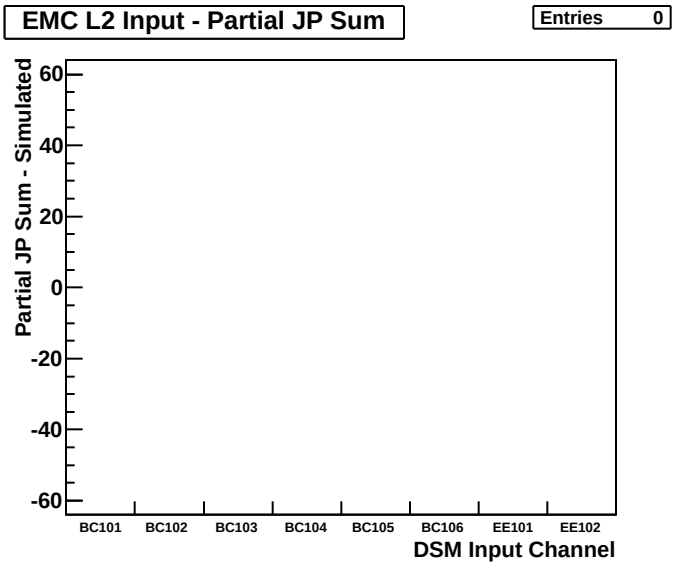

EMC L2 Input - JPY/JPB bits
Entries 4283040
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1362 896 925 768 834 1196 448 697
3700 2680 2912 2658 2914 3539 1121 1661
5937 4506 4991 4798 6448 6005 893 1132
524381 527298 526552 527166 525134 524640 532918 531890

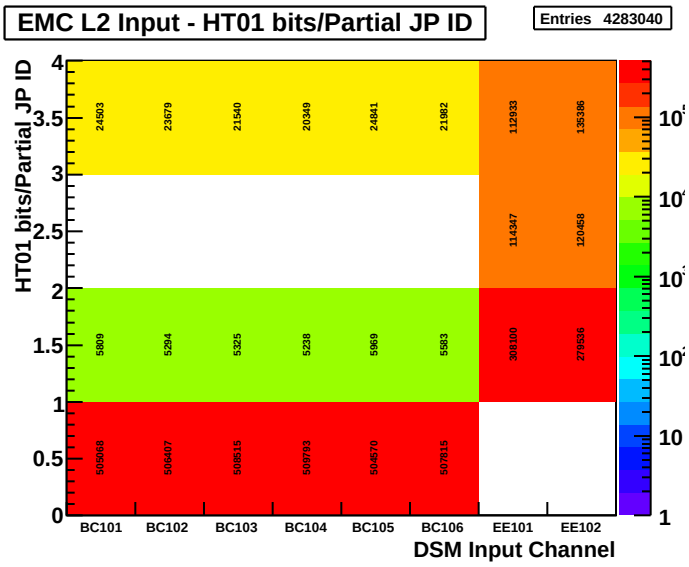

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

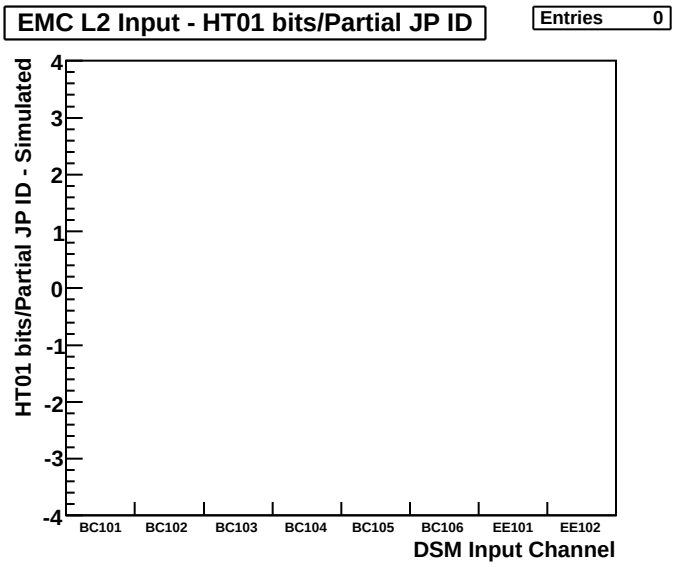

EMC L2 Input - JPZ/JPC bits
Entries 4283040
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1260 903 813 821 917 1190 471 594
3284 2598 2529 2537 2951 3602 1246 1411
5018 4364 4419 4831 6253 6573 970 1137
525318 527515 527619 527191 525399 524015 532693 532238

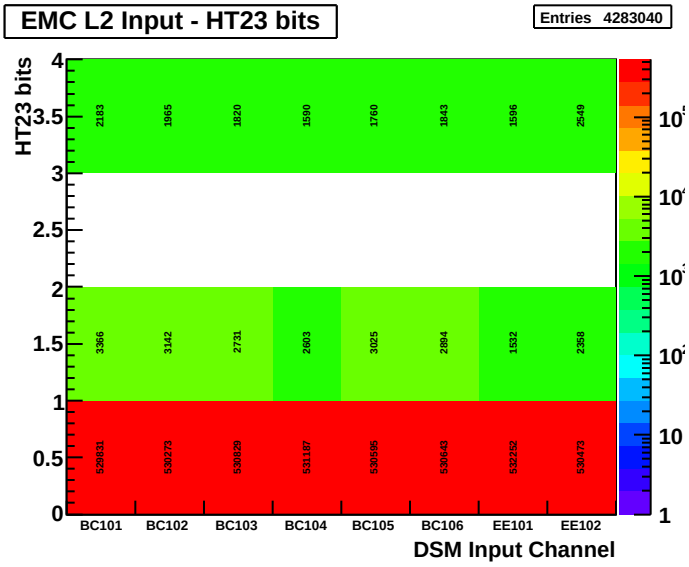

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

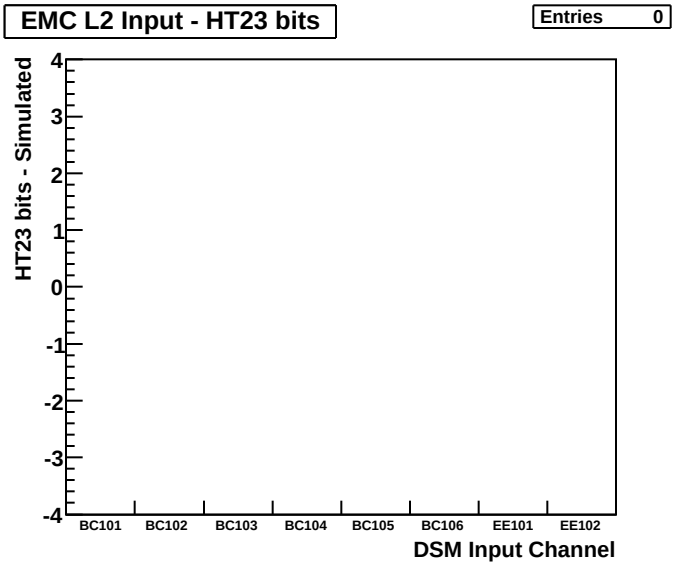

EMC L2 Input - Partial JP Sum
Entries 4283040
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

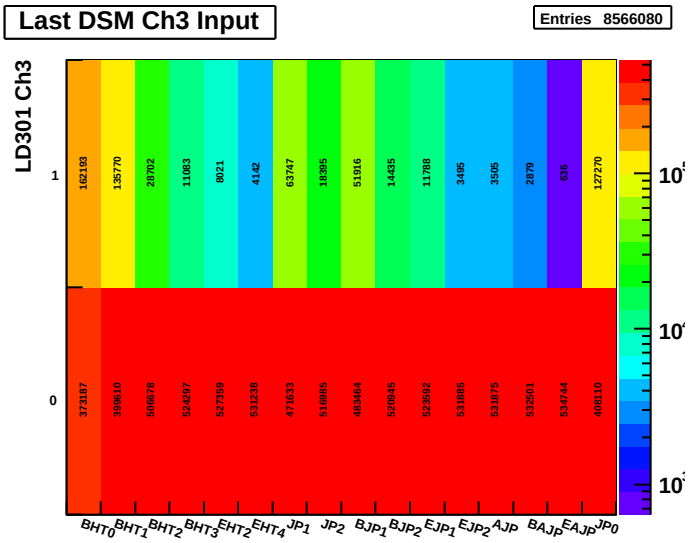

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

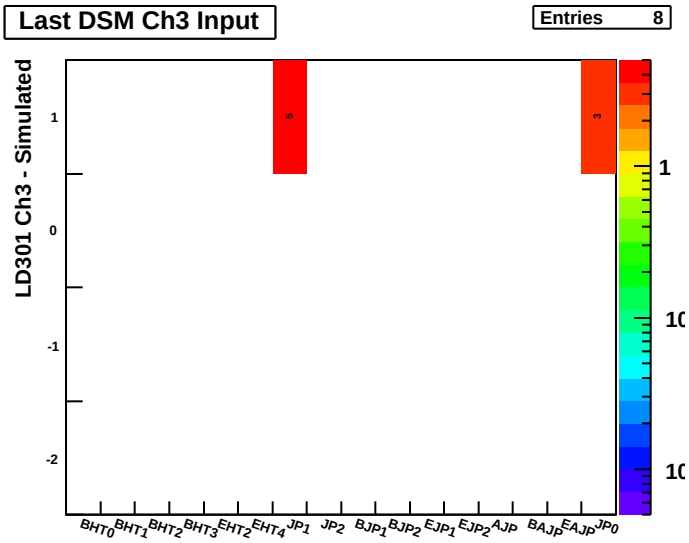

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4283040
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

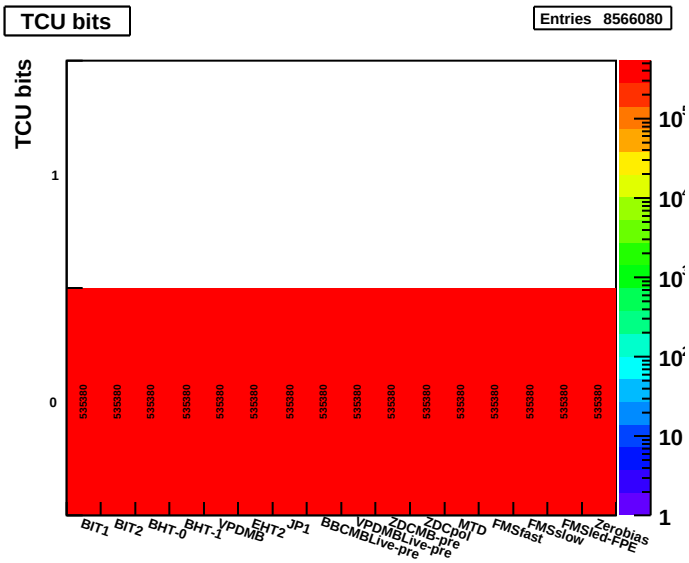

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

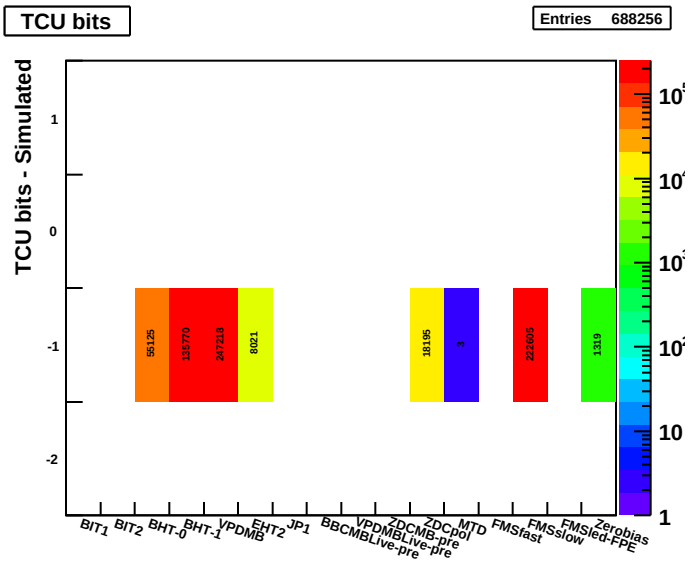

EMC L2 Input - HT23 bits
Entries 4283040
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

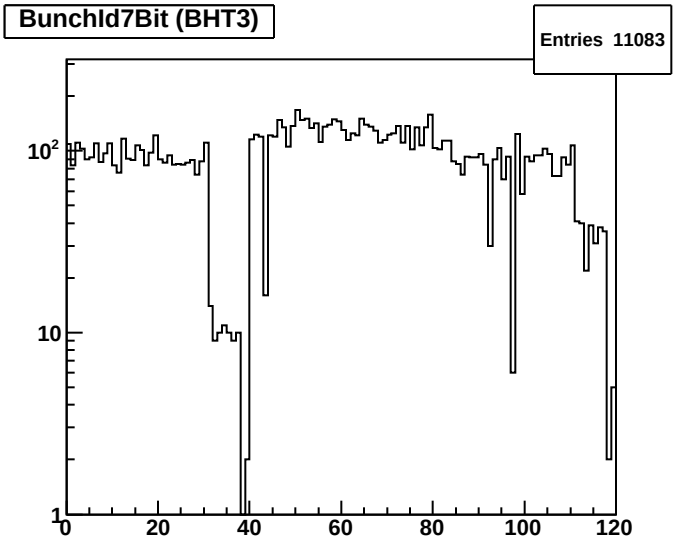

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

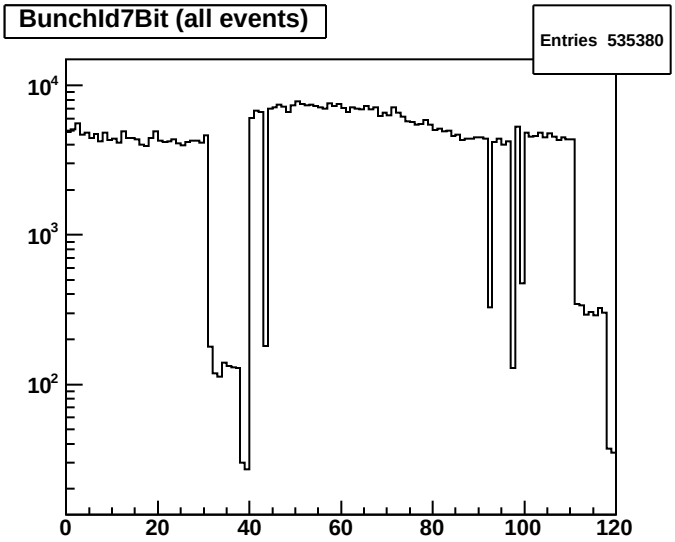

Last DSM Ch3 Input
Entries 8566080
LD301 Ch3
162193 135770 28702 11083 8021 4142 63747 18395 51916 14435 11788 3495 3505 2879 636 127270
373187 399610 506678 524297 527359 531238 471633 516935 483464 520945 522592 531885 531875 532501 534744 408110
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

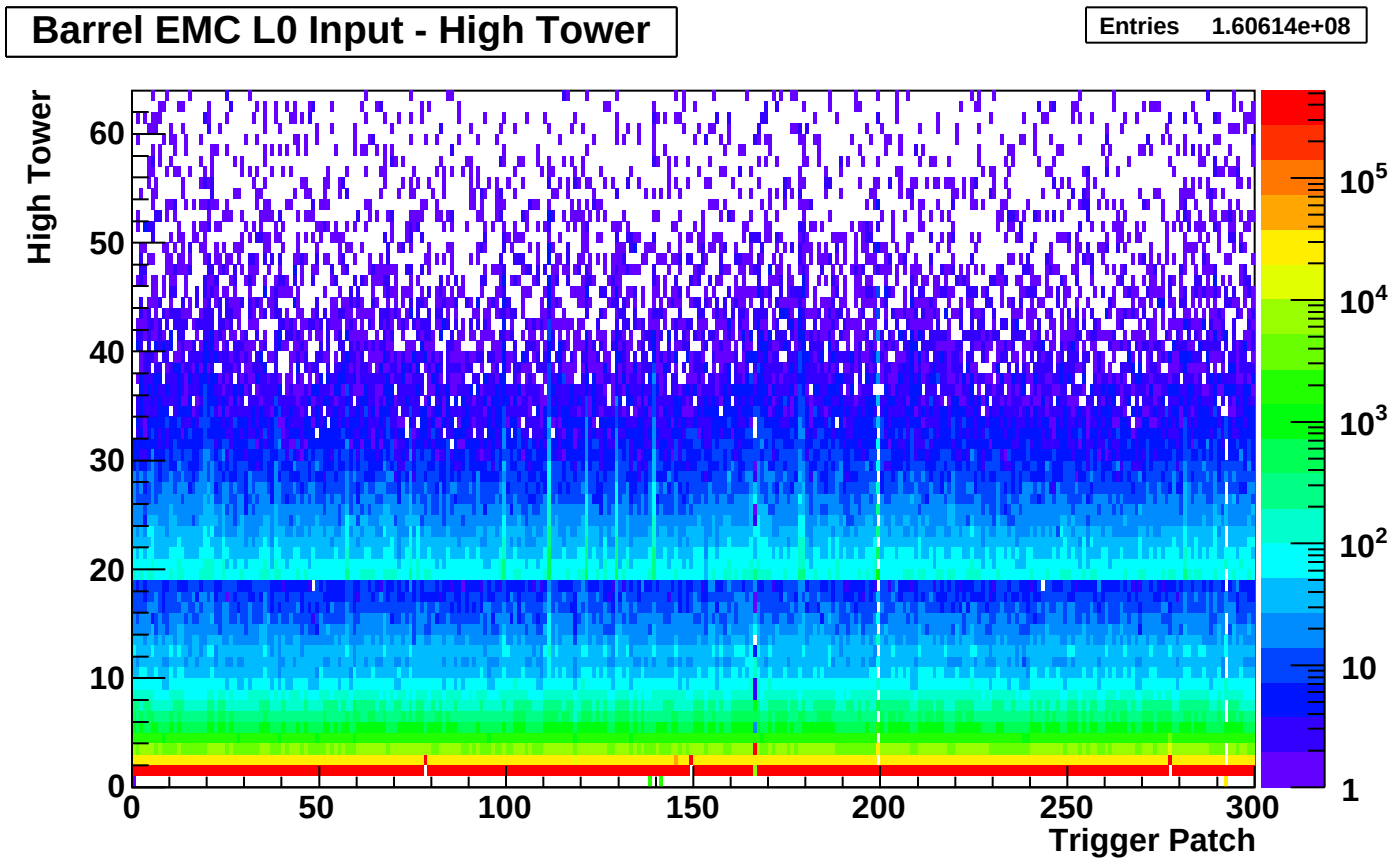

Last DSM Ch3 Input
Entries 8
LD301 Ch3 - Simulated
5
3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

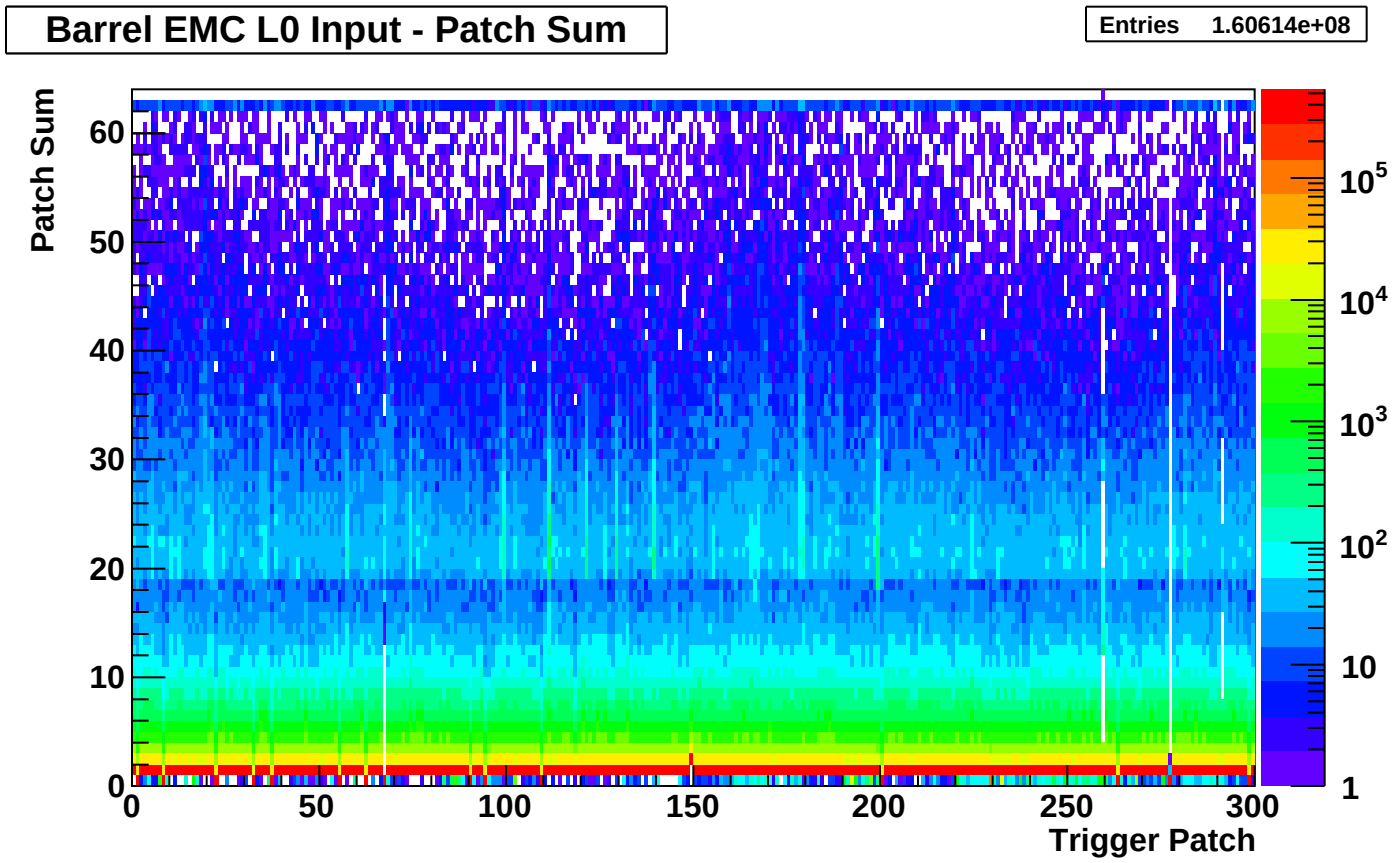

TCU bits
Entries 8566080
535380
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

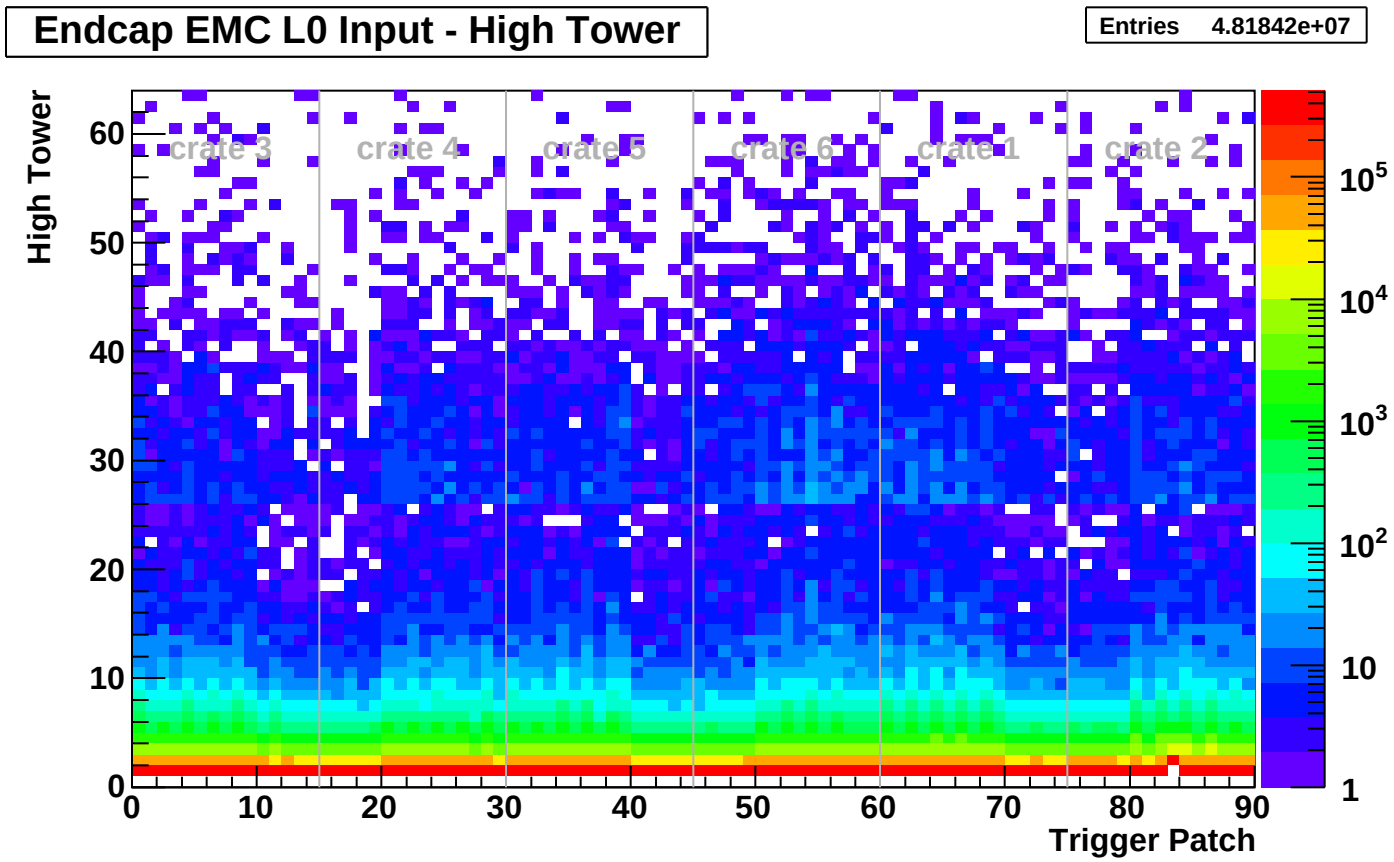

TCU bits
Entries 688256
TCU bits - Simulated
55125 135770 247218 8021 18195 3 222605 1319
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

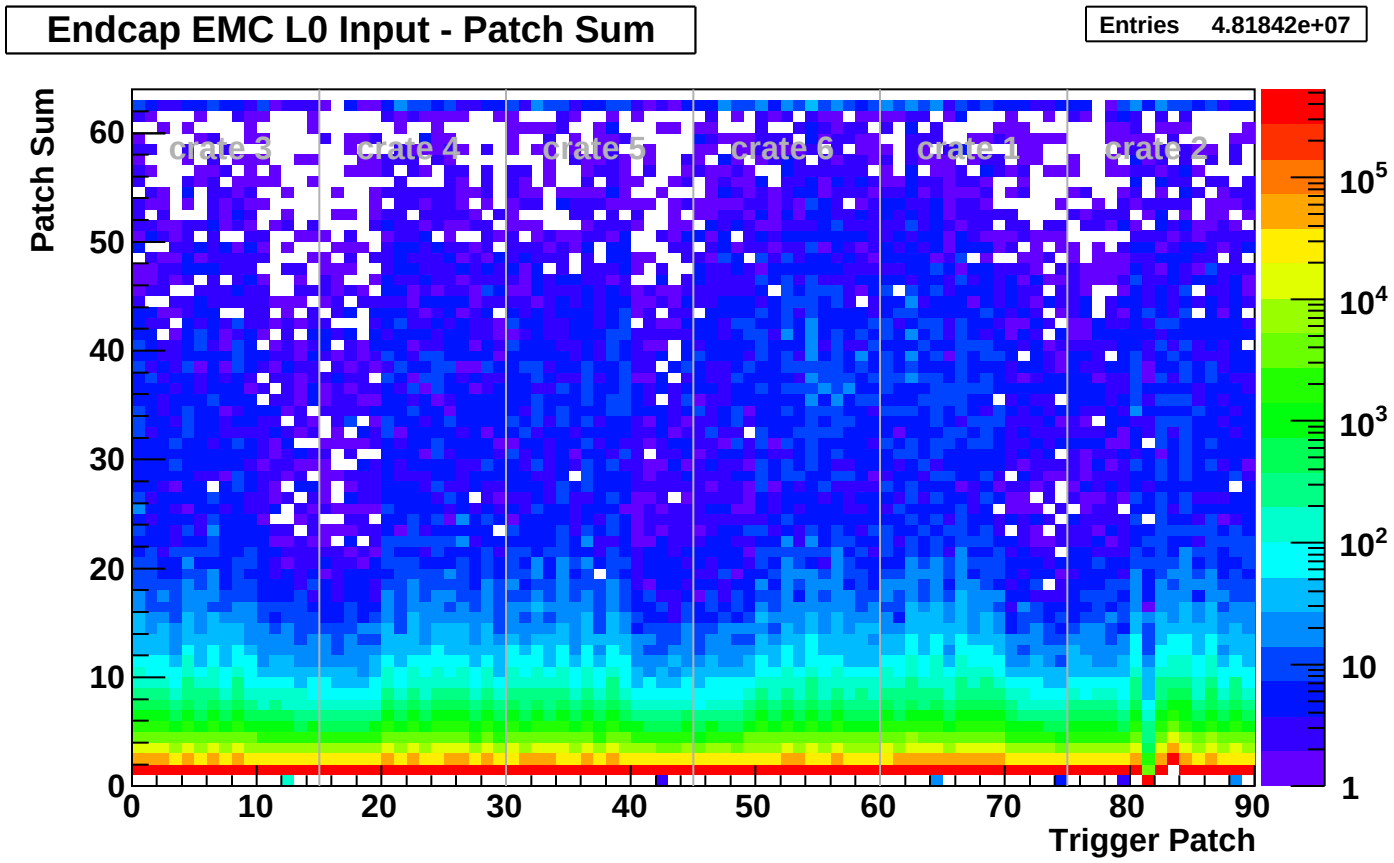

BunchId7Bit (BHT3)
Entries 11083

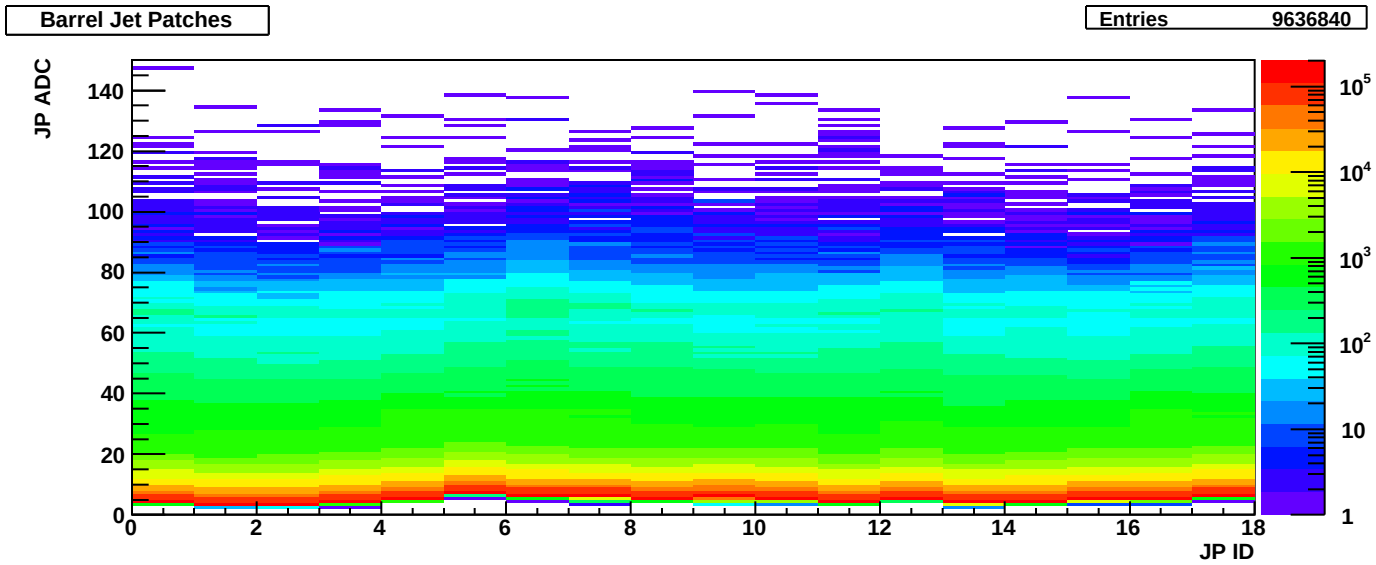

BunchId7Bit (all events)
Entries 535380

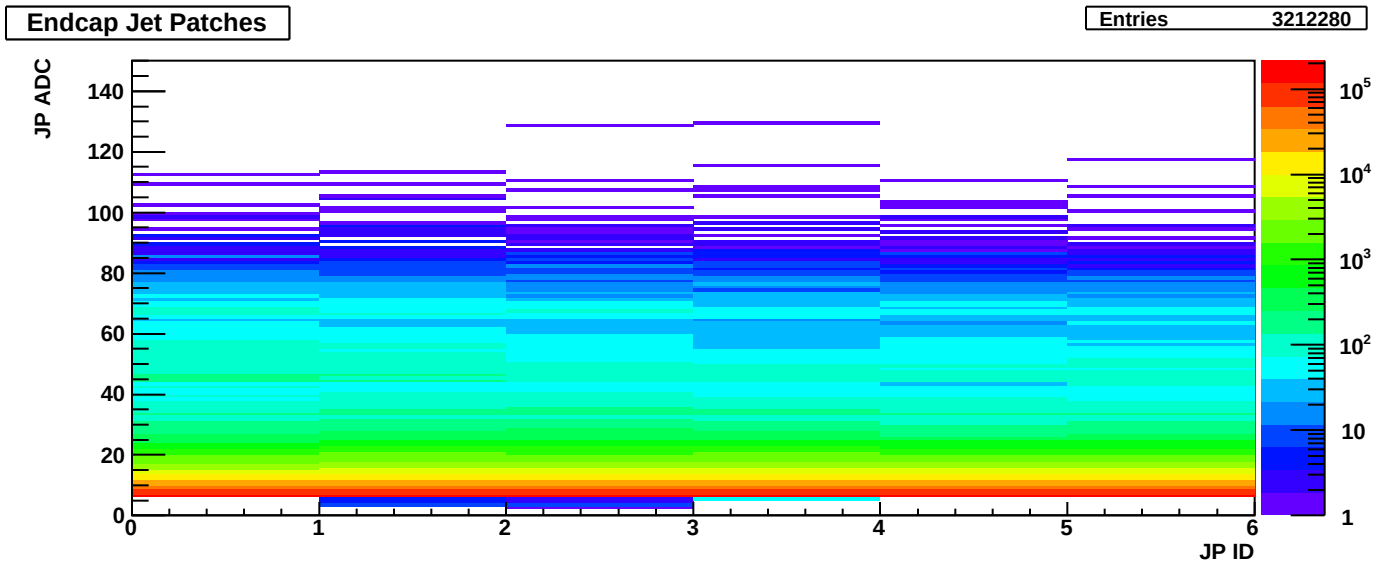

Barrel EMC L0 Input - High Tower
Entries 1.60614e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

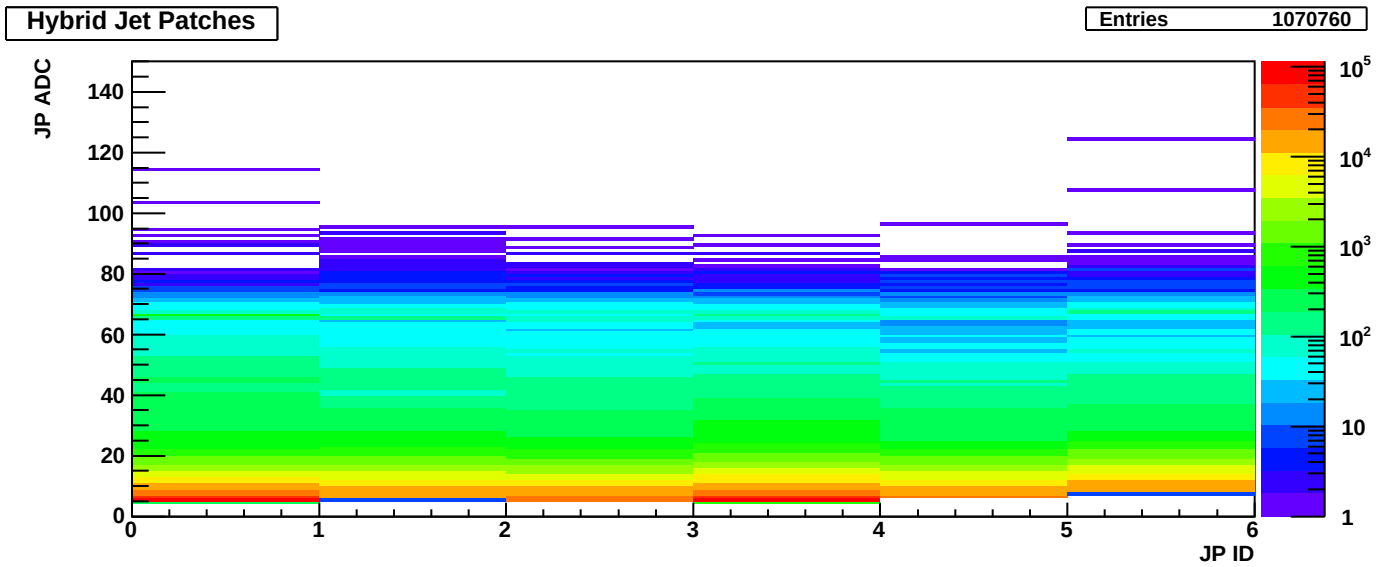

Barrel EMC L0 Input - Patch Sum
Entries 1.60614e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

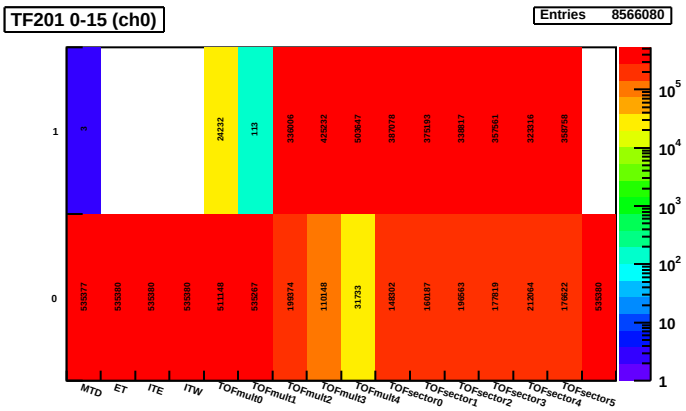

Endcap EMC L0 Input - High Tower
Entries 4.81842e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

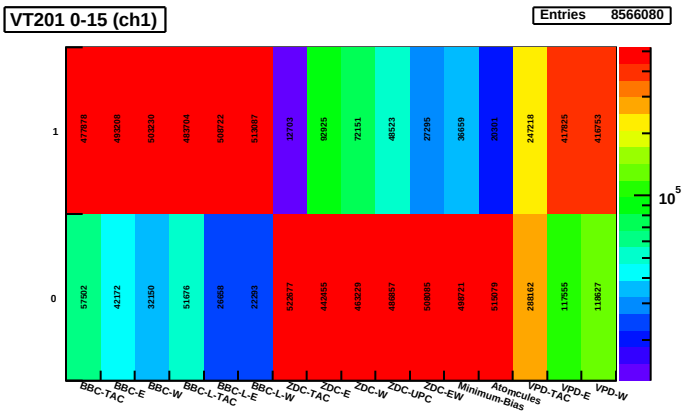

Endcap EMC L0 Input - Patch Sum
Entries 4.81842e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

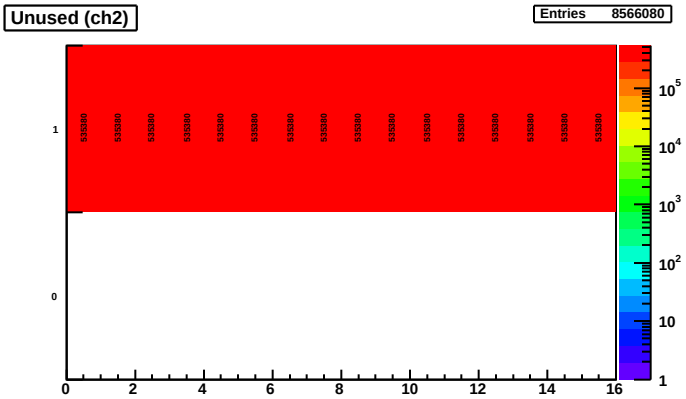

Barrel Jet Patches
Entries 9636840
JP ADC
JP ID

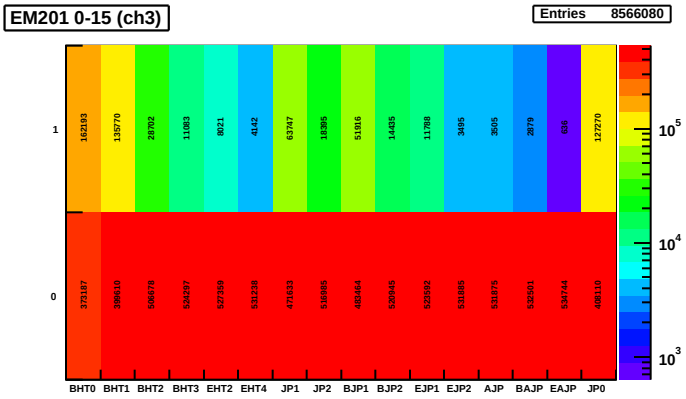

Endcap Jet Patches
Entries 3212280
JP ADC
JP ID

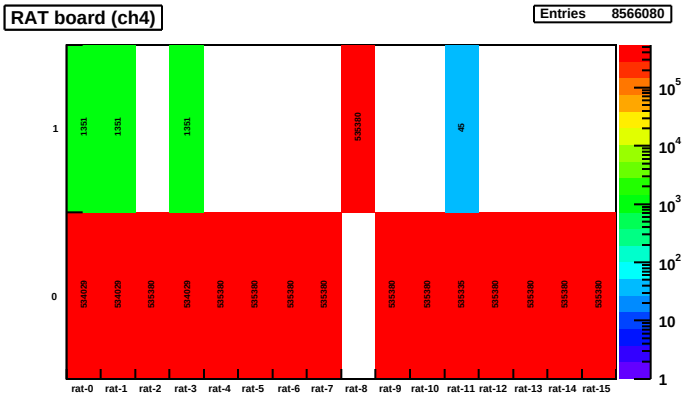

Hybrid Jet Patches
Entries 1070760
JP ADC
JP ID

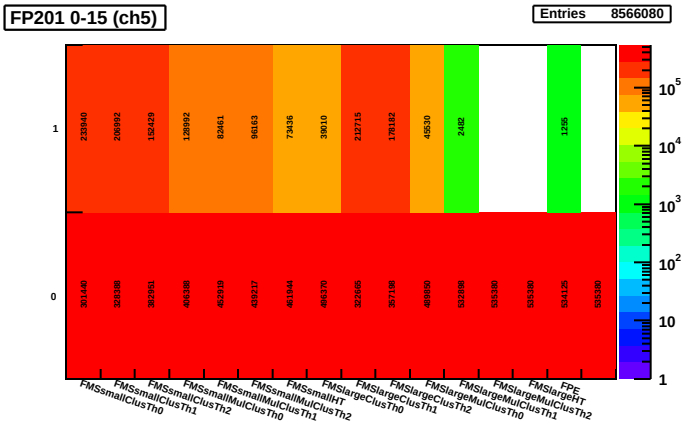

TF201 0-15 (ch0)
Entries 8566080
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

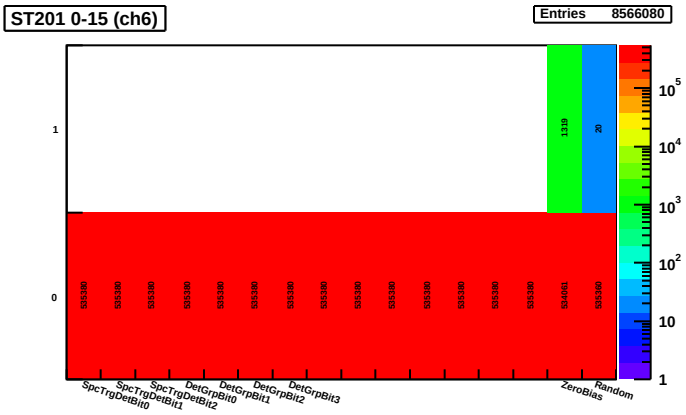

VT201 0-15 (ch1)
Entries 8566080
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

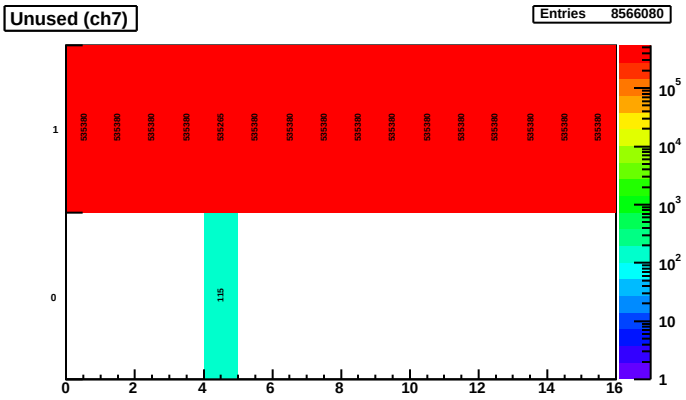

Unused (ch2)
Entries 8566080

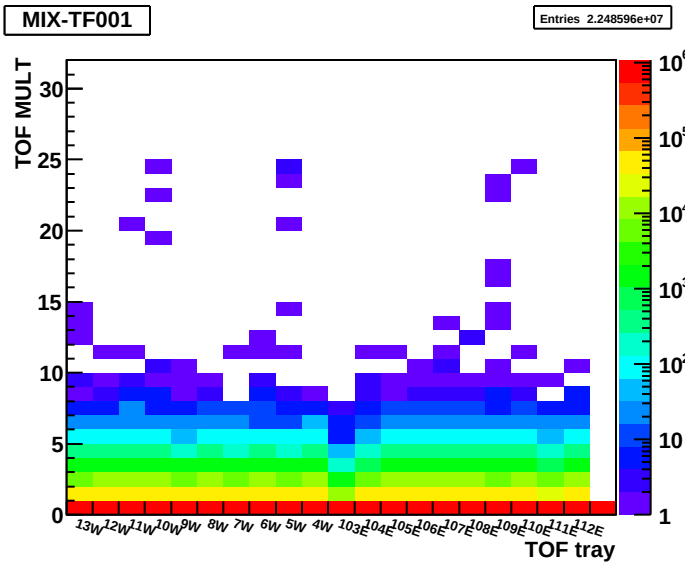

EM201 0-15 (ch3)
Entries 8566080
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

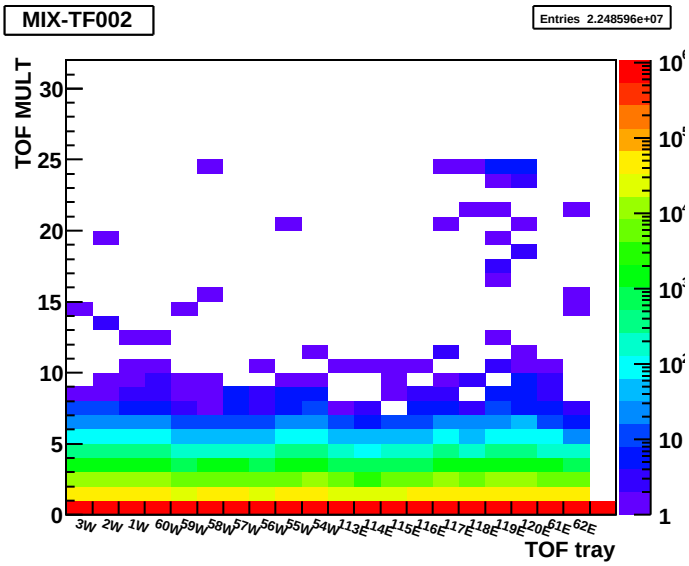

RAT board (ch4)
Entries 8566080
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

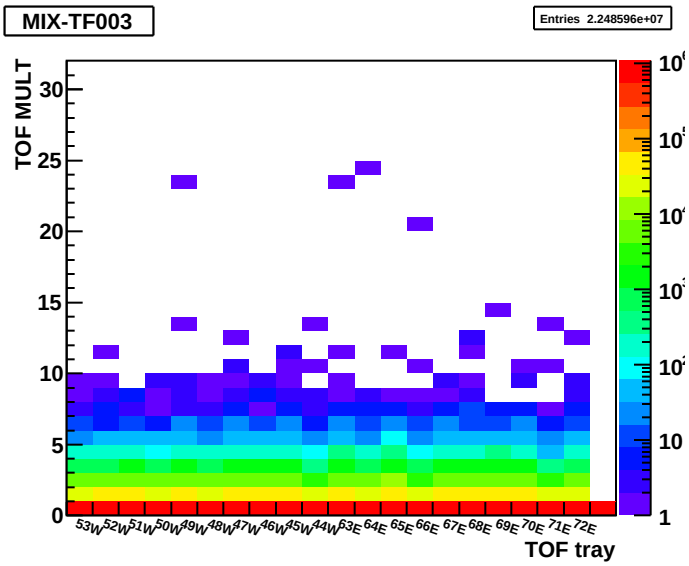

FP201 0-15 (ch5)
Entries 8566080
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

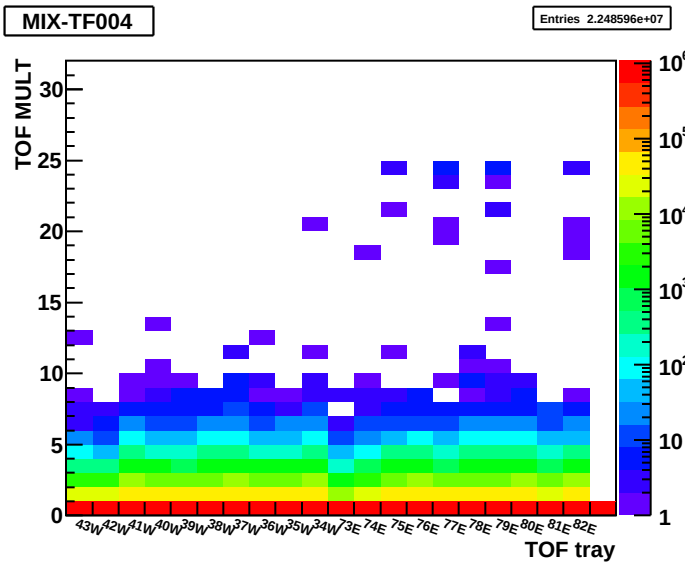

ST201 0-15 (ch6)
Entries 8566080
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

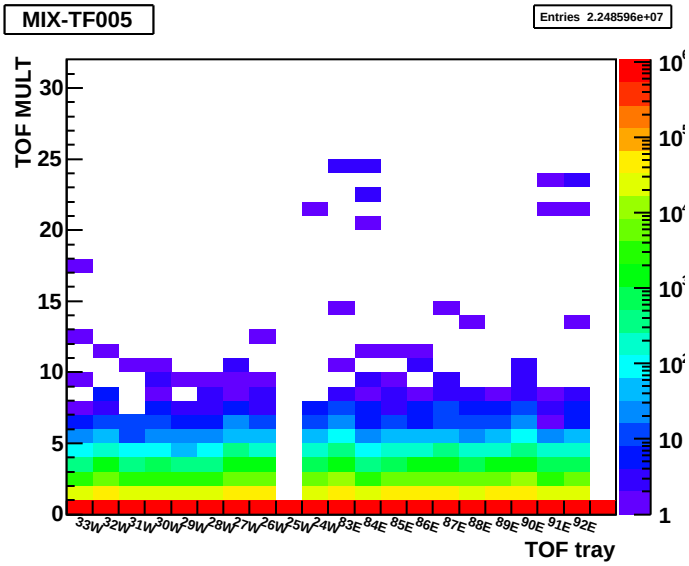

Unused (ch7)
Entries 8566080

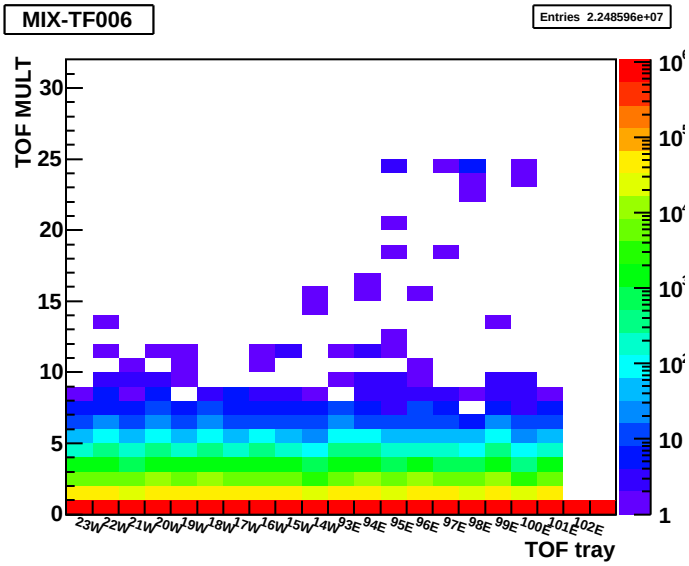

MIX-TF001
Entries 2.248596e+07
TOF MULT
TOF tray

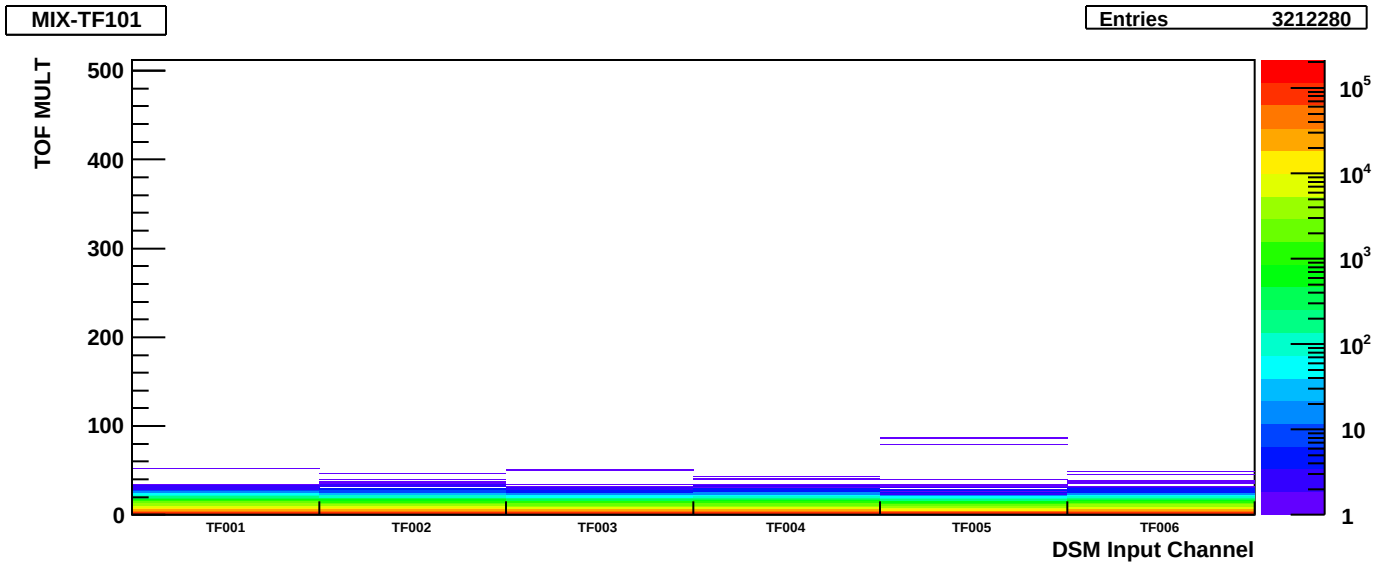

MIX-TF002
Entries 2.248596e+07
TOF MULT
TOF tray

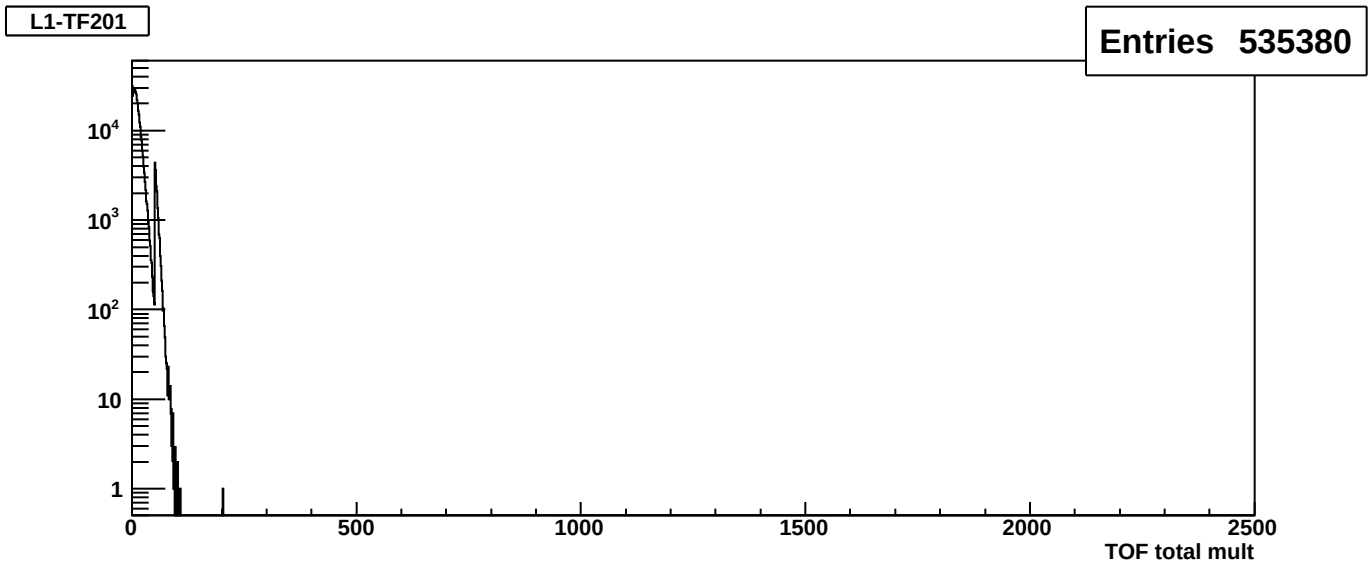

MIX-TF003
Entries 2.248596e+07
TOF MULT
TOF tray

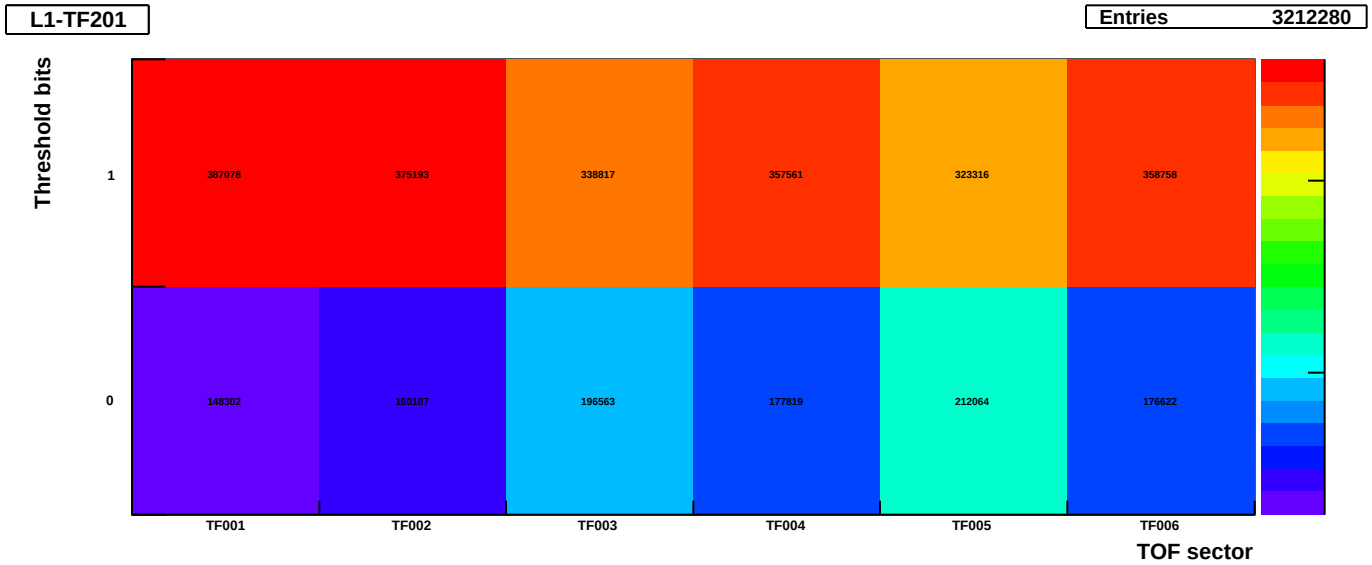

MIX-TF004
Entries 2.248596e+07
TOF MULT
TOF tray

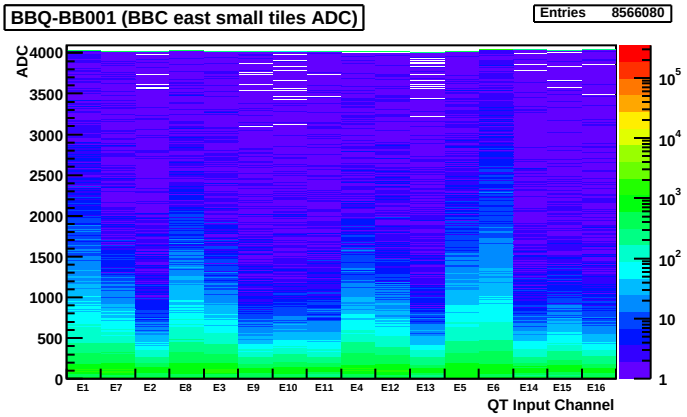

MIX-TF005
Entries 2.248596e+07
TOF MULT
TOF tray

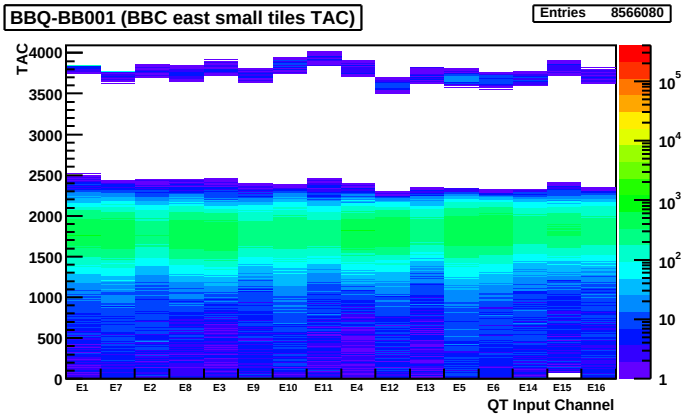

MIX-TF006
Entries 2.248596e+07
TOF MULT
TOF tray

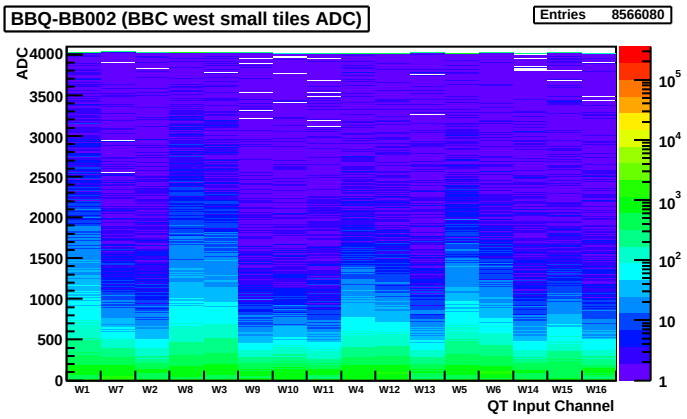

MIX-TF101
Entries 3212280
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

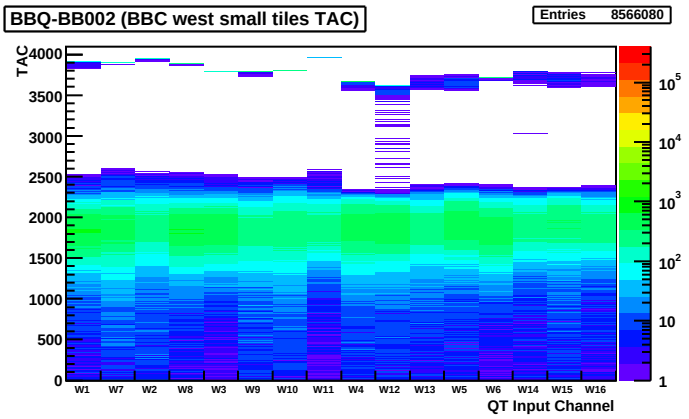

L1-TF201
Entries 535380
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

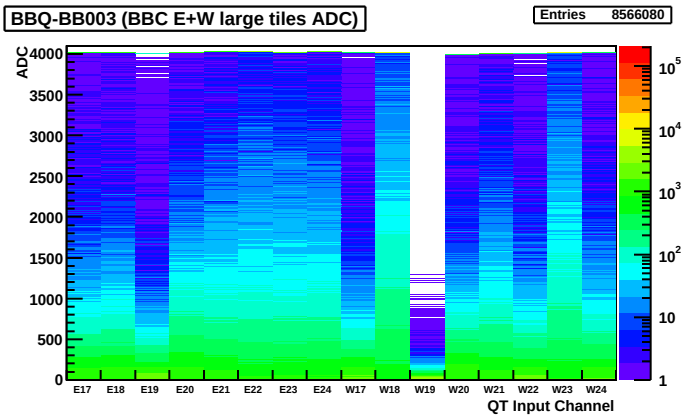

L1-TF201
Entries 3212280
Threshold bits
1
0
387078
375193
338817
357561
323316
358758
148302
160187
196563
177819
212064
176622
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

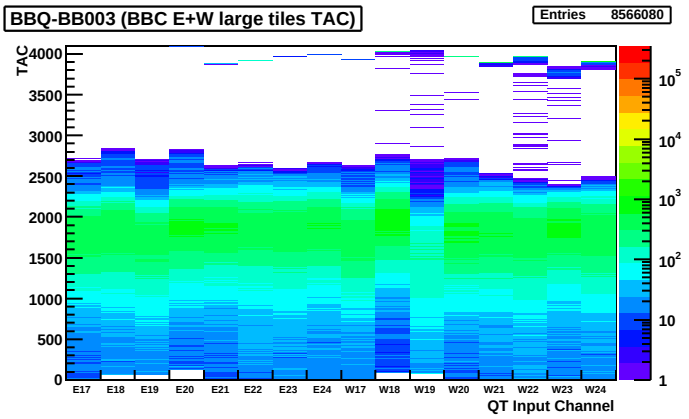

BBQ-BB001 (BBC east small tiles ADC)
Entries 8566080
ADC
QT Input Channel

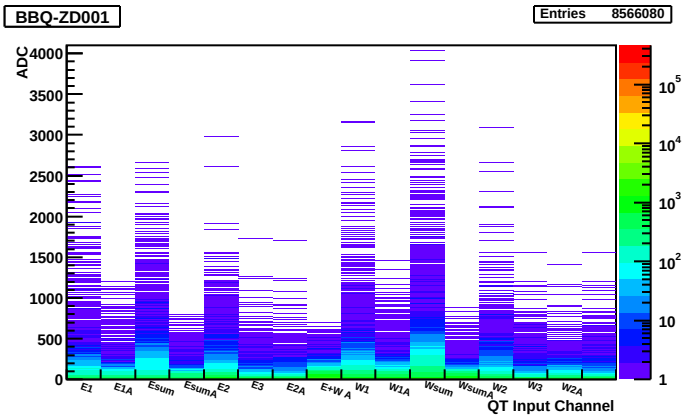

BBQ-BB001 (BBC east small tiles TAC)
Entries 8566080
TAC
QT Input Channel

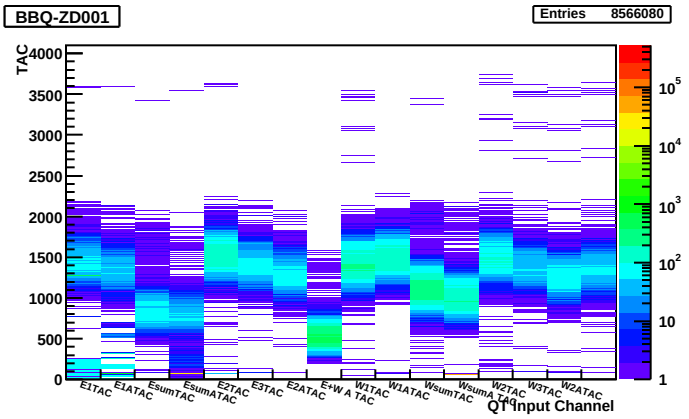

BBQ-BB002 (BBC west small tiles ADC)
Entries 8566080
ADC
QT Input Channel

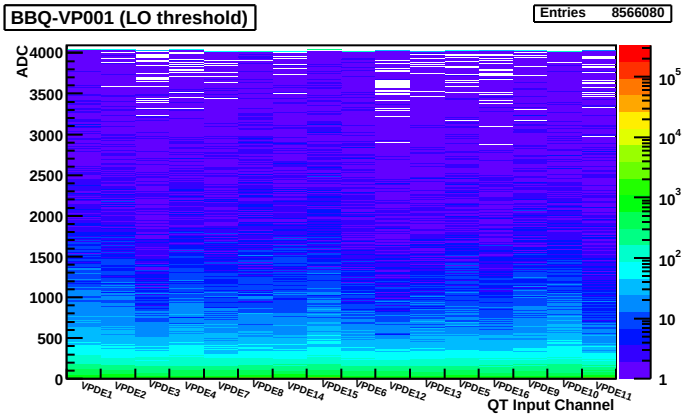

BBQ-BB002 (BBC west small tiles TAC)
Entries 8566080
TAC
QT Input Channel

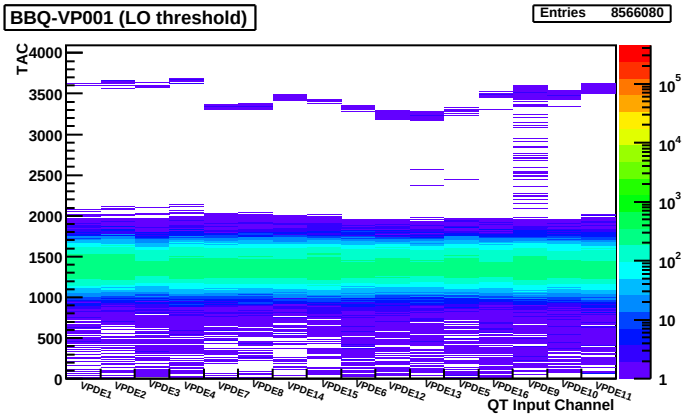

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8566080
ADC
QT Input Channel

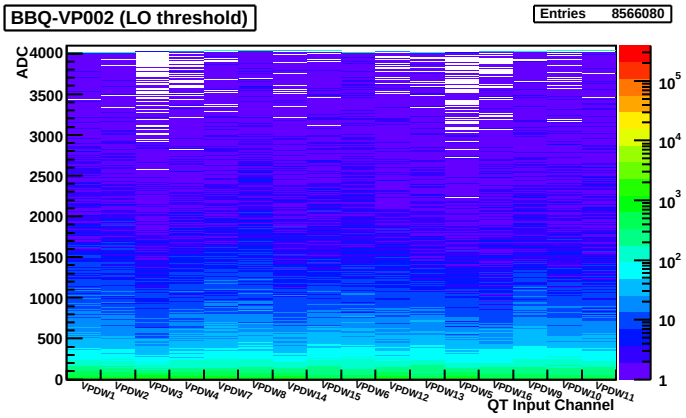

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8566080
TAC
QT Input Channel

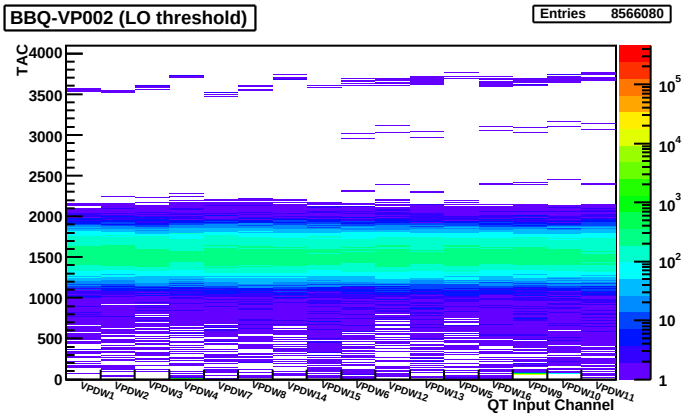

BBQ-ZD001
Entries 8566080
ADC
QT Input Channel

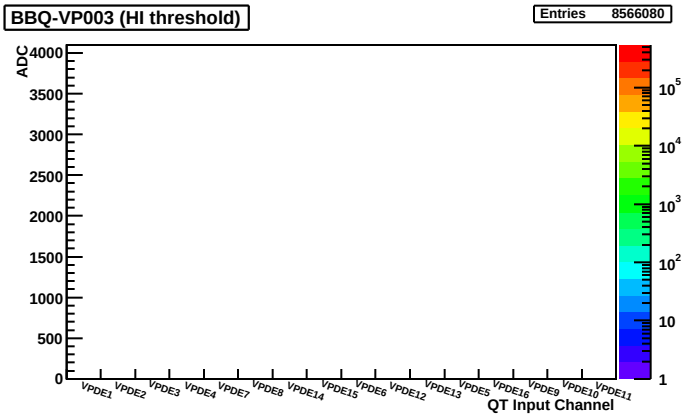

BBQ-ZD001
Entries 8566080
TAC
QT Input Channel

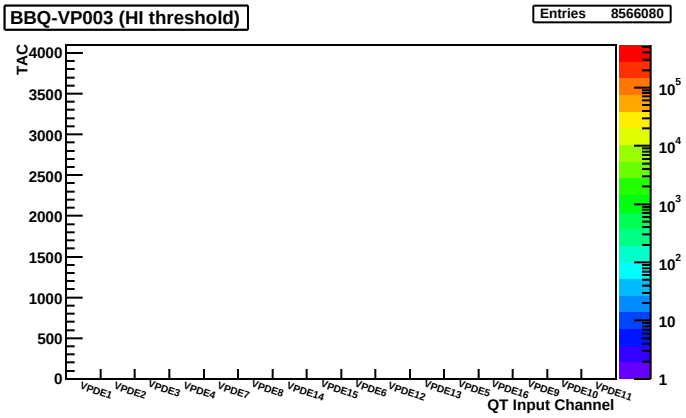

BBQ-VP001 (LO threshold)
Entries 8566080
ADC
QT Input Channel

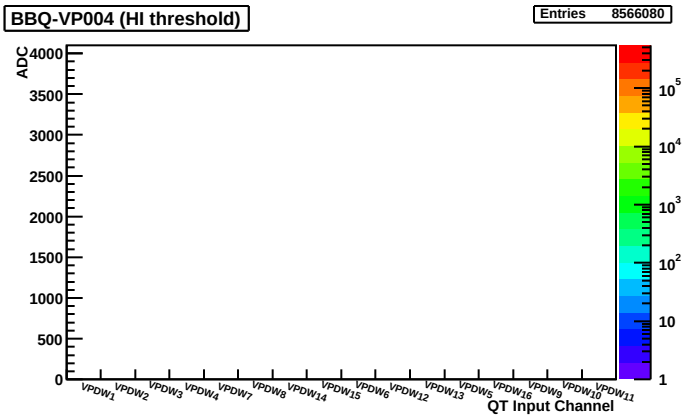

BBQ-VP001 (LO threshold)
Entries 8566080
TAC
QT Input Channel

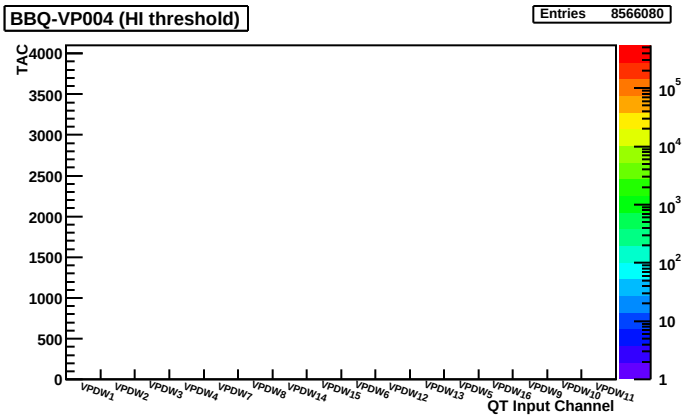

BBQ-VP002 (LO threshold)
Entries 8566080
ADC
QT Input Channel

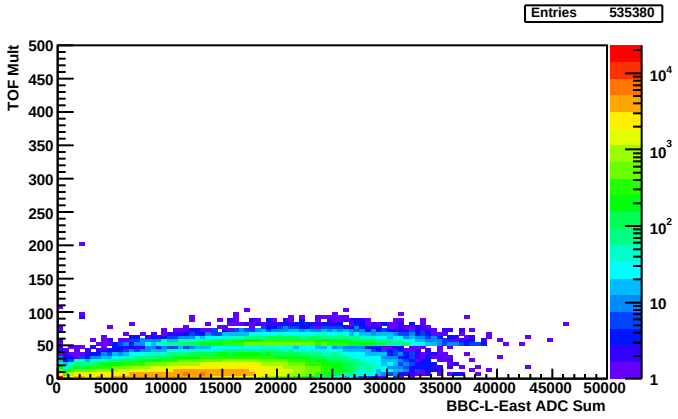

BBQ-VP002 (LO threshold)
Entries 8566080
TAC
QT Input Channel

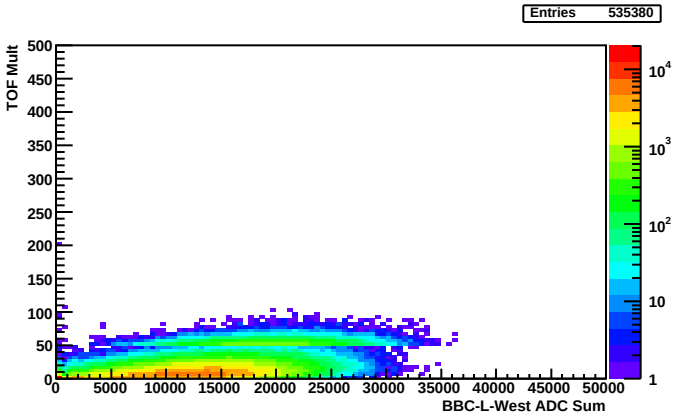

BBQ-VP003 (HI threshold)
Entries 8566080
ADC
QT Input Channel

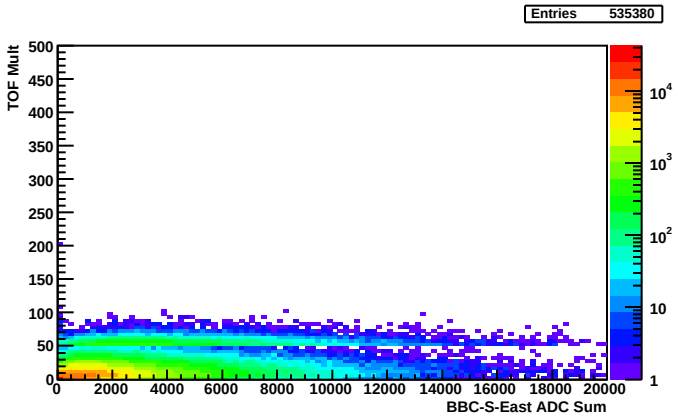

BBQ-VP003 (HI threshold)
Entries 8566080
TAC
QT Input Channel

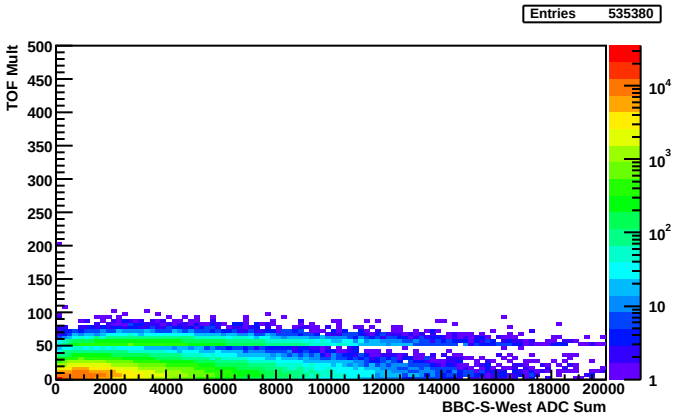

BBQ-VP004 (HI threshold)
Entries 8566080
ADC
QT Input Channel

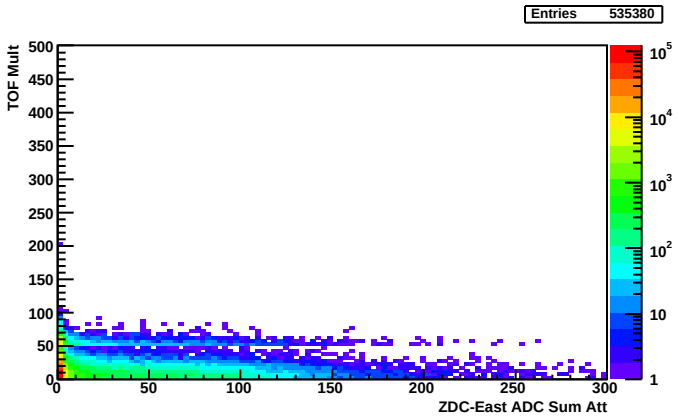

BBQ-VP004 (HI threshold)
Entries 8566080
TAC
QT Input Channel

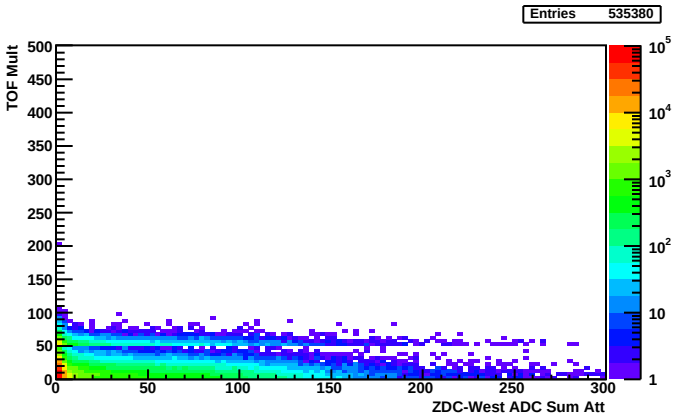

Entries 535380
TOF Mult
BBC-L-East ADC Sum

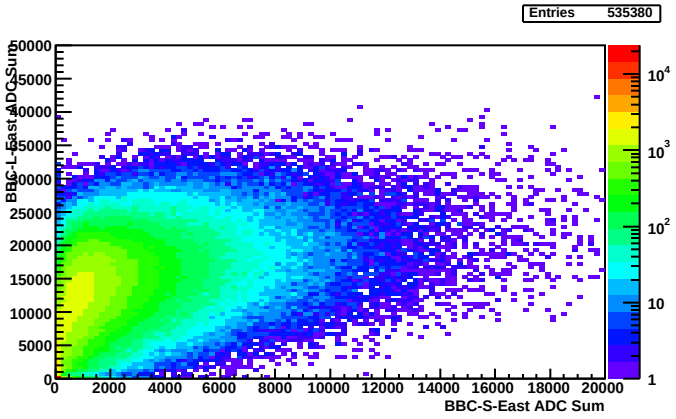

Entries 535380
TOF Mult
BBC-L-West ADC Sum

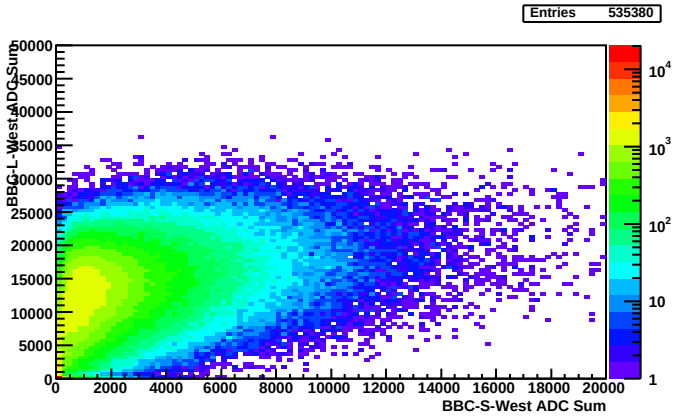

Entries 535380
TOF Mult
BBC-S-East ADC Sum

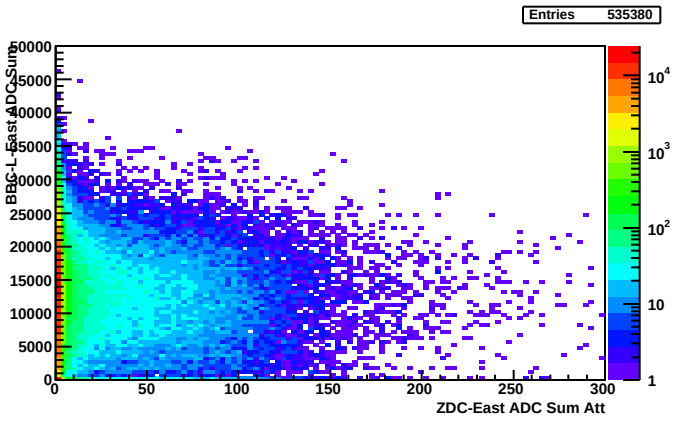

Entries 535380
TOF Mult
BBC-S-West ADC Sum

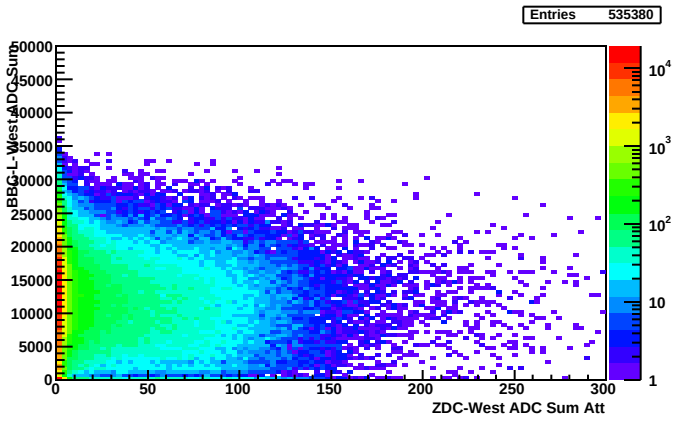

Entries 535380
TOF Mult
ZDC-East ADC Sum Att

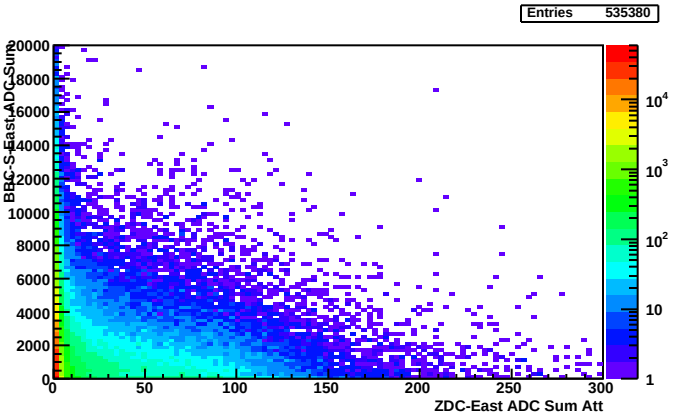

Entries 535380
TOF Mult
ZDC-West ADC Sum Att

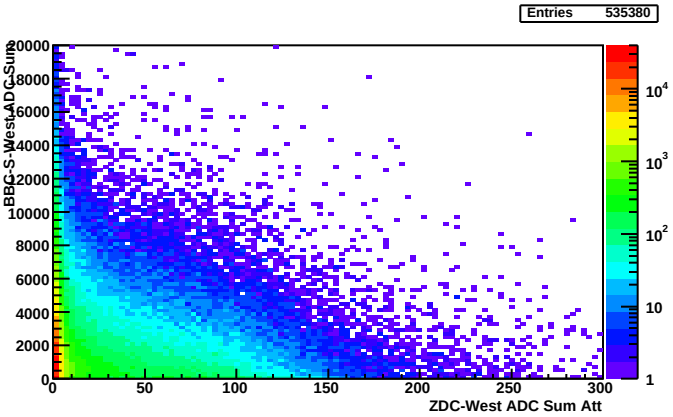

Entries 535380
BBC-L-East ADC Sum
BBC-S-East ADC Sum

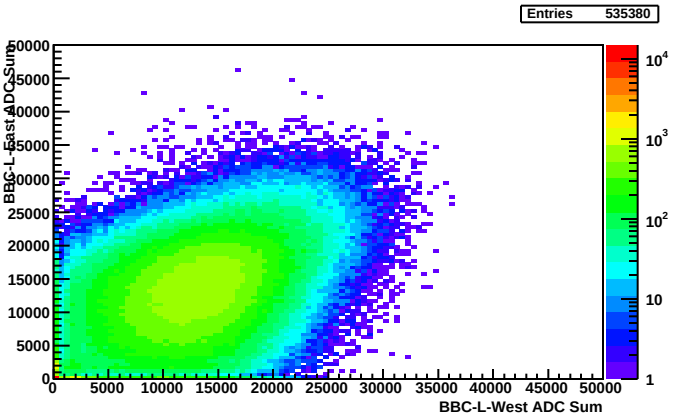

Entries 535380
BBC-L-West ADC Sum
BBC-S-West ADC Sum

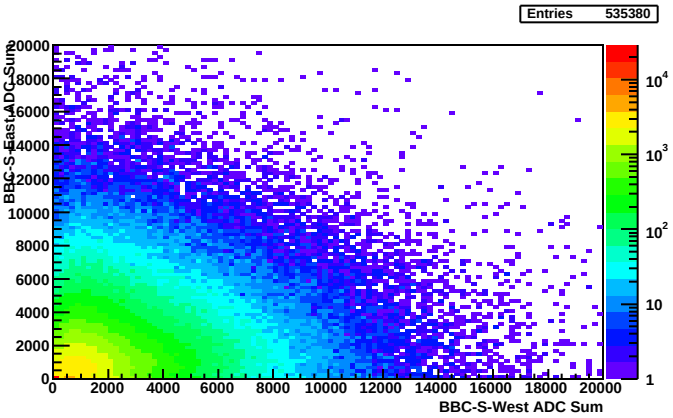

Entries 535380
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

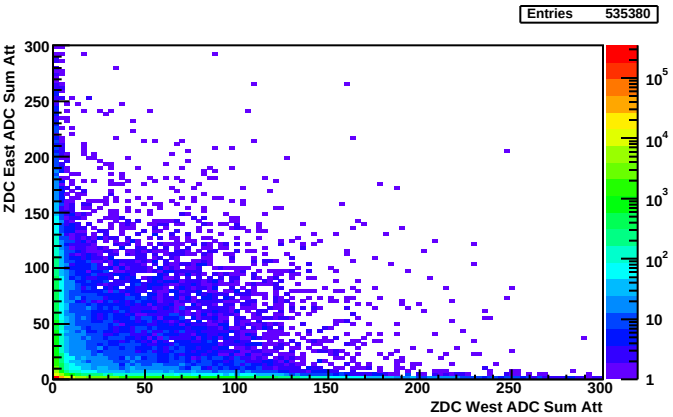

Entries 535380
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

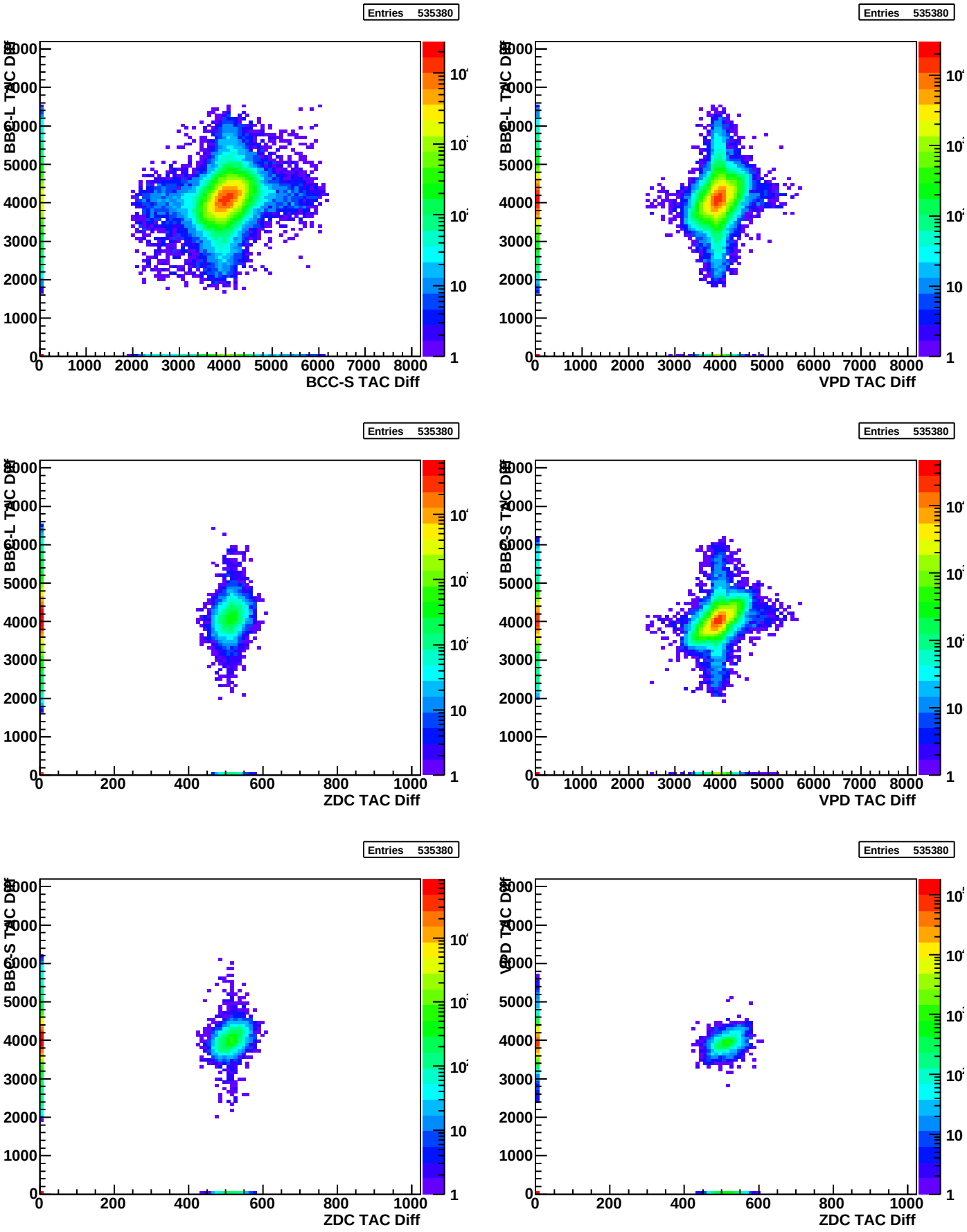

Entries 535380
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

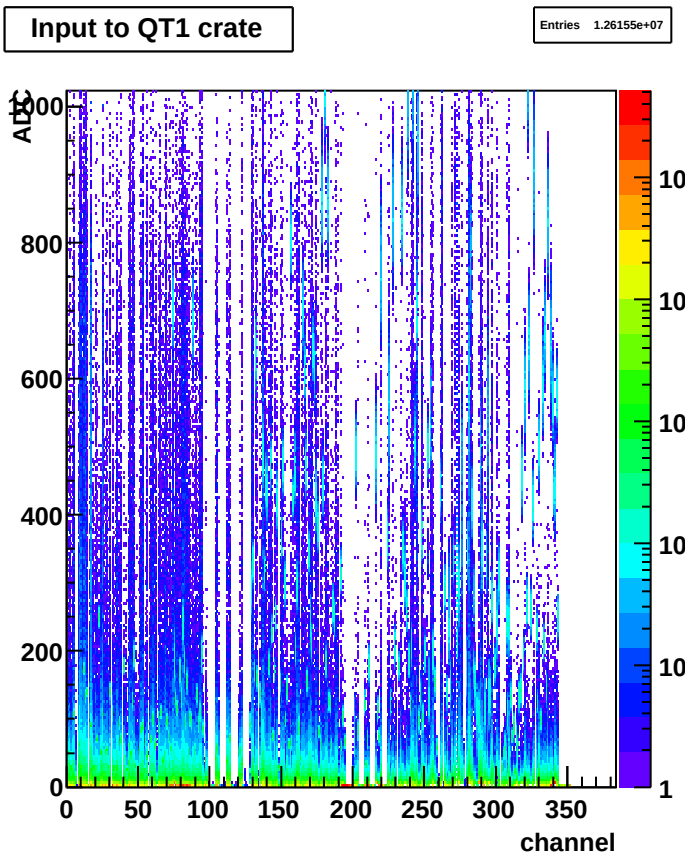

Entries 535380
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

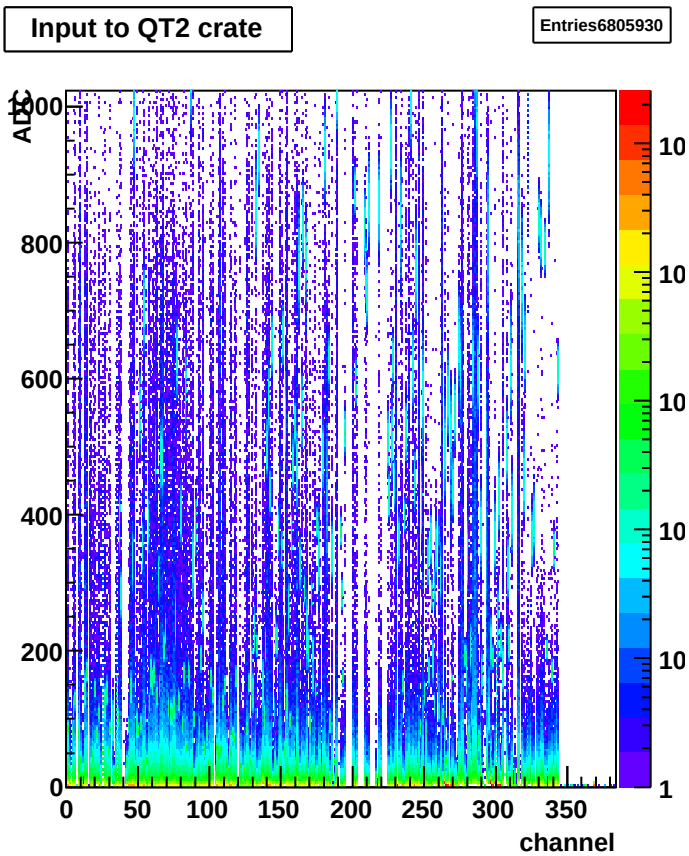

Entries 535380
BBC-L-East ADC Sum
BBC-L-West ADC Sum

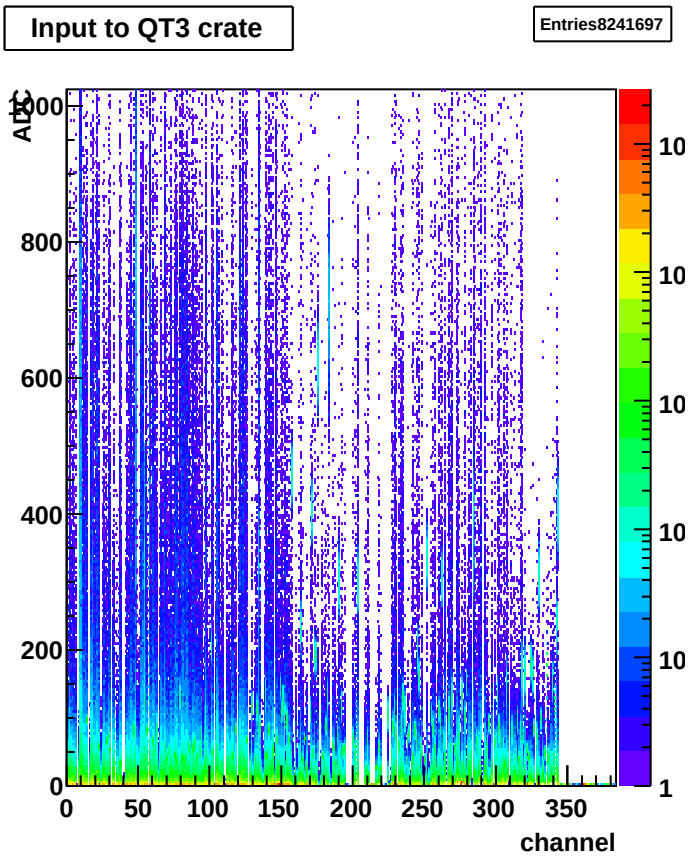

Entries 535380
BBC-S-East ADC Sum
BBC-S-West ADC Sum

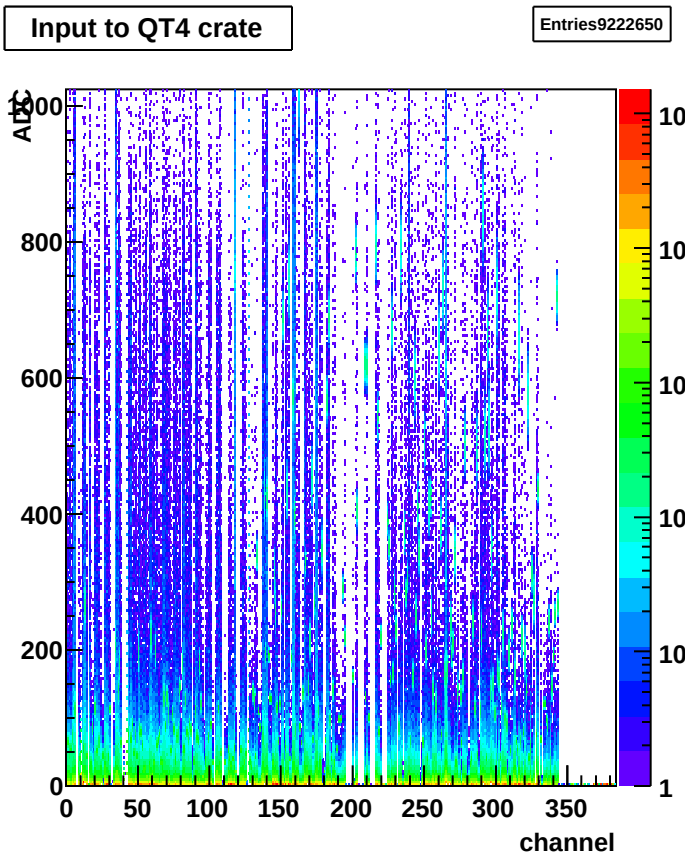

Entries 535380
ZDC East ADC Sum Att
ZDC West ADC Sum Att

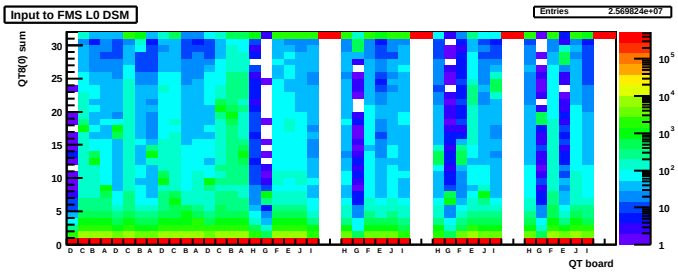

Entries 535380
BBC-L TAC Diff
BCC-S TAC Diff
Entries 535380
BBC-L TAC Diff
VPD TAC Diff
Entries 535380
BBC-L TAC Diff
ZDC TAC Diff
Entries 535380
BBC-S TAC Diff
VPD TAC Diff
Entries 535380
BBC-S TAC Diff
ZDC TAC Diff
Entries 535380
VPD TAC Diff
ZDC TAC Diff

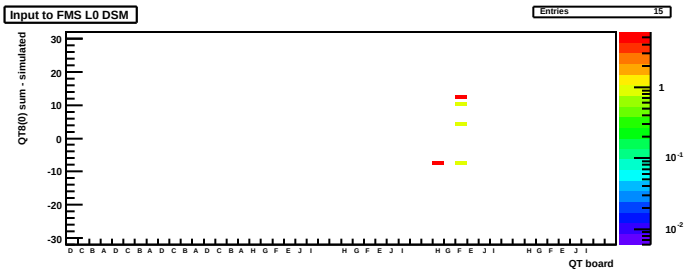

Input to QT1 crate
Entries 1.26155e+07
ADC
channel

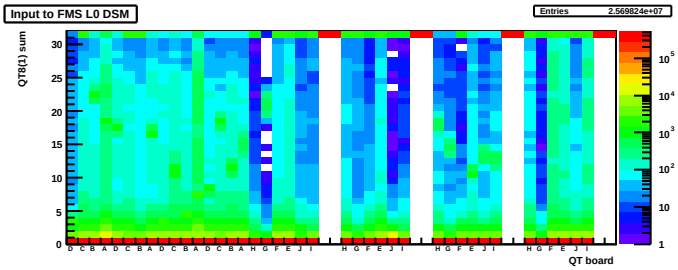

Input to QT2 crate
Entries6805930
ADC
channel

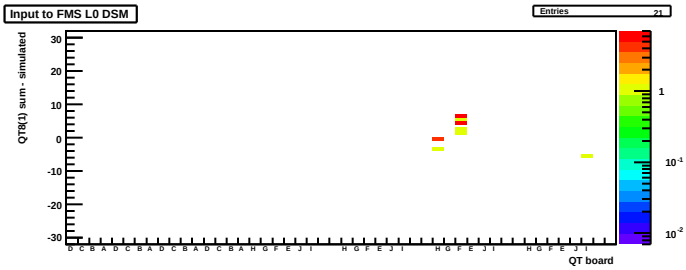

Input to QT3 crate
Entries8241697
ADC
channel

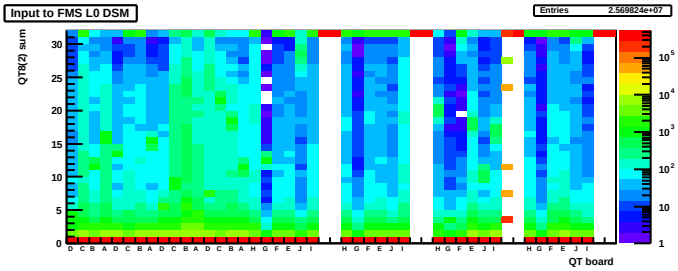

Input to QT4 crate
Entries9222650
ADC
channel

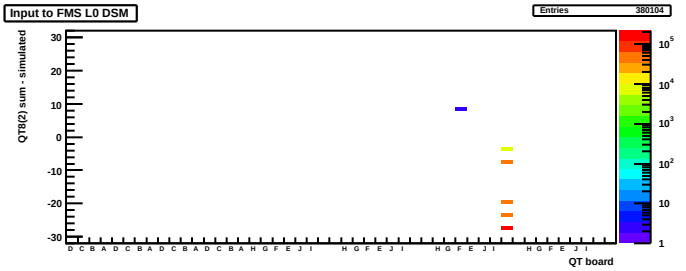

Input to FMS L0 DSM
Entries 2.569824e+07
QT8(0) sum
QT board

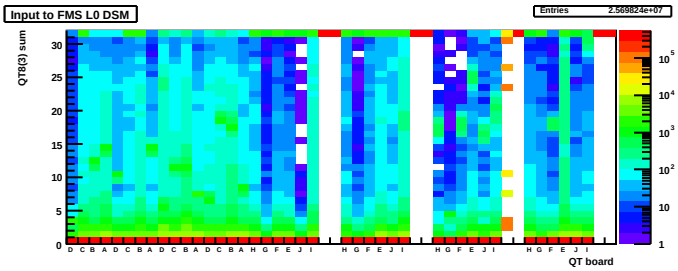

Input to FMS L0 DSM
Entries 15
QT8(0) sum - simulated
QT board

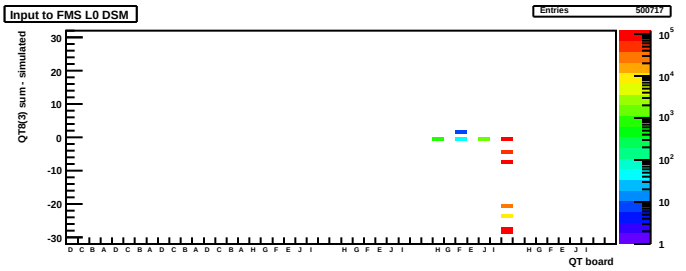

Input to FMS L0 DSM
Entries 2.569824e+07
QT8(1) sum
QT board

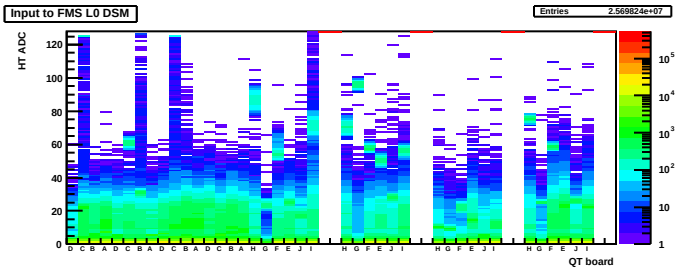

Input to FMS L0 DSM
Entries 21
QT8(1) sum - simulated
QT board

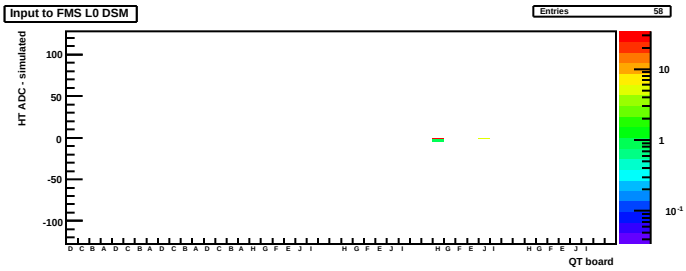

Input to FMS L0 DSM
Entries 2.569824e+07
QT8(2) sum
QT board

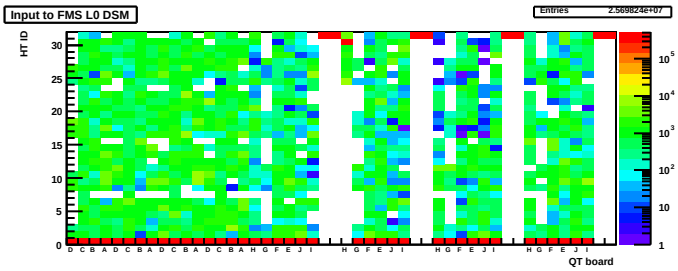

Input to FMS L0 DSM
Entries 380104
QT8(2) sum - simulated
QT board

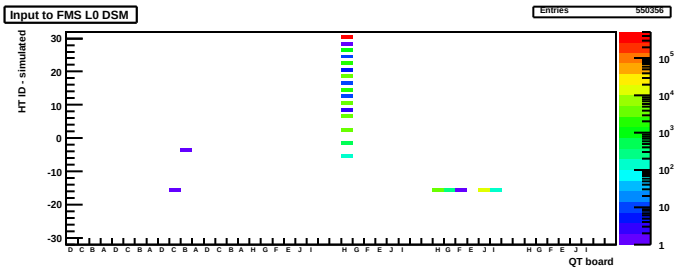

Input to FMS L0 DSM
Entries 2.569824e+07
QT8(3) sum
QT board

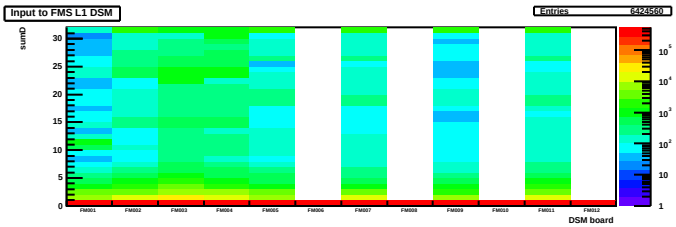

Input to FMS L0 DSM
Entries 500717
QT8(3) sum - simulated
QT board

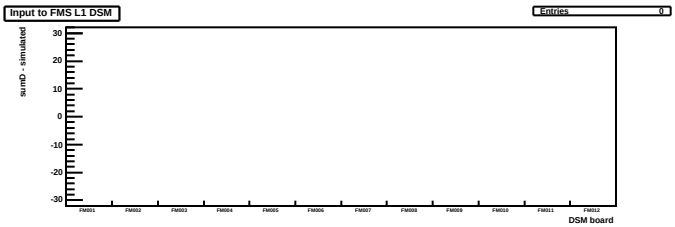

Input to FMS L0 DSM
Entries 2.569824e+07
HT ADC
QT board

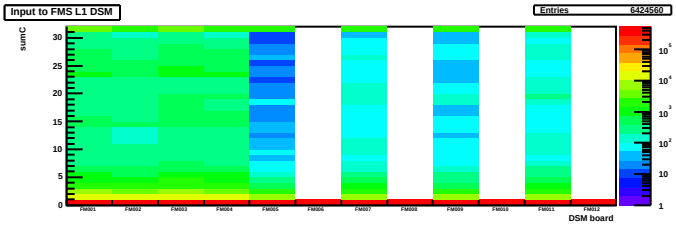

Input to FMS L0 DSM
Entries 58
HT ADC - simulated
QT board

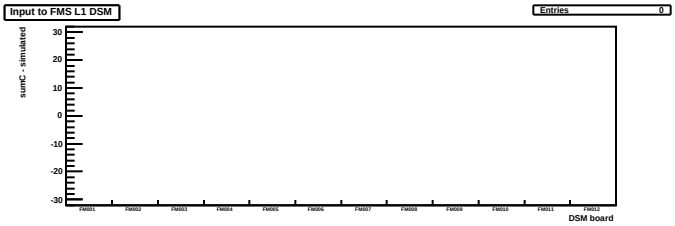

Input to FMS L0 DSM
Entries 2.569824e+07
HT ID
QT board

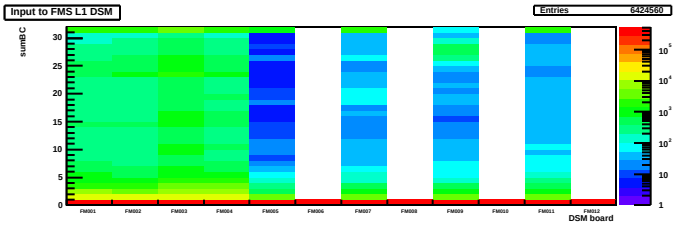

Input to FMS L0 DSM
Entries 550356
HT ID - simulated
QT board

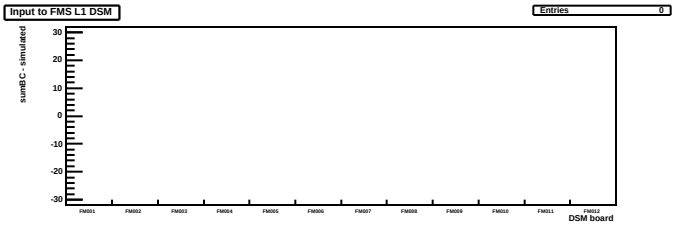

Input to FMS L1 DSM
Entries 6424560
sumD
DSM board

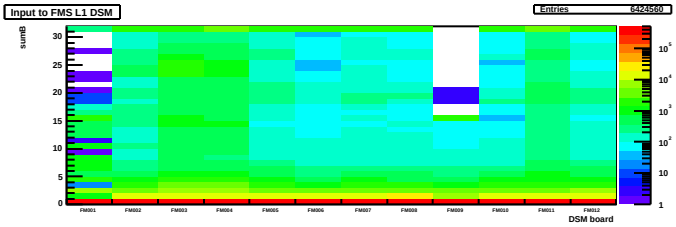

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

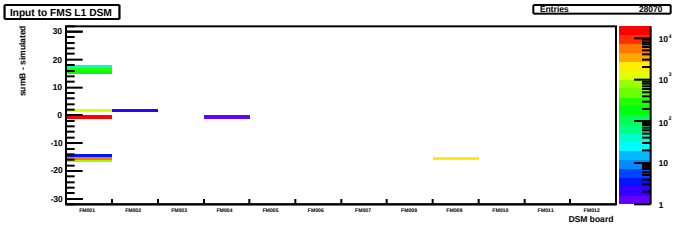

Input to FMS L1 DSM
Entries 6424560
sumC
DSM board

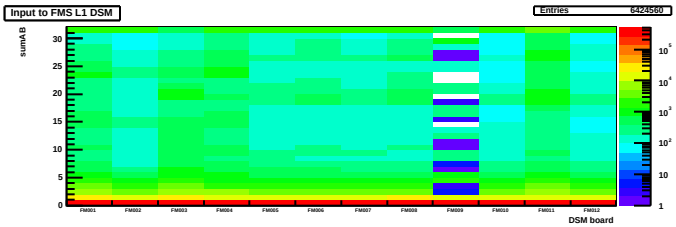

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

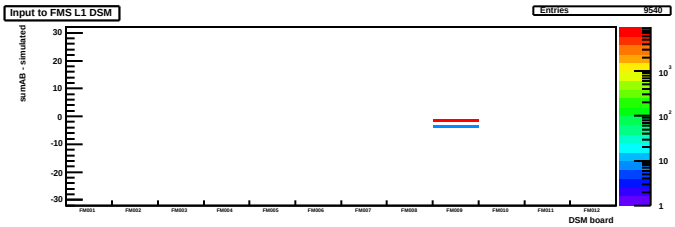

Input to FMS L1 DSM
Entries 6424560
sumBC
DSM board

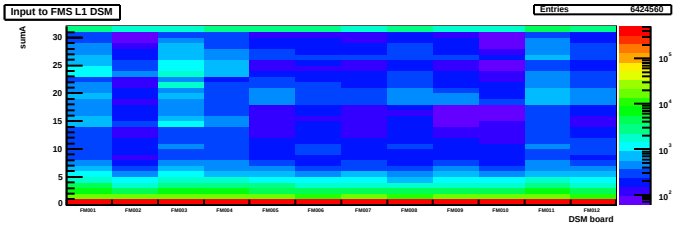

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

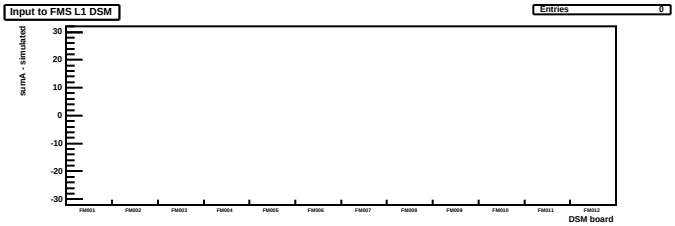

Input to FMS L1 DSM
Entries 6424560
sumB
DSM board

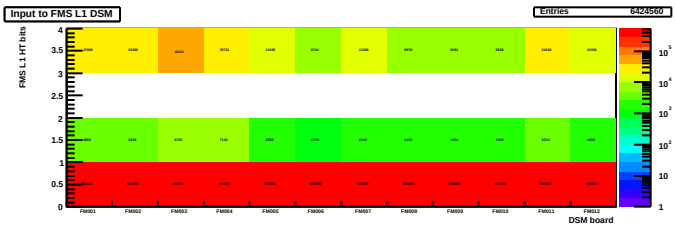

Input to FMS L1 DSM
Entries 28070
sumB - simulated
DSM board

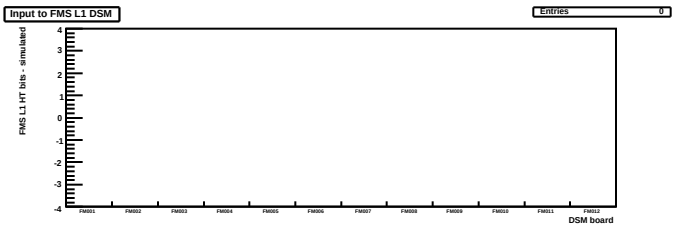

Input to FMS L1 DSM
Entries 6424560
sumAB
DSM board

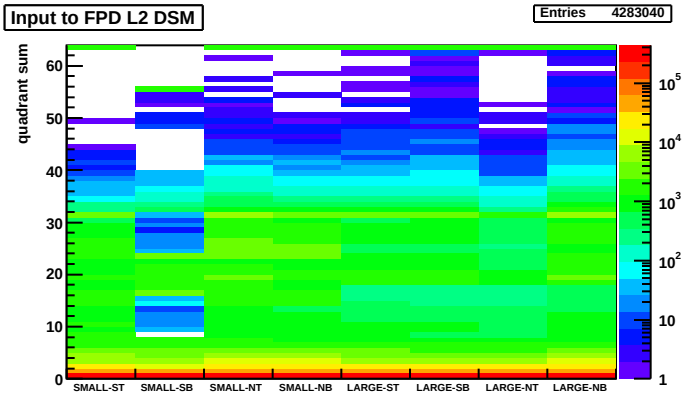

Input to FMS L1 DSM
Entries 9540
sumAB - simulated
DSM board

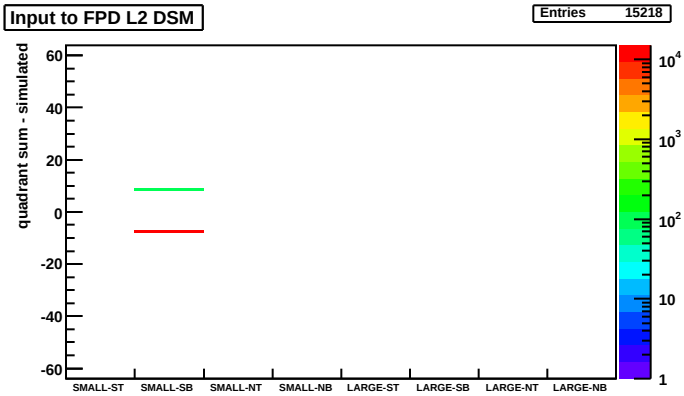

Input to FMS L1 DSM
Entries 6424560
sumA
DSM board

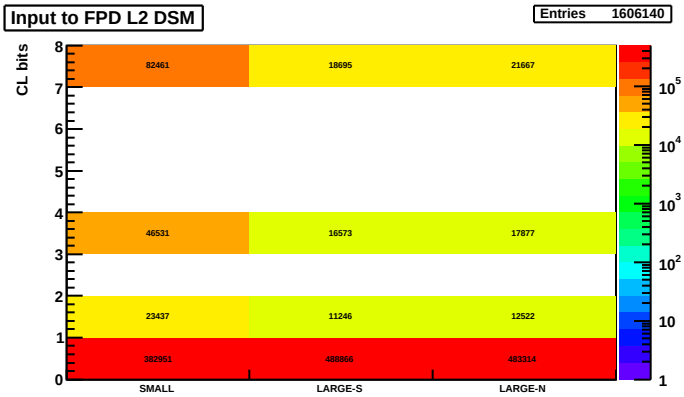

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

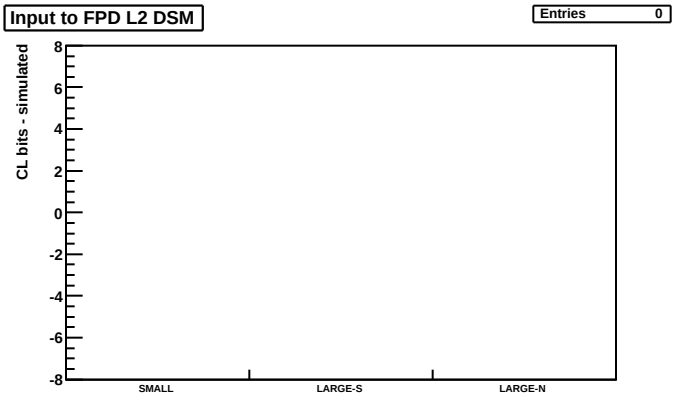

Input to FMS L1 DSM
Entries 6424560
FMS L1 HT bits
DSM board

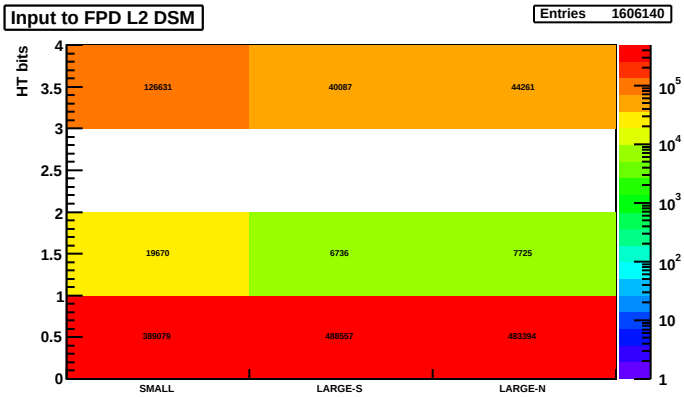

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

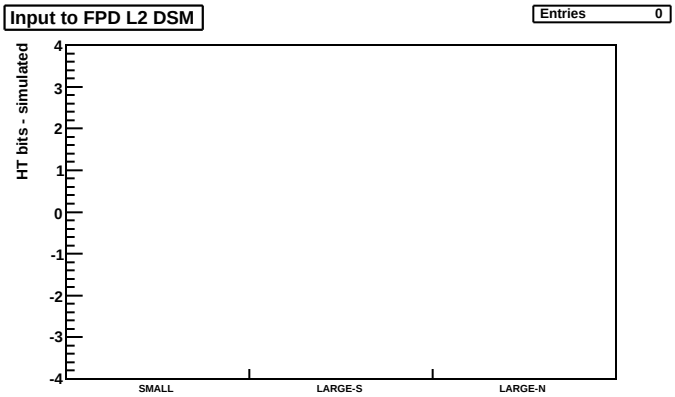

Input to FPD L2 DSM
Entries 4283040
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

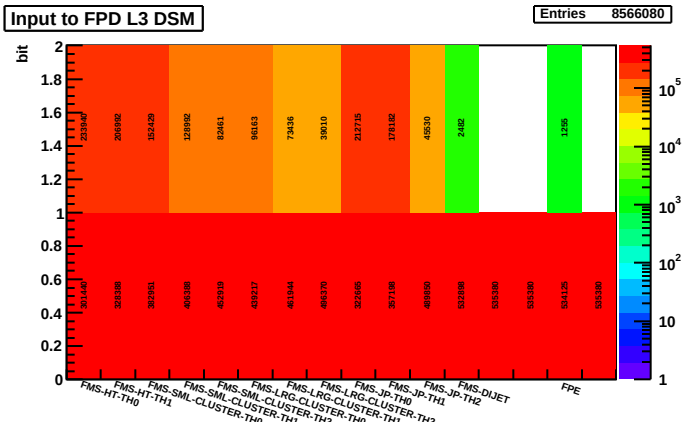

Input to FPD L2 DSM
Entries 15218
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

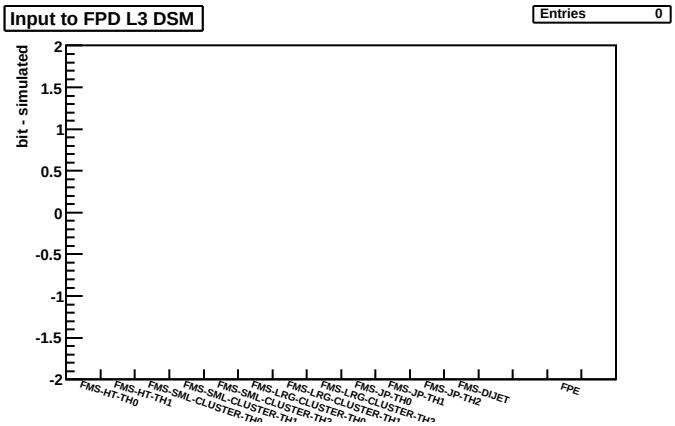

Input to FPD L2 DSM
Entries 1606140
CL bits
82461
18695
21667
46531
16573
17877
23437
11246
12522
382951
488866
483314
SMALL
LARGE-S
LARGE-N

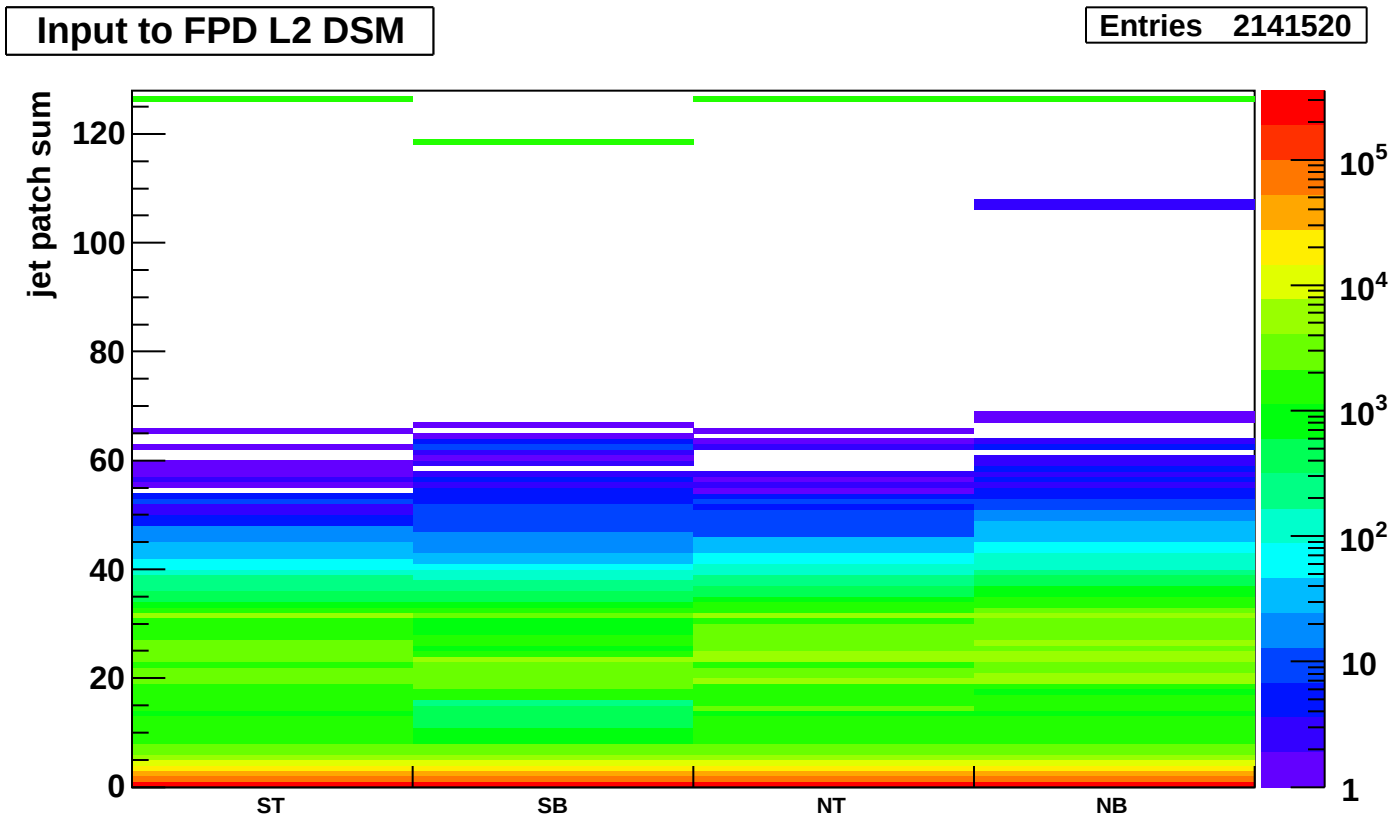

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

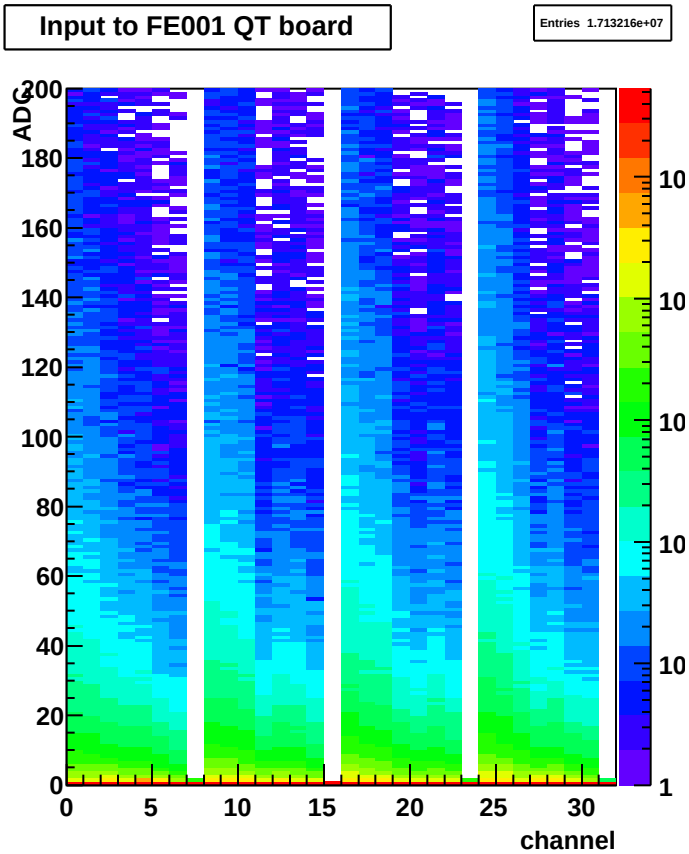

Input to FPD L2 DSM
Entries 1606140
HT bits
126631
40087
44261
19670
6736
7725
389079
488557
483394
SMALL
LARGE-S
LARGE-N

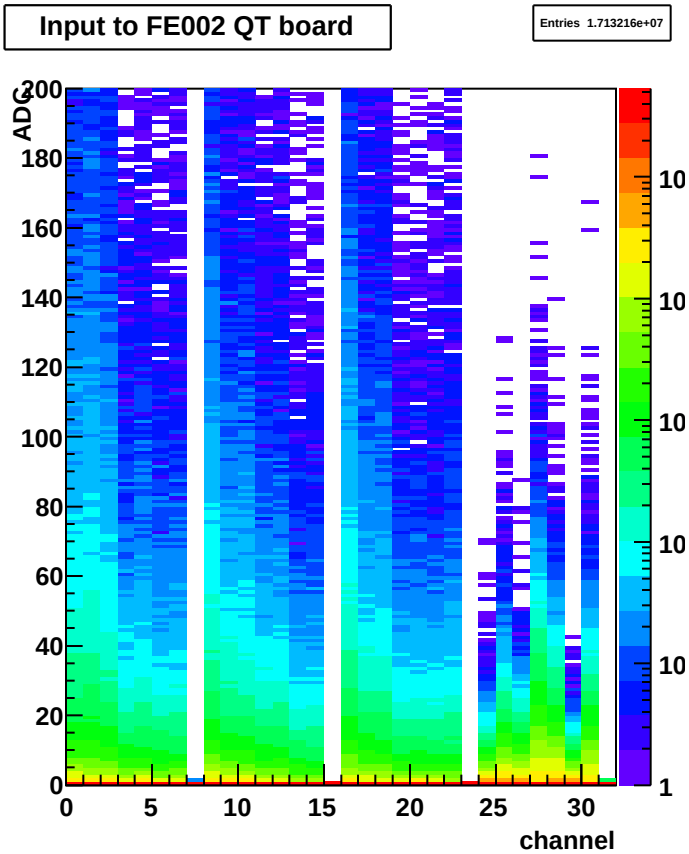

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

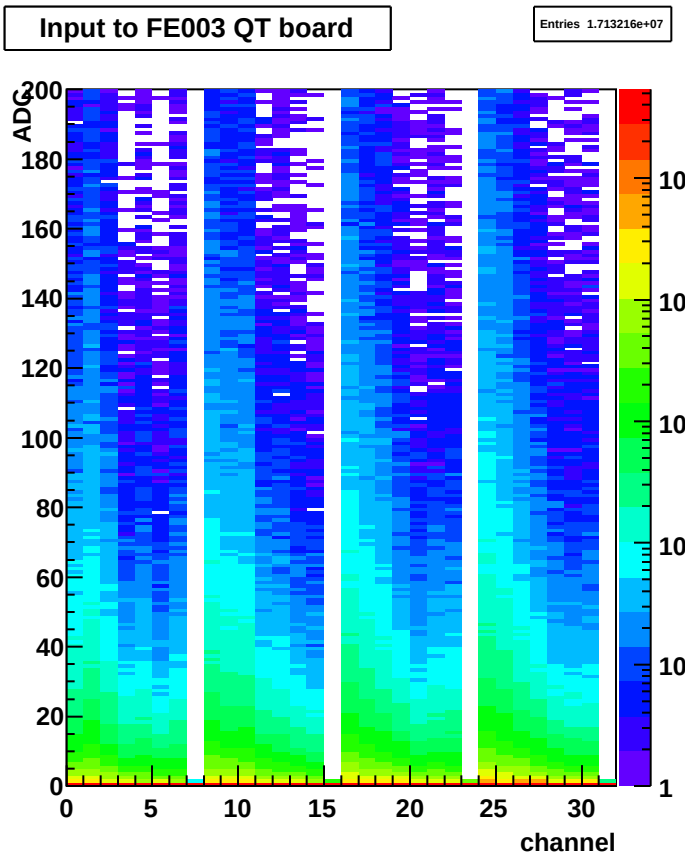

Input to FPD L3 DSM
Entries 8566080
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

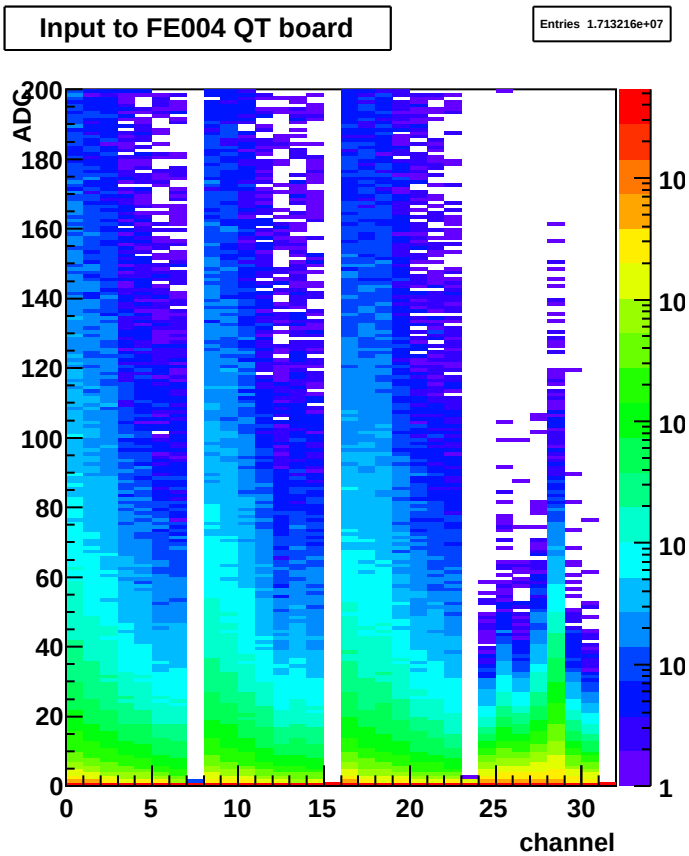

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

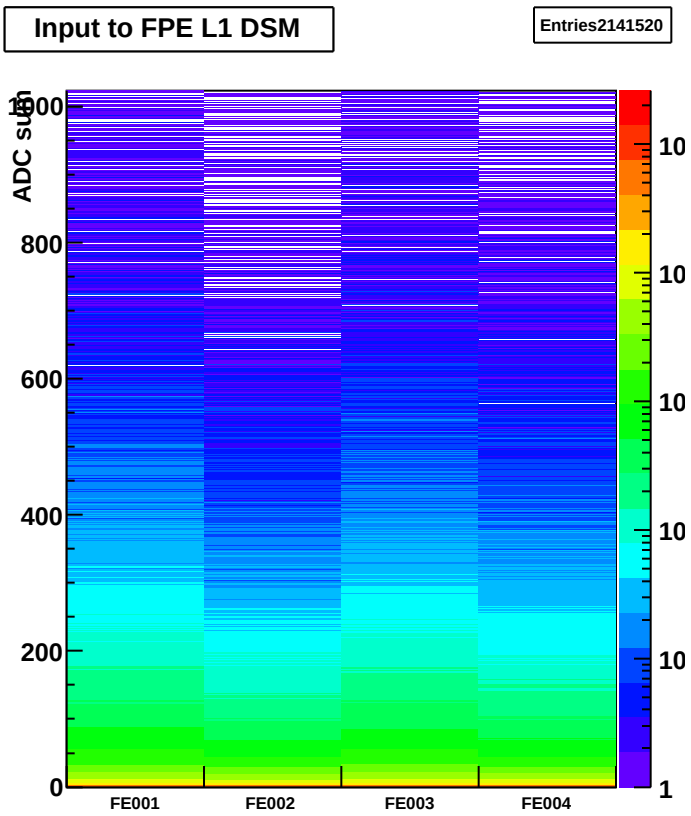

Input to FPD L2 DSM
Entries 2141520
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

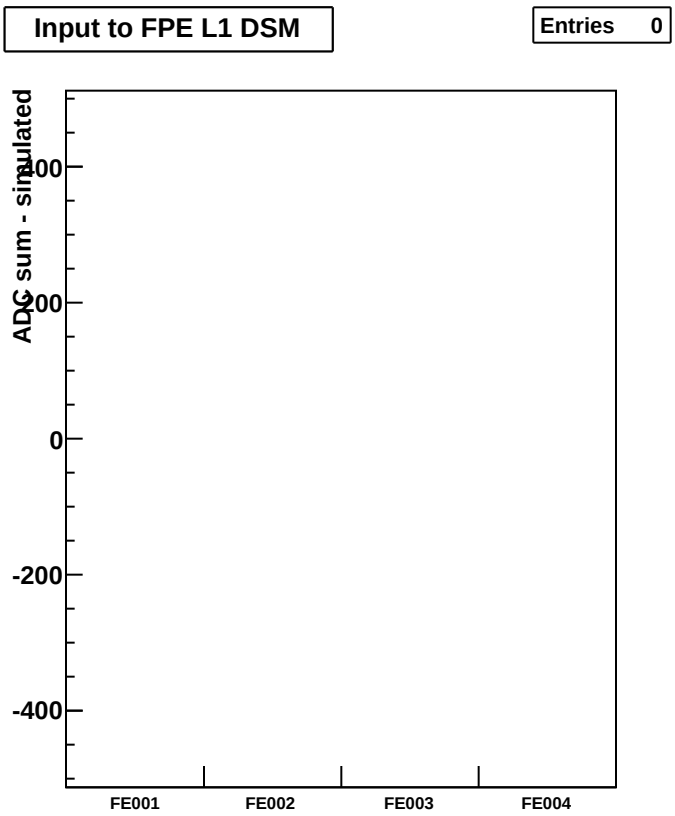

Input to FE001 QT board
Entries 1.713216e+07
ADC
channel

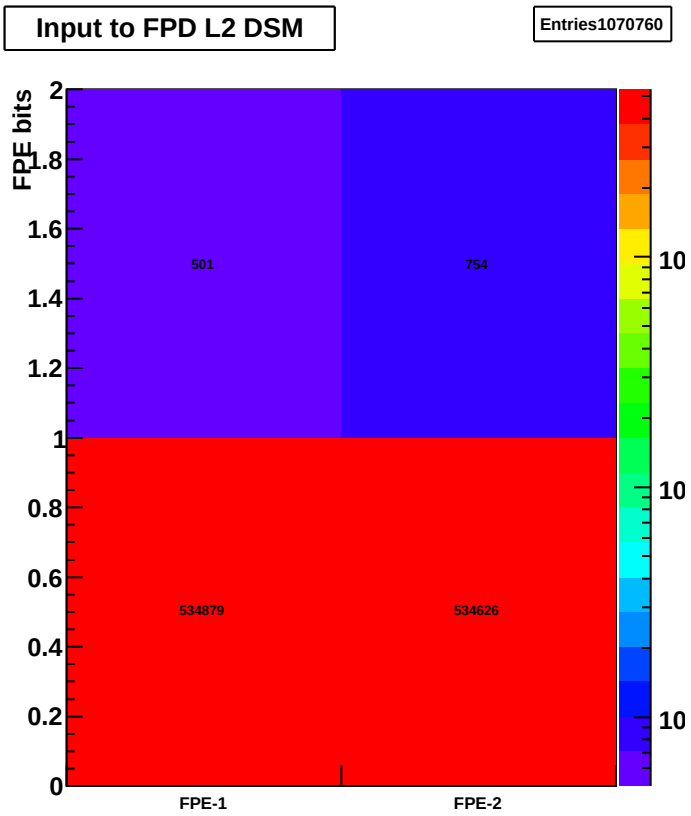

Input to FE002 QT board
Entries 1.713216e+07
ADC
channel

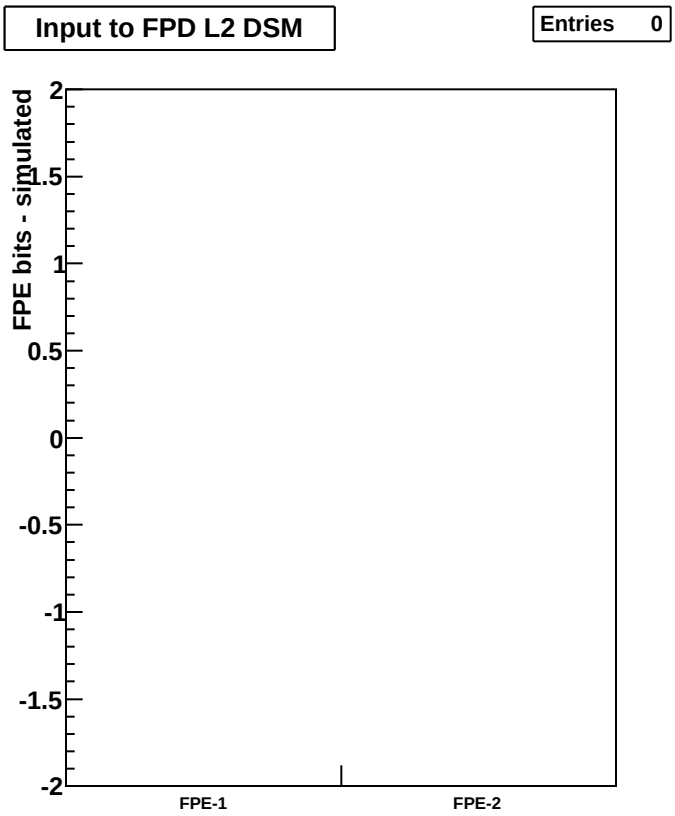

Input to FE003 QT board
Entries 1.713216e+07
ADC
channel


Input to FE004 QT board
Entries 1.713216e+07
ADC
channel


Input to FPE L1 DSM
Entries2141520
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1070760
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
501
754
534879
534626
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2