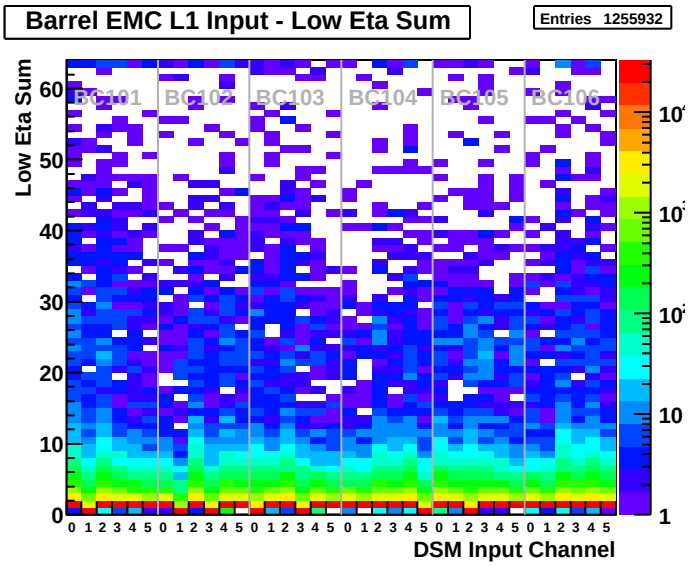
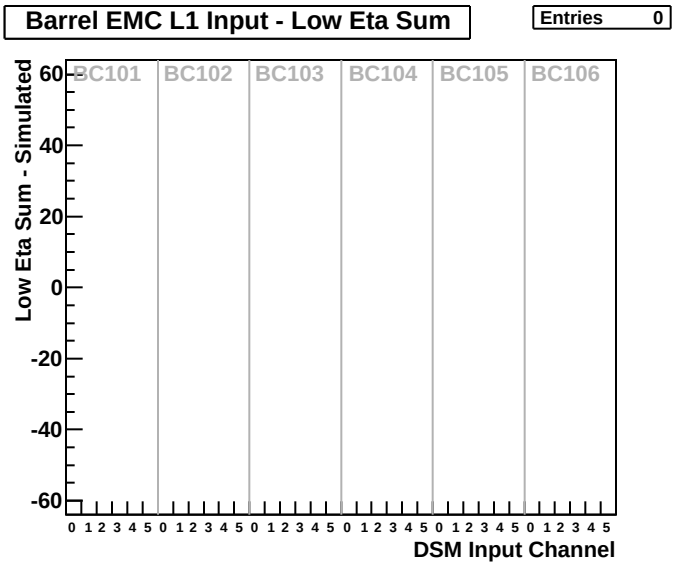
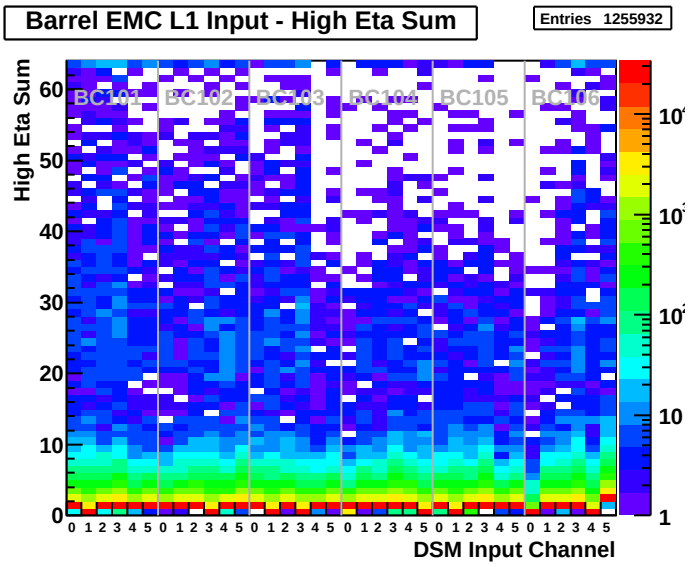
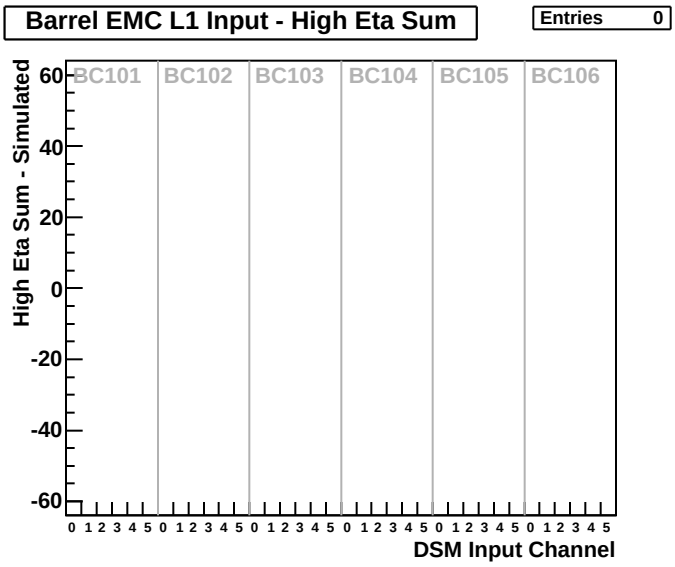
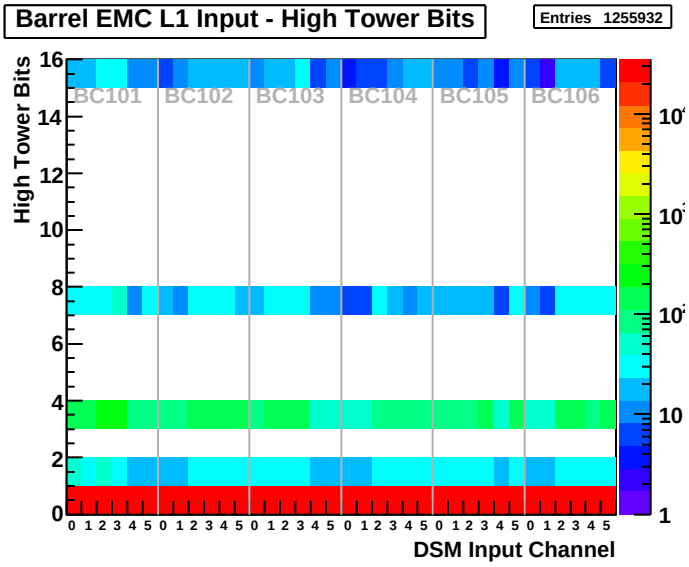
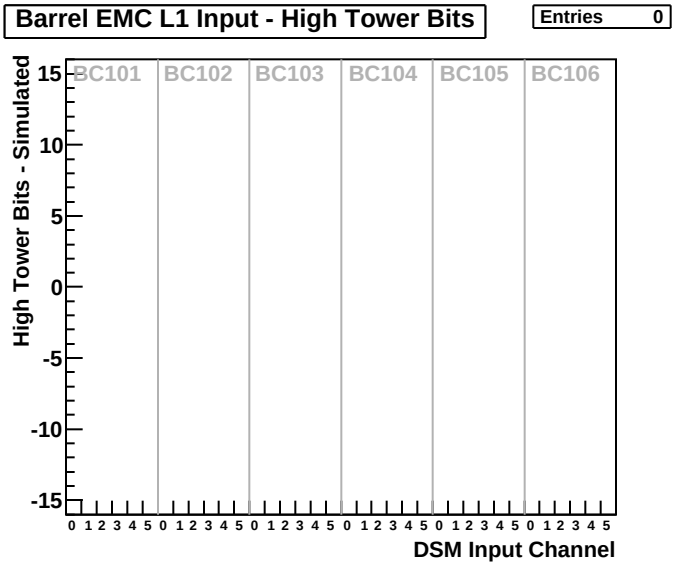

Barrel EMC L1 Input - Low Eta Sum
Entries 1255932
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

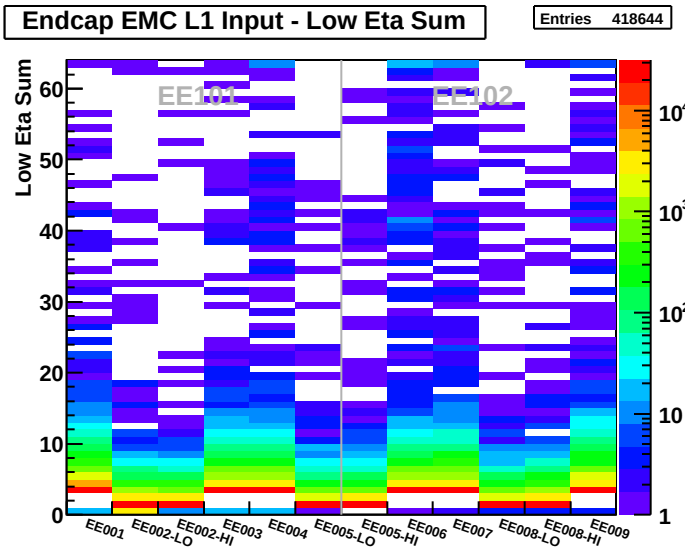

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

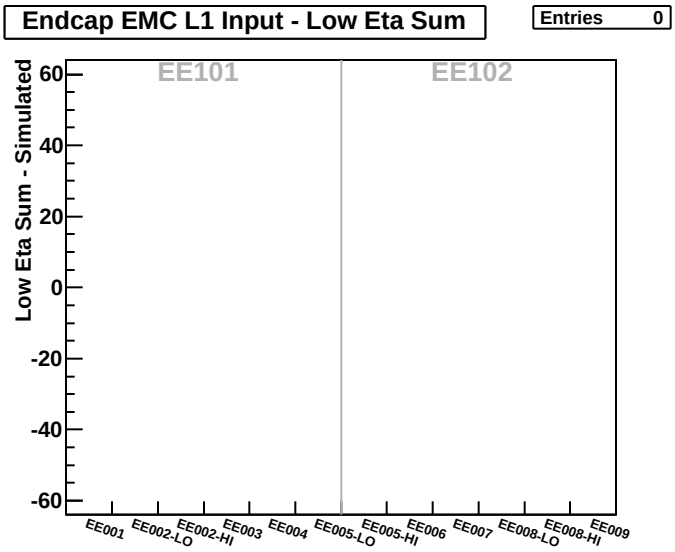

Barrel EMC L1 Input - High Eta Sum
Entries 1255932
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

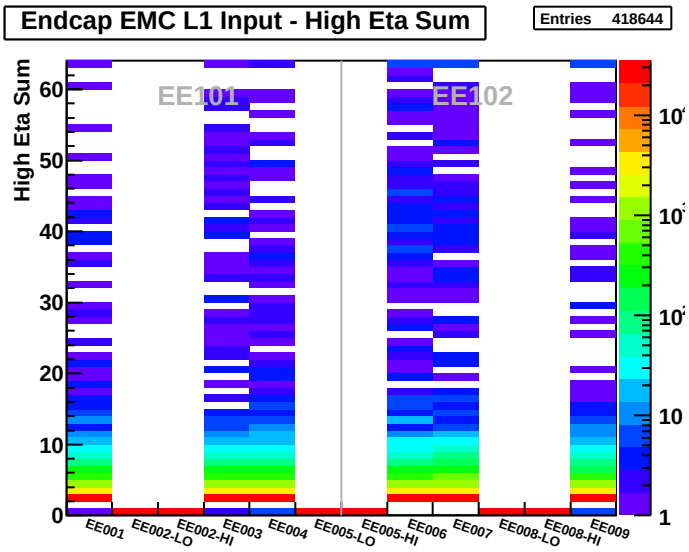

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

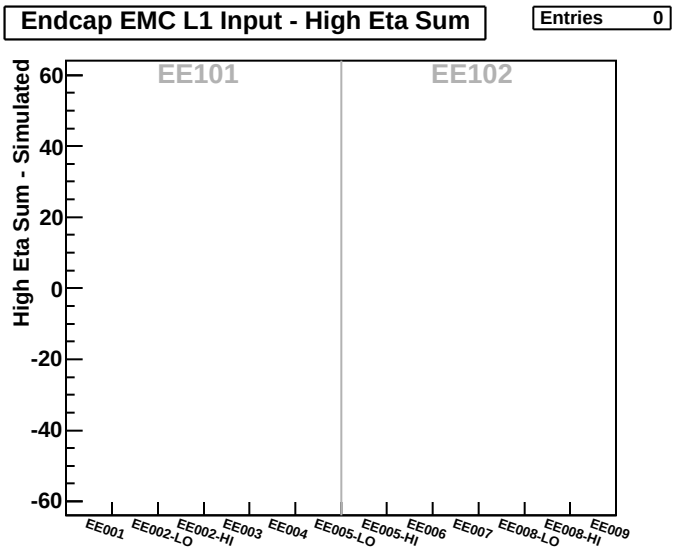

Barrel EMC L1 Input - High Tower Bits
Entries 1255932
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

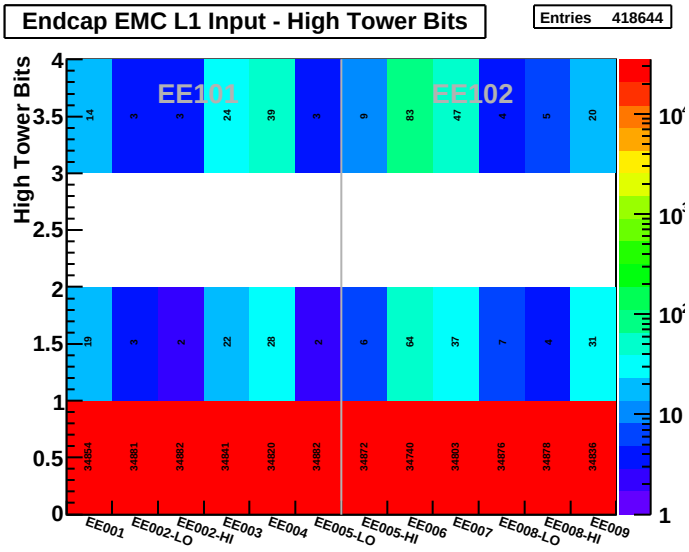

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

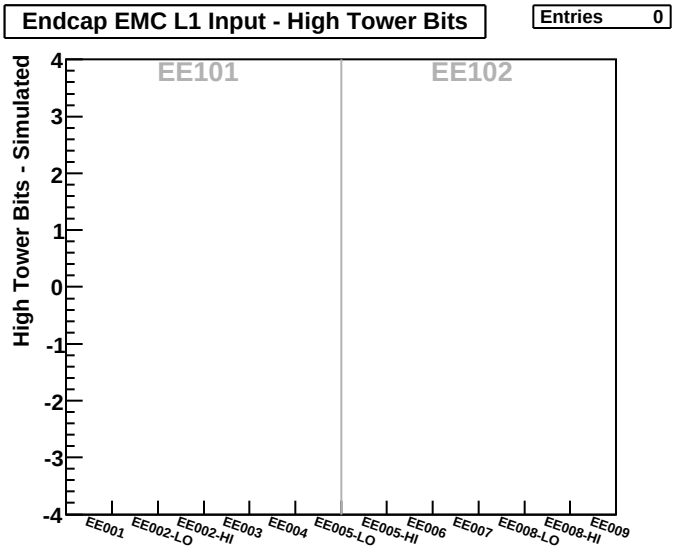

Endcap EMC L1 Input - Low Eta Sum
Entries 418644
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

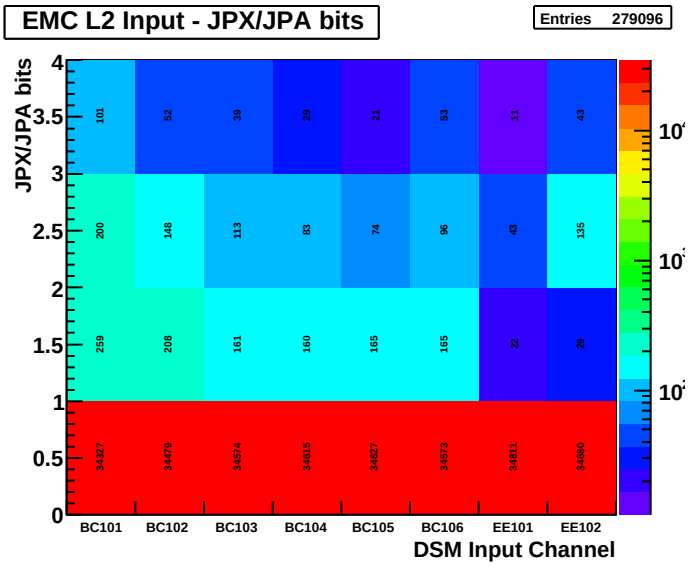

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

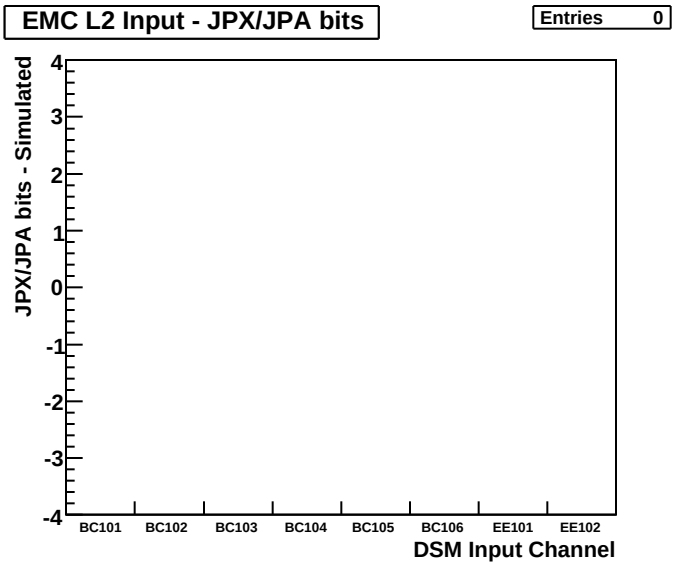

Endcap EMC L1 Input - High Eta Sum
Entries 418644
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

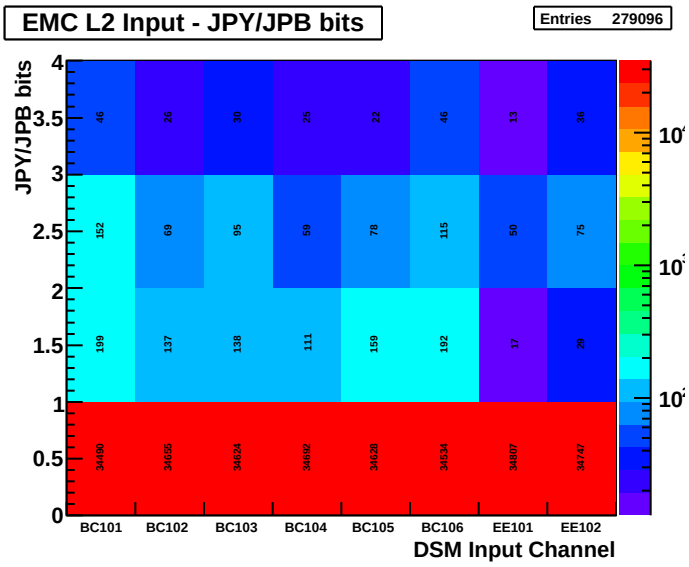

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

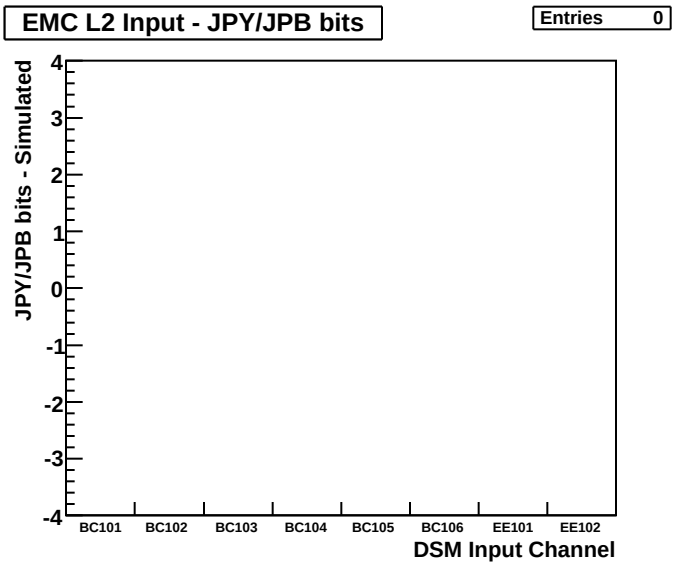

Endcap EMC L1 Input - High Tower Bits
Entries 418644
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

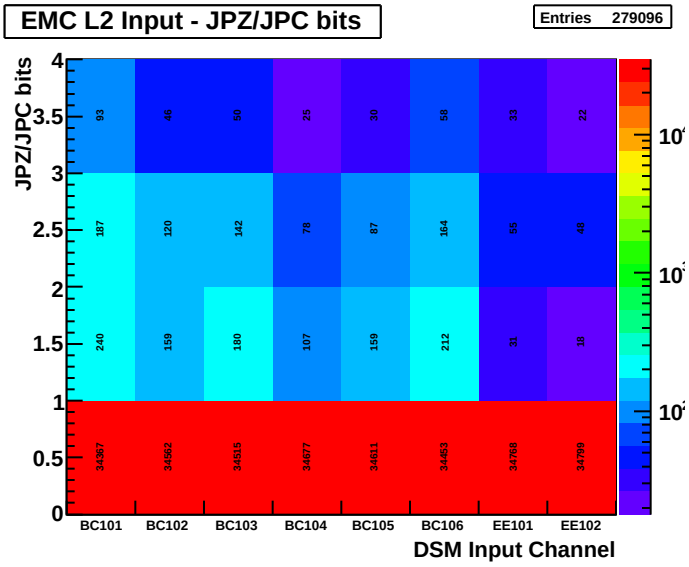

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

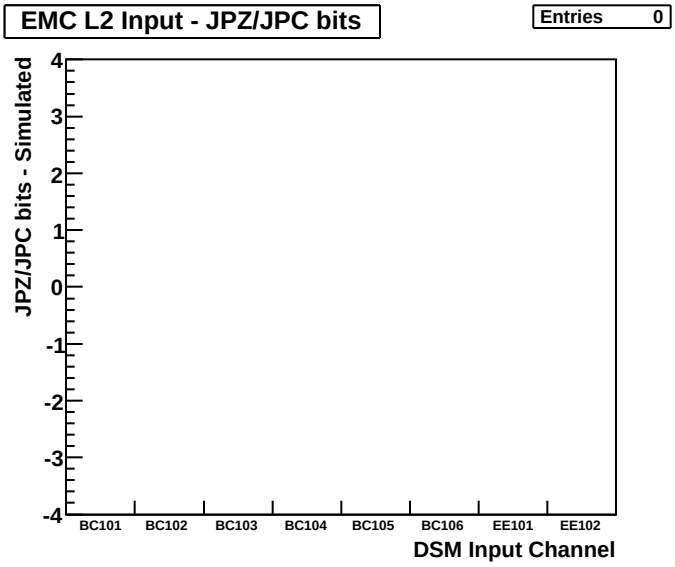

EMC L2 Input - JPX/JPA bits
Entries 279096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
101 52 39 29 21 53 11 43
200 148 113 83 74 96 43 135
259 208 161 160 165 165 22 29
34327 34479 34574 34615 34627 34573 34811 34680

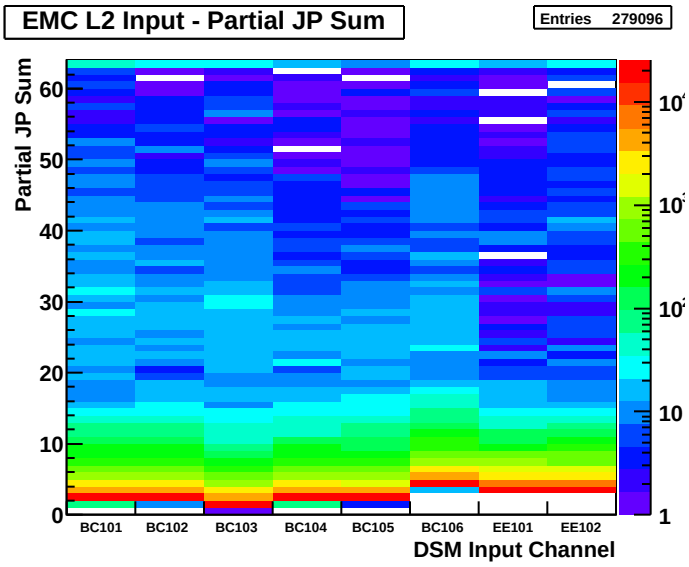

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

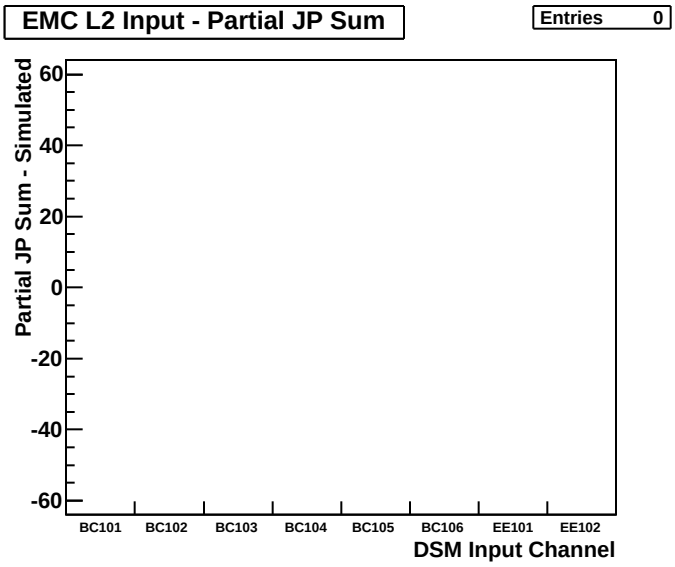

EMC L2 Input - JPY/JPB bits
Entries 279096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
46 26 30 25 22 46 13 36
152 69 95 59 78 115 50 75
199 137 138 111 159 192 17 29
34490 34655 34624 34692 34628 34534 34807 34747

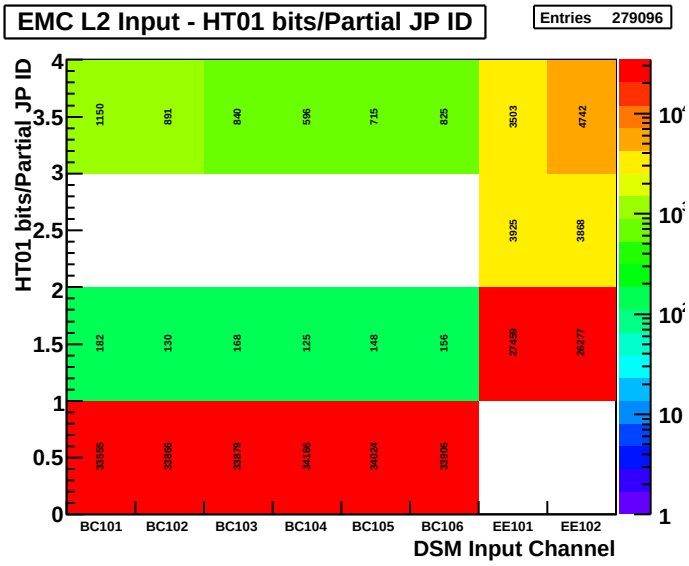

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

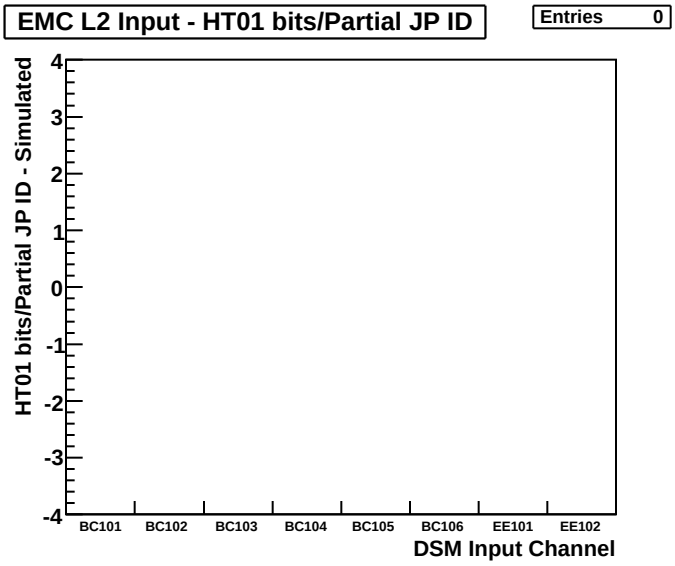

EMC L2 Input - JPZ/JPC bits
Entries 279096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
93 46 50 25 30 58 33 22
187 120 142 78 87 164 55 48
240 159 180 107 159 212 31 18
34567 34562 34515 34677 34611 34453 34768 34799

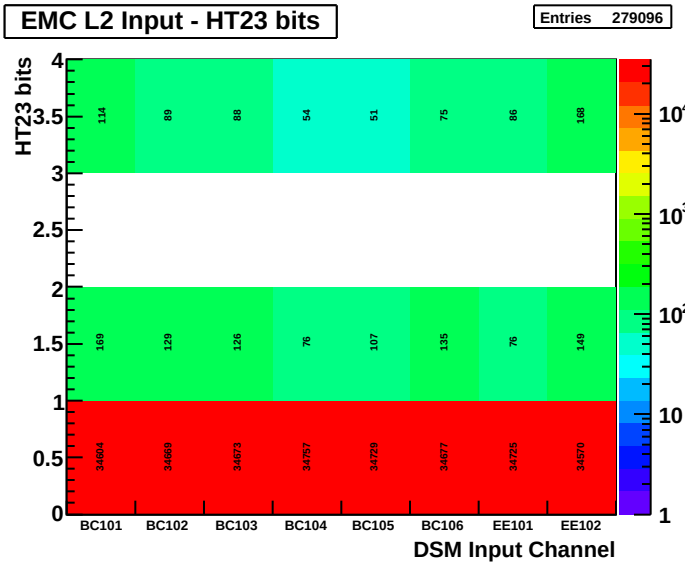

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

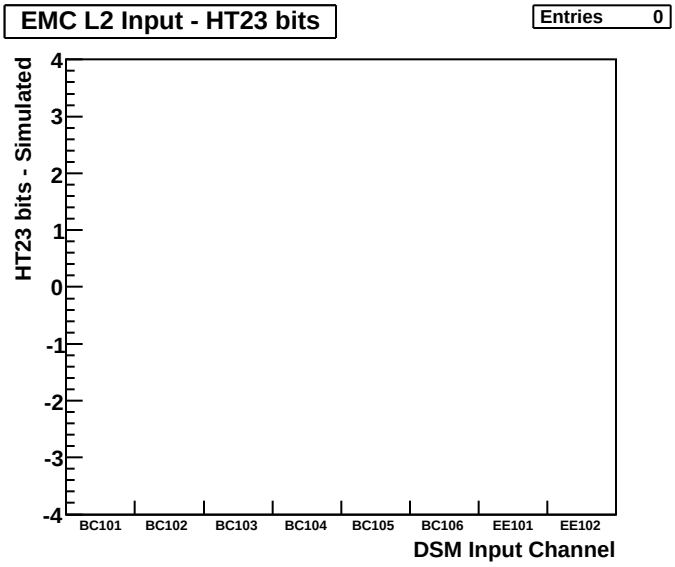

EMC L2 Input - Partial JP Sum
Entries 279096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

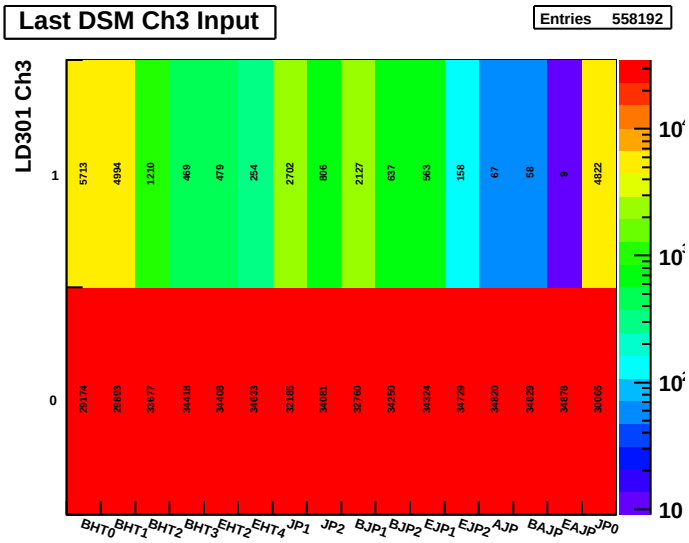

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

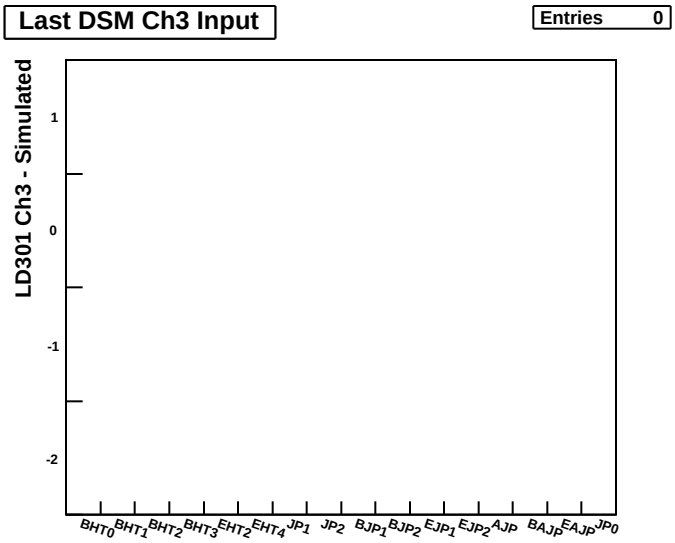

EMC L2 Input - HT01 bits/Partial JP ID
Entries 279096
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

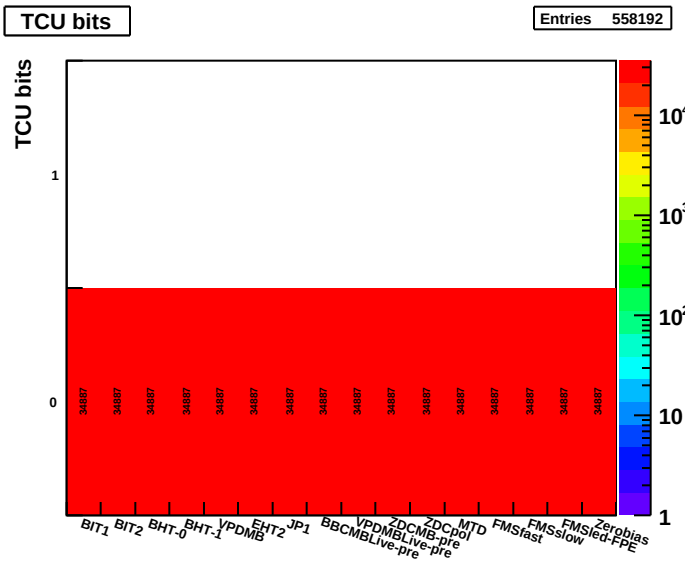

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

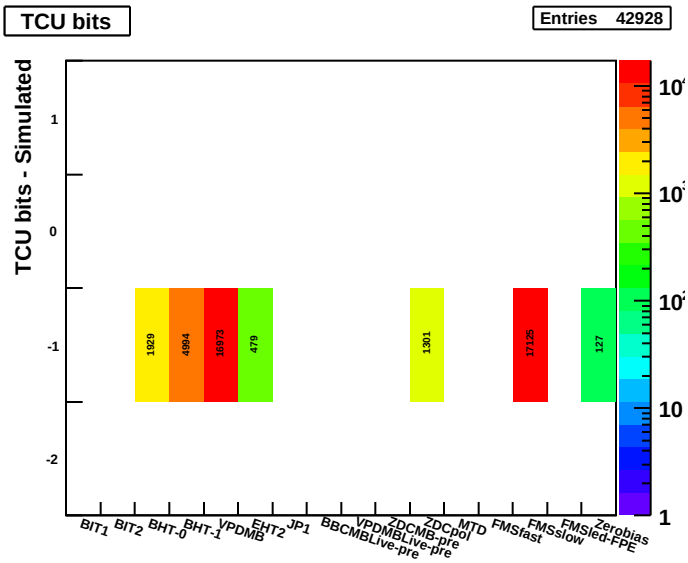

EMC L2 Input - HT23 bits
Entries 279096
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

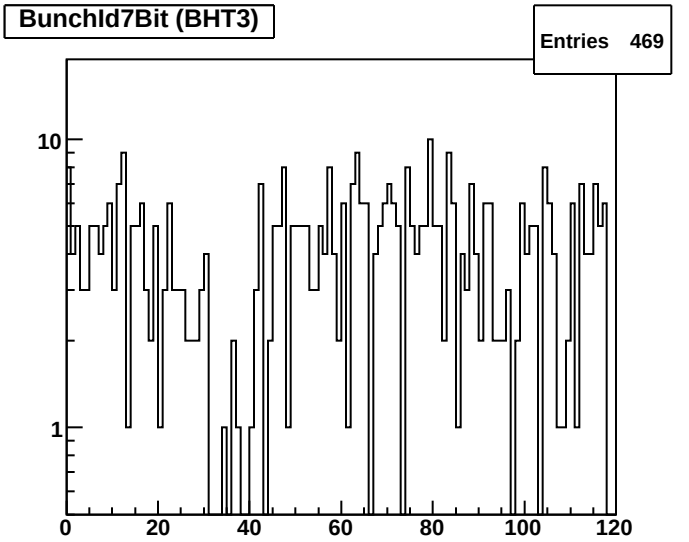

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

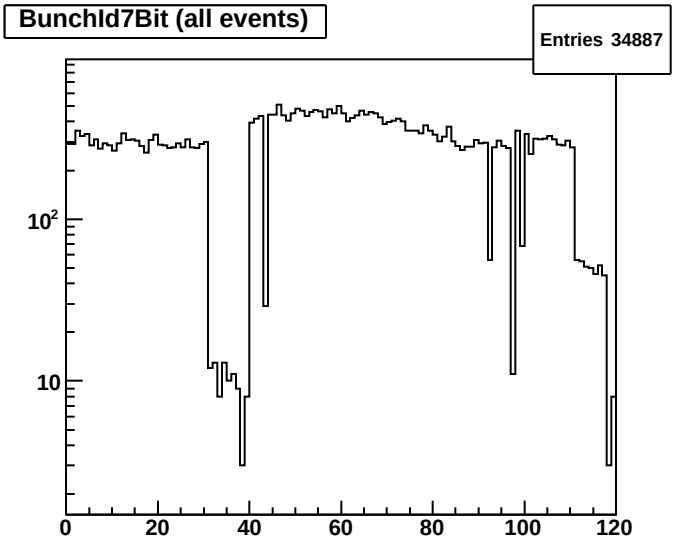

Last DSM Ch3 Input
Entries 558192
LD301 Ch3
5713 4994 1210 469 479 254 2702 806 2127 637 563 158 67 58 9 4822
29174 29893 33677 34418 34408 34633 32185 34081 32760 34250 34324 34729 34820 34829 34878 30065
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

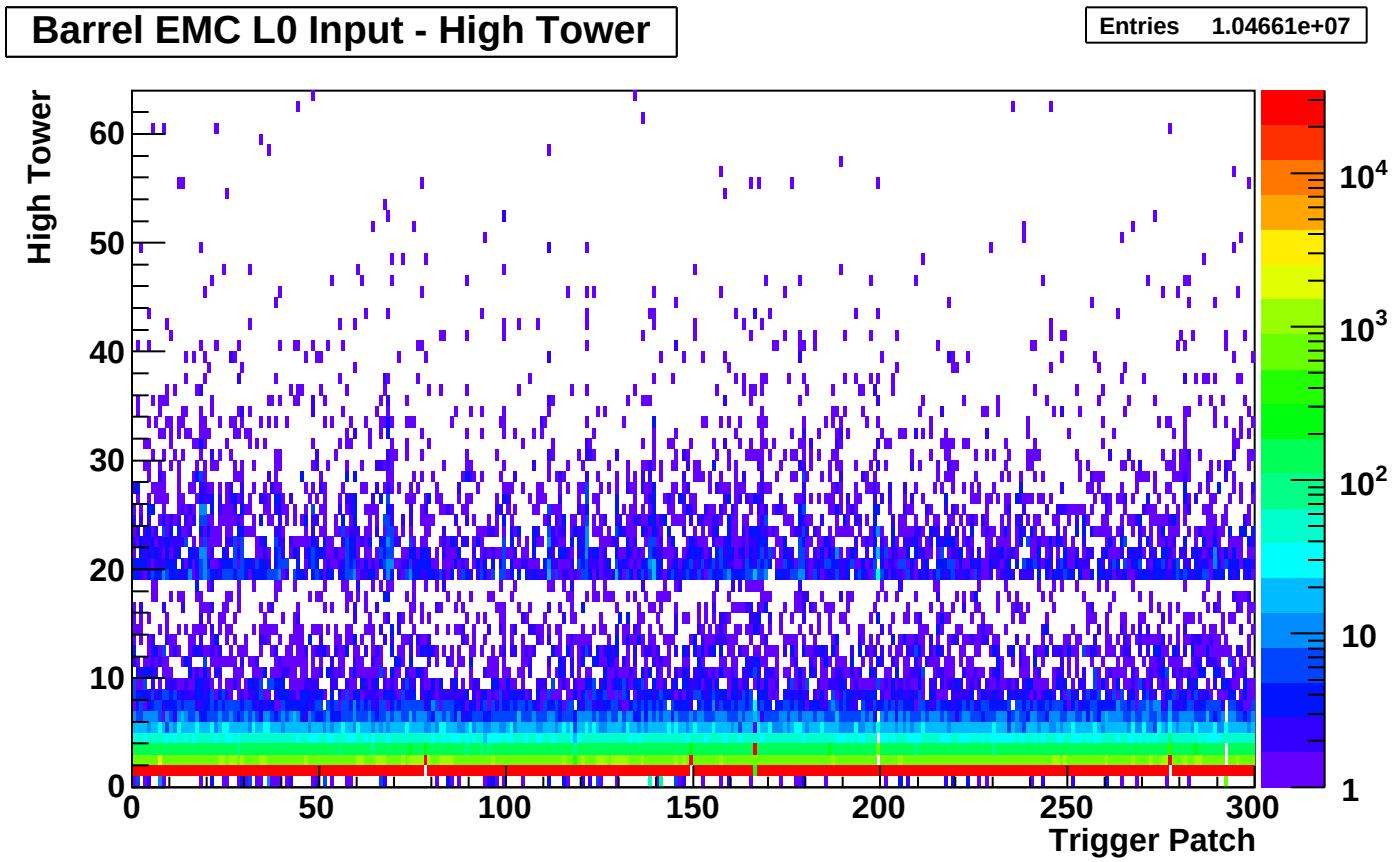

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

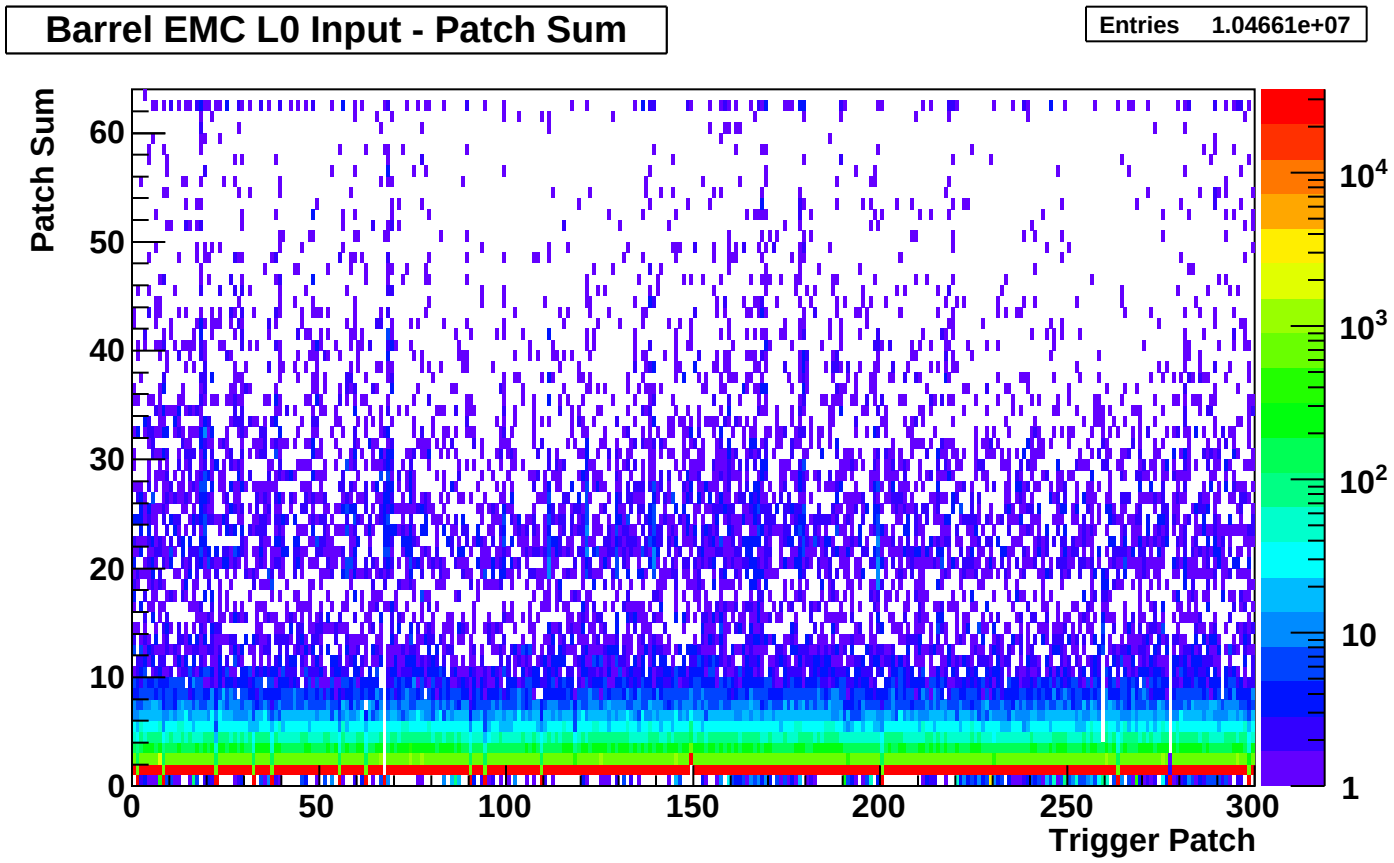

TCU bits
Entries 558192
TCU bits
34887 34887 34887 34887 34887 34887 34887 34887 34887 34887 34887 34887 34887 34887 34887 34887
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

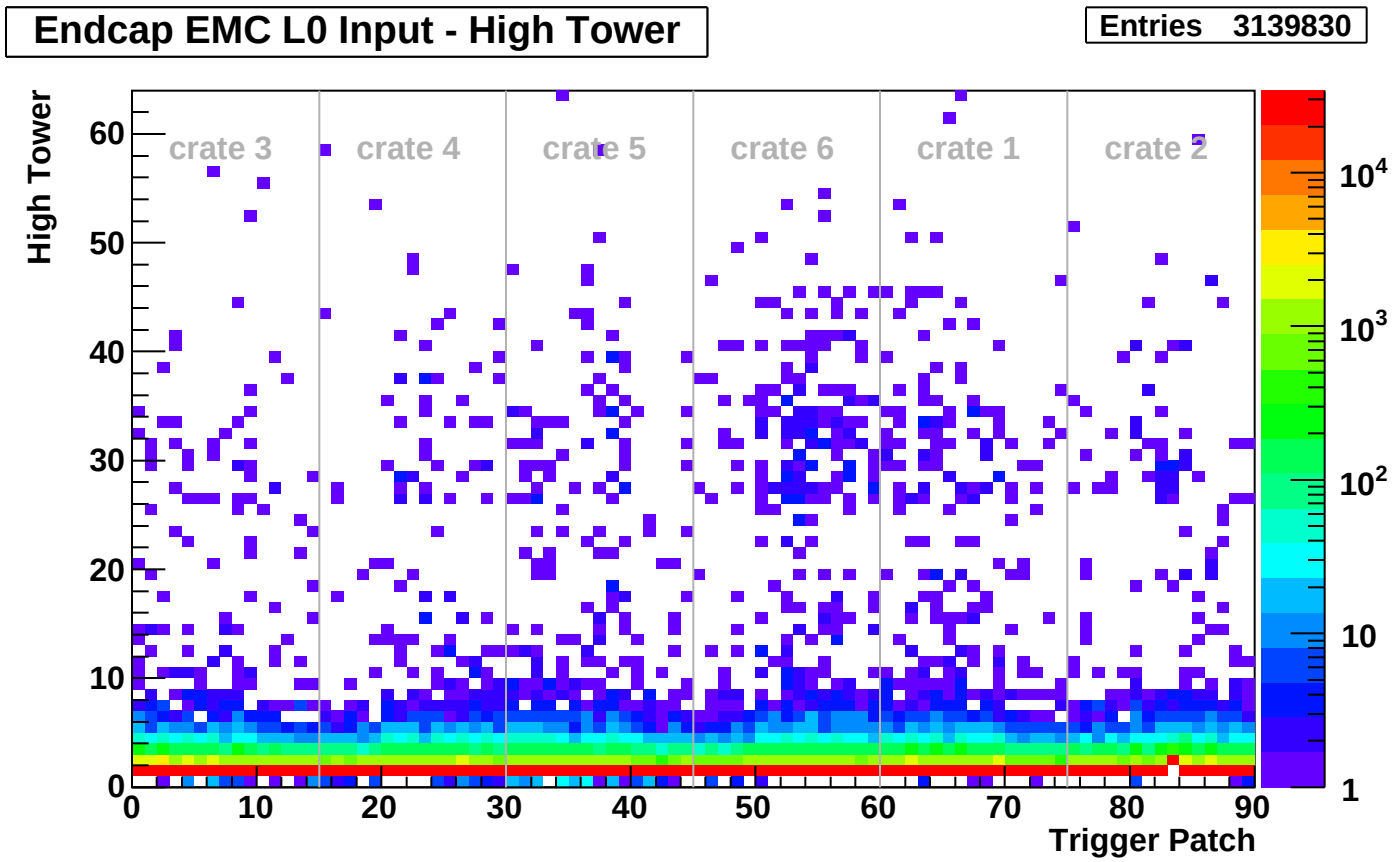

TCU bits
Entries 42928
TCU bits - Simulated
1929 4994 16973 479 1301 17125 127
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

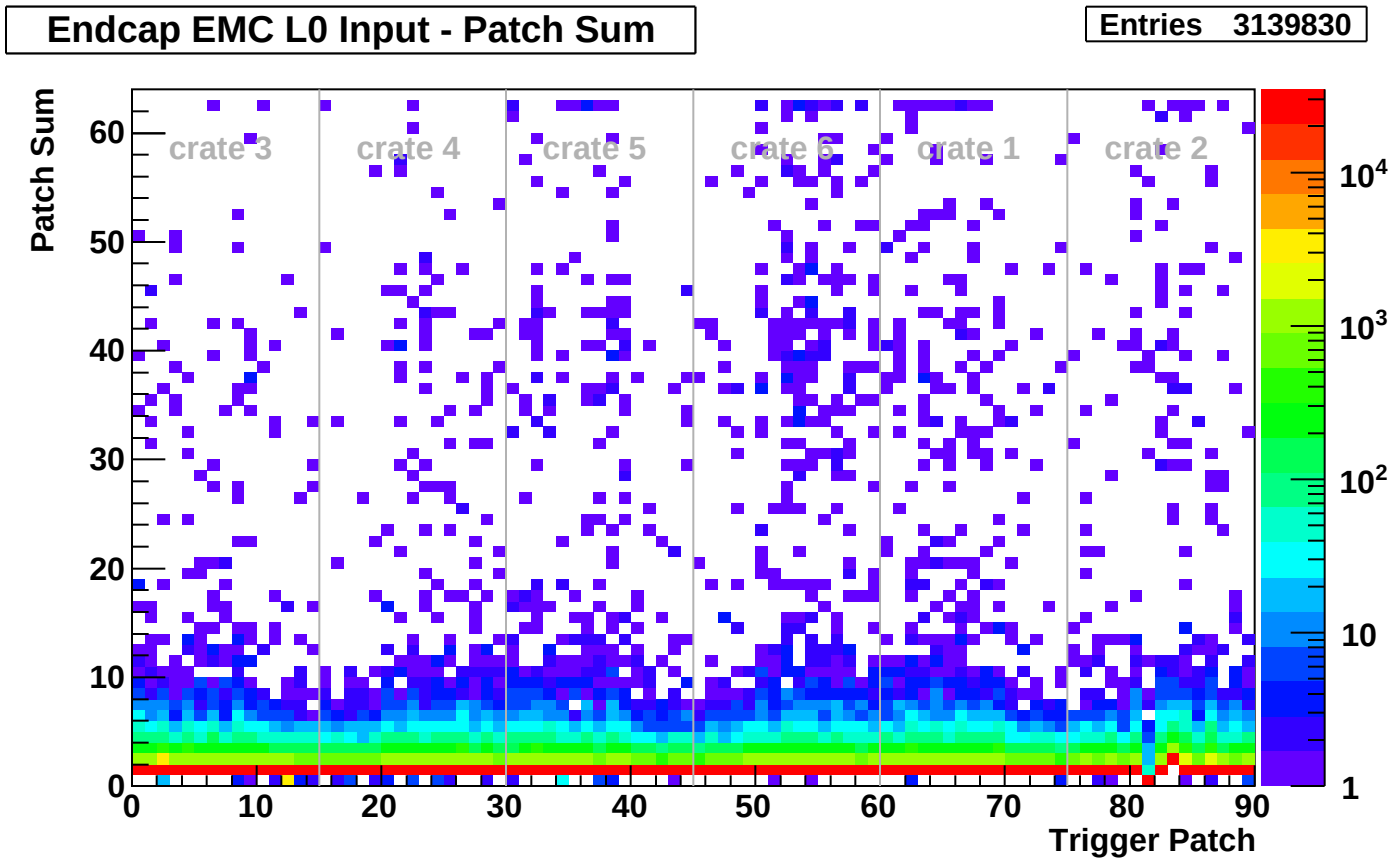

BunchId7Bit (BHT3)
Entries 469

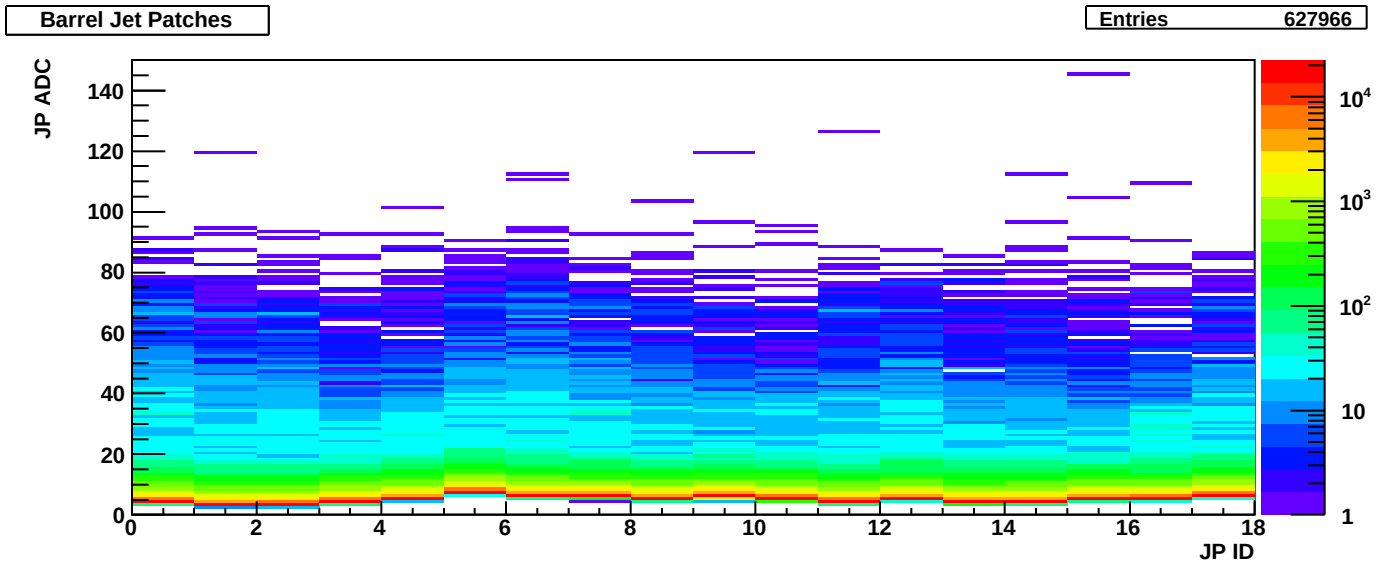

BunchId7Bit (all events)
Entries 34887

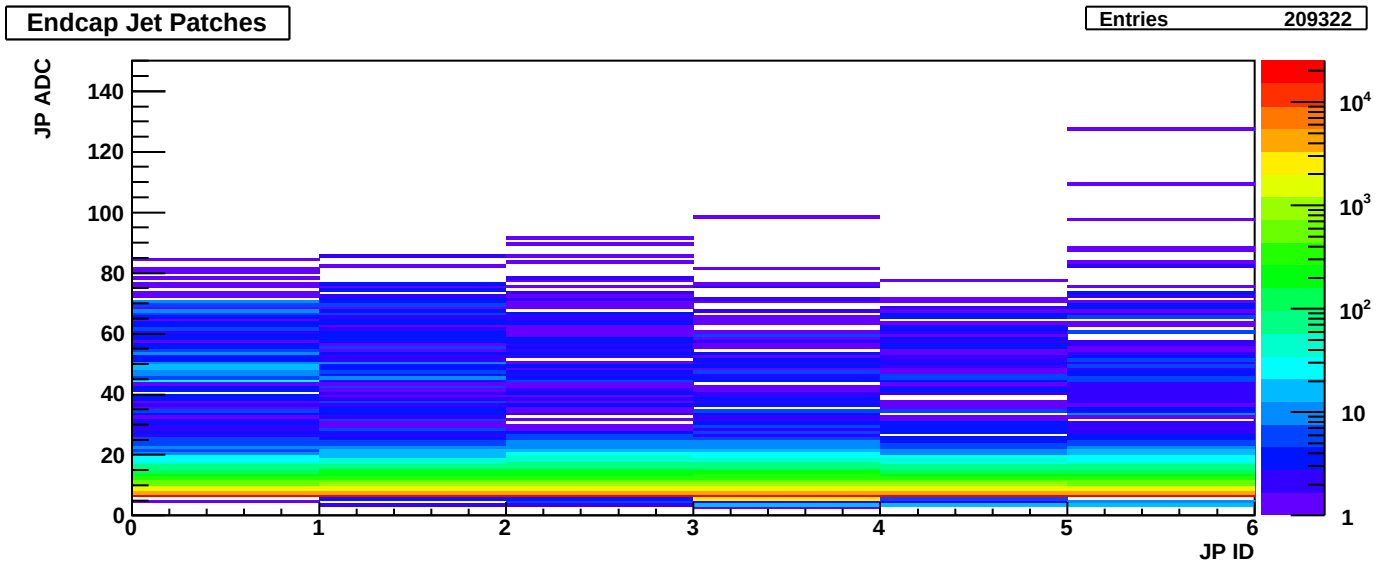

Barrel EMC L0 Input - High Tower
Entries 1.04661e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

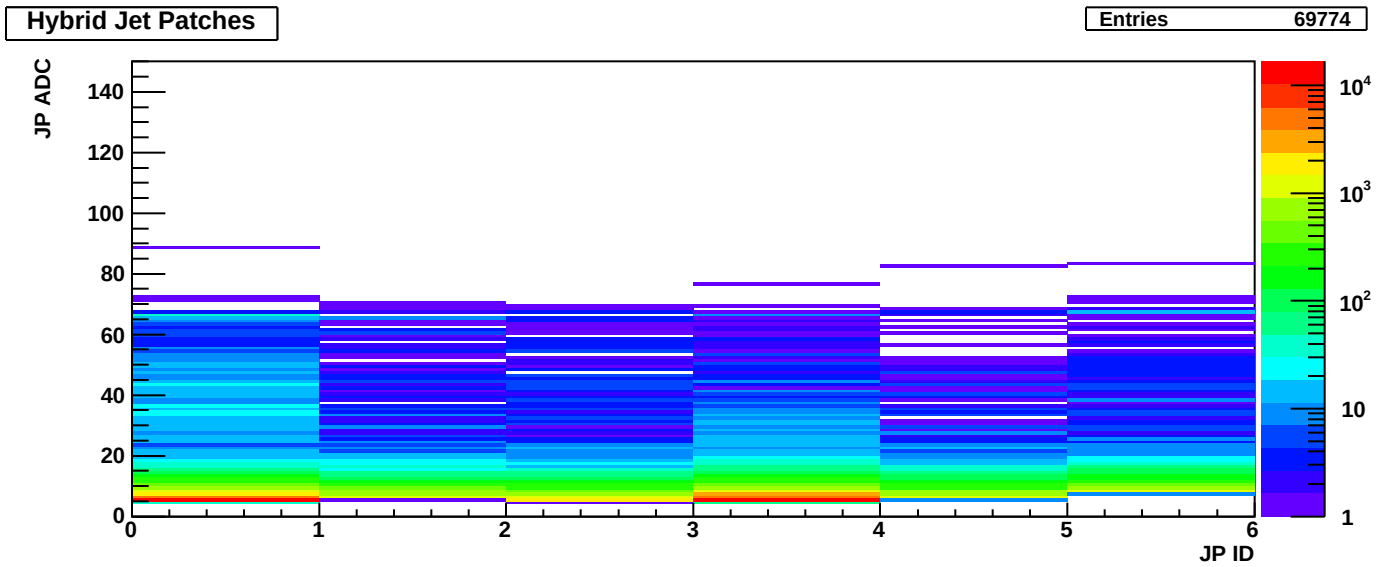

Barrel EMC L0 Input - Patch Sum
Entries 1.04661e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

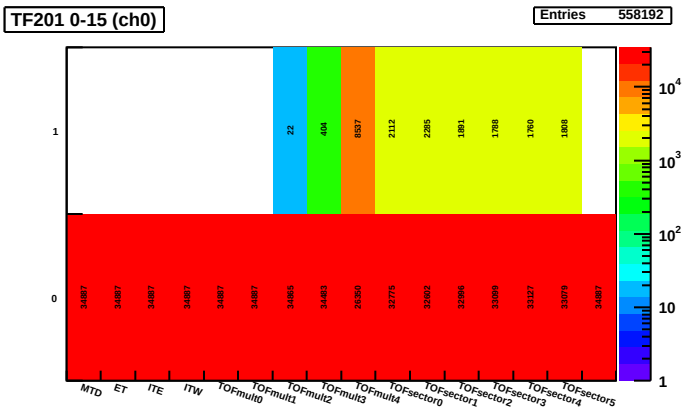

Endcap EMC L0 Input - High Tower
Entries 3139830
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

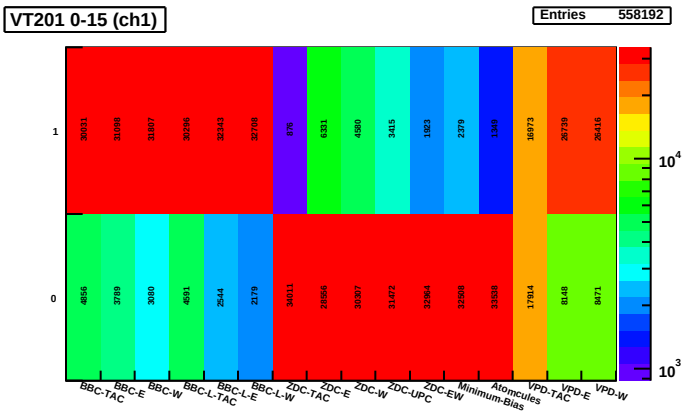

Endcap EMC L0 Input - Patch Sum
Entries 3139830
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

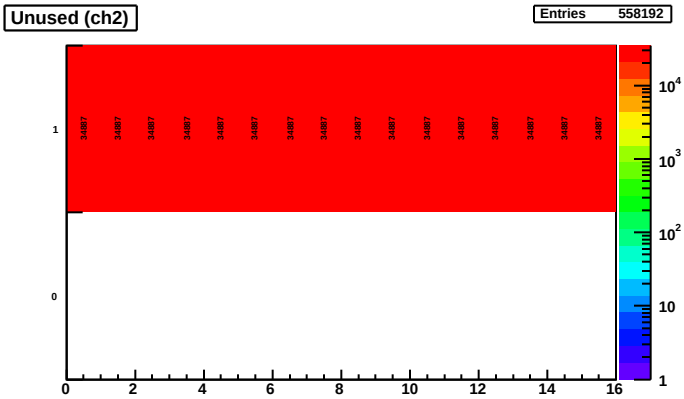

Barrel Jet Patches
Entries 627966
JP ADC
JP ID

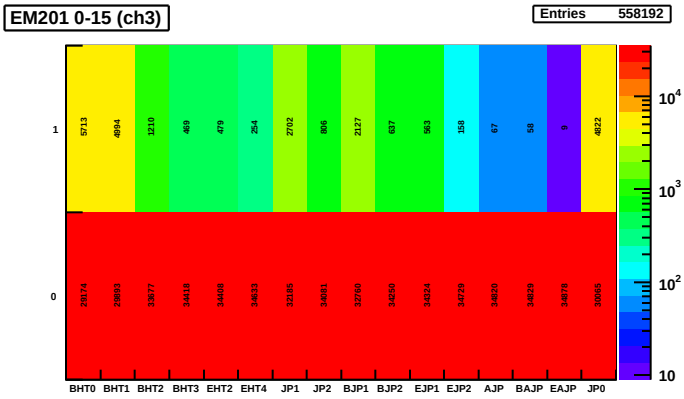

Endcap Jet Patches
Entries 209322
JP ADC
JP ID

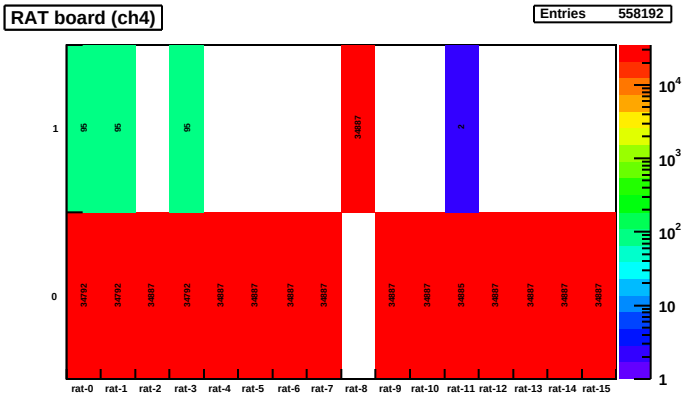

Hybrid Jet Patches
Entries 69774
JP ADC
JP ID

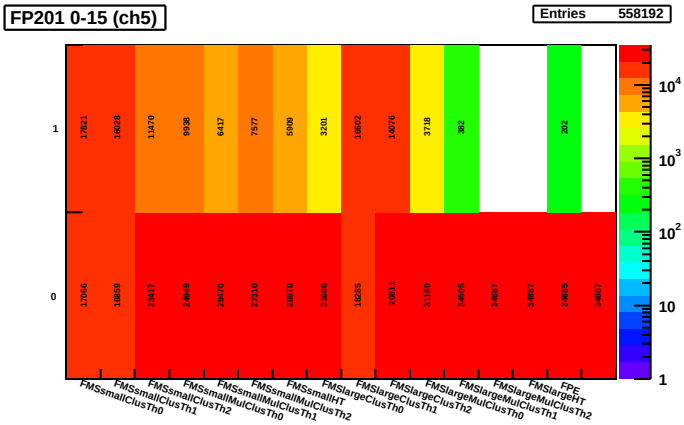

TF201 0-15 (ch0)
Entries 558192
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

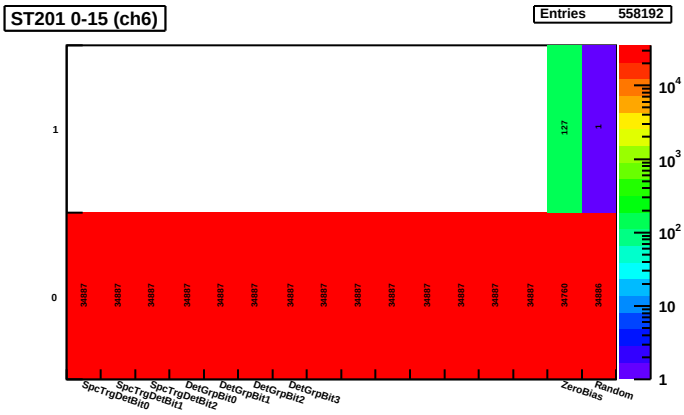

VT201 0-15 (ch1)
Entries 558192
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

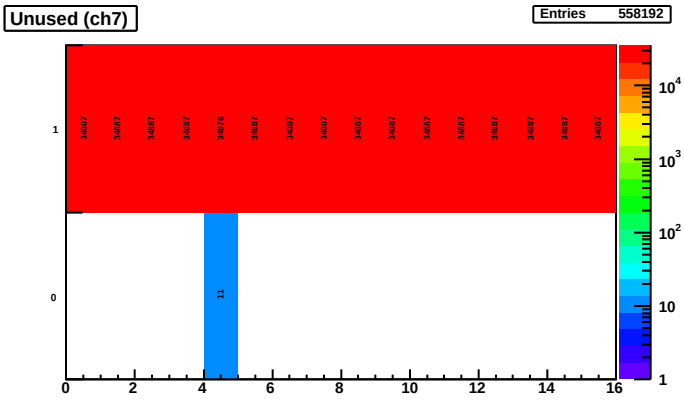

Unused (ch2)
Entries 558192

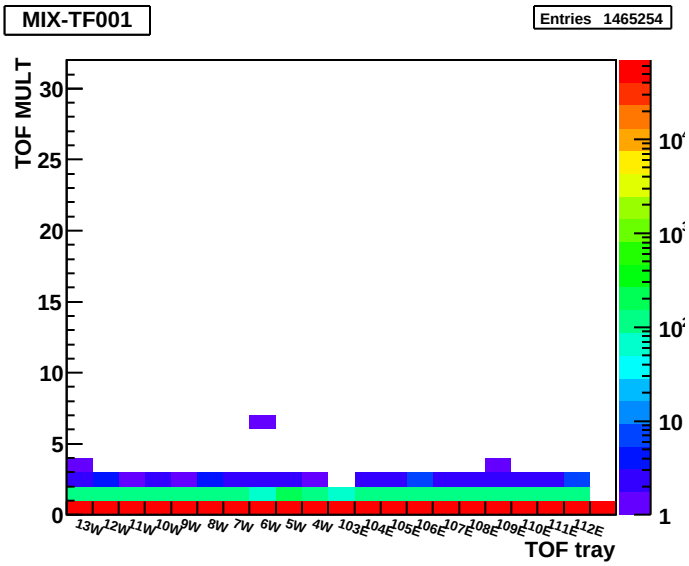

EM201 0-15 (ch3)
Entries 558192
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

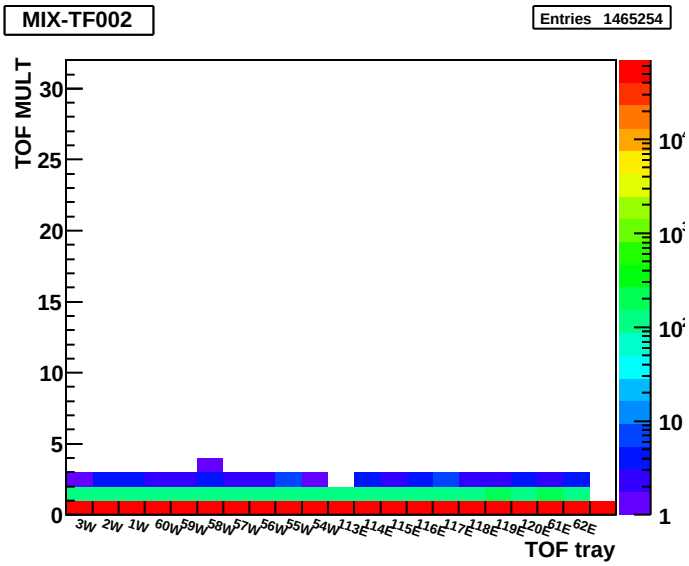

RAT board (ch4)
Entries 558192
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

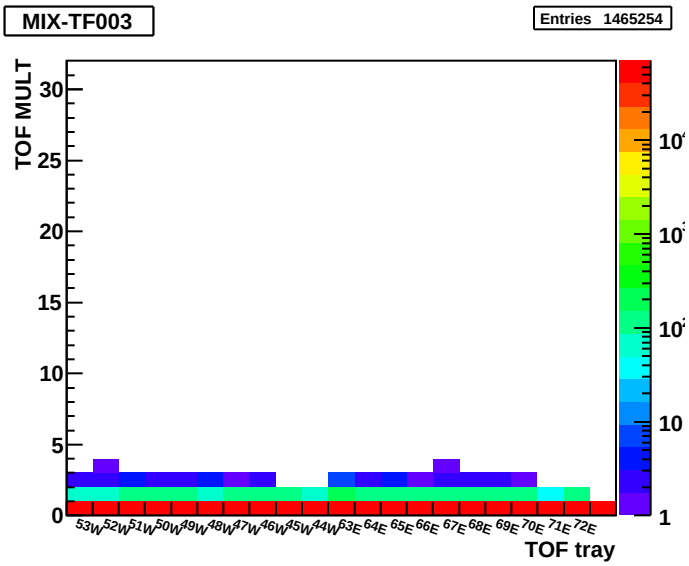

FP201 0-15 (ch5)
Entries 558192
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

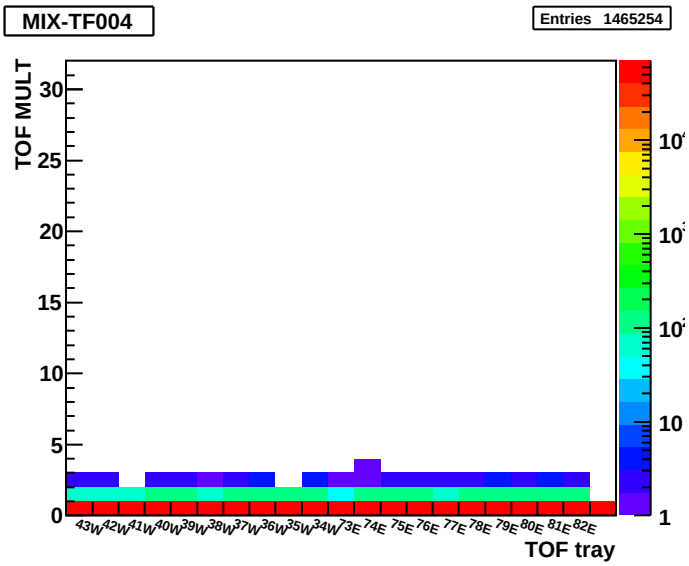

ST201 0-15 (ch6)
Entries 558192
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

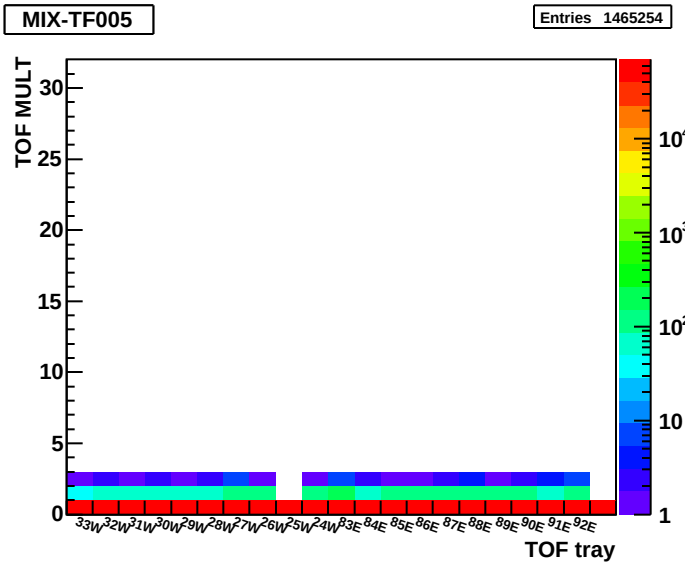

Unused (ch7)
Entries 558192

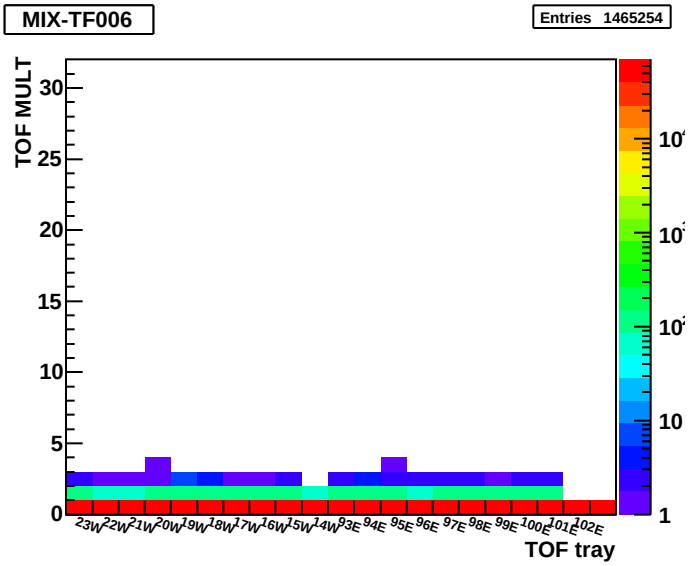

MIX-TF001
Entries 1465254
TOF MULT
TOF tray

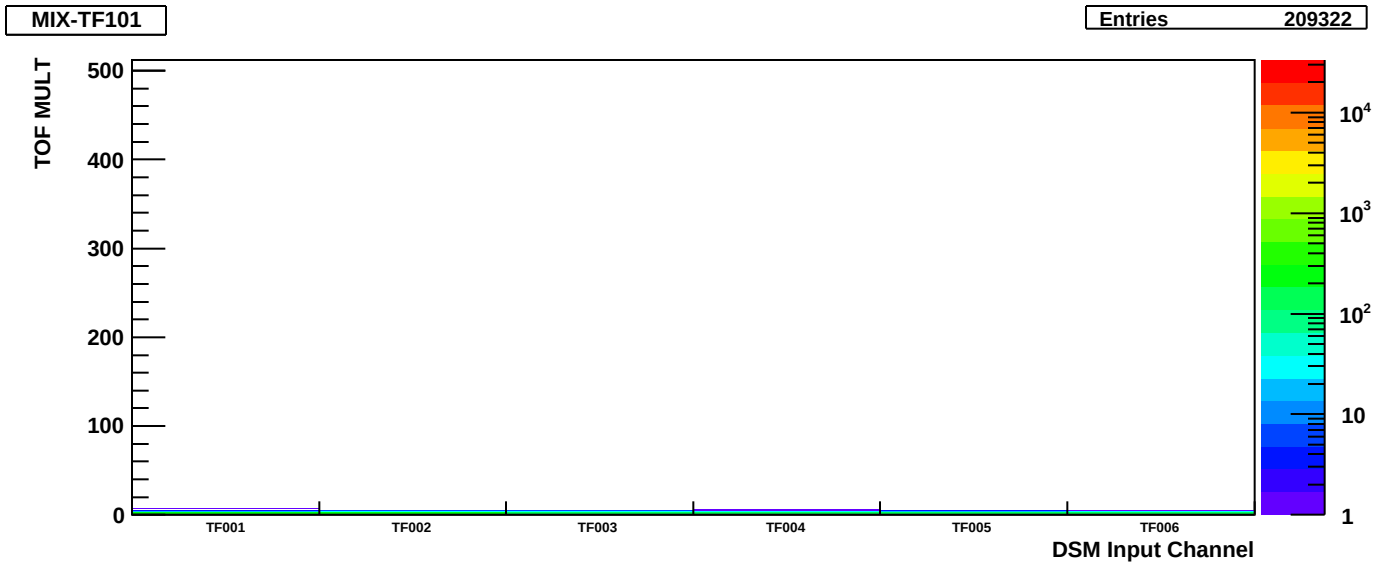

MIX-TF002
Entries 1465254
TOF MULT
TOF tray

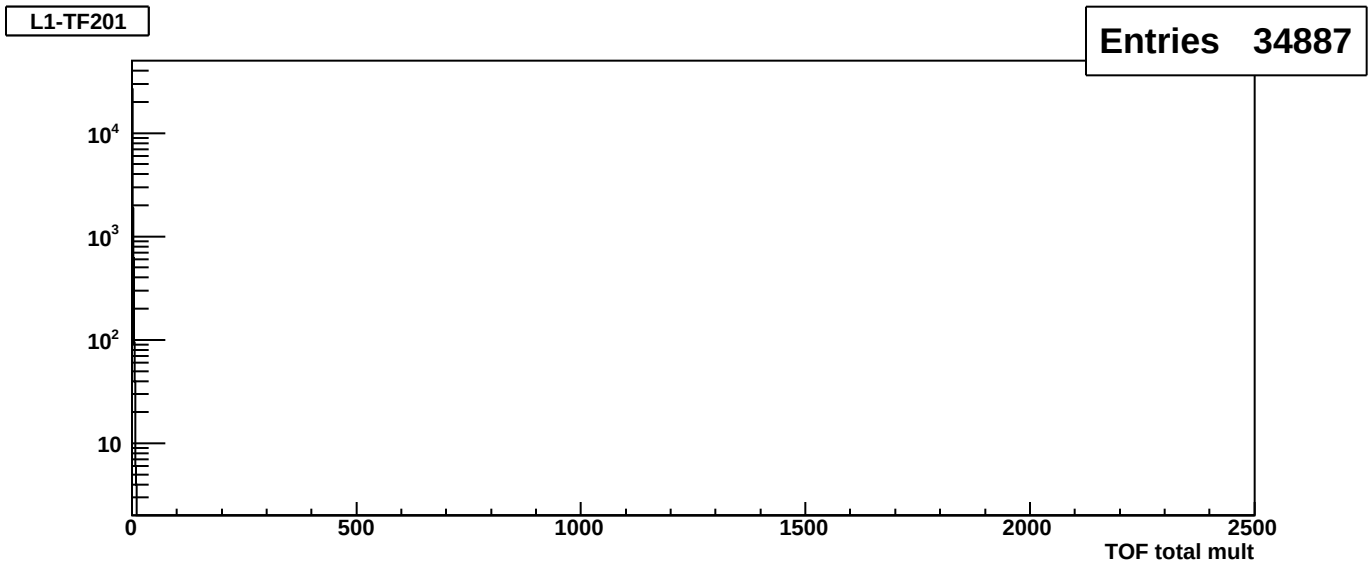

MIX-TF003
Entries 1465254
TOF MULT
TOF tray

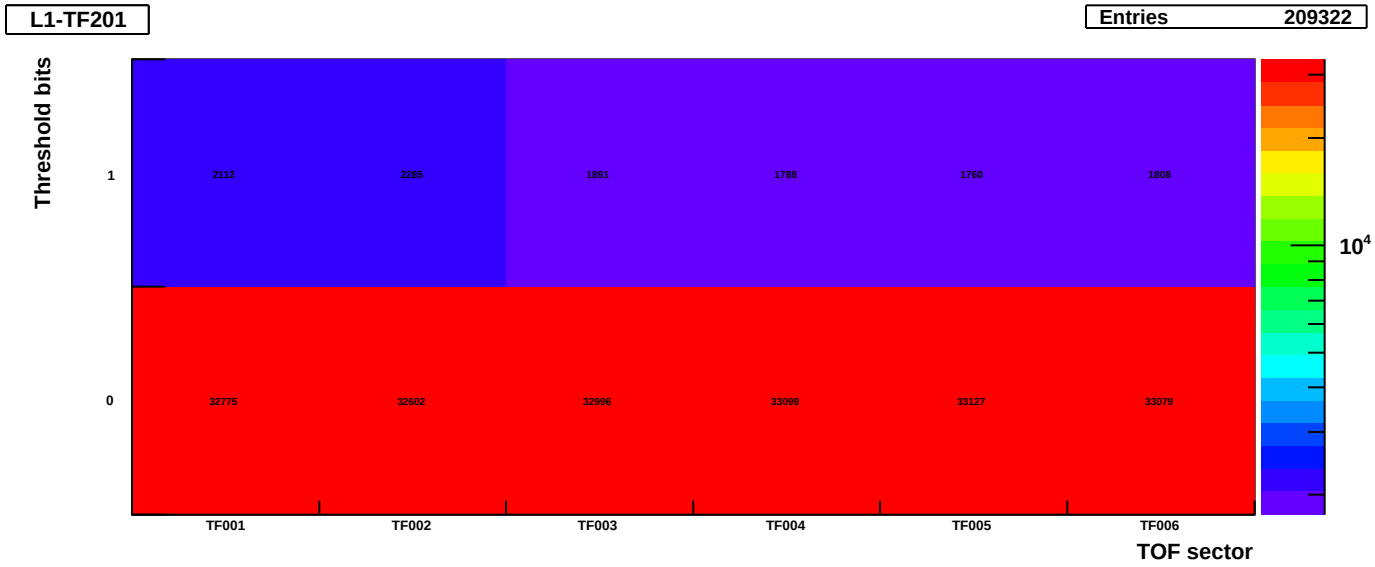

MIX-TF004
Entries 1465254
TOF MULT
TOF tray

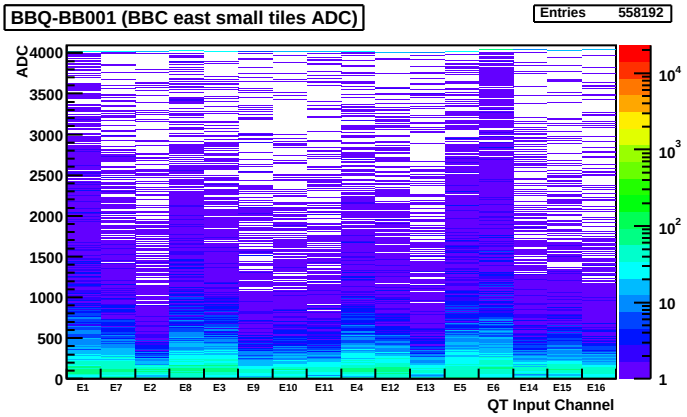

MIX-TF005
Entries 1465254
TOF MULT
TOF tray

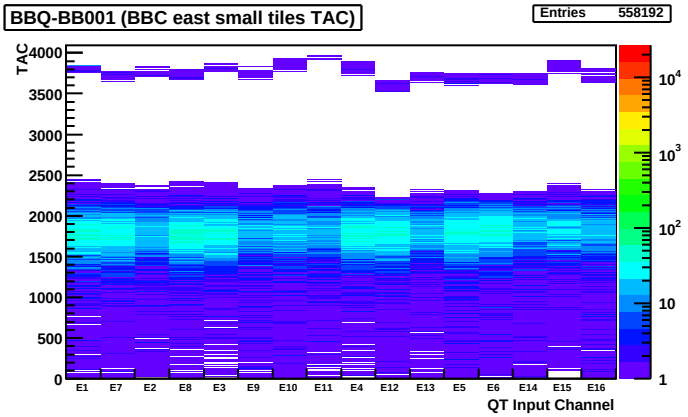

MIX-TF006
Entries 1465254
TOF MULT
TOF tray

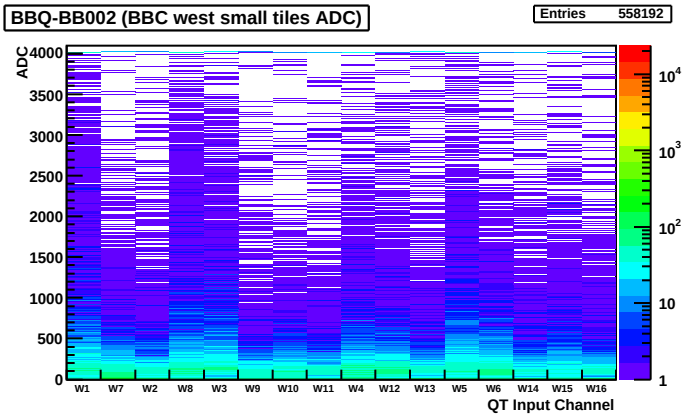

MIX-TF101
Entries 209322
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

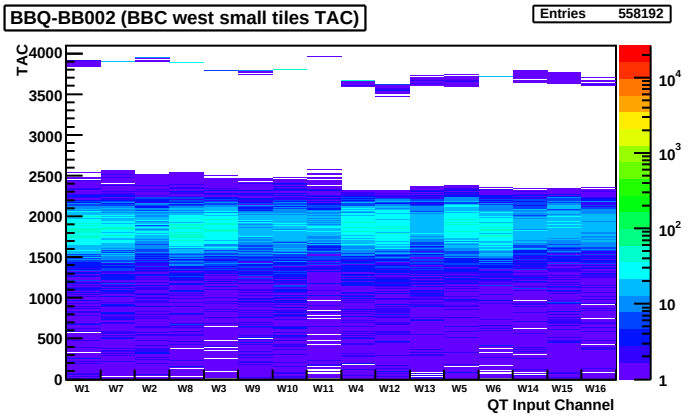

L1-TF201
Entries 34887
10^4
10^3
10^2
10
0
500
1000
1500
2000
2500
TOF total mult

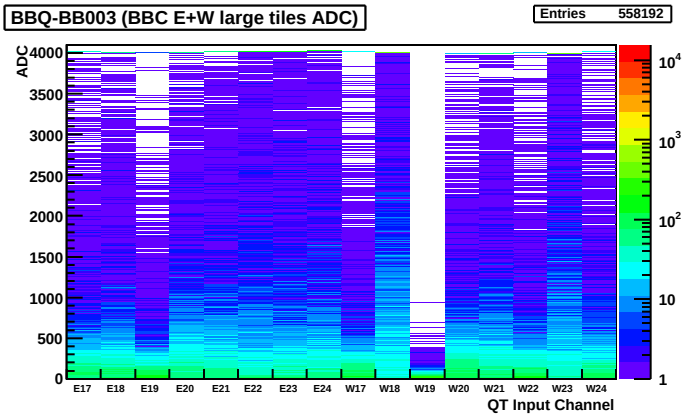

L1-TF201
Entries 209322
Threshold bits
1
0
2112
2285
1891
1788
1760
1808
32775
32602
32996
33099
33127
33079
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

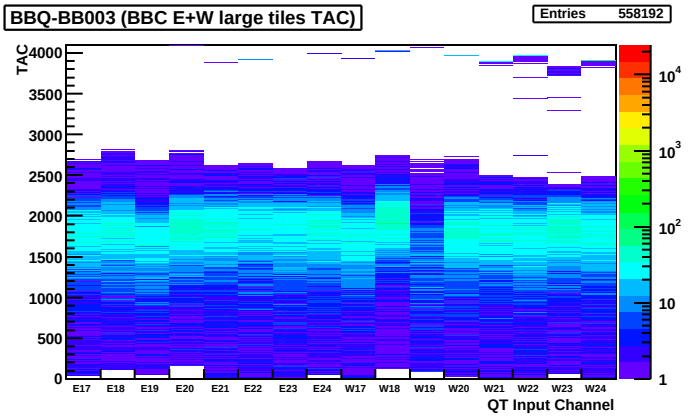

BBQ-BB001 (BBC east small tiles ADC)
Entries 558192
ADC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

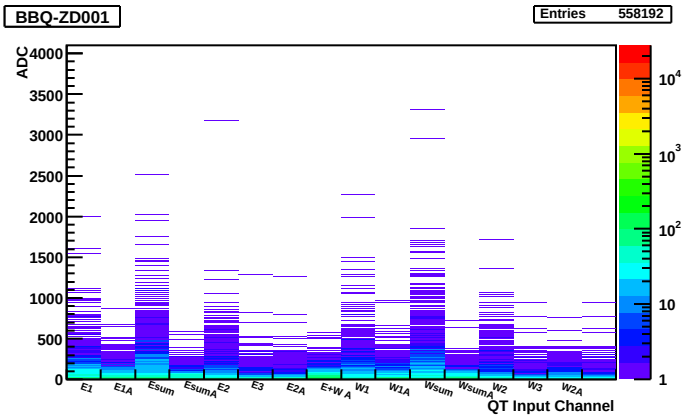

BBQ-BB001 (BBC east small tiles TAC)
Entries 558192
TAC
QT Input Channel
E1 E7 E2 E8 E3 E9 E10 E11 E4 E12 E13 E5 E6 E14 E15 E16

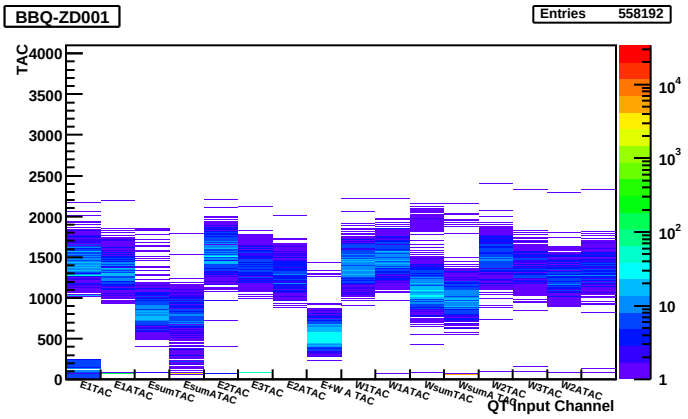

BBQ-BB002 (BBC west small tiles ADC)
Entries 558192
ADC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

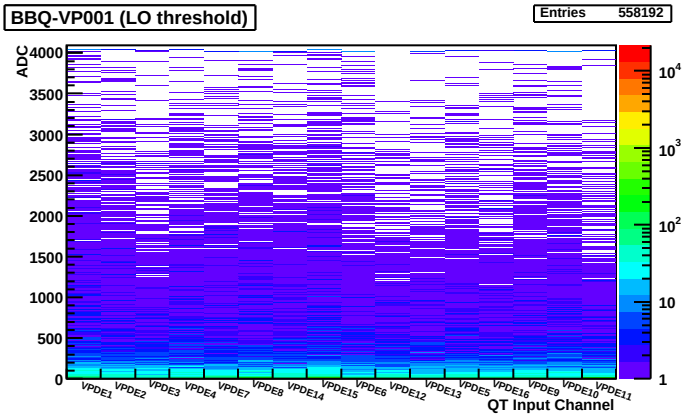

BBQ-BB002 (BBC west small tiles TAC)
Entries 558192
TAC
QT Input Channel
W1 W7 W2 W8 W3 W9 W10 W11 W4 W12 W13 W5 W6 W14 W15 W16

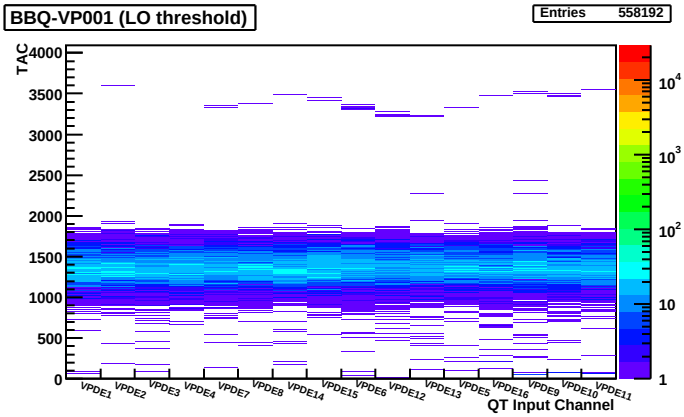

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 558192
ADC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

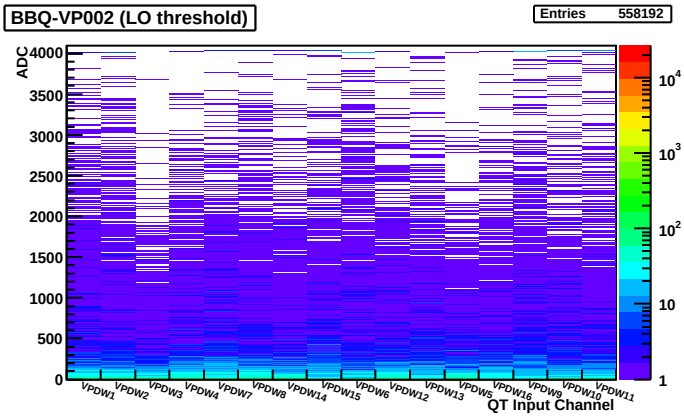

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 558192
TAC
QT Input Channel
E17 E18 E19 E20 E21 E22 E23 E24 W17 W18 W19 W20 W21 W22 W23 W24

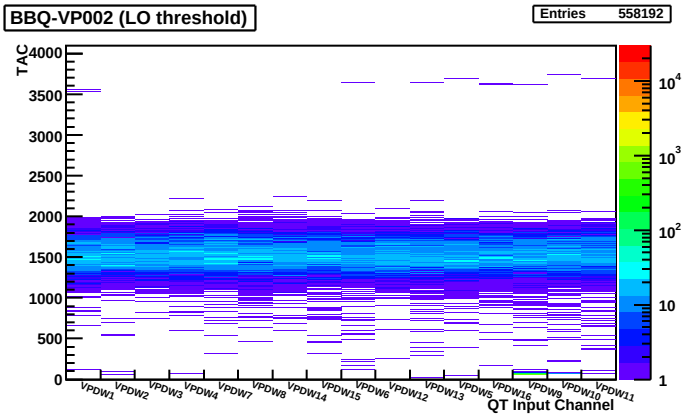

BBQ-ZD001
Entries 558192
ADC
QT Input Channel
E1 E1A Esum EsumA E2 E3 E2A E+W A W1 W1A Wsum WsumA W2 W3 W2A

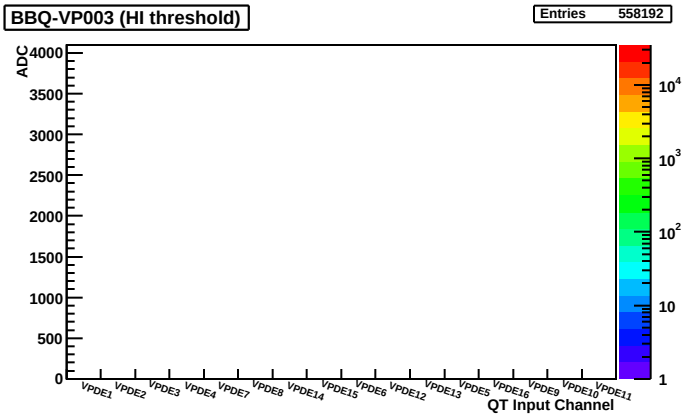

BBQ-ZD001
Entries 558192
TAC
QT Input Channel
E1TAC E1ATAC EsumTAC EsumATAC E2TAC E3TAC E2ATAC E+W A TAC W1TAC W1ATAC WsumTAC WsumATAC W2TAC W3TAC W2ATAC

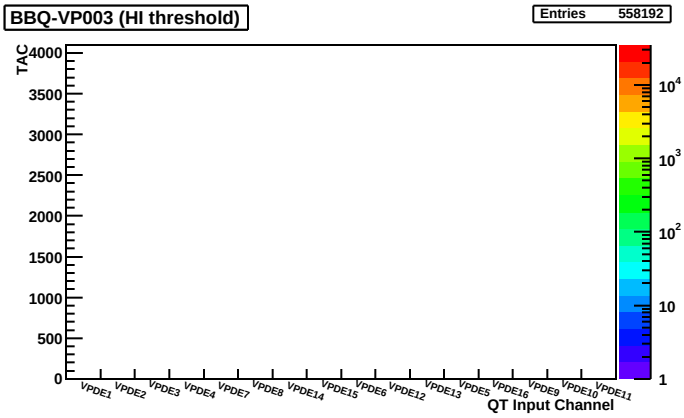

BBQ-VP001 (LO threshold)
Entries 558192
ADC
QT Input Channel

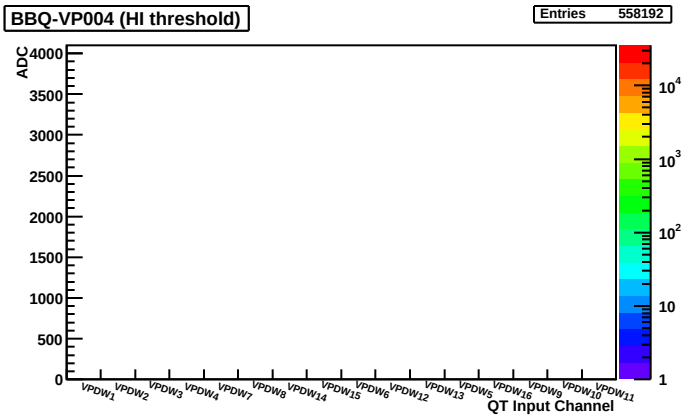

BBQ-VP001 (LO threshold)
Entries 558192
TAC
QT Input Channel

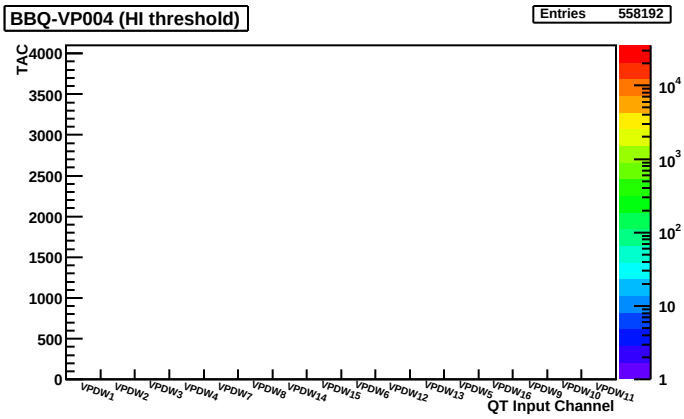

BBQ-VP002 (LO threshold)
Entries 558192
ADC
QT Input Channel

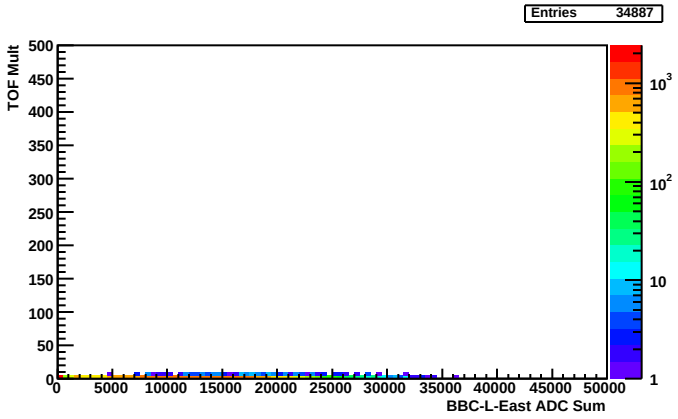

BBQ-VP002 (LO threshold)
Entries 558192
TAC
QT Input Channel

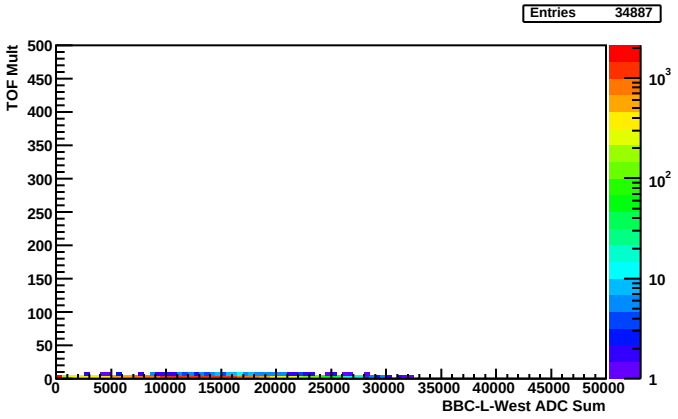

BBQ-VP003 (HI threshold)
Entries 558192
ADC
QT Input Channel

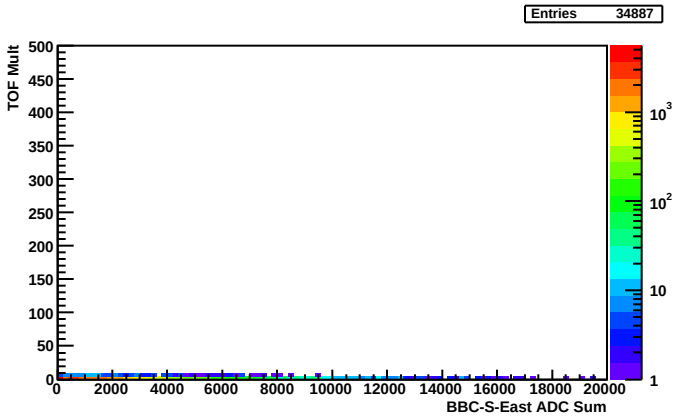

BBQ-VP003 (HI threshold)
Entries 558192
TAC
QT Input Channel

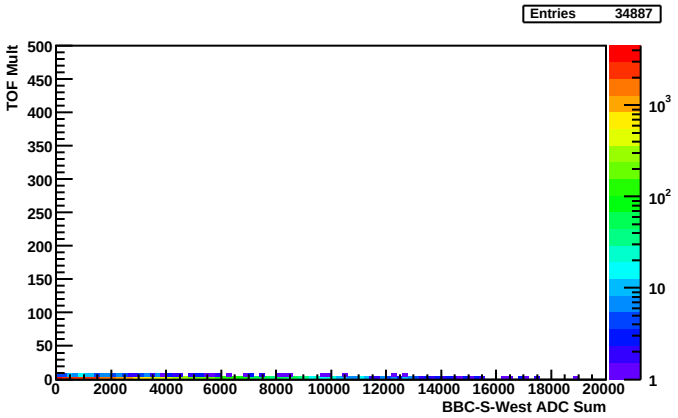

BBQ-VP004 (HI threshold)
Entries 558192
ADC
QT Input Channel

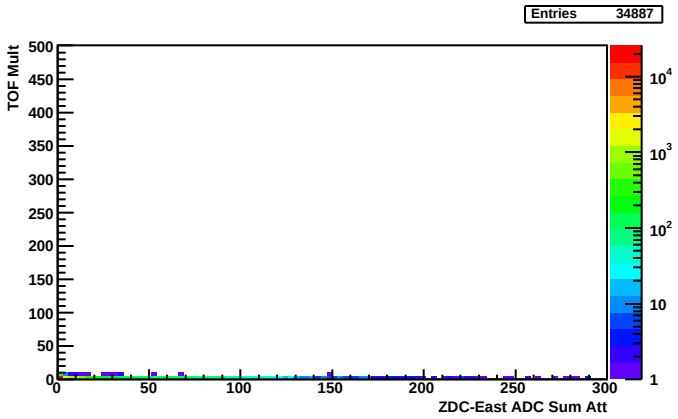

BBQ-VP004 (HI threshold)
Entries 558192
TAC
QT Input Channel

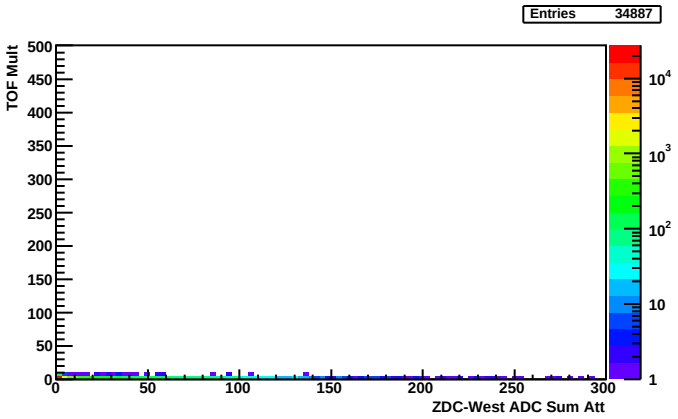

Entries 34887
TOF Mult
BBC-L-East ADC Sum

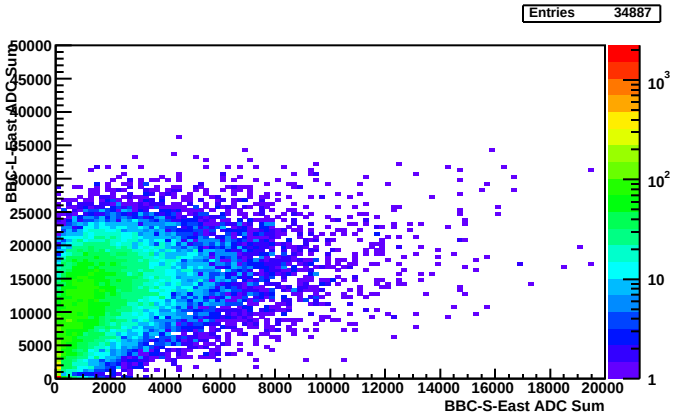

Entries 34887
TOF Mult
BBC-L-West ADC Sum

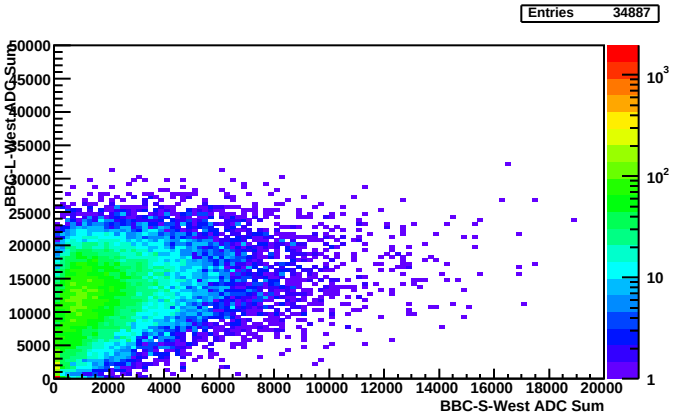

Entries 34887
TOF Mult
BBC-S-East ADC Sum

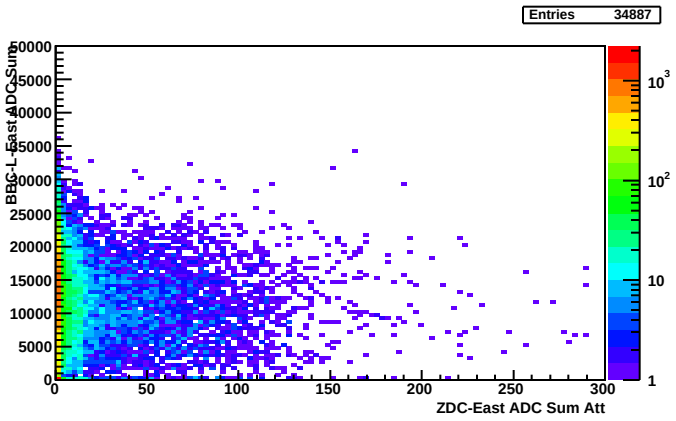

Entries 34887
TOF Mult
BBC-S-West ADC Sum

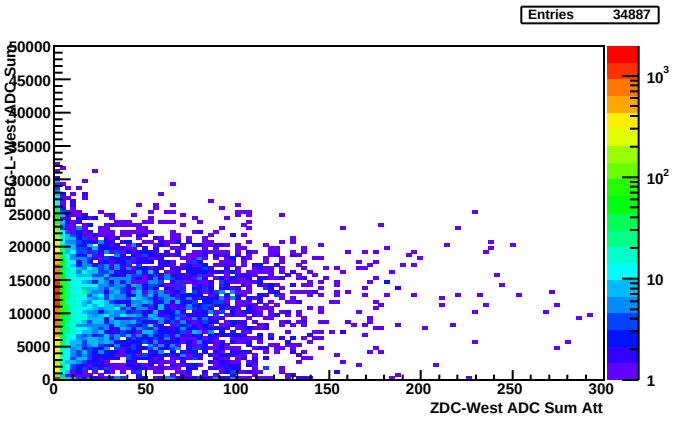

Entries 34887
TOF Mult
ZDC-East ADC Sum Att

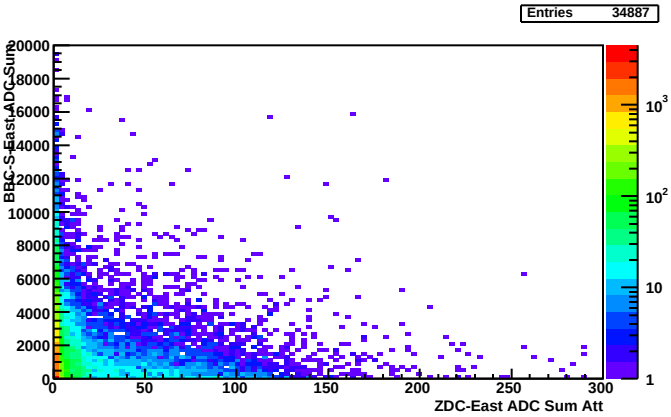

Entries 34887
TOF Mult
ZDC-West ADC Sum Att

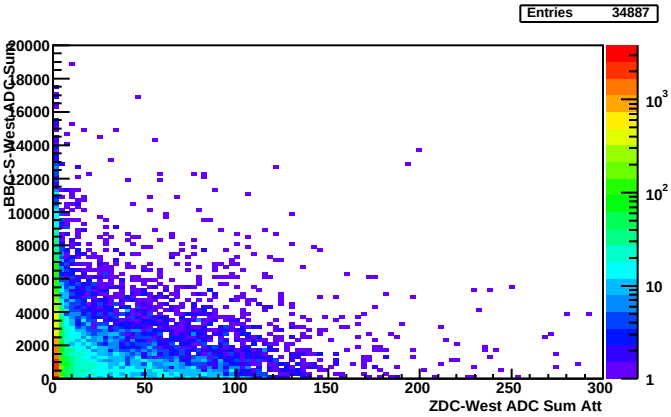

Entries 34887
BBC-L-East ADC Sum
BBC-S-East ADC Sum

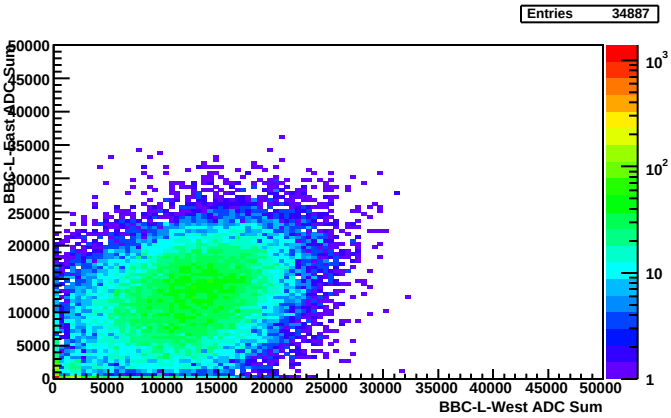

Entries 34887
BBC-L-West ADC Sum
BBC-S-West ADC Sum

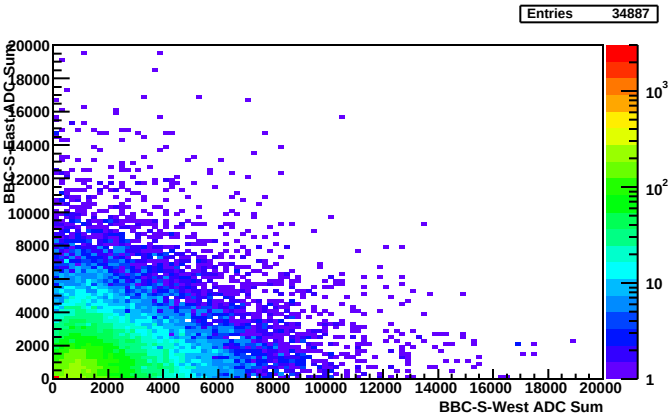

Entries 34887
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

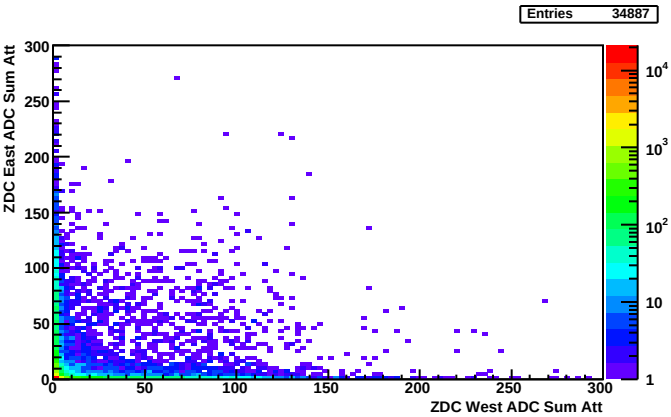

Entries 34887
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

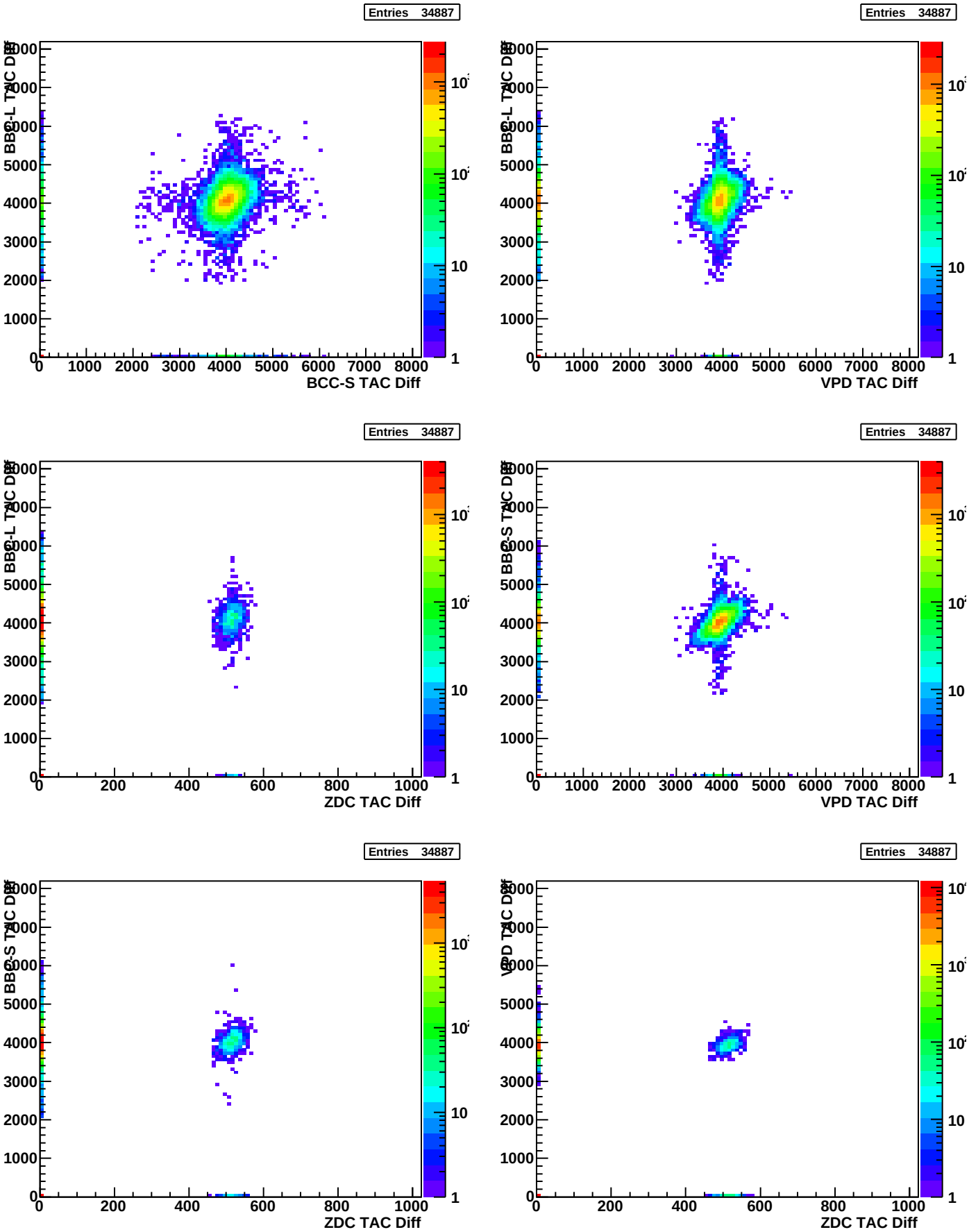

Entries 34887
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

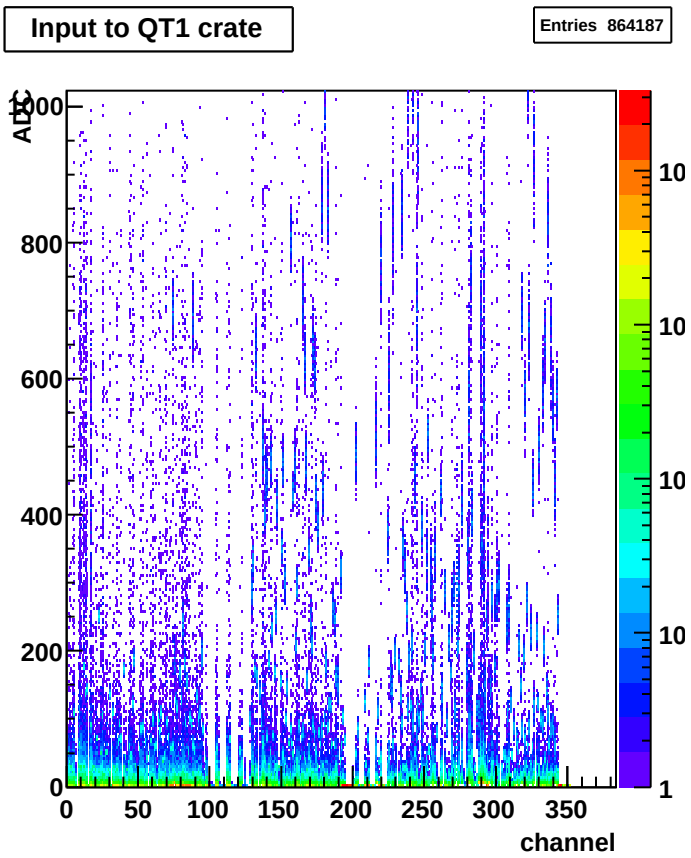

Entries 34887
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

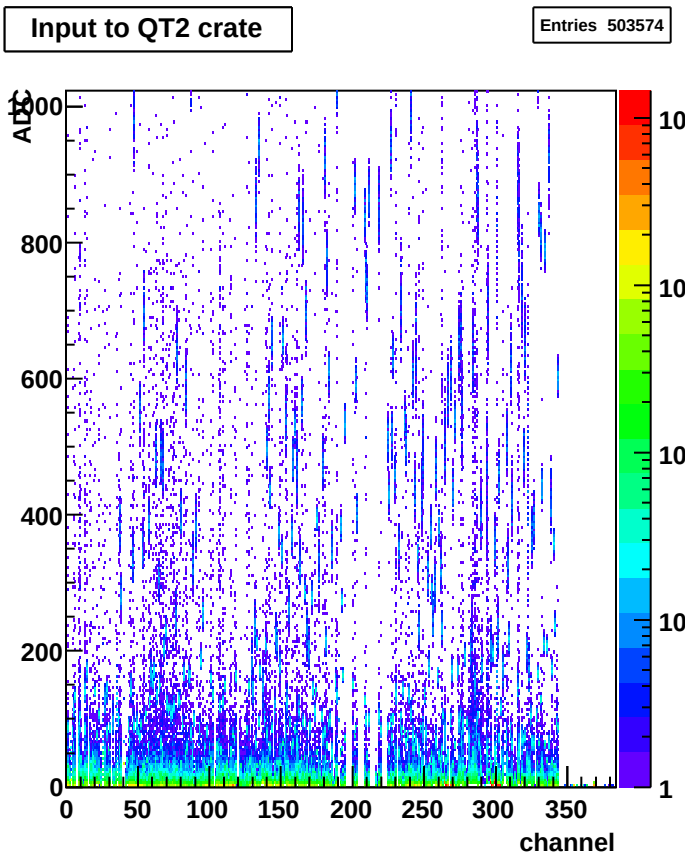

Entries 34887
BBC-L-East ADC Sum
BBC-L-West ADC Sum

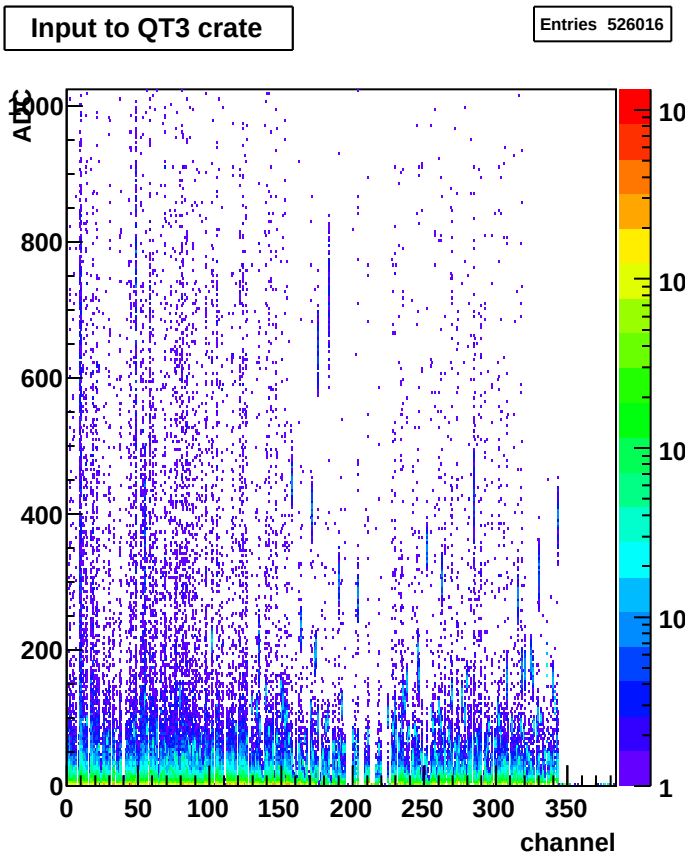

Entries 34887
BBC-S-East ADC Sum
BBC-S-West ADC Sum

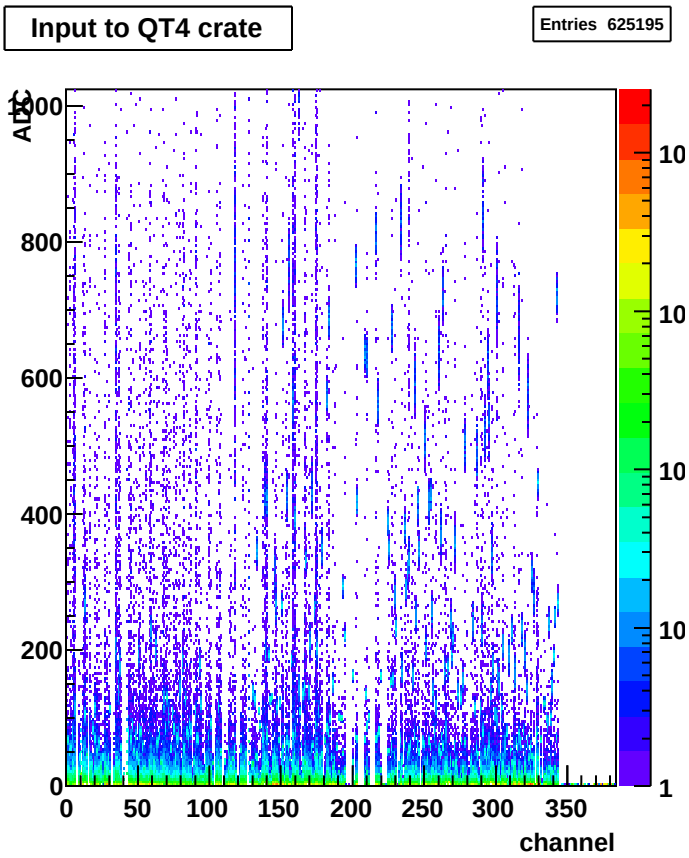

Entries 34887
ZDC East ADC Sum Att
ZDC West ADC Sum Att

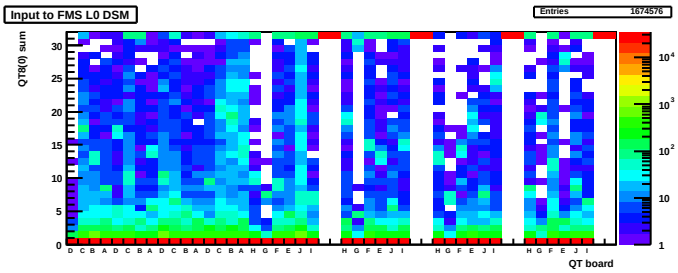

Entries 34887
BBC-L TAC Diff
BCC-S TAC Diff
VPD TAC Diff
ZDC TAC Diff
BBC-S TAC Diff

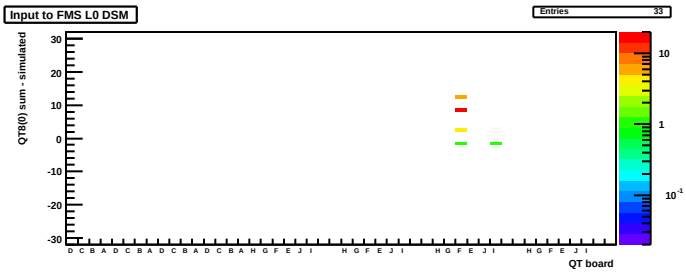

Input to QT1 crate
Entries 864187
ADC
channel

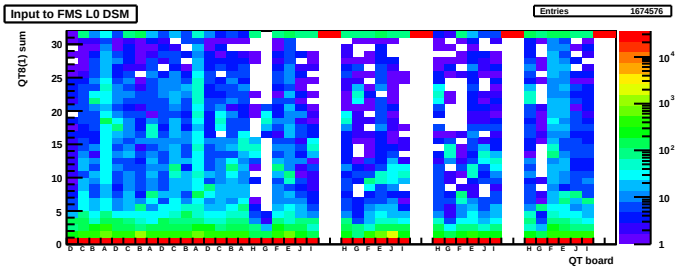

Input to QT2 crate
Entries 503574
ADC
channel

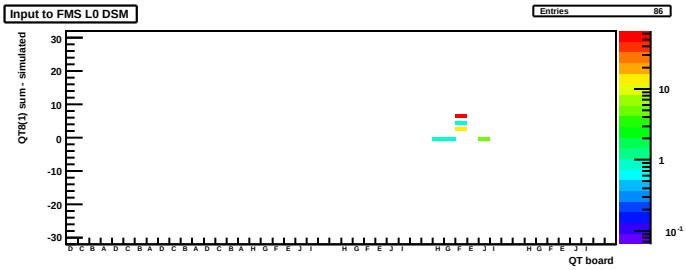

Input to QT3 crate
Entries 526016
ADC
channel

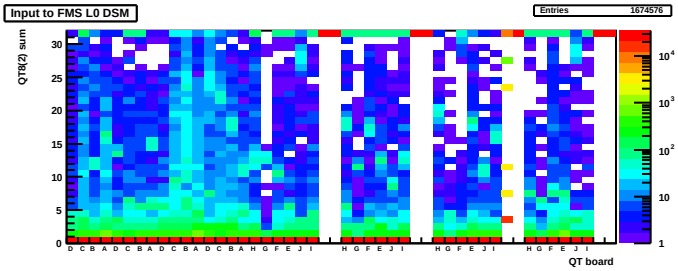

Input to QT4 crate
Entries 625195
ADC
channel

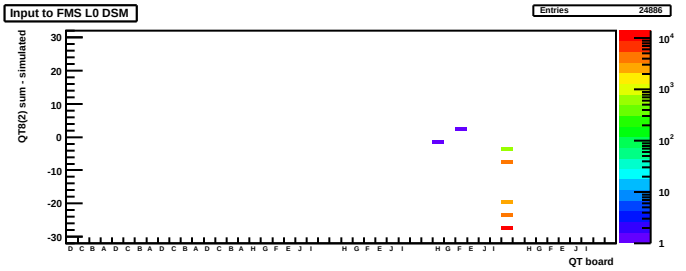

Input to FMS L0 DSM
Entries 1674576
QT8(0) sum
QT board

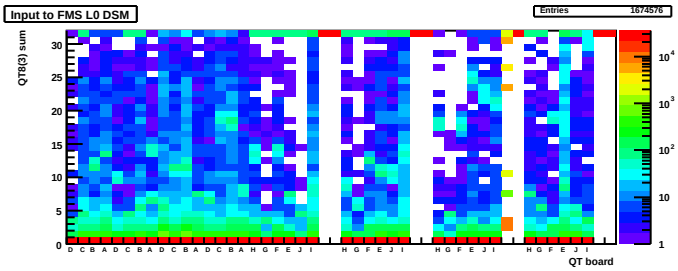

Input to FMS L0 DSM
Entries 33
QT8(0) sum - simulated
QT board

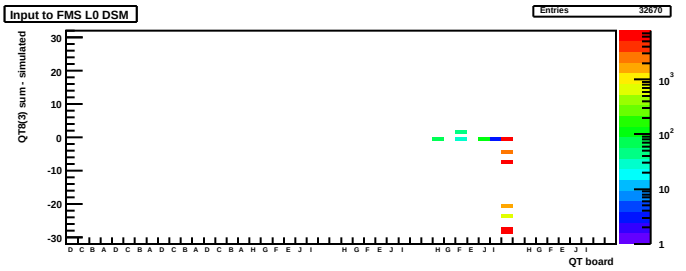

Input to FMS L0 DSM
Entries 1674576
QT8(1) sum
QT board

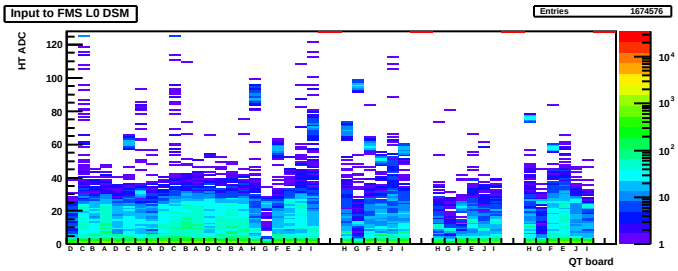

Input to FMS L0 DSM
Entries 86
QT8(1) sum - simulated
QT board

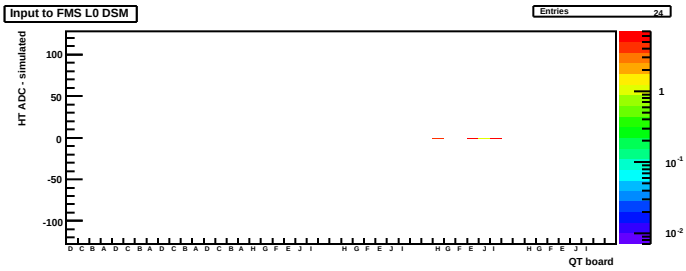

Input to FMS L0 DSM
Entries 1674576
QT8(2) sum
QT board

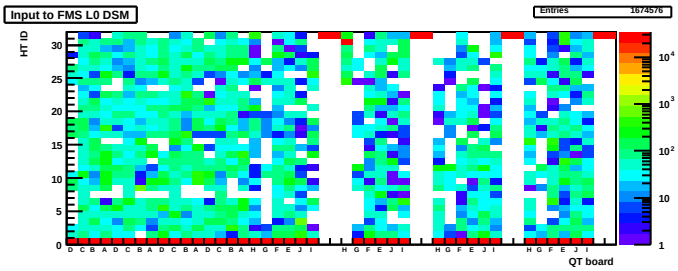

Input to FMS L0 DSM
Entries 24886
QT8(2) sum - simulated
QT board

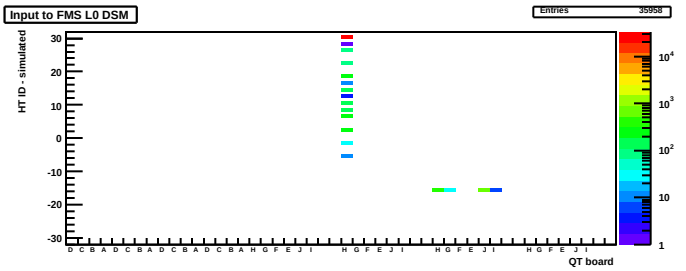

Input to FMS L0 DSM
Entries 1674576
QT8(3) sum
QT board

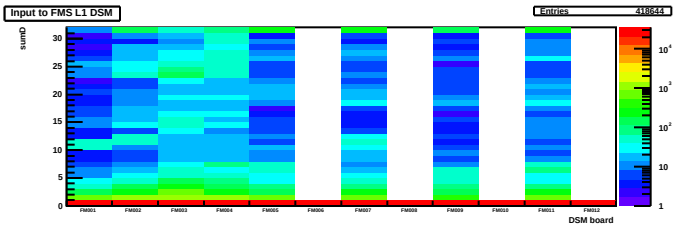

Input to FMS L0 DSM
Entries 32670
QT8(3) sum - simulated
QT board

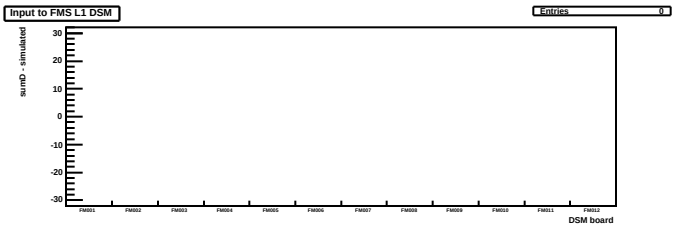

Input to FMS L0 DSM
Entries 1674576
HT ADC
QT board

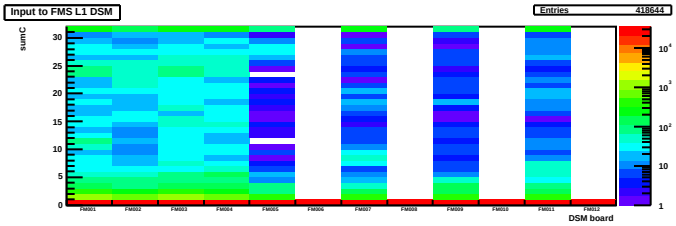

Input to FMS L0 DSM
Entries 24
HT ADC - simulated
QT board

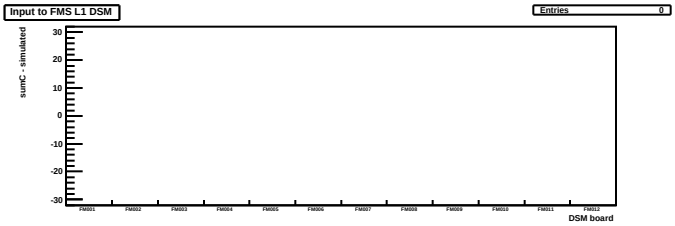

Input to FMS L0 DSM
Entries 1674576
HT ID
QT board

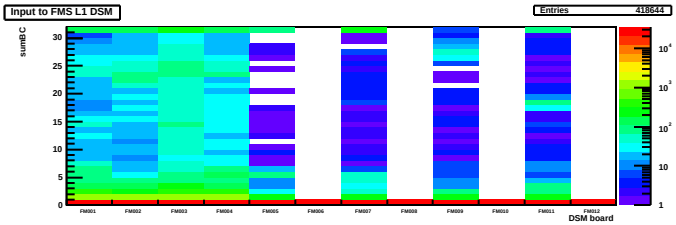

Input to FMS L0 DSM
Entries 35958
HT ID - simulated
QT board

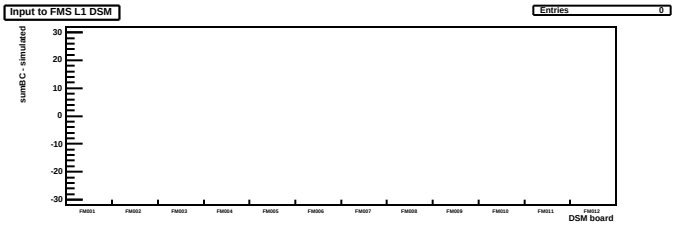

Input to FMS L1 DSM
Entries 418644
sumD
DSM board

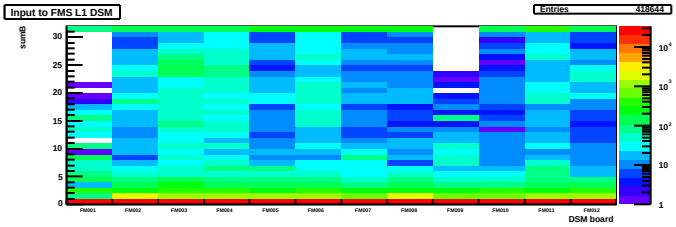

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

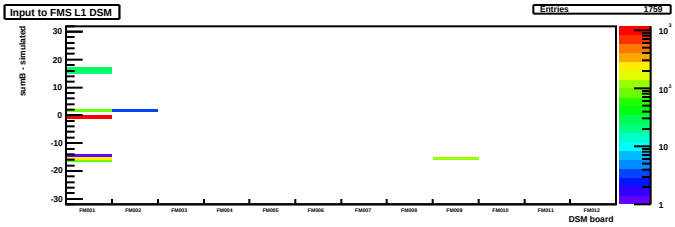

Input to FMS L1 DSM
Entries 418644
sumC
DSM board

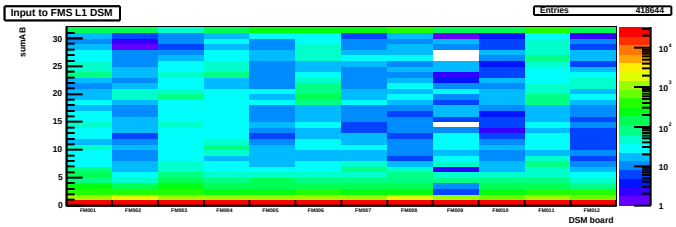

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

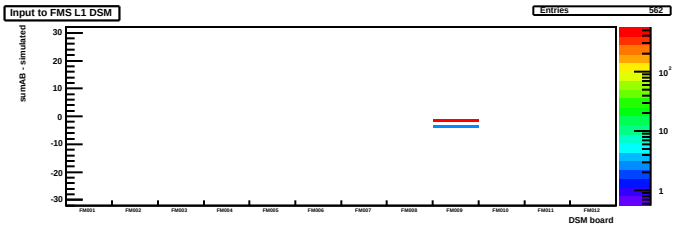

Input to FMS L1 DSM
Entries 418644
sumBC
DSM board

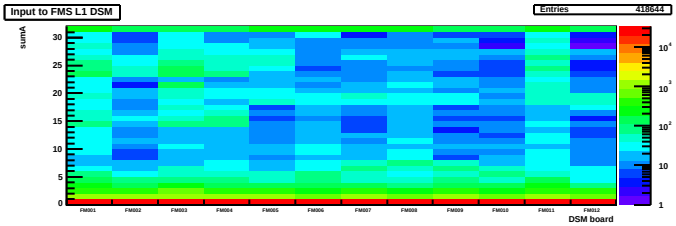

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

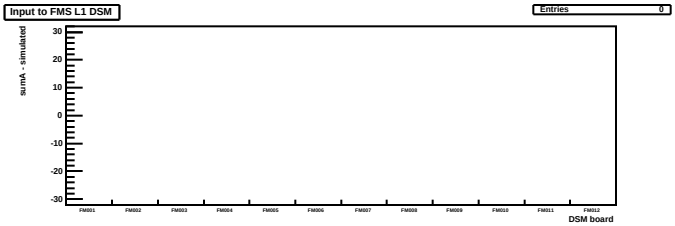

Input to FMS L1 DSM
Entries 418644
sumB
DSM board

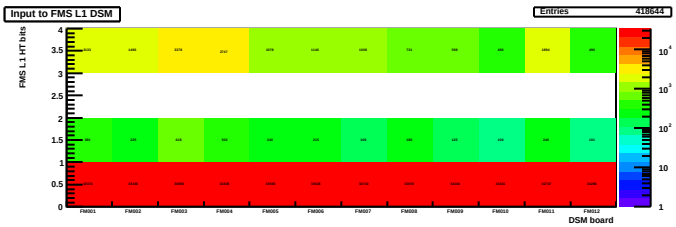

Input to FMS L1 DSM
Entries 1759
sumB - simulated
DSM board

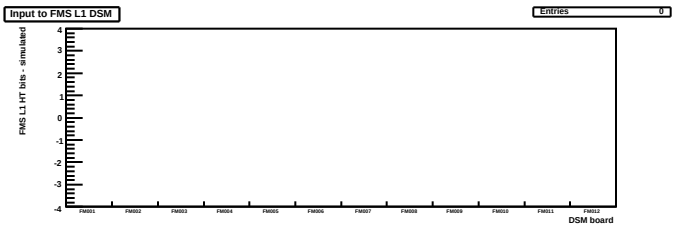

Input to FMS L1 DSM
Entries 418644
sumAB
DSM board

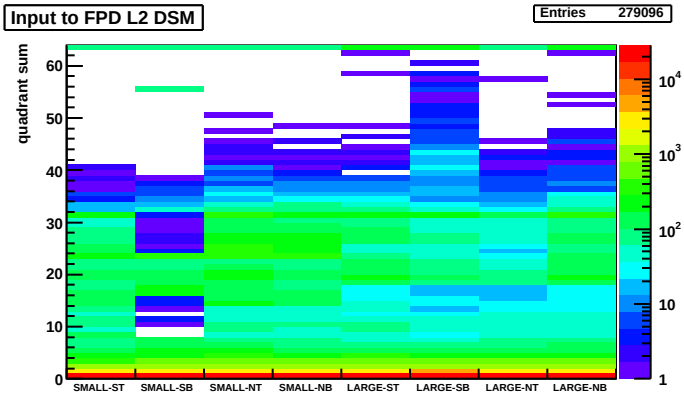

Input to FMS L1 DSM
Entries 562
sumAB - simulated
DSM board

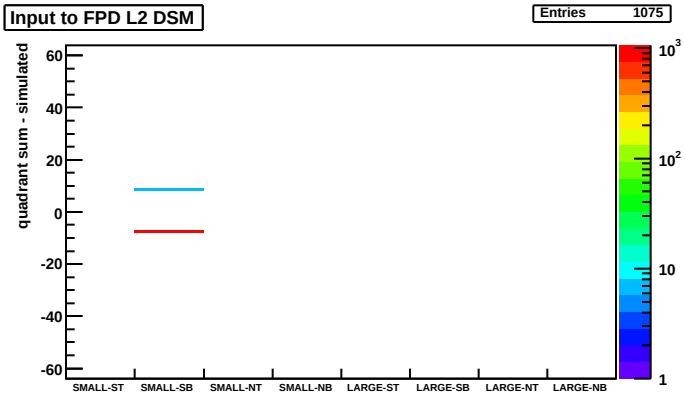

Input to FMS L1 DSM
Entries 418644
sumA
DSM board

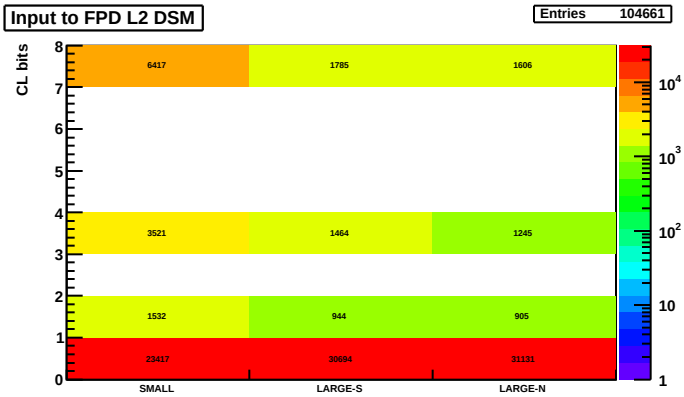

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

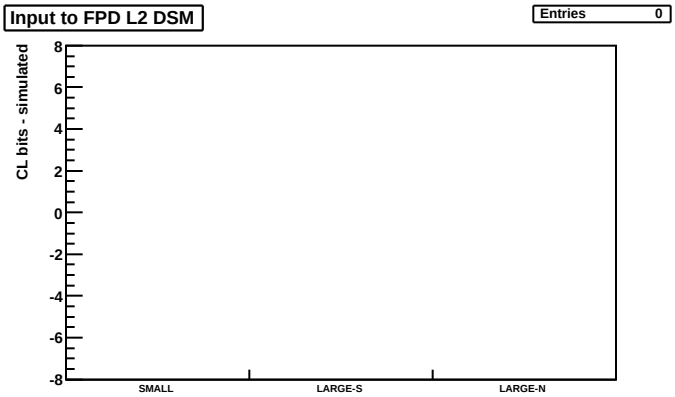

Input to FMS L1 DSM
Entries 418644
FMS L1 HT bits
DSM board

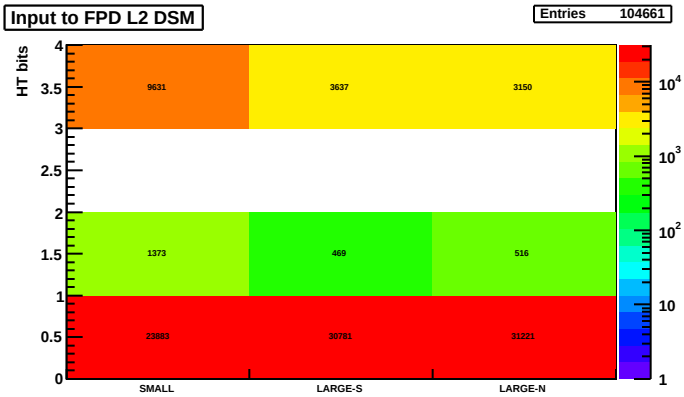

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

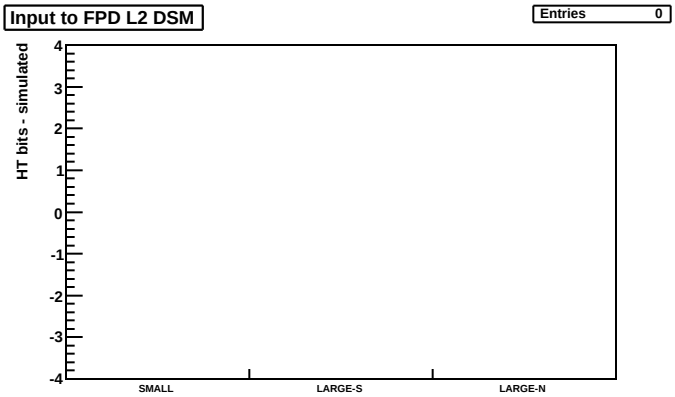

Input to FPD L2 DSM
Entries 279096
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

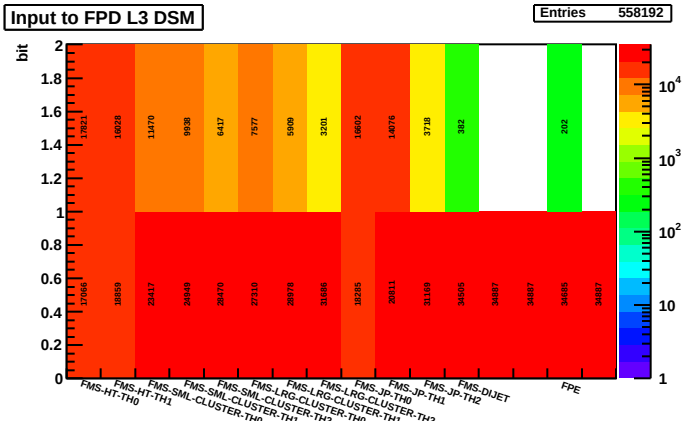

Input to FPD L2 DSM
Entries 1075
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

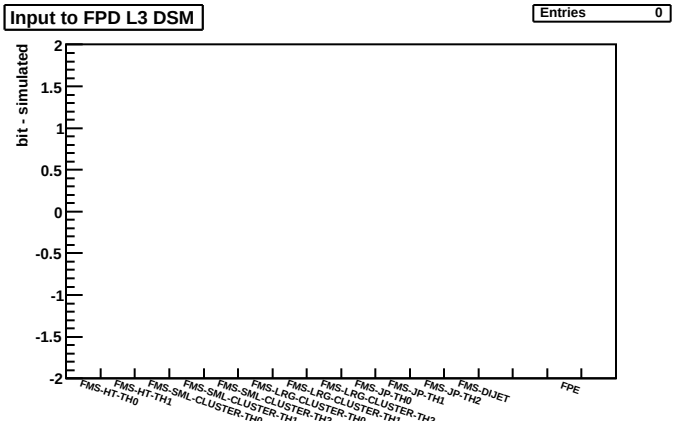

Input to FPD L2 DSM
Entries 104661
CL bits
6417
1785
1606
3521
1464
1245
1532
944
905
23417
30694
31131
SMALL
LARGE-S
LARGE-N

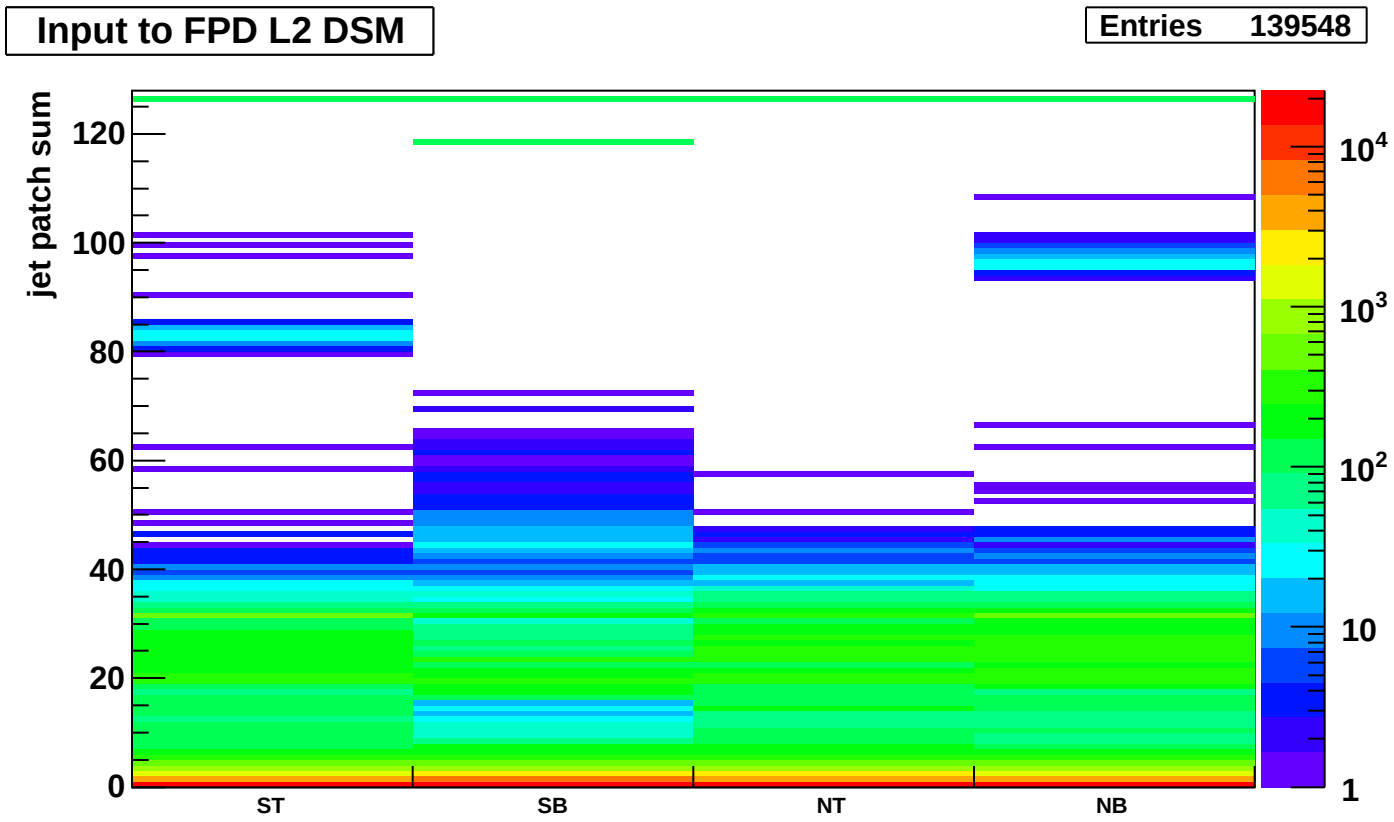

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

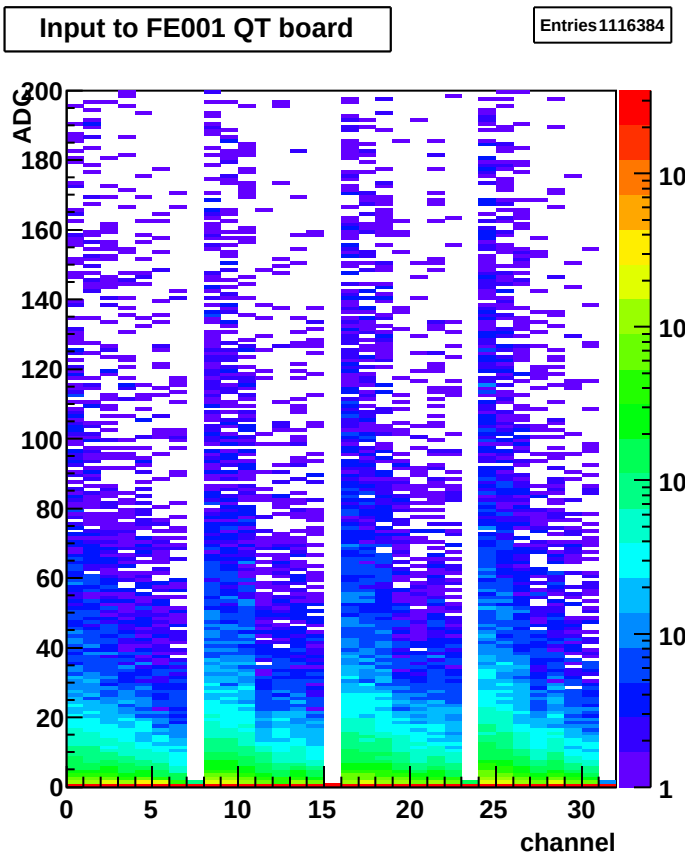

Input to FPD L2 DSM
Entries 104661
HT bits
9631
3637
3150
1373
469
516
23883
30781
31221
SMALL
LARGE-S
LARGE-N

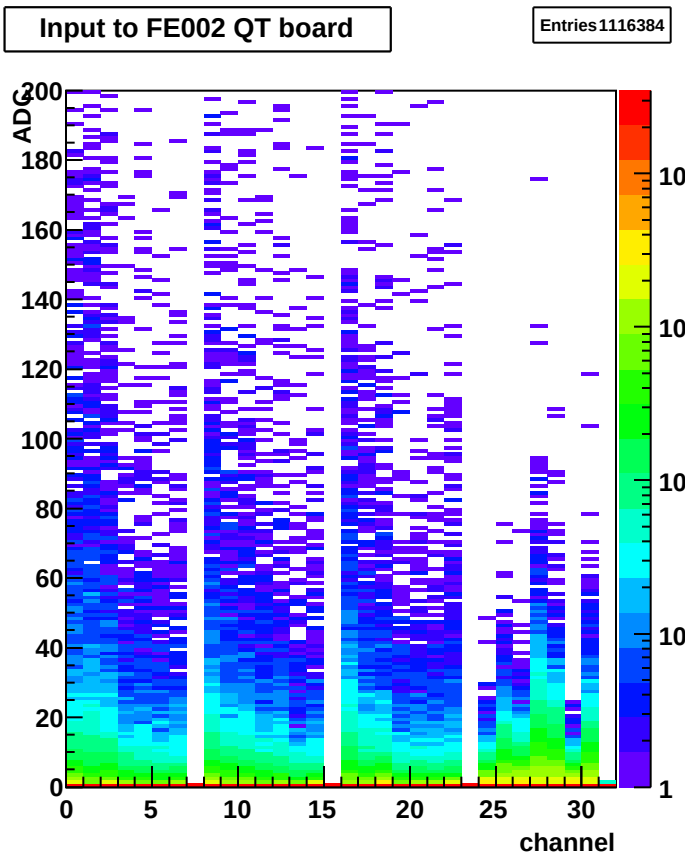

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

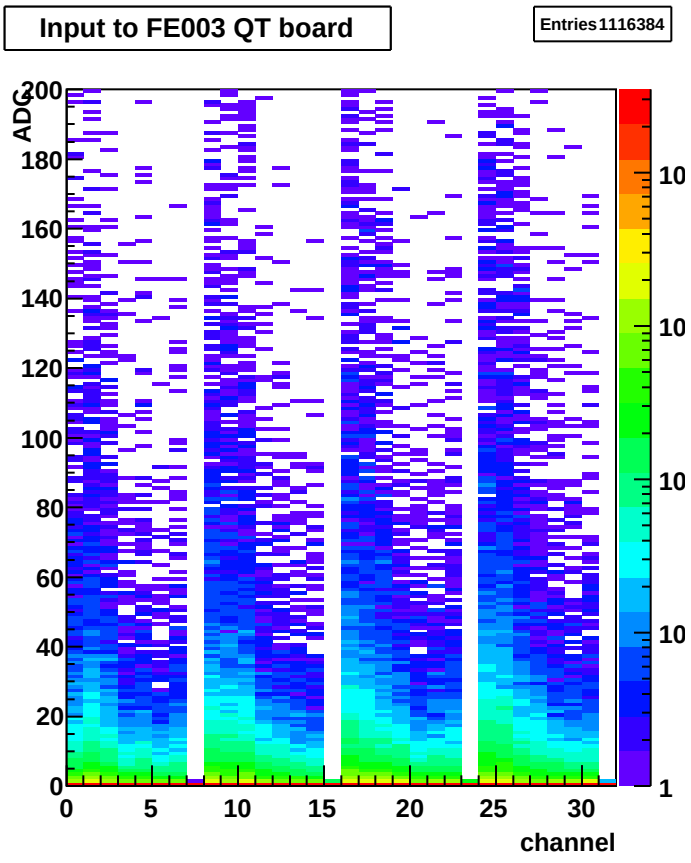

Input to FPD L3 DSM
Entries 558192
bit
17821
16028
11470
9938
6417
7577
5909
3201
16602
14076
3718
382
202
17065
18859
22417
24949
29470
27310
29978
31696
18285
20811
31169
34505
34887
34887
34695
34887
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

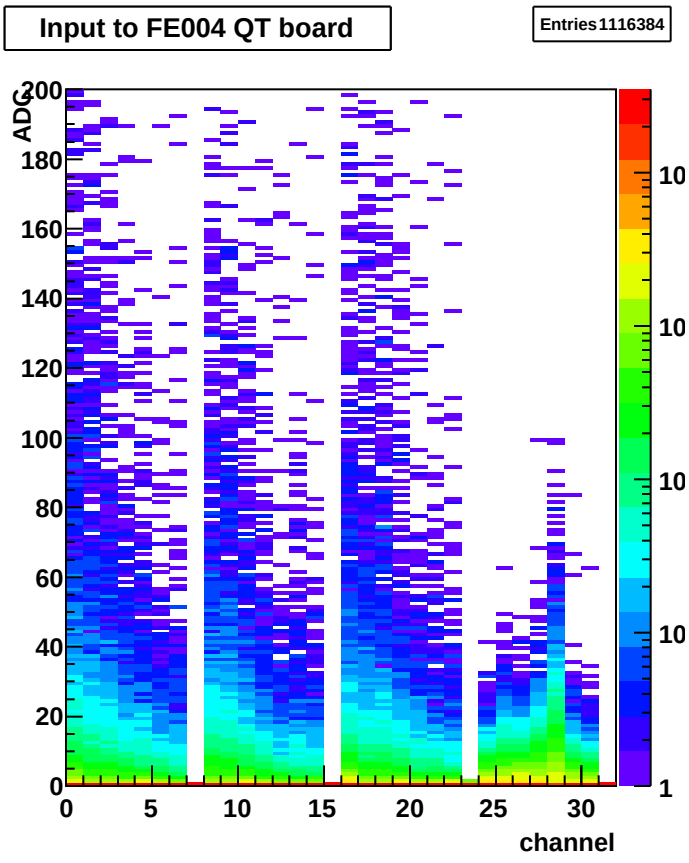

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

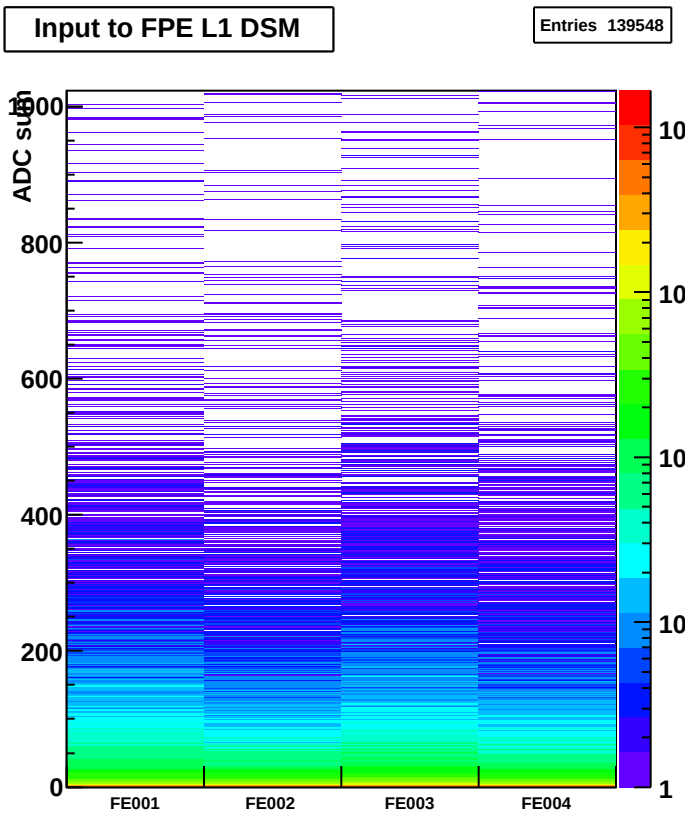

Input to FPD L2 DSM
Entries 139548
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

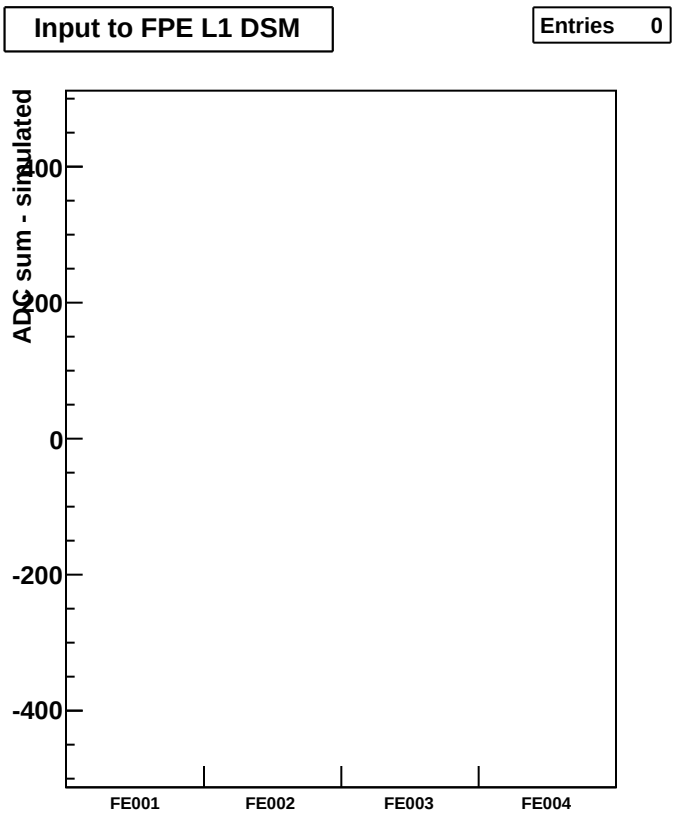

Input to FE001 QT board
Entries 1116384
ADC
channel

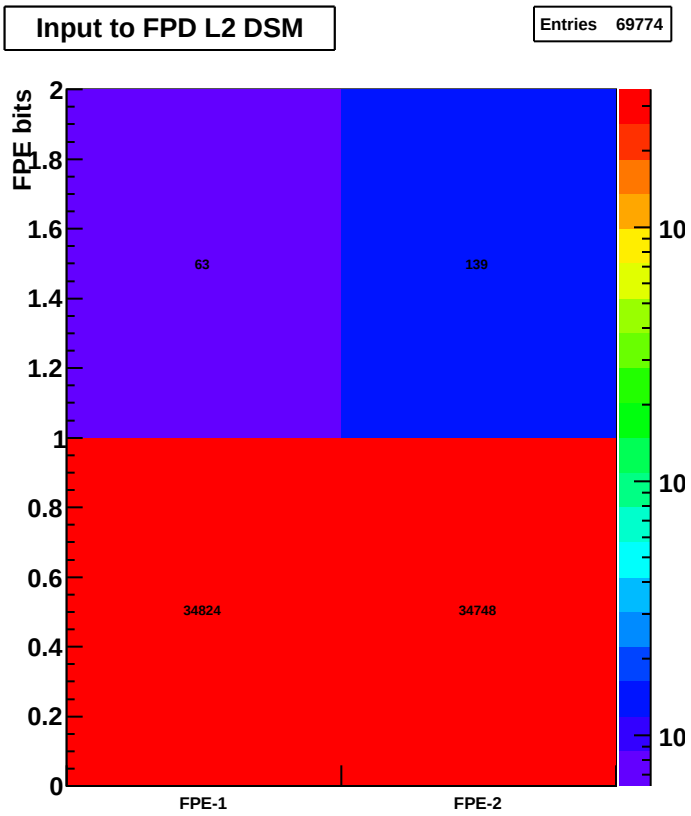

Input to FE002 QT board
Entries 1116384
ADC
channel

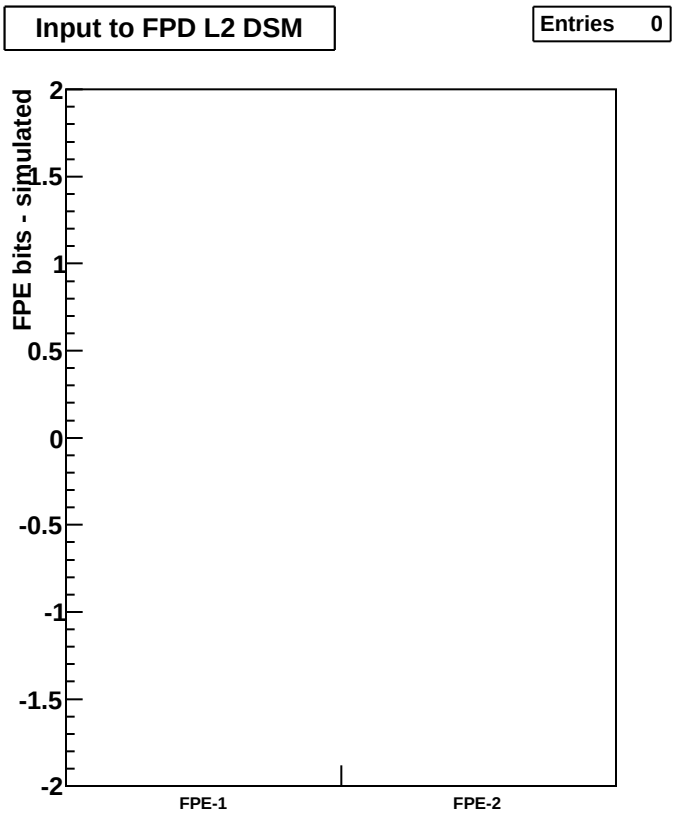

Input to FE003 QT board
Entries 1116384
ADC
channel


Input to FE004 QT board
Entries 1116384
ADC
channel


Input to FPE L1 DSM
Entries 139548
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 69774
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
63
139
34824
34748
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2