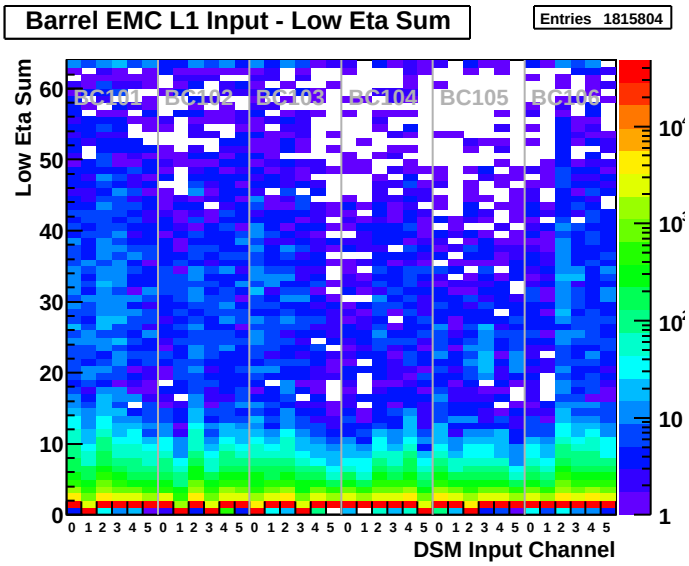
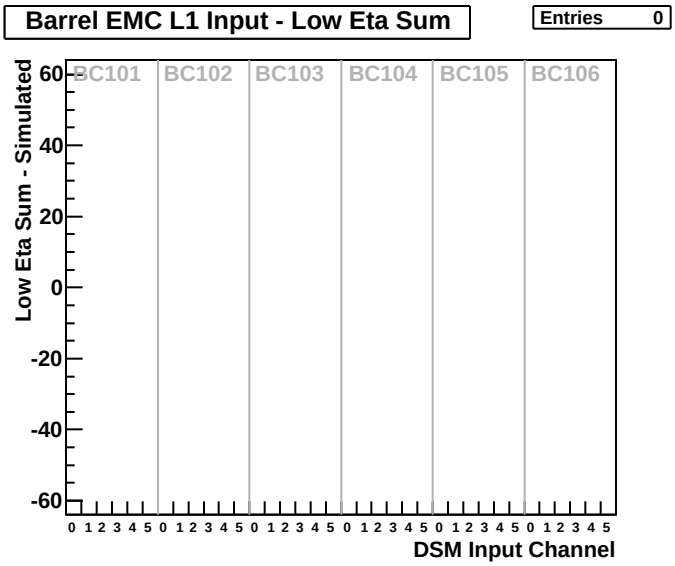
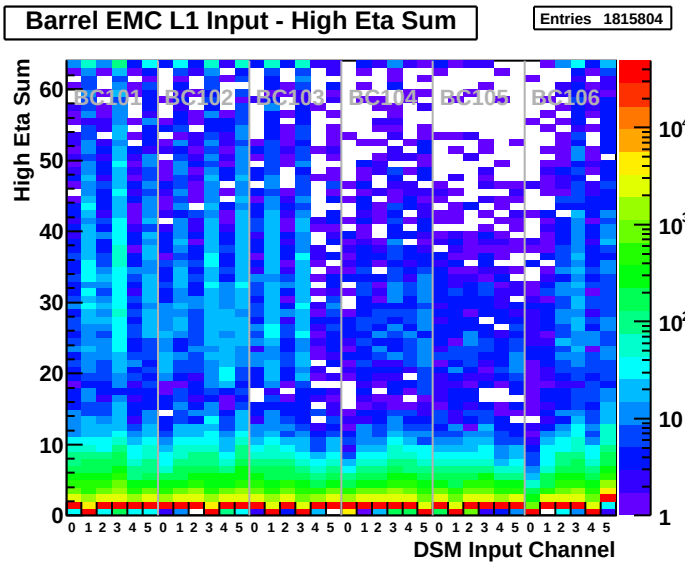
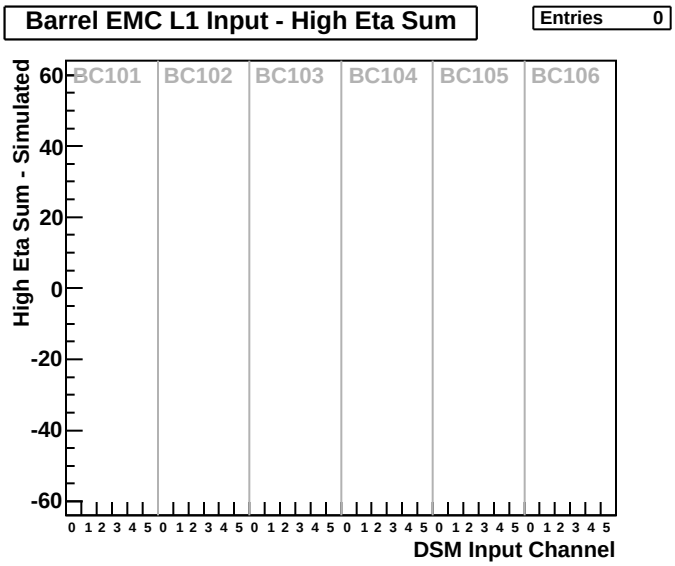
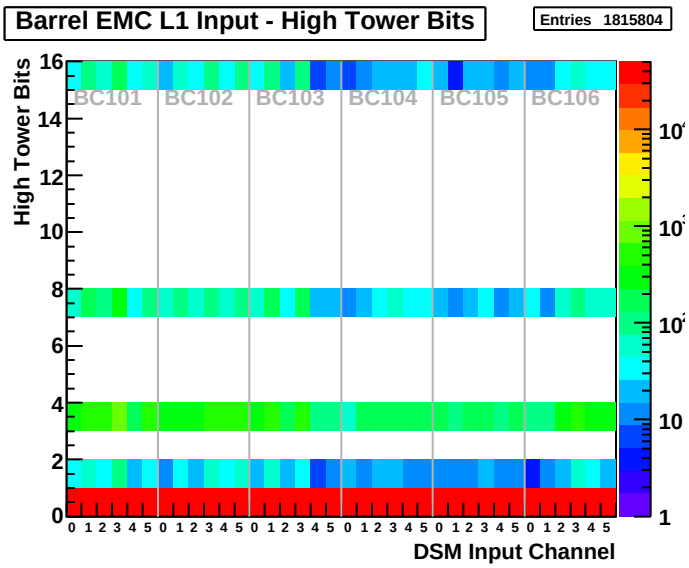
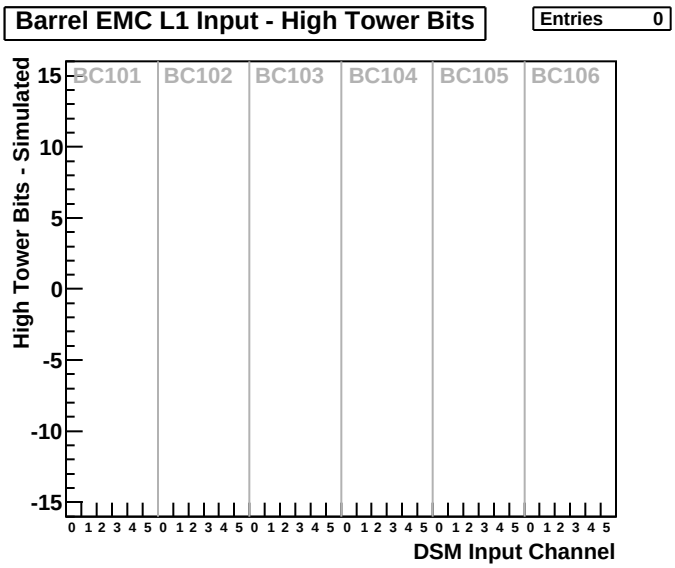

Barrel EMC L1 Input - Low Eta Sum
Entries 1815804
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

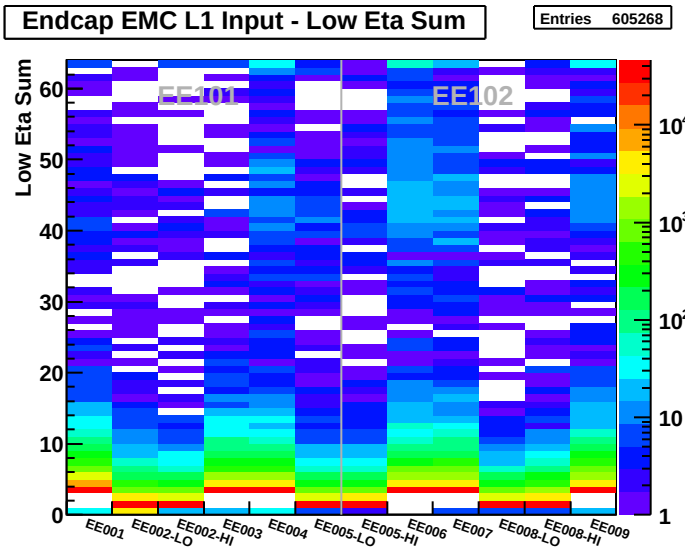

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

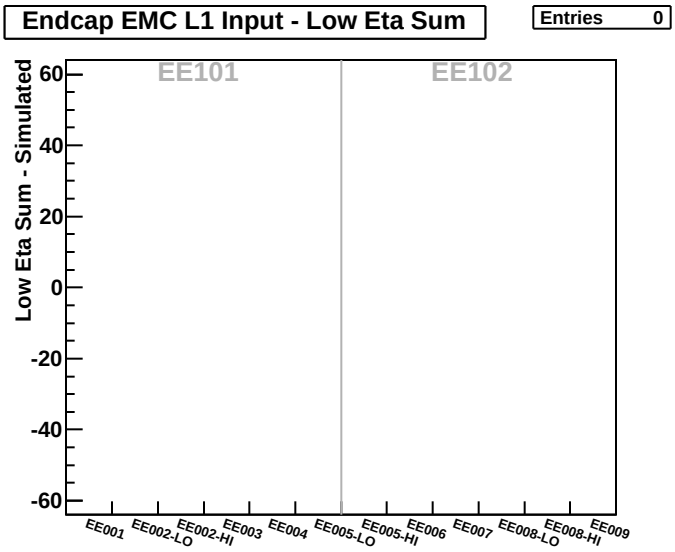

Barrel EMC L1 Input - High Eta Sum
Entries 1815804
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

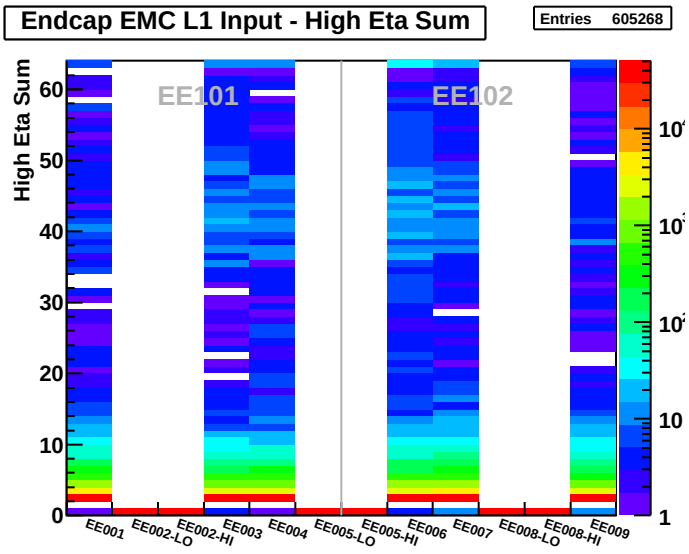

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

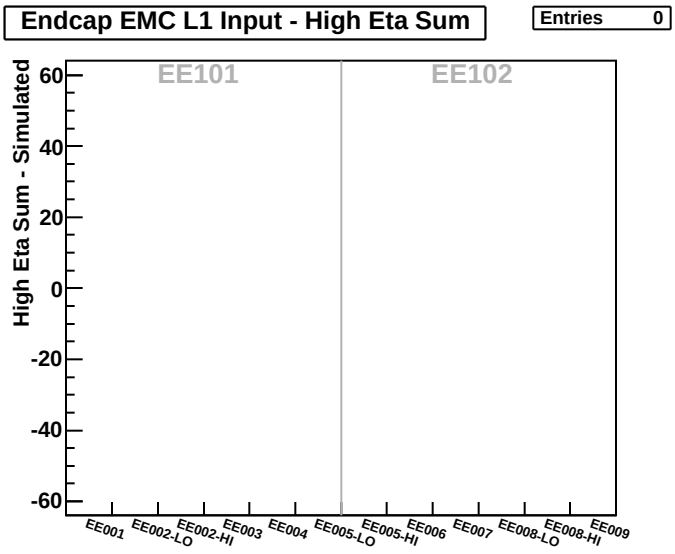

Barrel EMC L1 Input - High Tower Bits
Entries 1815804
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

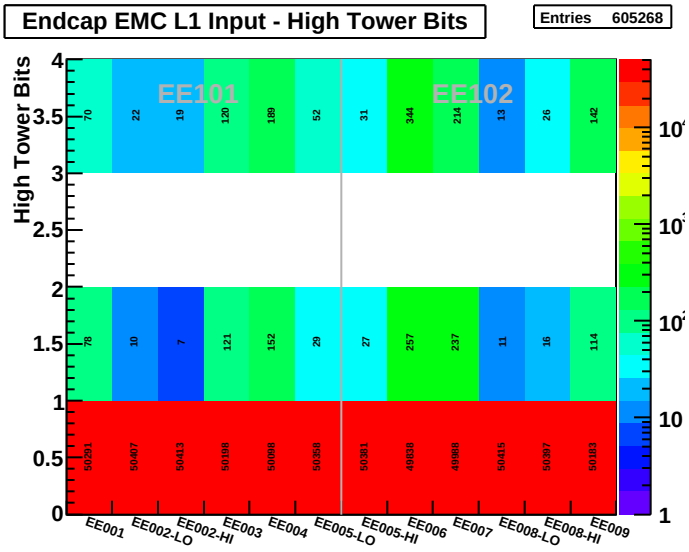

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

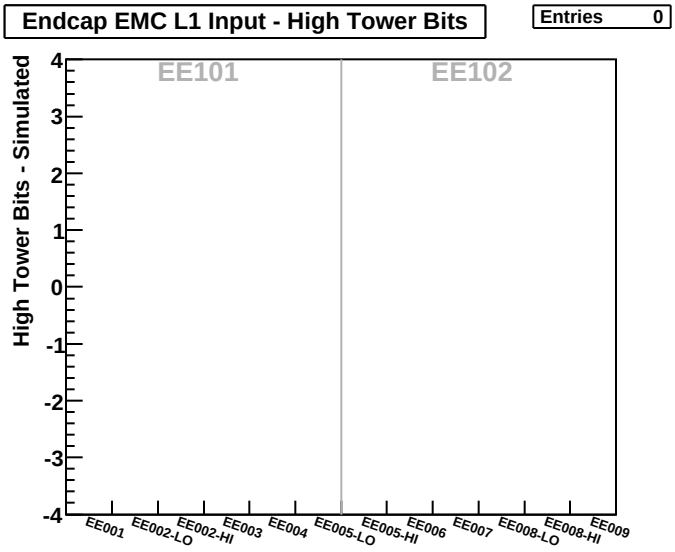

Endcap EMC L1 Input - Low Eta Sum
Entries 605268
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

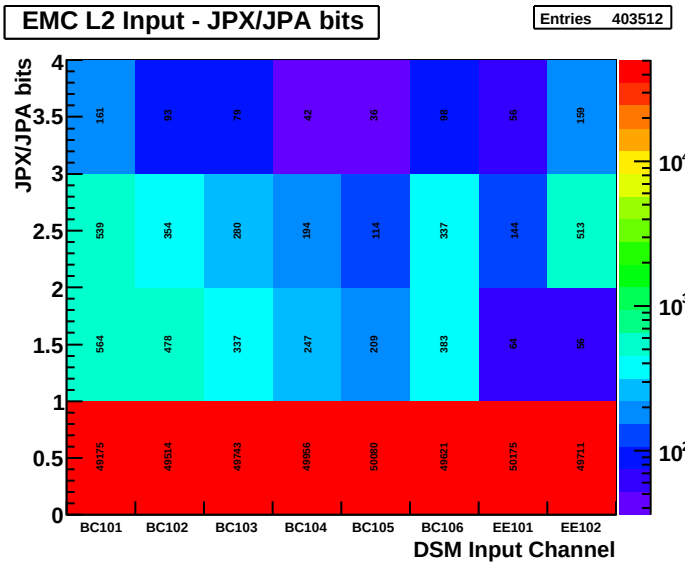

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

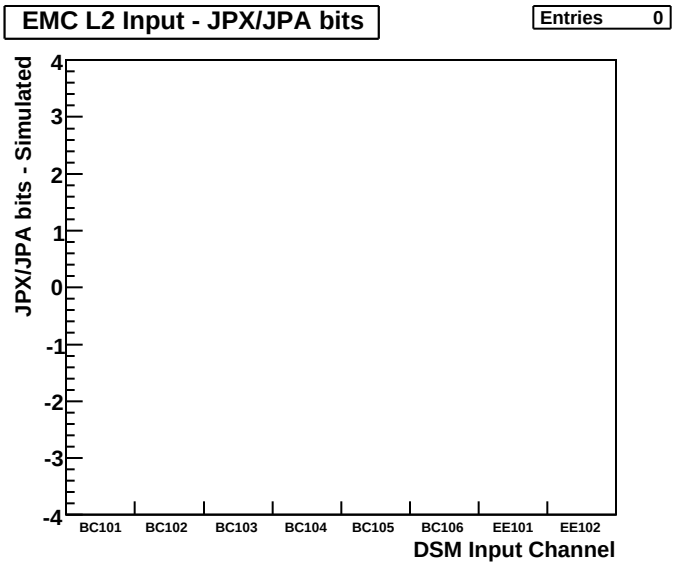

Endcap EMC L1 Input - High Eta Sum
Entries 605268
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

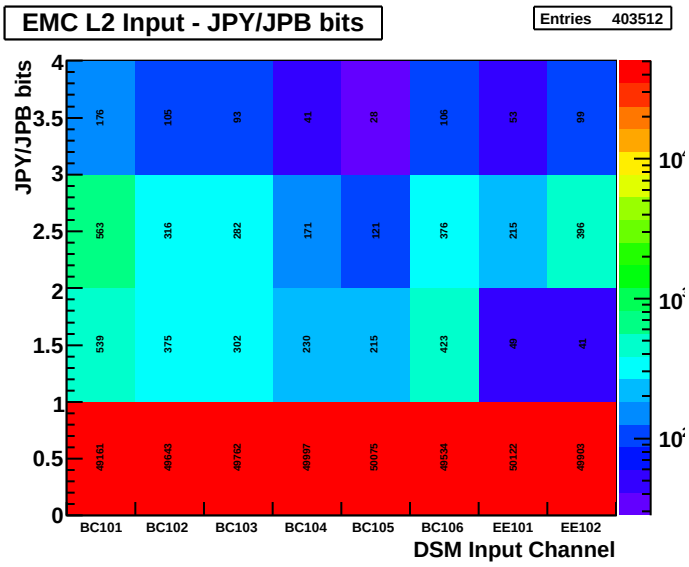

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

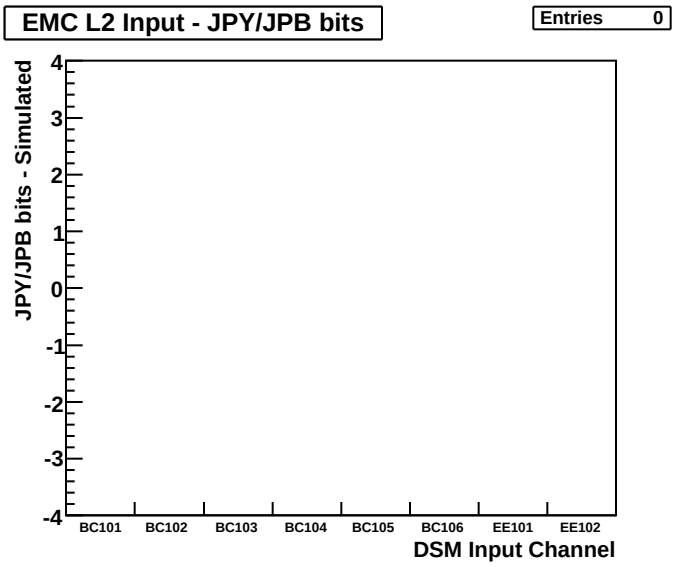

Endcap EMC L1 Input - High Tower Bits
Entries 605268
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

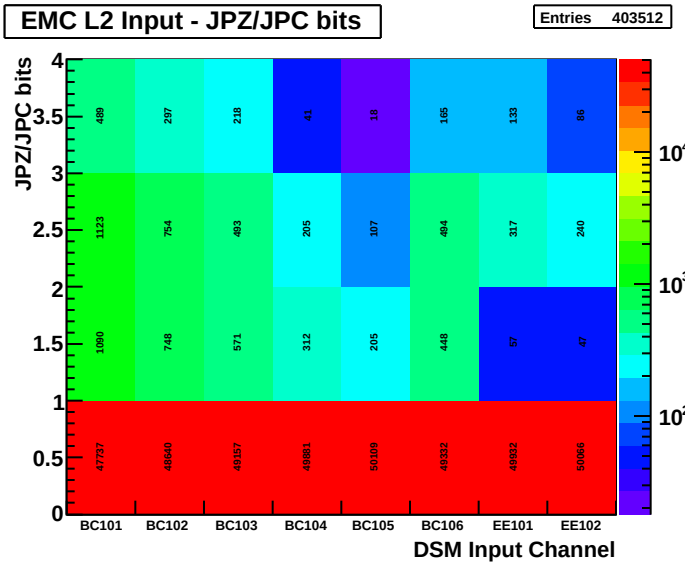

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

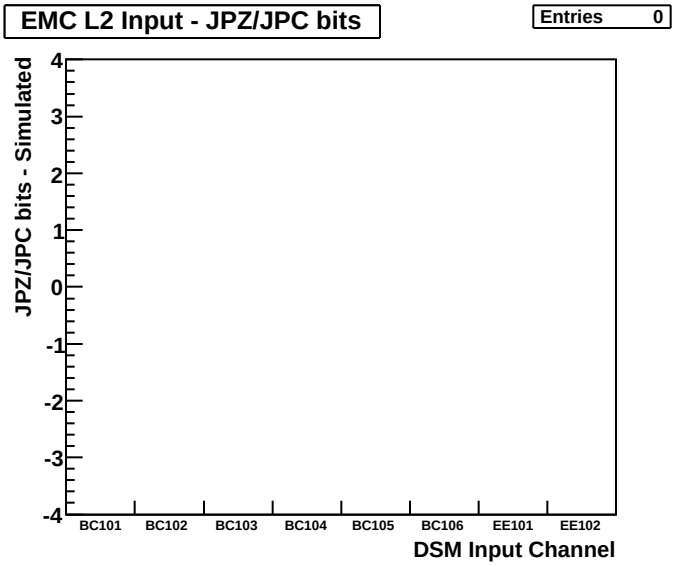

EMC L2 Input - JPX/JPA bits
Entries 403512
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
161 93 79 42 36 98 56 159
539 354 280 194 114 337 144 513
564 478 337 247 209 383 64 56
49175 49514 49743 49956 50080 49921 50175 49711

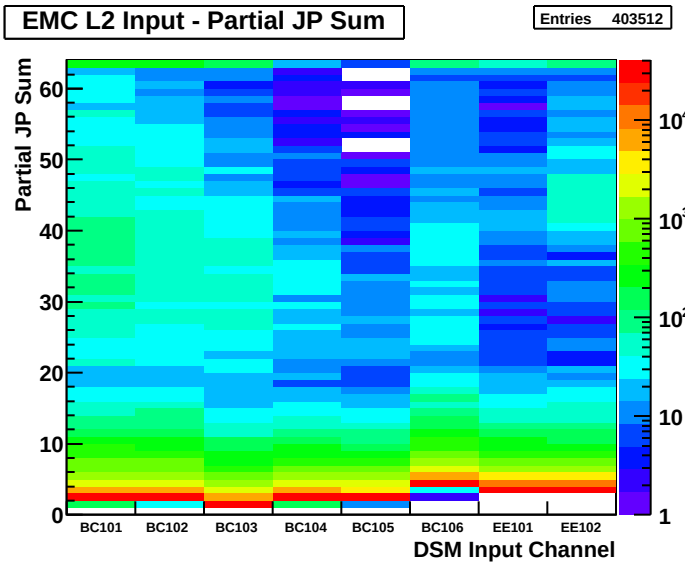

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

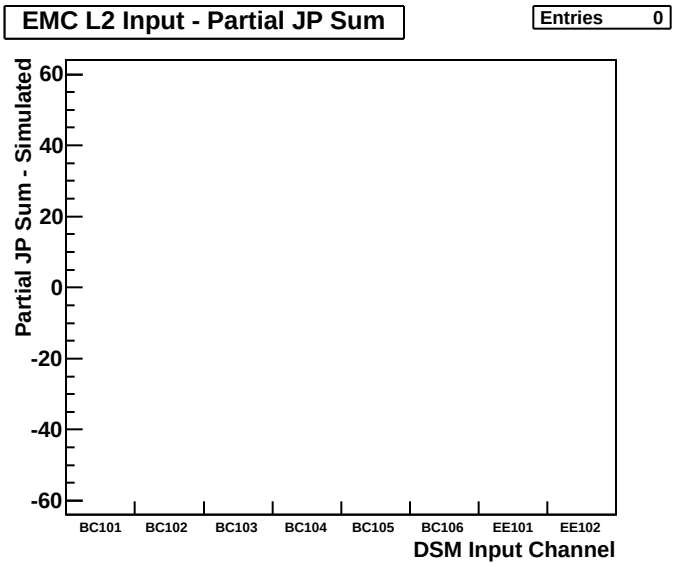

EMC L2 Input - JPY/JPB bits
Entries 403512
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
176 105 93 41 28 106 53 99
563 316 282 171 121 376 215 396
539 375 302 230 215 423 49 41
49161 49543 49762 49997 50075 49534 50122 49903

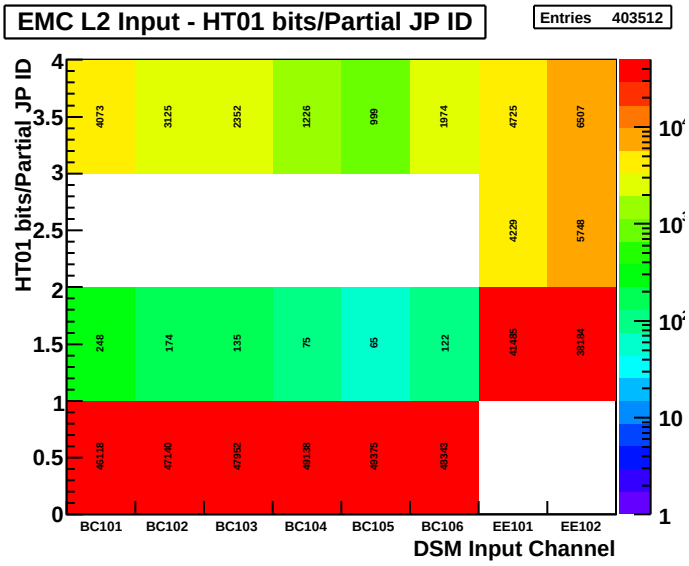

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

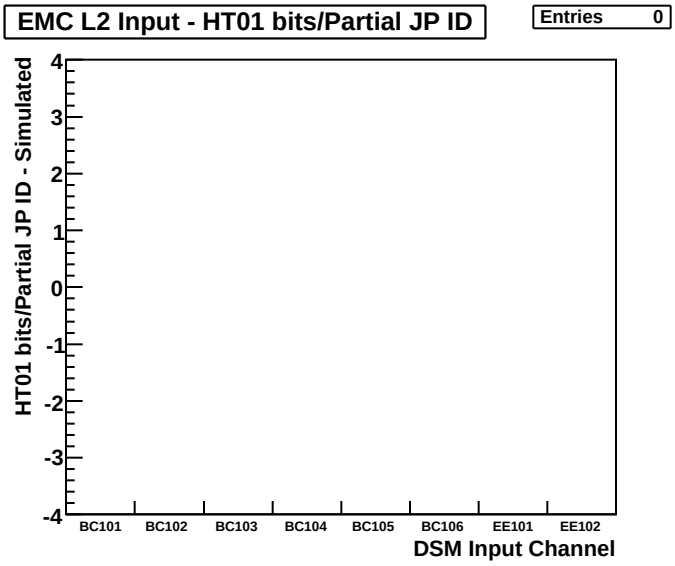

EMC L2 Input - JPZ/JPC bits
Entries 403512
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
489 297 218 41 18 165 133 86
1123 754 493 205 107 494 317 240
1060 748 571 312 205 448 57 47
47737 48640 49157 49881 50109 49332 49932 50066

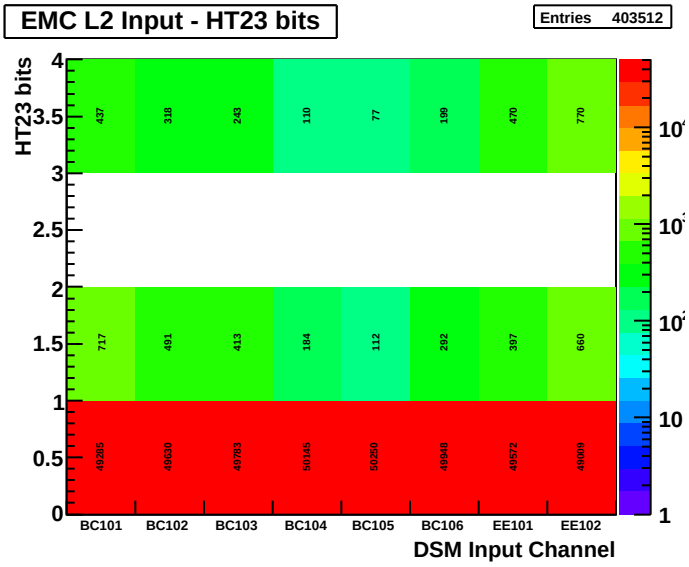

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

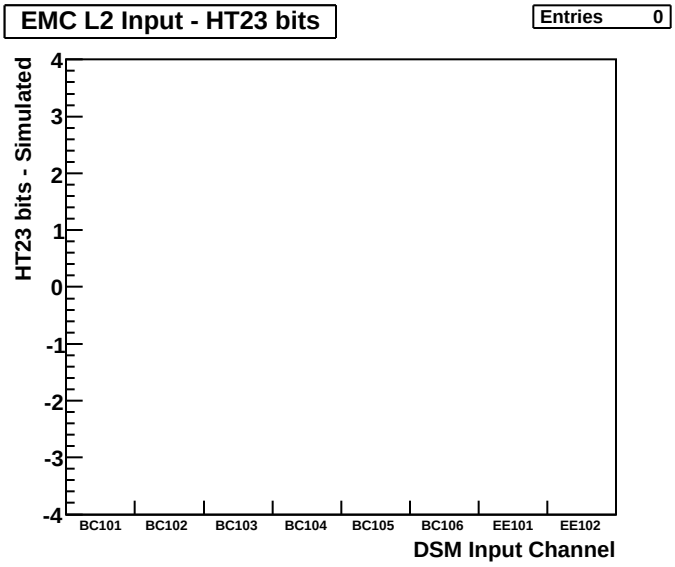

EMC L2 Input - Partial JP Sum
Entries 403512
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

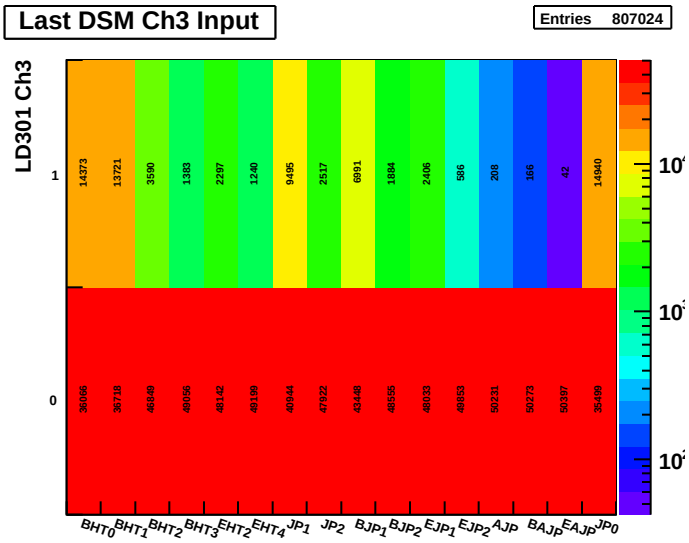

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

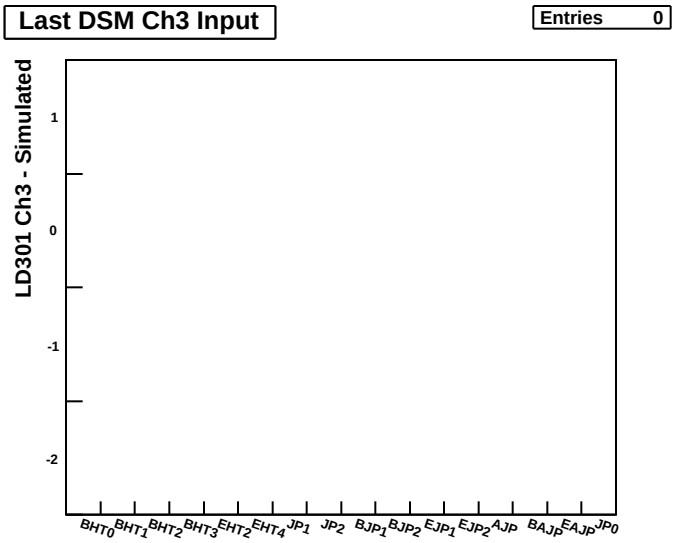

EMC L2 Input - HT01 bits/Partial JP ID
Entries 403512
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

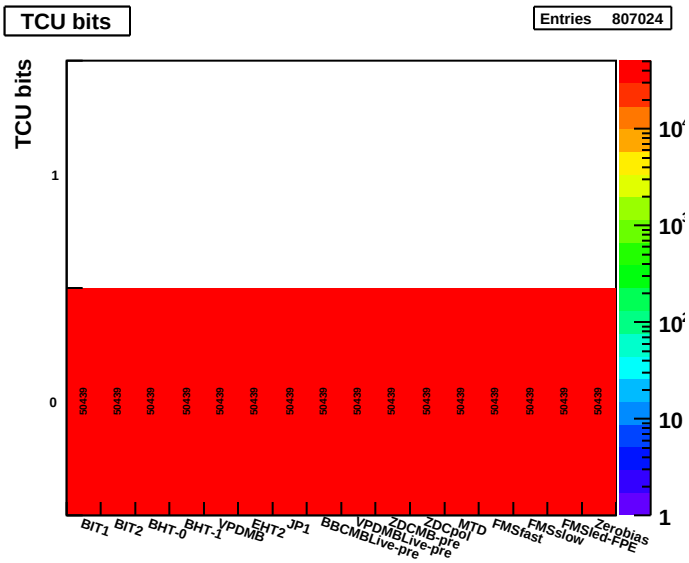

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

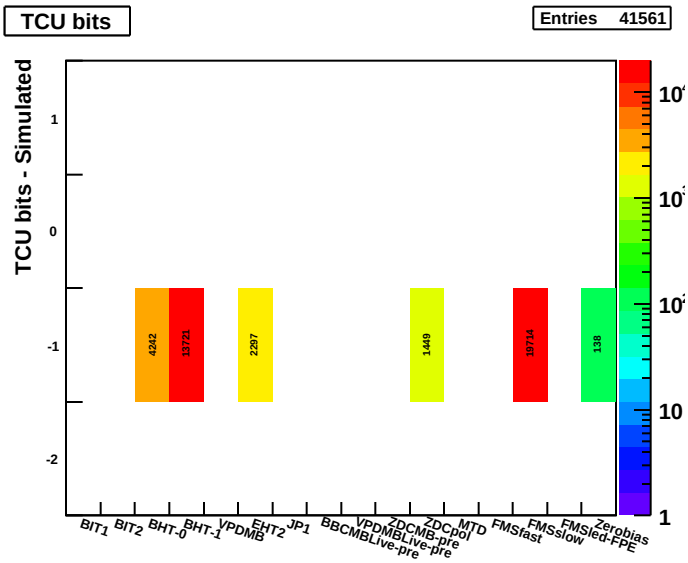

EMC L2 Input - HT23 bits
Entries 403512
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

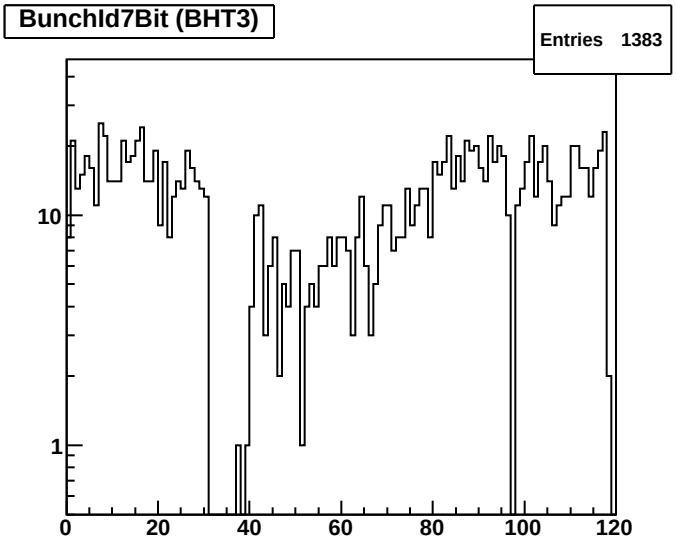

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

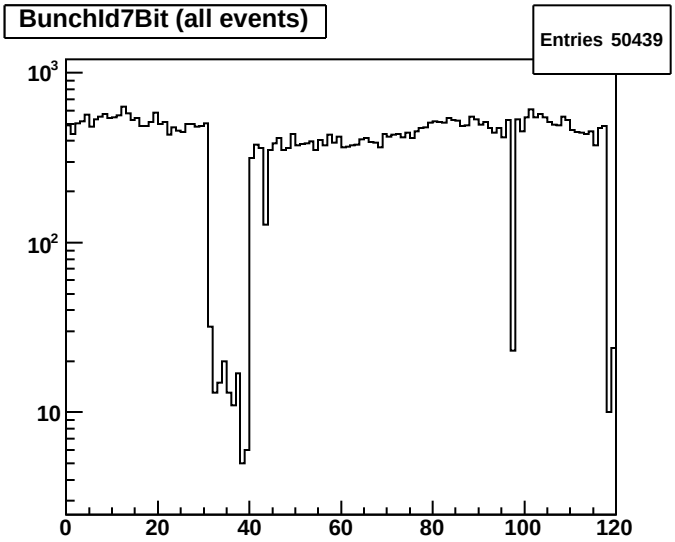

Last DSM Ch3 Input
Entries 807024
LD301 Ch3
14373 13721 3590 1383 2297 1240 9495 2517 6991 1884 2406 586 208 166 42 14940
36066 36718 46849 49056 49142 49199 49944 47922 43448 48555 48033 49853 50231 50273 50397 35499
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

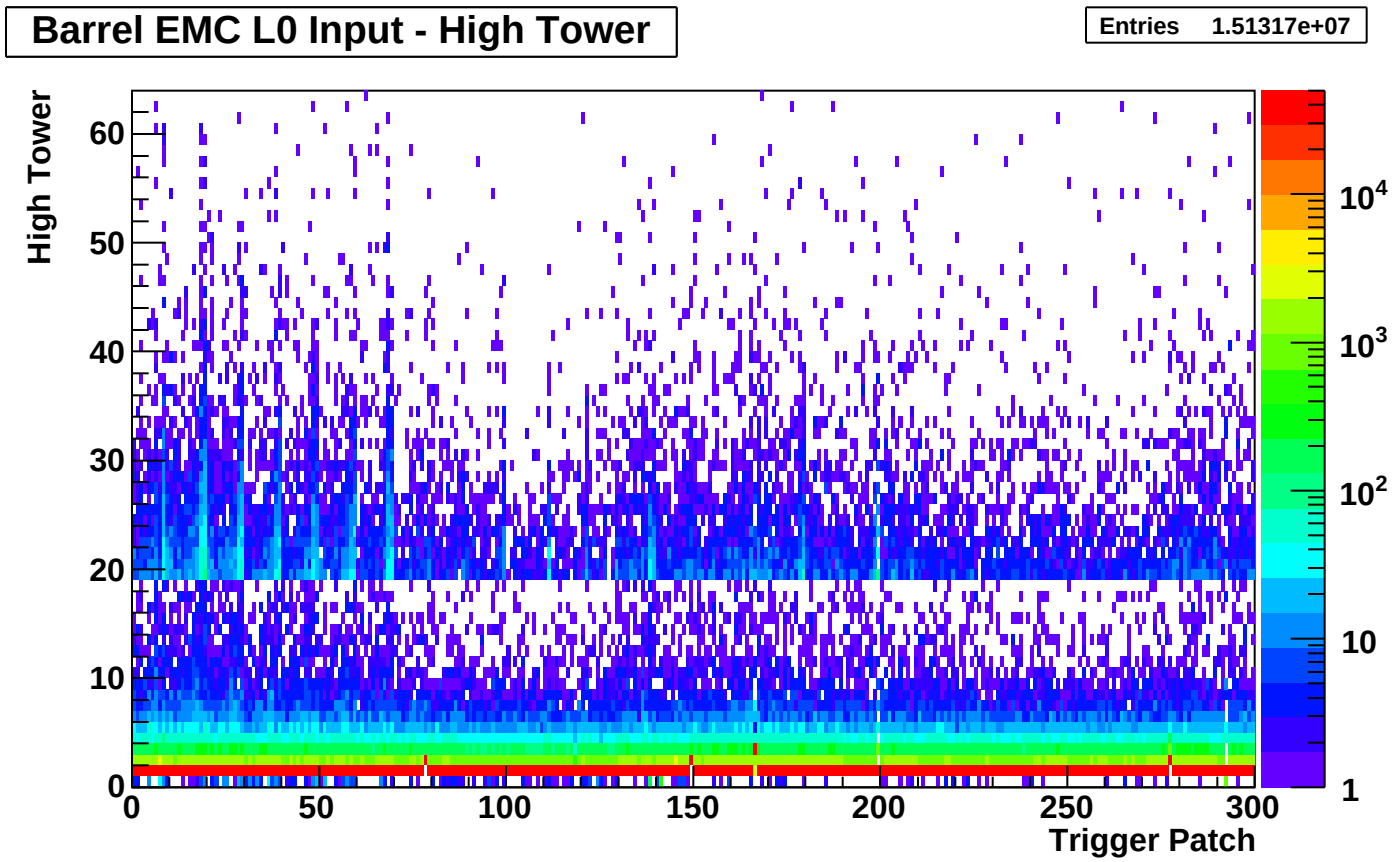

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

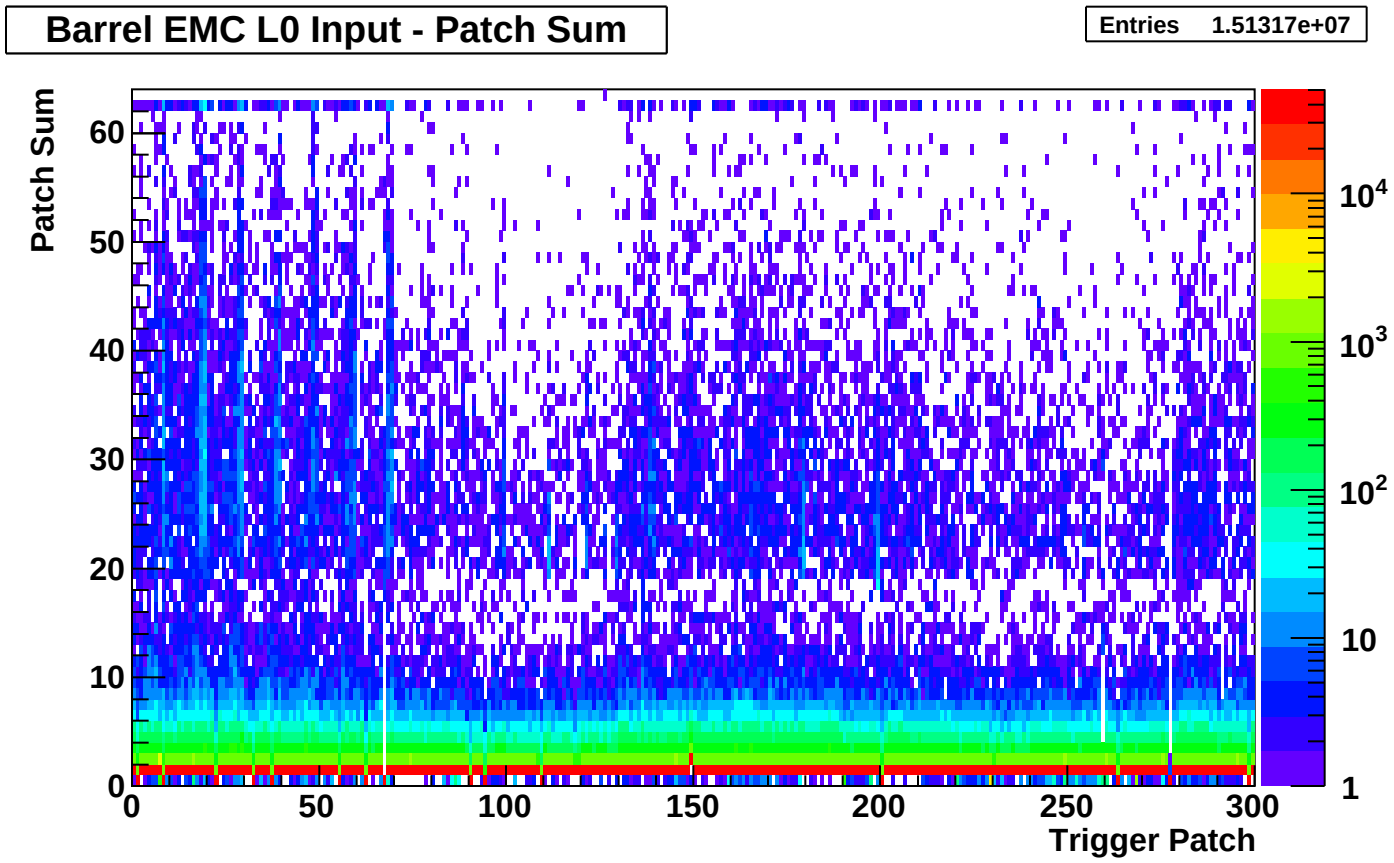

TCU bits
Entries 807024
TCU bits
50439 50439 50439 50439 50439 50439 50439 50439 50439 50439 50439 50439 50439 50439 50439 50439
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

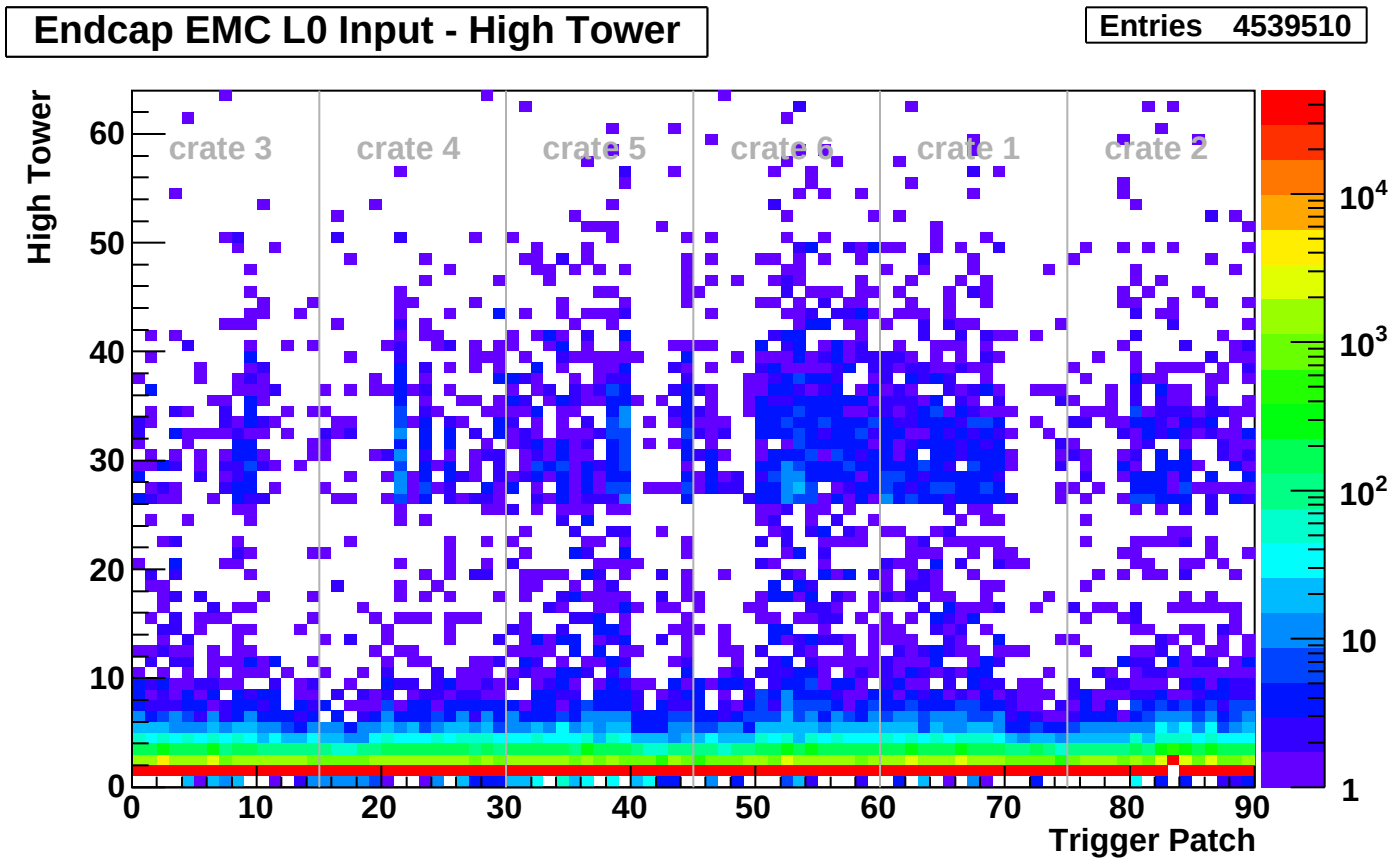

TCU bits
Entries 41561
TCU bits - Simulated
4242 13721 2297 1449 19714 138
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

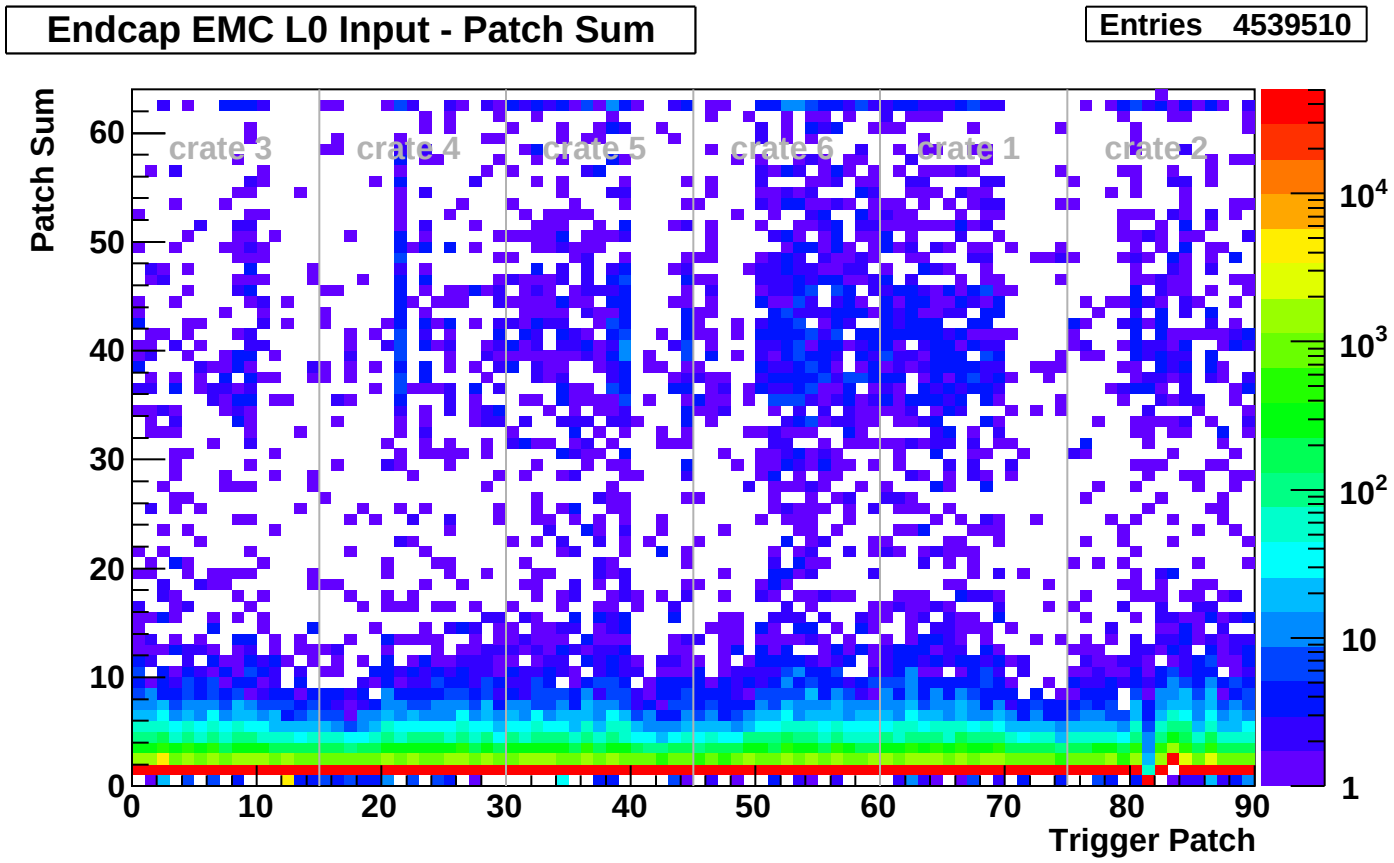

BunchId7Bit (BHT3)
Entries 1383

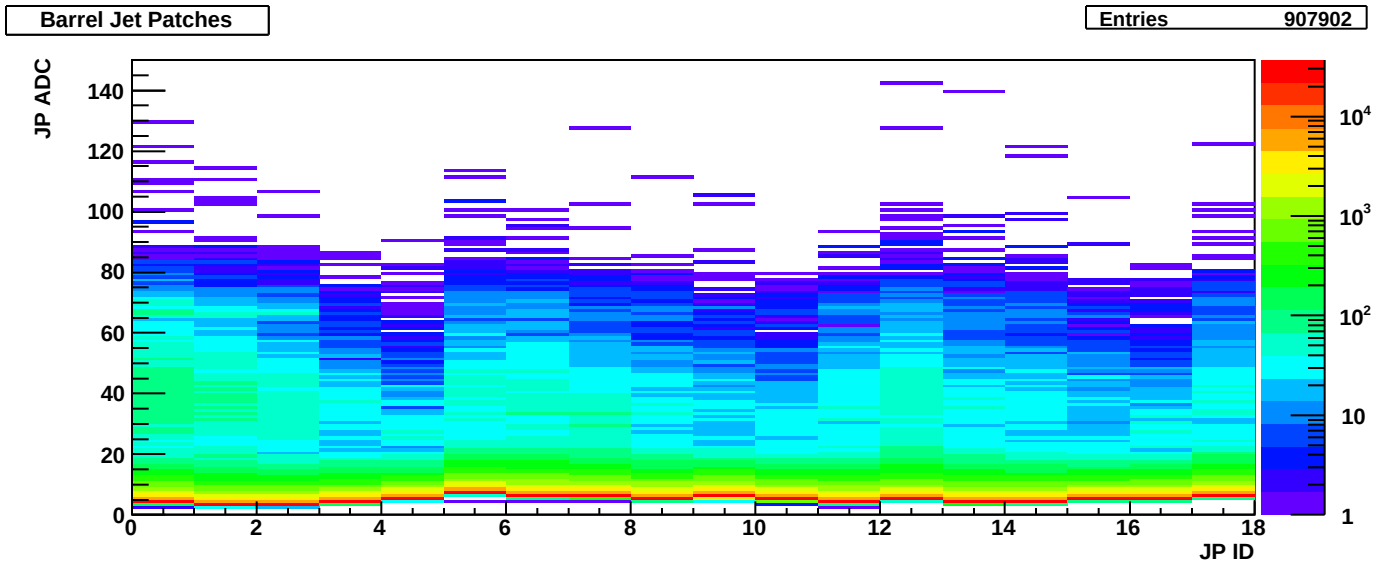

BunchId7Bit (all events)
Entries 50439

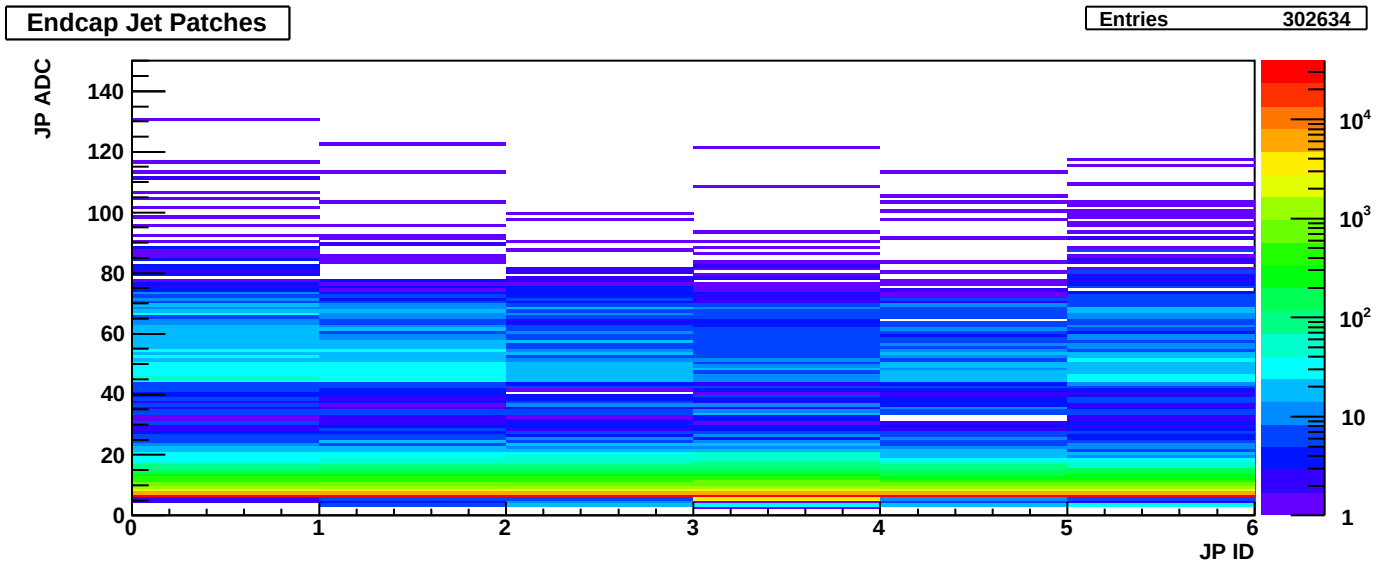

Barrel EMC L0 Input - High Tower
Entries 1.51317e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

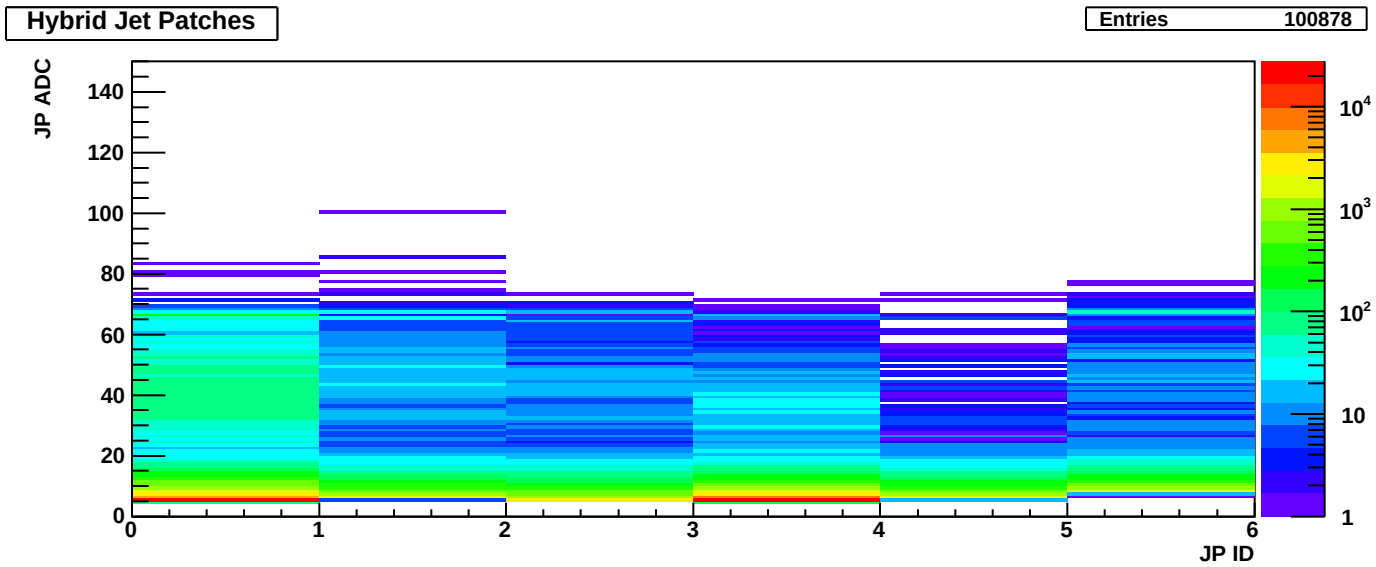

Barrel EMC L0 Input - Patch Sum
Entries 1.51317e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

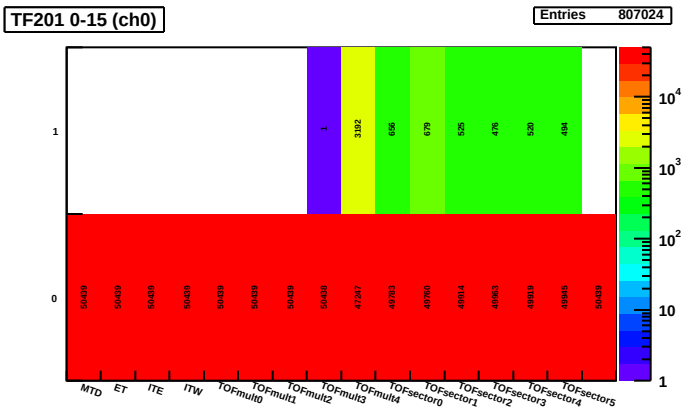

Endcap EMC L0 Input - High Tower
Entries 4539510
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

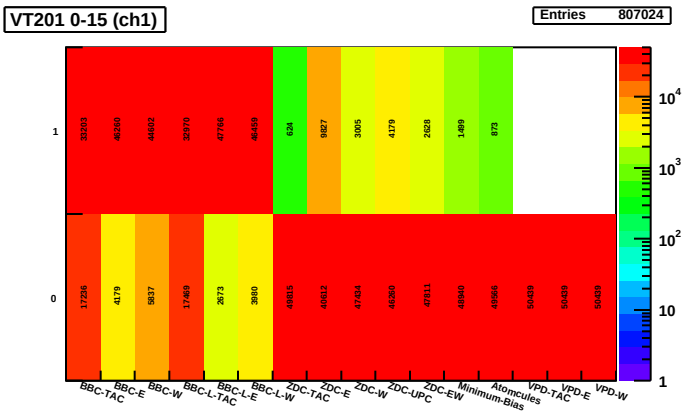

Endcap EMC L0 Input - Patch Sum
Entries 4539510
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

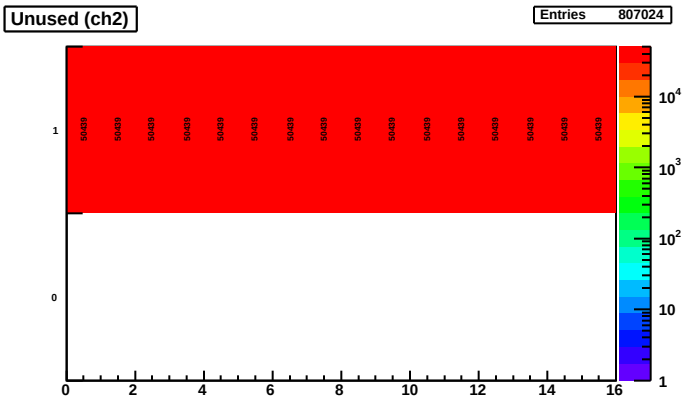

Barrel Jet Patches
Entries 907902
JP ADC
JP ID

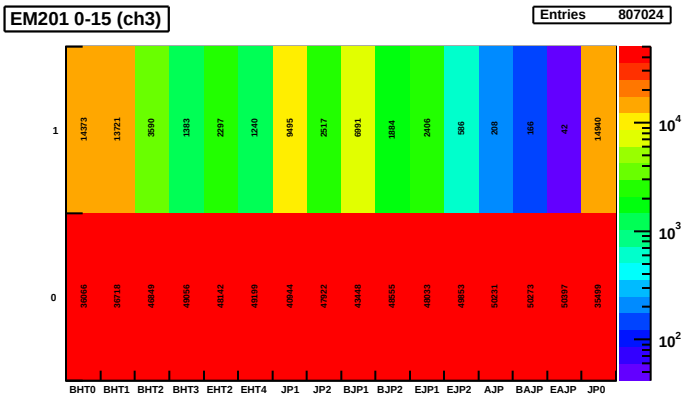

Endcap Jet Patches
Entries 302634
JP ADC
JP ID

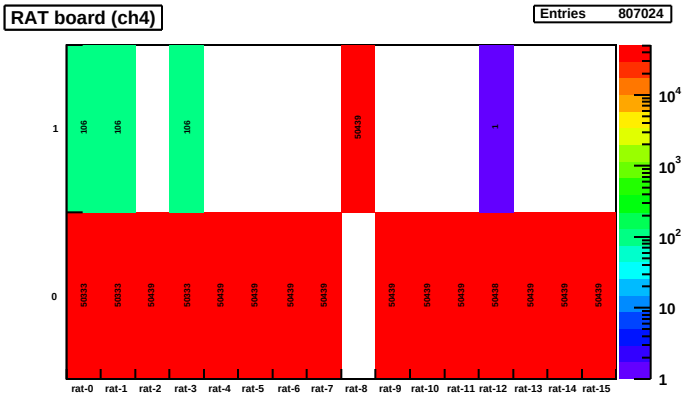

Hybrid Jet Patches
Entries 100878
JP ADC
JP ID

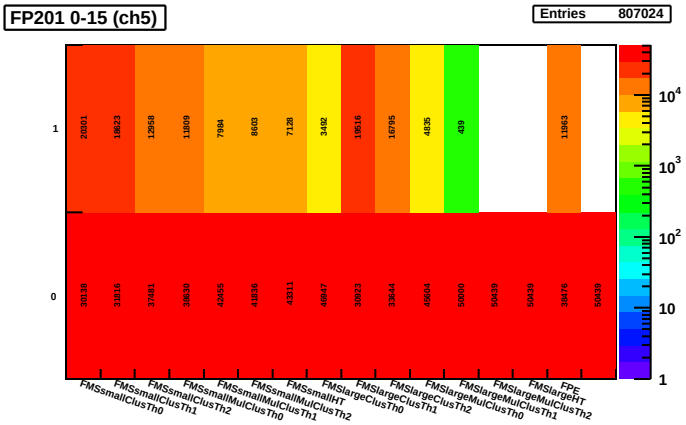

TF201 0-15 (ch0)
Entries 807024
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

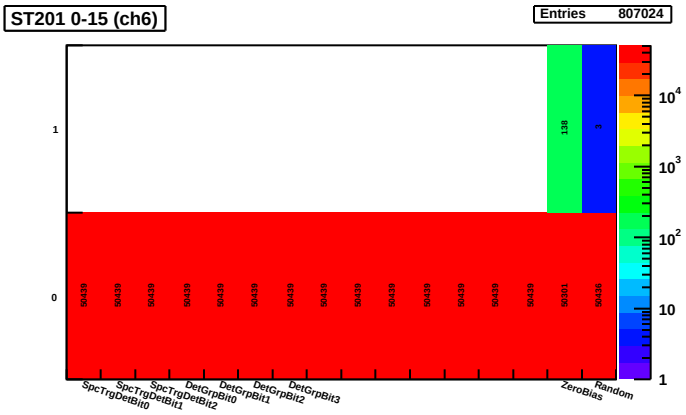

VT201 0-15 (ch1)
Entries 807024
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

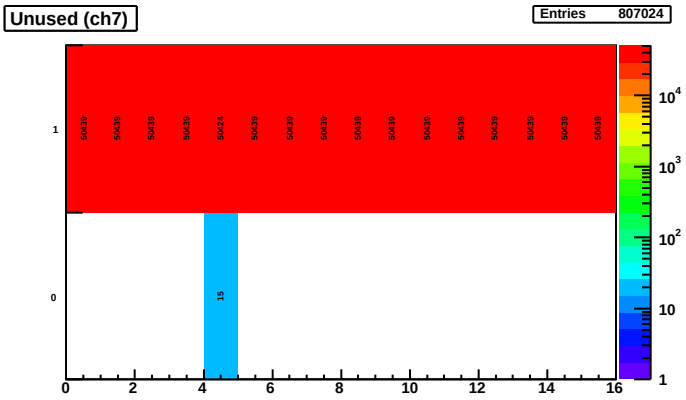

Unused (ch2)
Entries 807024

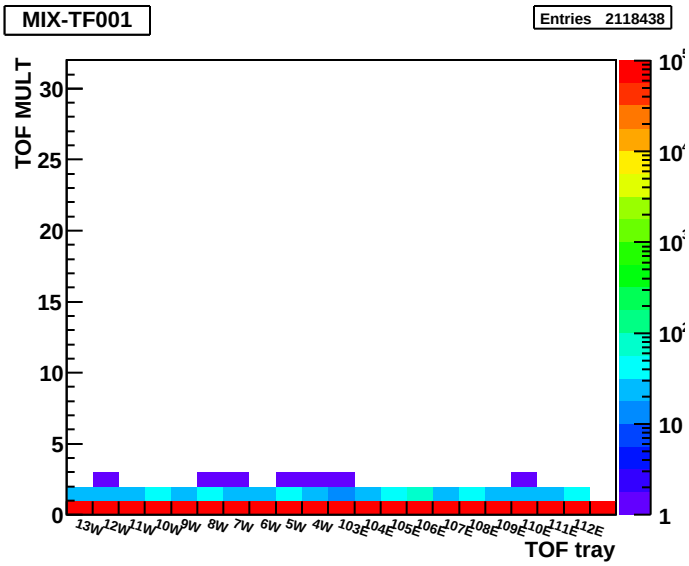

EM201 0-15 (ch3)
Entries 807024
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

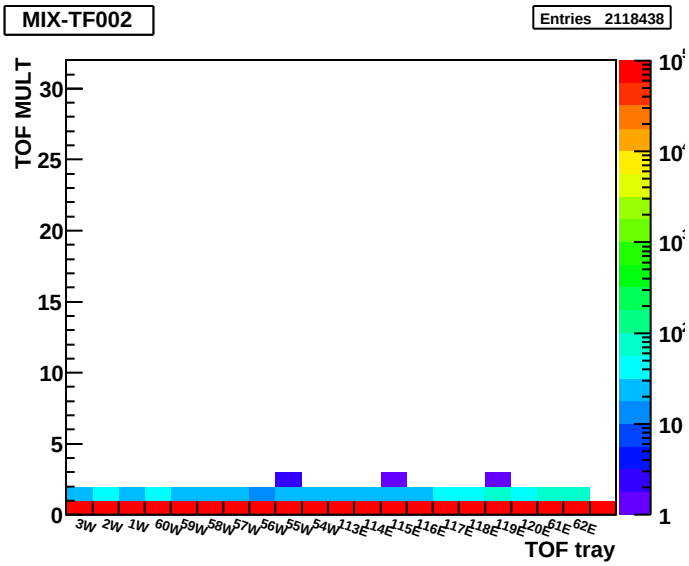

RAT board (ch4)
Entries 807024
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

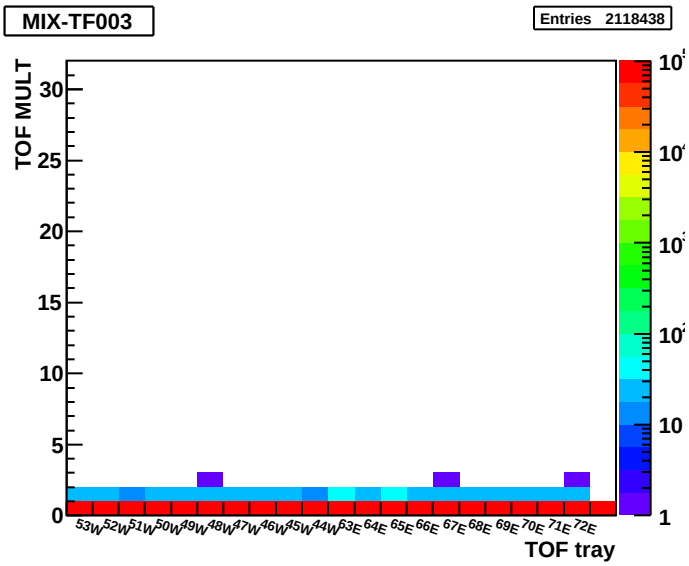

FP201 0-15 (ch5)
Entries 807024
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

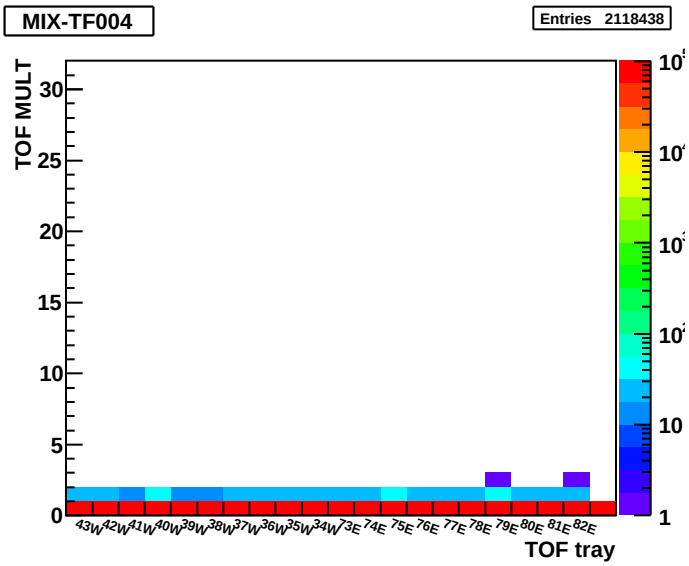

ST201 0-15 (ch6)
Entries 807024
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

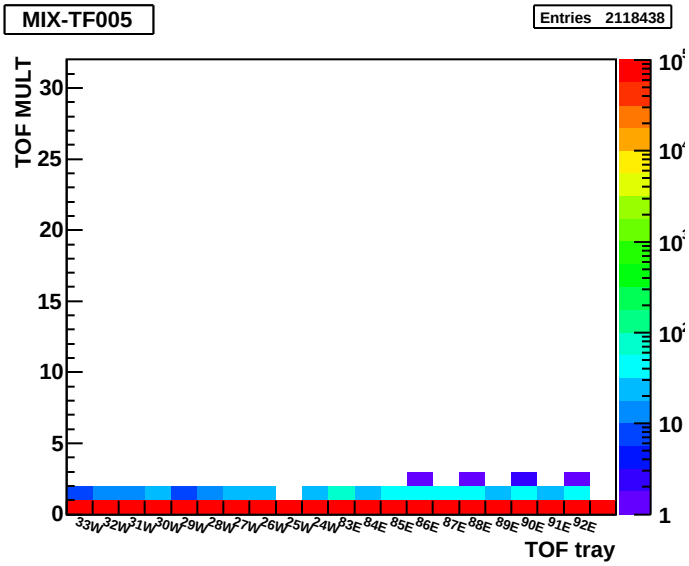

Unused (ch7)
Entries 807024

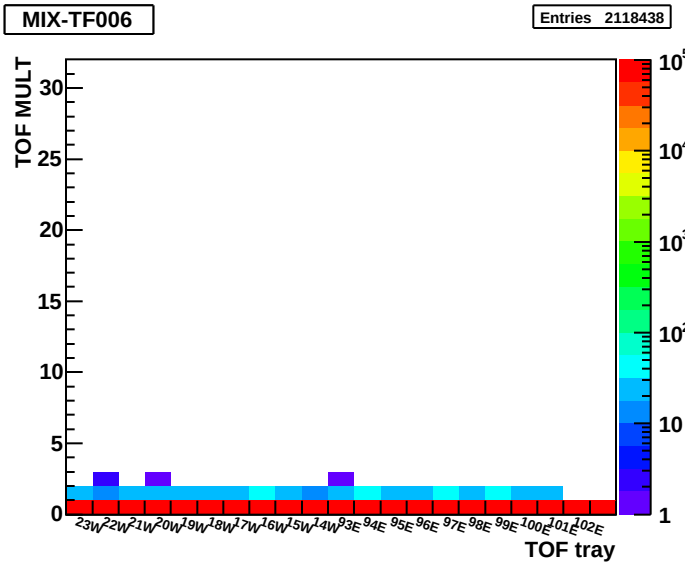

MIX-TF001
Entries 2118438
TOF MULT
TOF tray

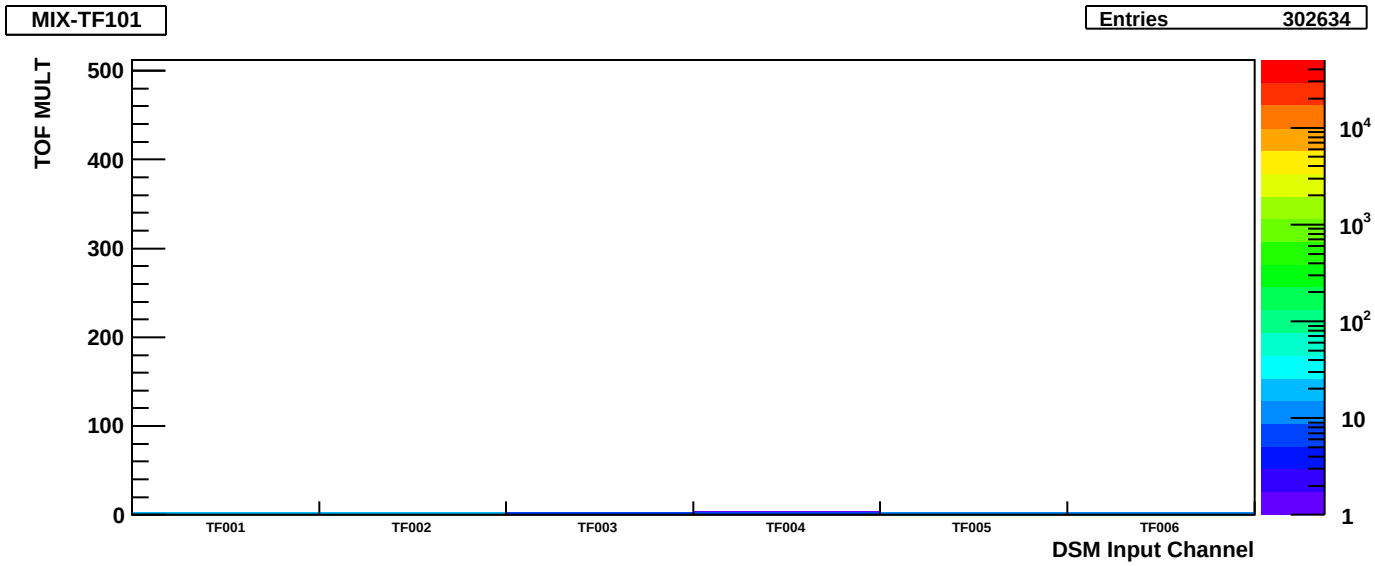

MIX-TF002
Entries 2118438
TOF MULT
TOF tray

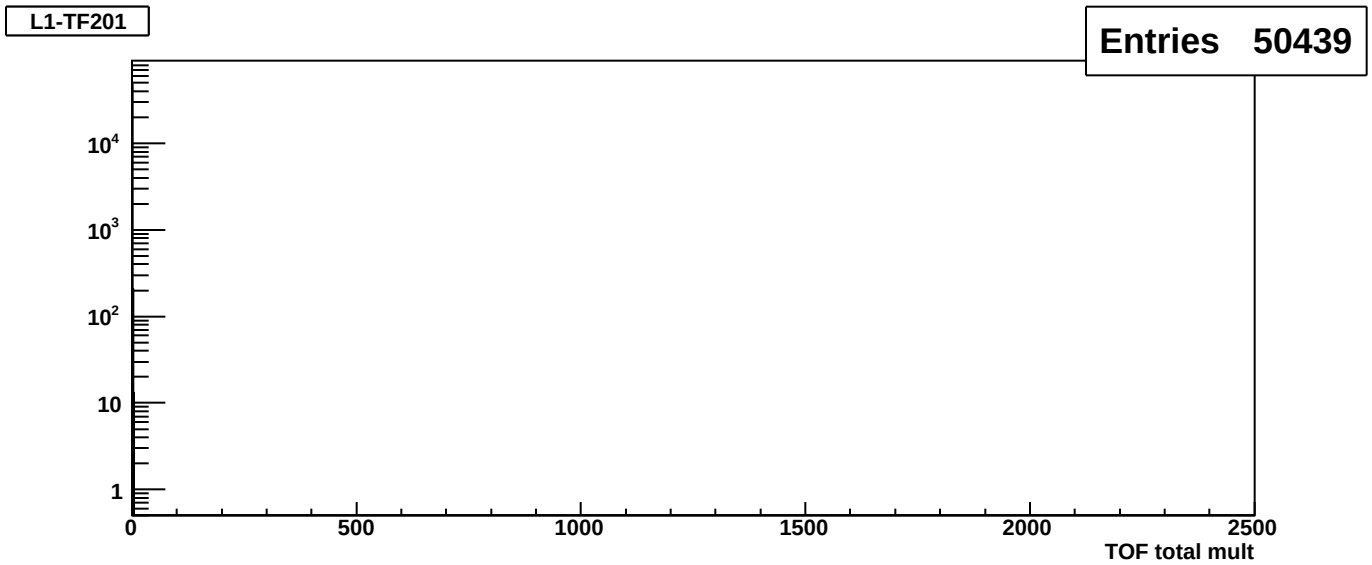

MIX-TF003
Entries 2118438
TOF MULT
TOF tray

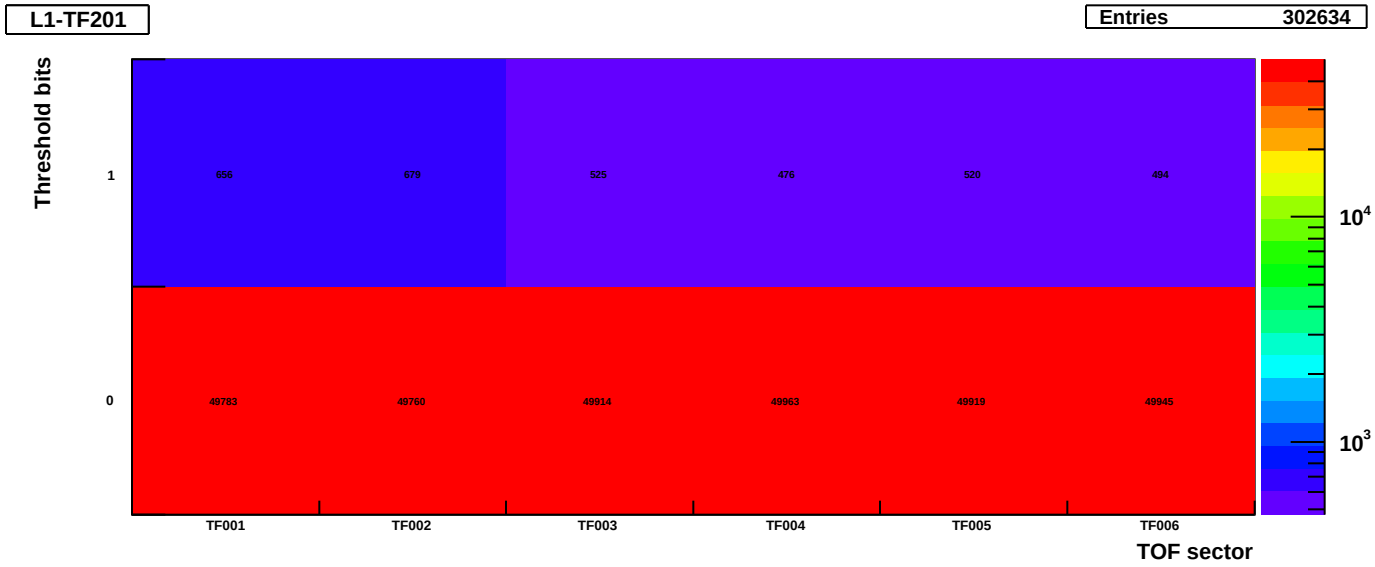

MIX-TF004
Entries 2118438
TOF MULT
TOF tray

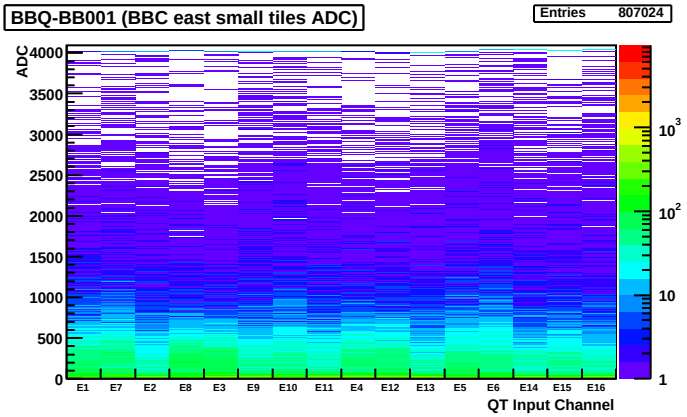

MIX-TF005
Entries 2118438
TOF MULT
TOF tray

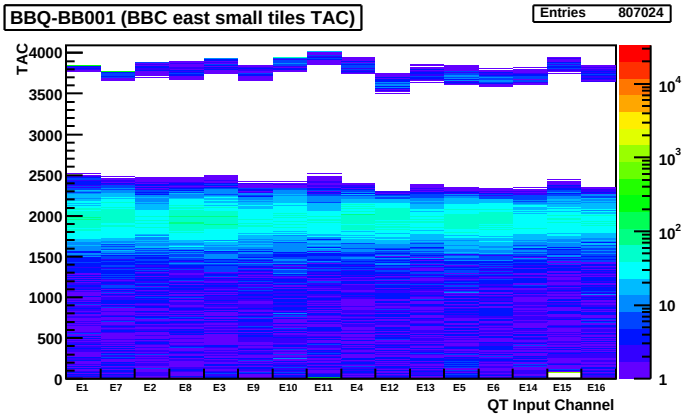

MIX-TF006
Entries 2118438
TOF MULT
TOF tray

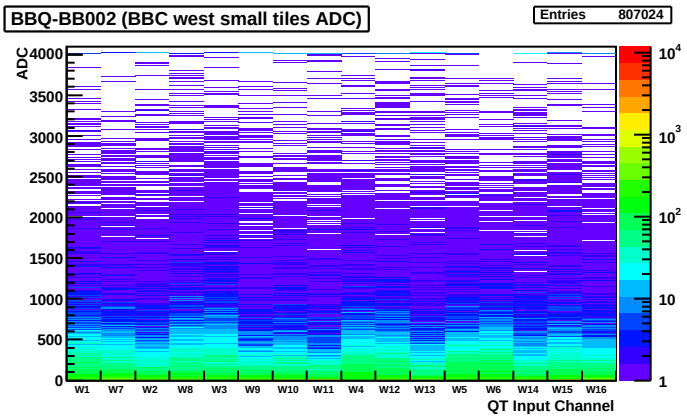

MIX-TF101
Entries 302634
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

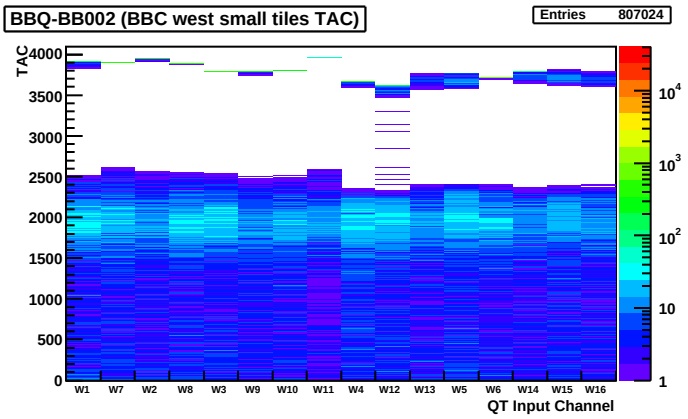

L1-TF201
Entries 50439
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

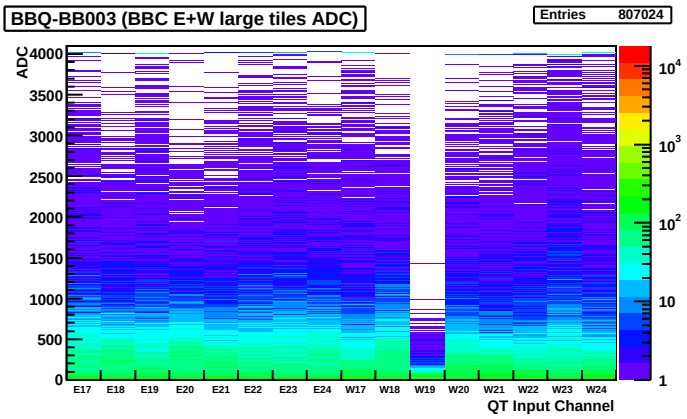

L1-TF201
Entries 302634
Threshold bits
1
0
656
679
525
476
520
494
49783
49760
49914
49963
49919
49945
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4
10^3

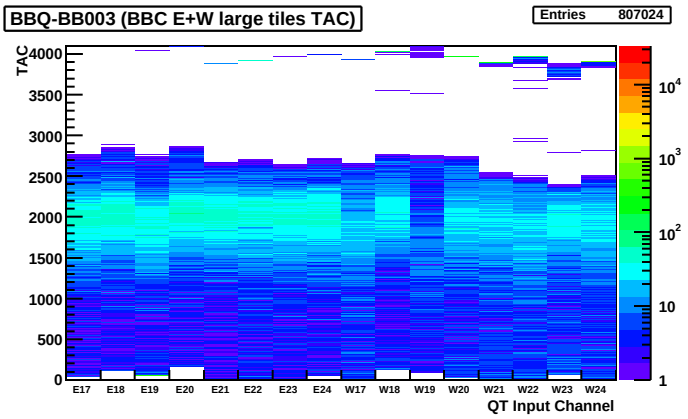

BBQ-BB001 (BBC east small tiles ADC)
Entries 807024
ADC
QT Input Channel

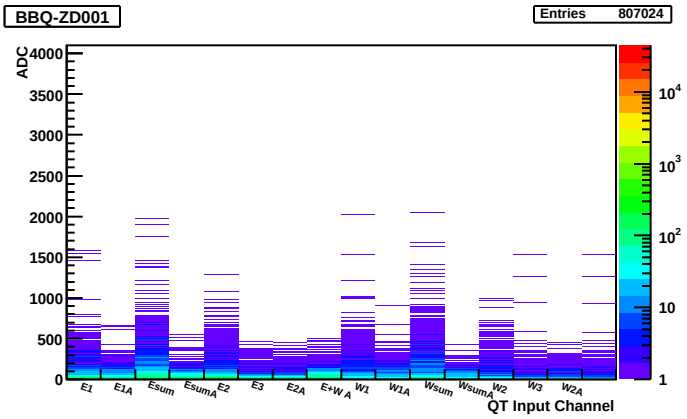

BBQ-BB001 (BBC east small tiles TAC)
Entries 807024
TAC
QT Input Channel

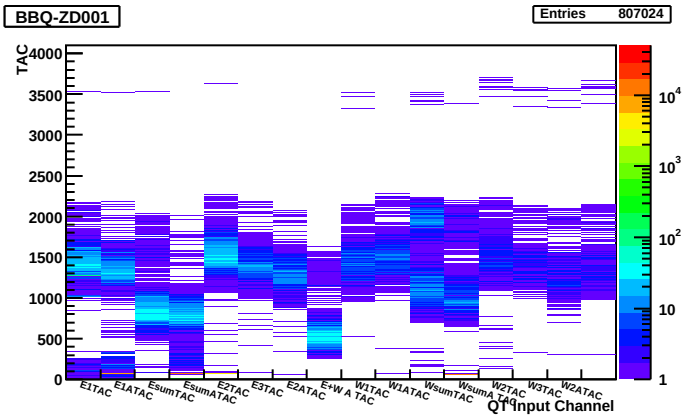

BBQ-BB002 (BBC west small tiles ADC)
Entries 807024
ADC
QT Input Channel

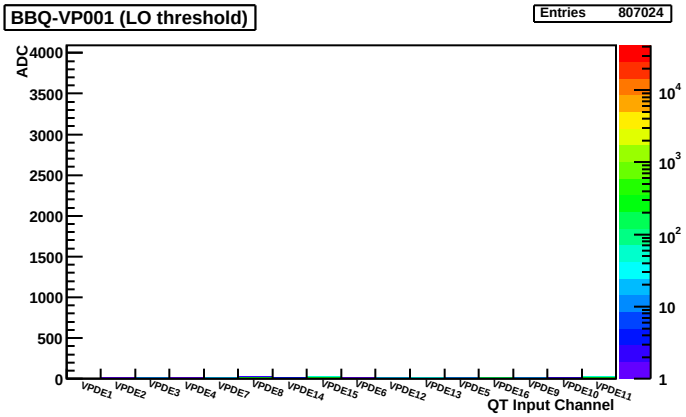

BBQ-BB002 (BBC west small tiles TAC)
Entries 807024
TAC
QT Input Channel

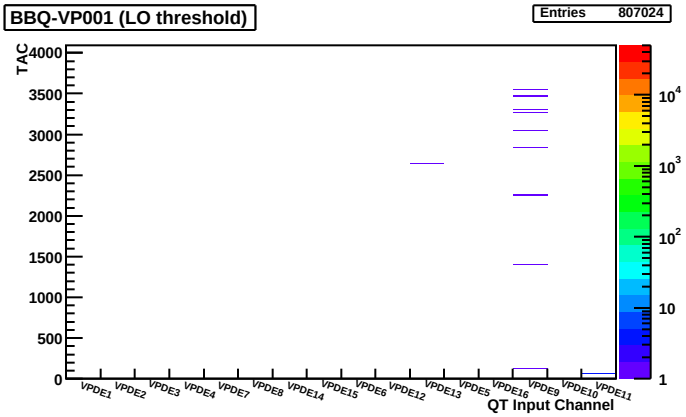

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 807024
ADC
QT Input Channel

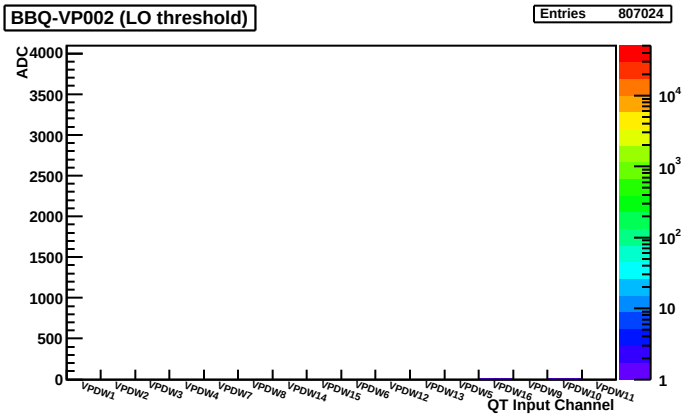

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 807024
TAC
QT Input Channel

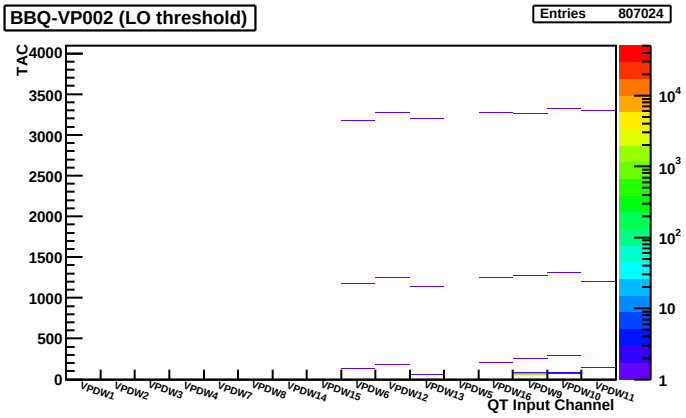

BBQ-ZD001
Entries 807024
ADC
QT Input Channel

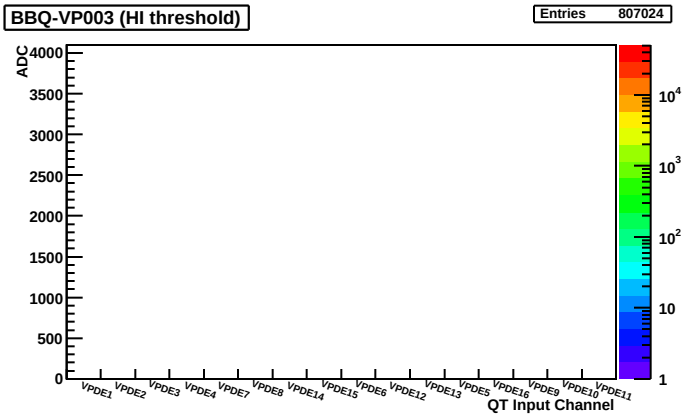

BBQ-ZD001
Entries 807024
TAC
QT Input Channel

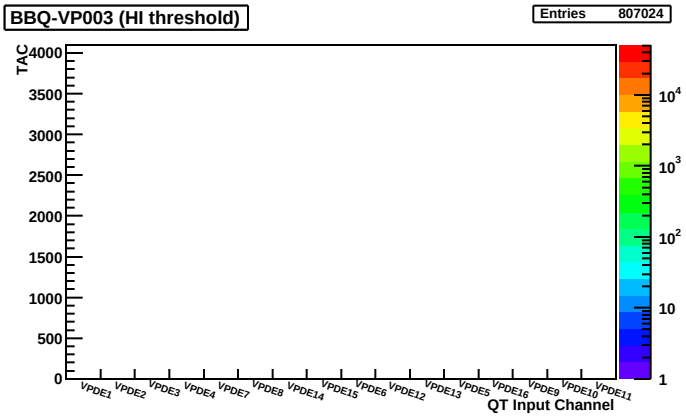

BBQ-VP001 (LO threshold)
Entries 807024
ADC
QT Input Channel

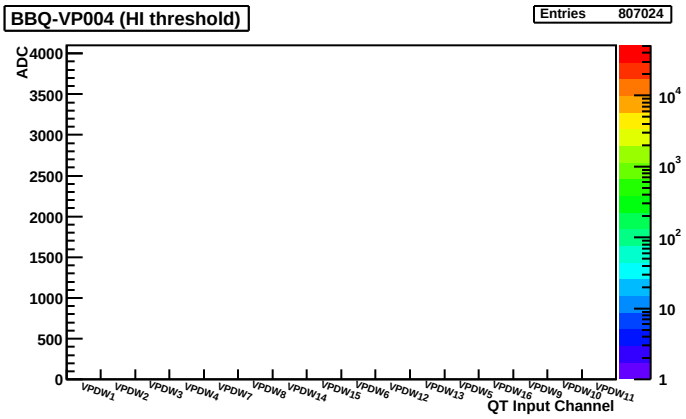

BBQ-VP001 (LO threshold)
Entries 807024
TAC
QT Input Channel

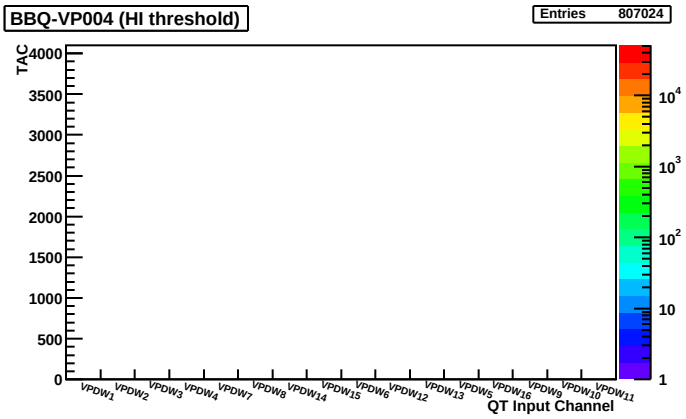

BBQ-VP002 (LO threshold)
Entries 807024
ADC
QT Input Channel

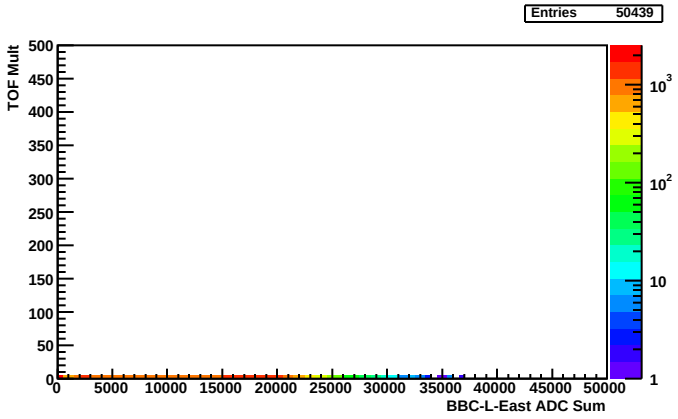

BBQ-VP002 (LO threshold)
Entries 807024
TAC
QT Input Channel

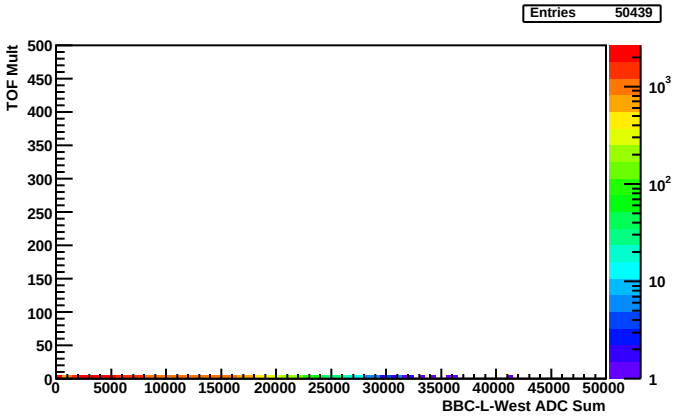

BBQ-VP003 (HI threshold)
Entries 807024
ADC
QT Input Channel

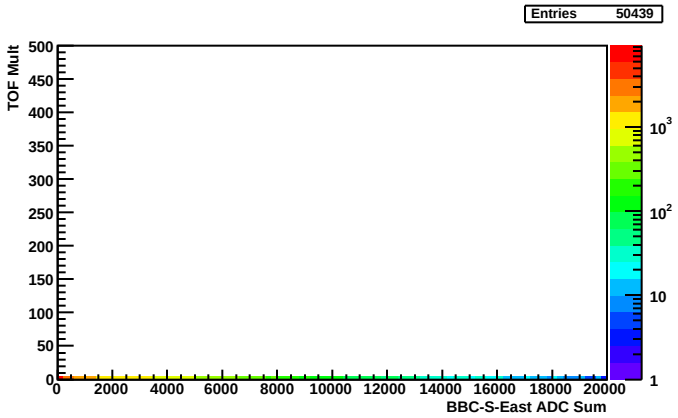

BBQ-VP003 (HI threshold)
Entries 807024
TAC
QT Input Channel

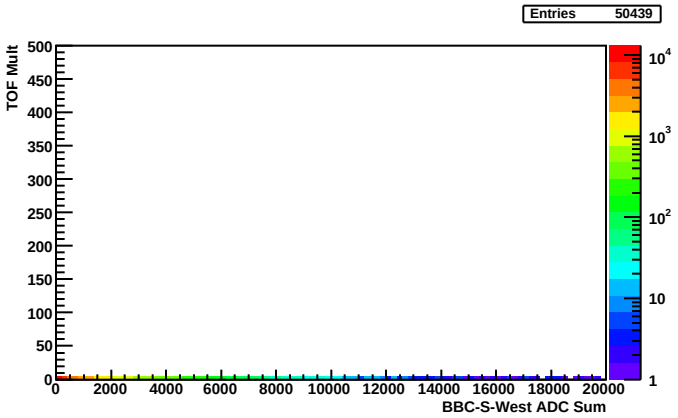

BBQ-VP004 (HI threshold)
Entries 807024
ADC
QT Input Channel

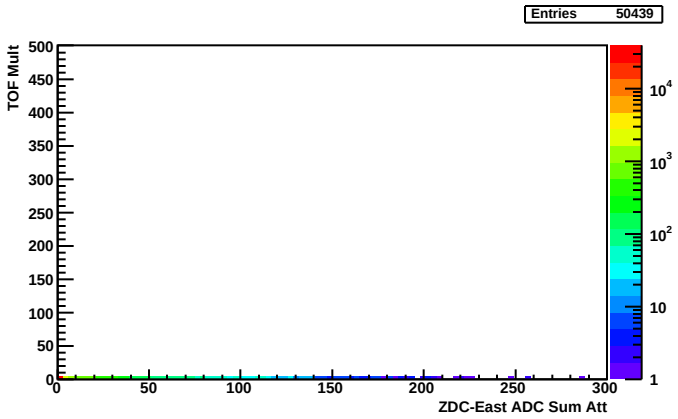

BBQ-VP004 (HI threshold)
Entries 807024
TAC
QT Input Channel

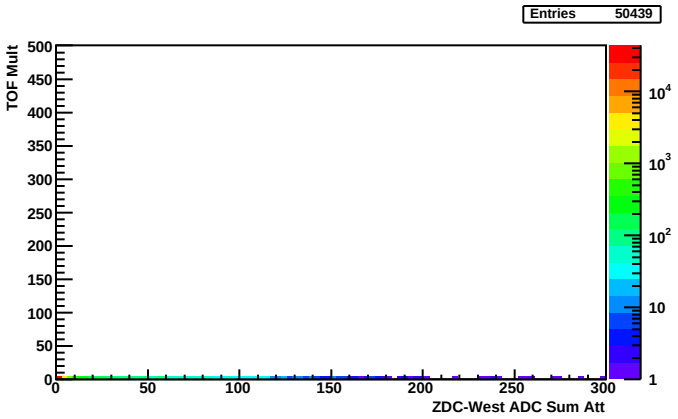

Entries 50439
TOF Mult
BBC-L-East ADC Sum

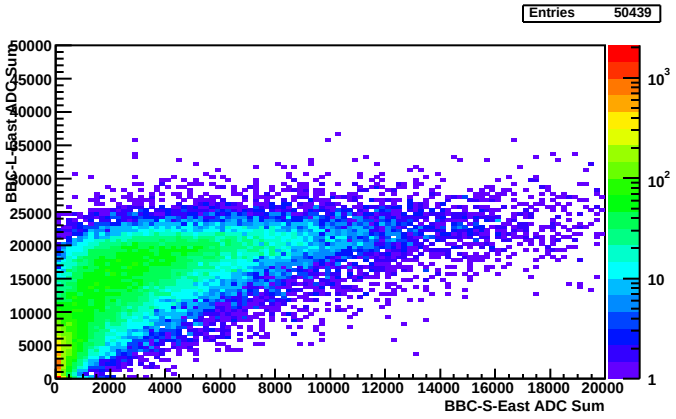

Entries 50439
TOF Mult
BBC-L-West ADC Sum

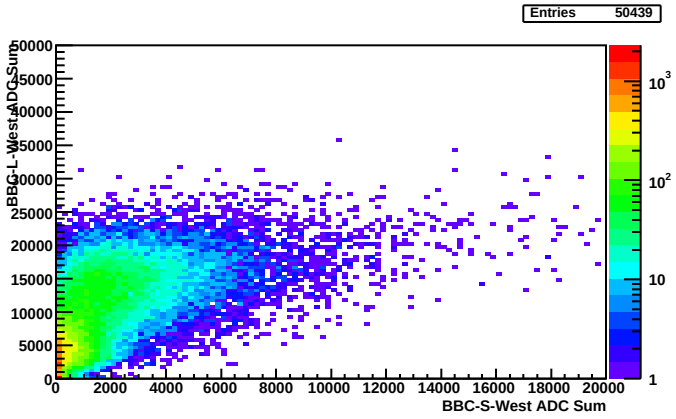

Entries 50439
TOF Mult
BBC-S-East ADC Sum

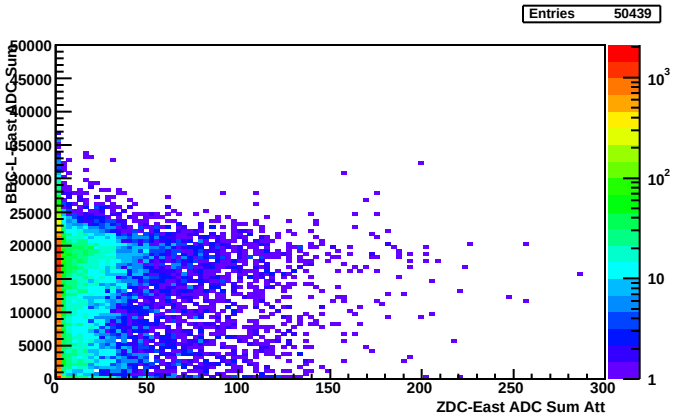

Entries 50439
TOF Mult
BBC-S-West ADC Sum

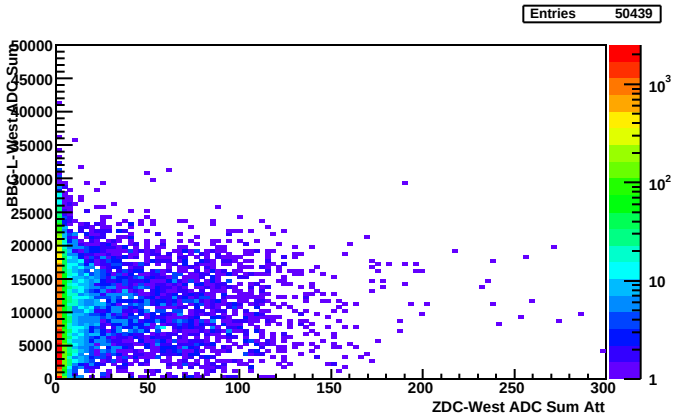

Entries 50439
TOF Mult
ZDC-East ADC Sum Att

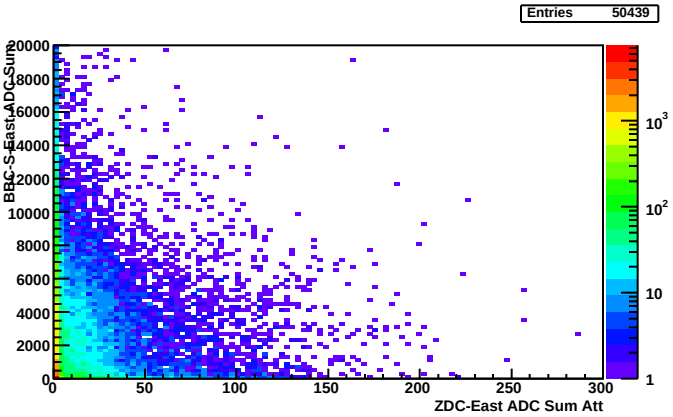

Entries 50439
TOF Mult
ZDC-West ADC Sum Att

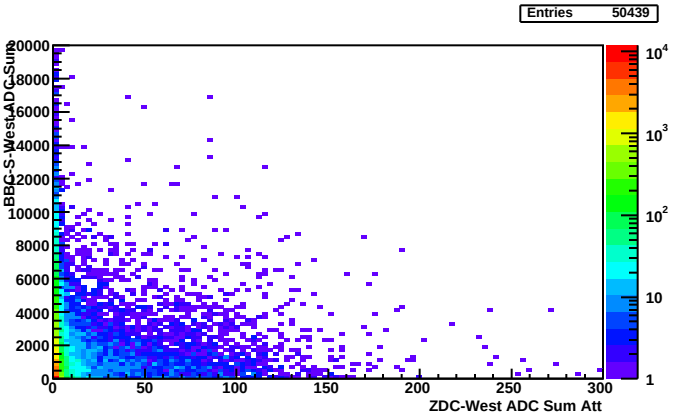

Entries 50439
BBC-L-East ADC Sum
BBC-S-East ADC Sum

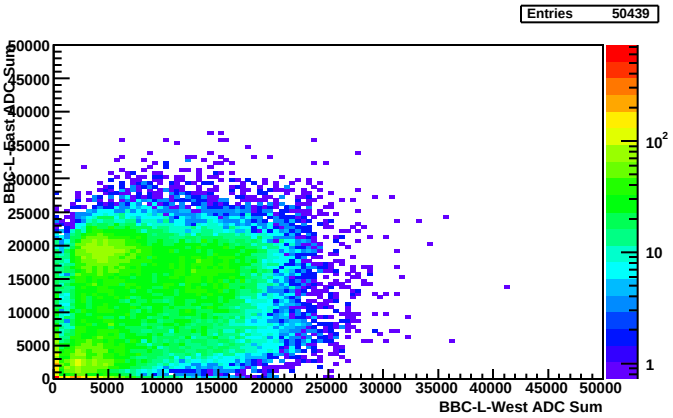

Entries 50439
BBC-L-West ADC Sum
BBC-S-West ADC Sum

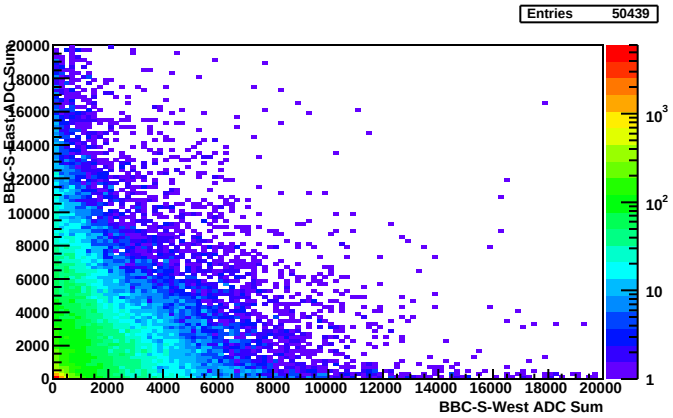

Entries 50439
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

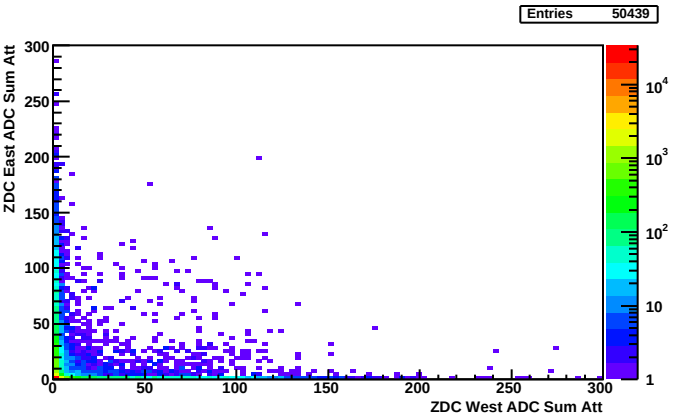

Entries 50439
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

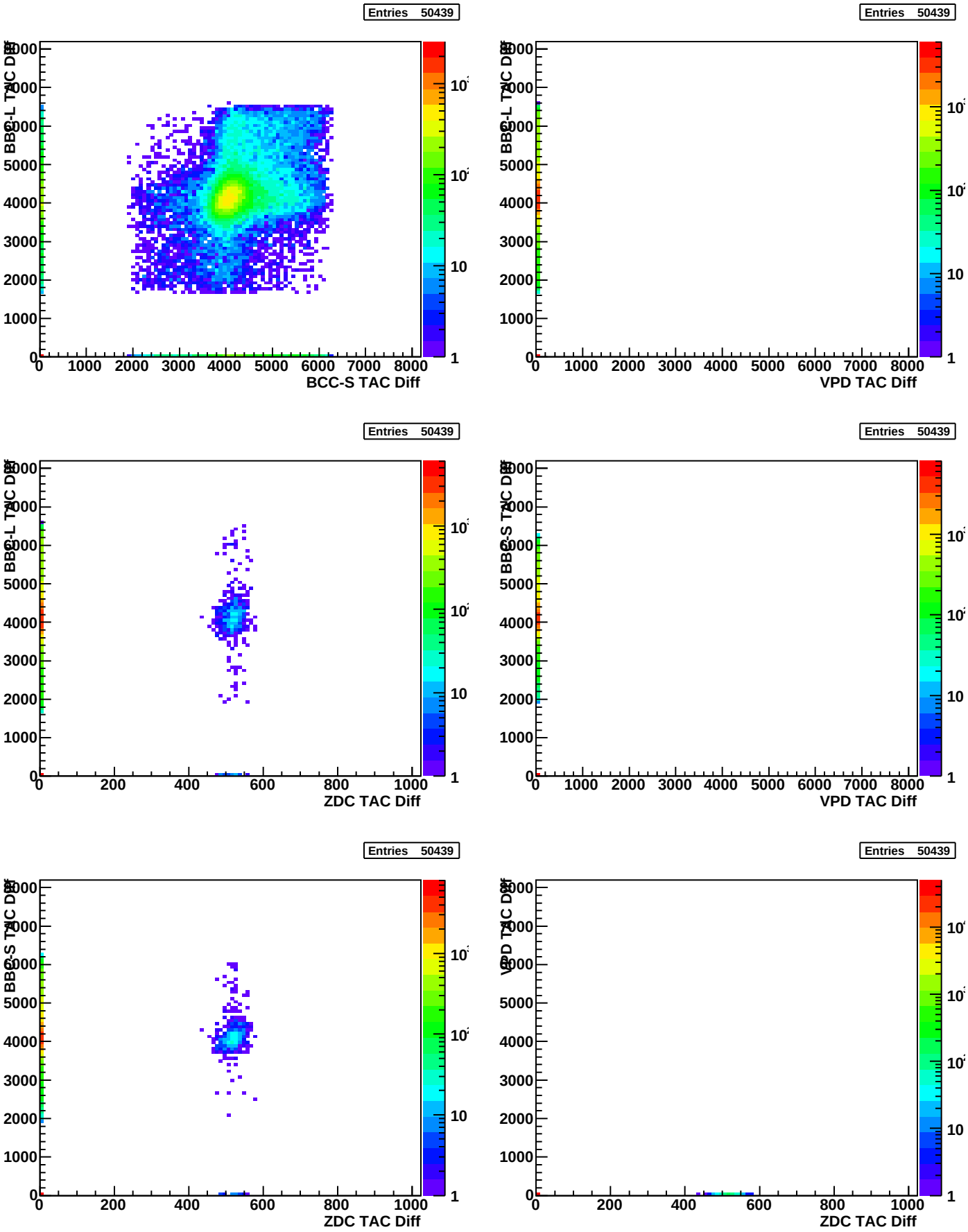

Entries 50439
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

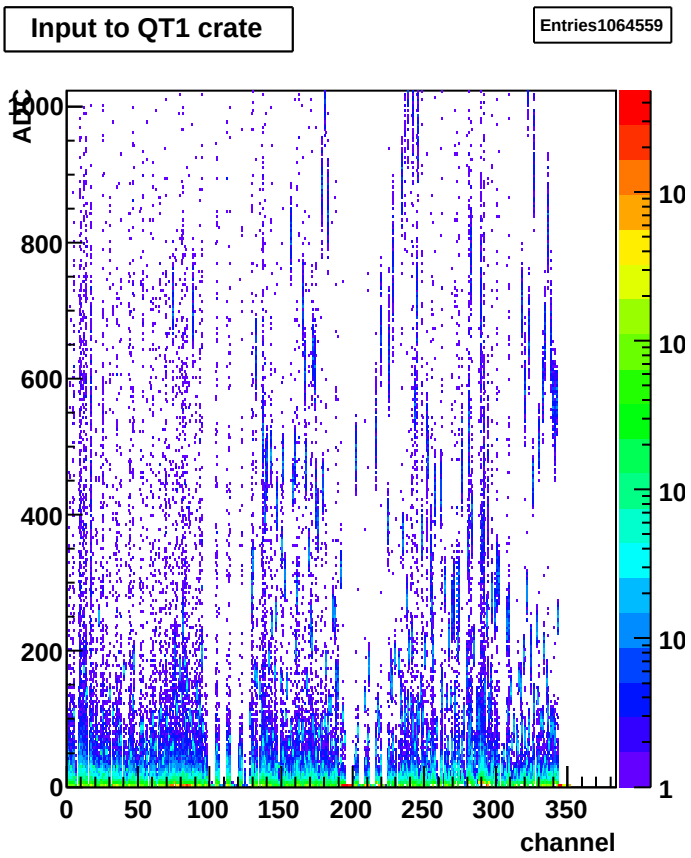

Entries 50439
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

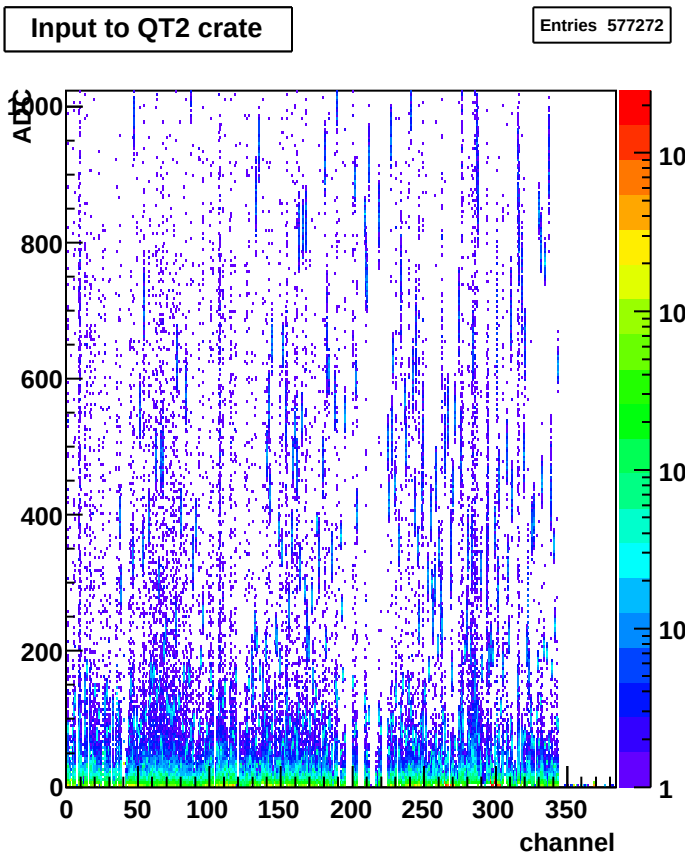

Entries 50439
BBC-L-East ADC Sum
BBC-L-West ADC Sum

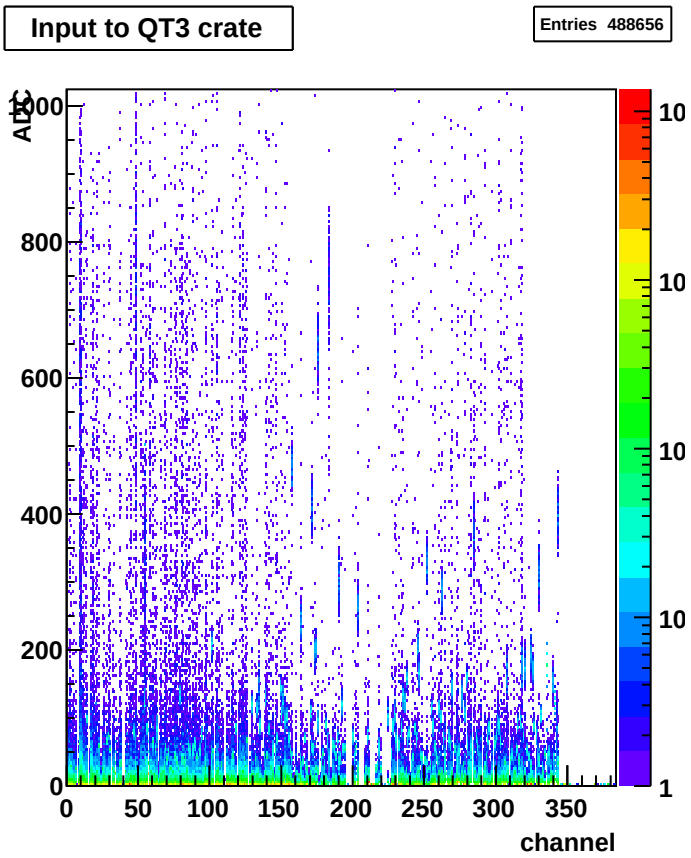

Entries 50439
BBC-S-East ADC Sum
BBC-S-West ADC Sum

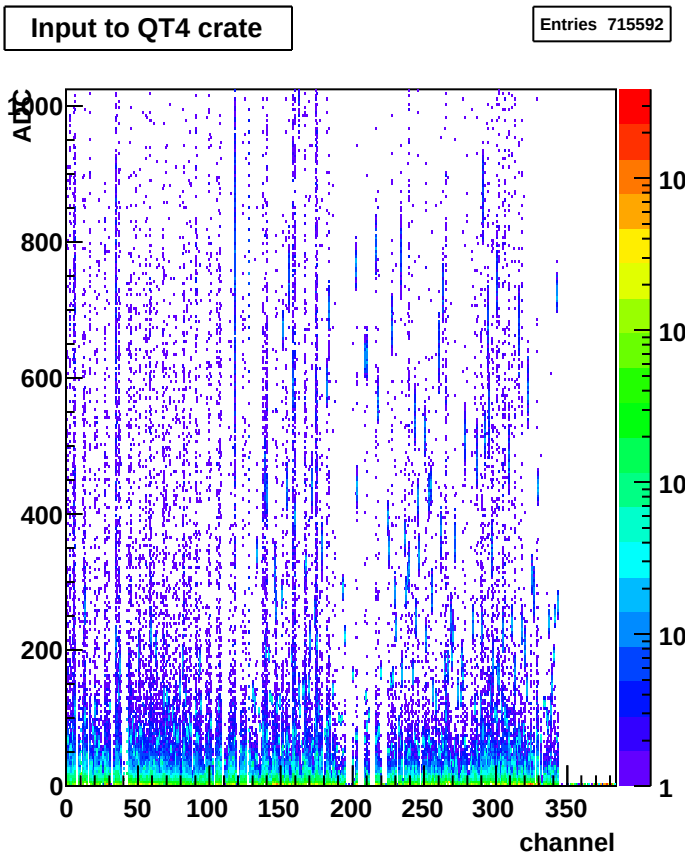

Entries 50439
ZDC East ADC Sum Att
ZDC West ADC Sum Att

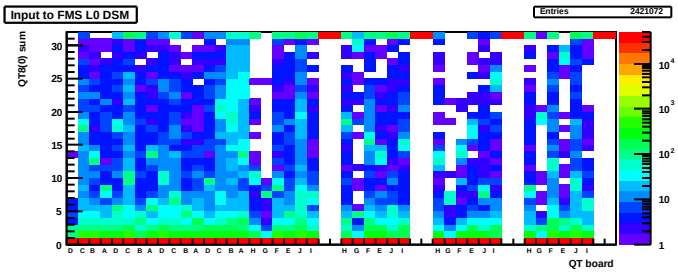

Entries 50439
BBC-L TAC Diff
BCC-S TAC Diff
Entries 50439
BBC-L TAC Diff
VPD TAC Diff
Entries 50439
BBC-L TAC Diff
ZDC TAC Diff
Entries 50439
BBC-S TAC Diff
VPD TAC Diff
Entries 50439
BBC-S TAC Diff
ZDC TAC Diff
Entries 50439
VPD TAC Diff
ZDC TAC Diff

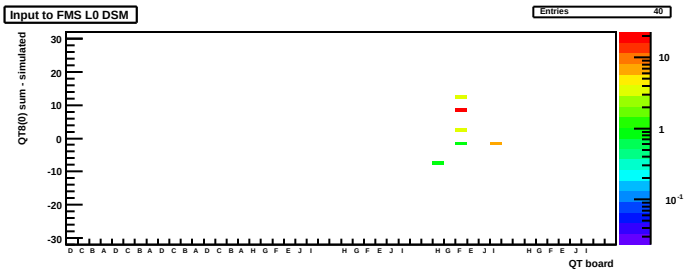

Input to QT1 crate
Entries1064559
ADC
channel

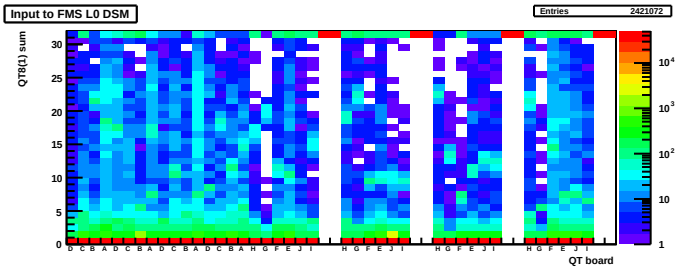

Input to QT2 crate
Entries 577272
ADC
channel

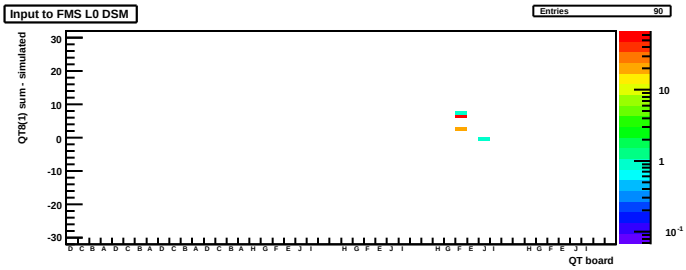

Input to QT3 crate
Entries 488656
ADC
channel

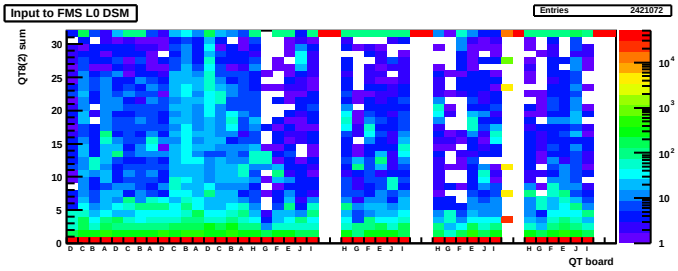

Input to QT4 crate
Entries 715592
ADC
channel

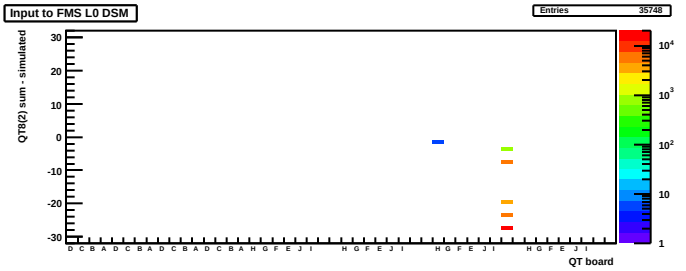

Input to FMS L0 DSM
Entries 2421072
QT8(0) sum
QT board

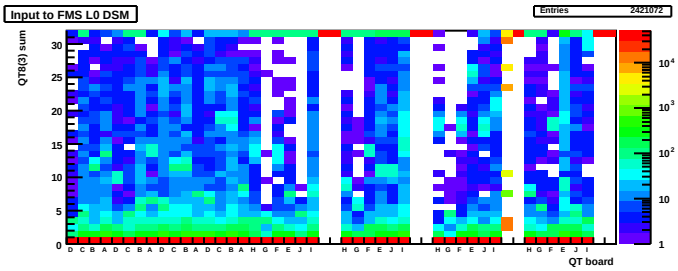

Input to FMS L0 DSM
Entries 40
QT8(0) sum - simulated
QT board

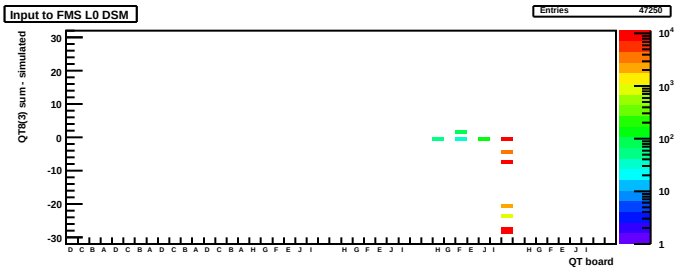

Input to FMS L0 DSM
Entries 2421072
QT8(1) sum
QT board

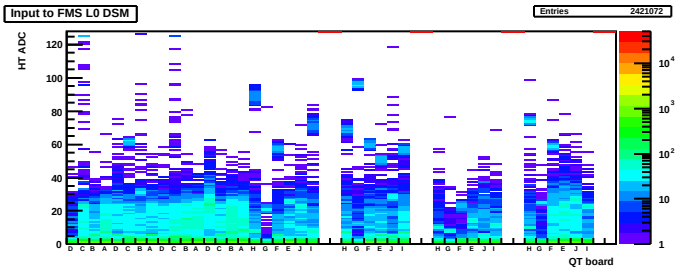

Input to FMS L0 DSM
Entries 90
QT8(1) sum - simulated
QT board

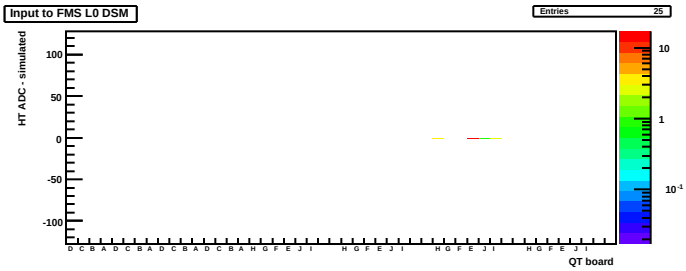

Input to FMS L0 DSM
Entries 2421072
QT8(2) sum
QT board

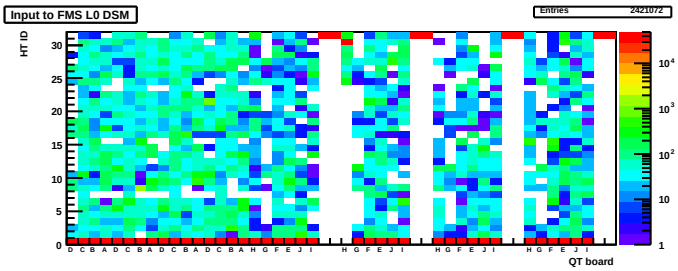

Input to FMS L0 DSM
Entries 35748
QT8(2) sum - simulated
QT board

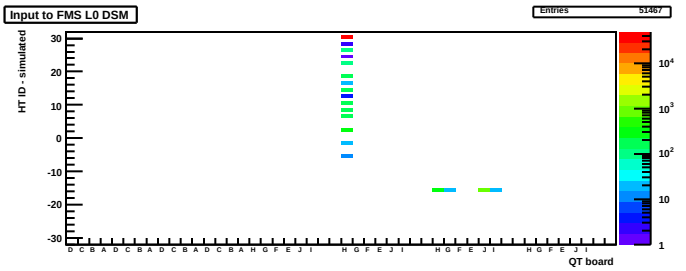

Input to FMS L0 DSM
Entries 2421072
QT8(3) sum
QT board

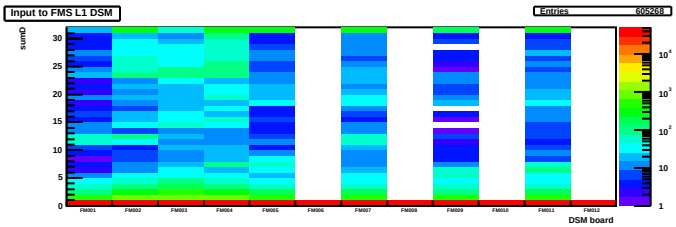

Input to FMS L0 DSM
Entries 47250
QT8(3) sum - simulated
QT board

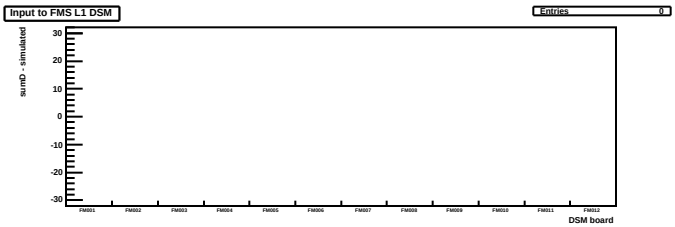

Input to FMS L0 DSM
Entries 2421072
HT ADC
QT board

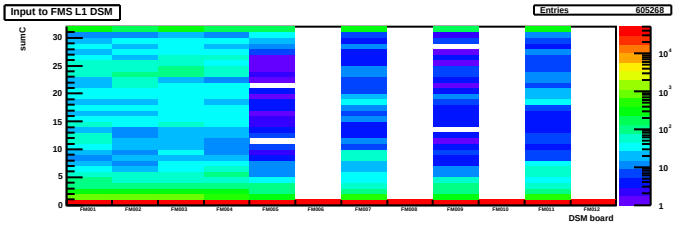

Input to FMS L0 DSM
Entries 25
HT ADC - simulated
QT board

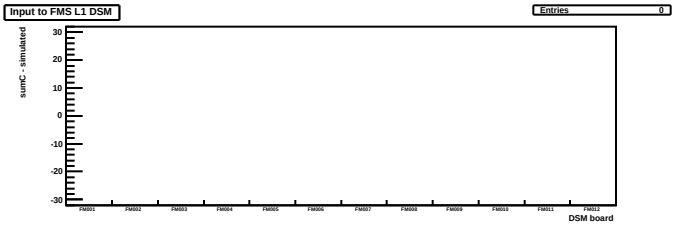

Input to FMS L0 DSM
Entries 2421072
HT ID
QT board

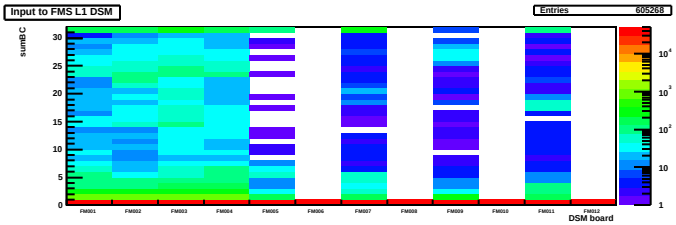

Input to FMS L0 DSM
Entries 51467
HT ID - simulated
QT board

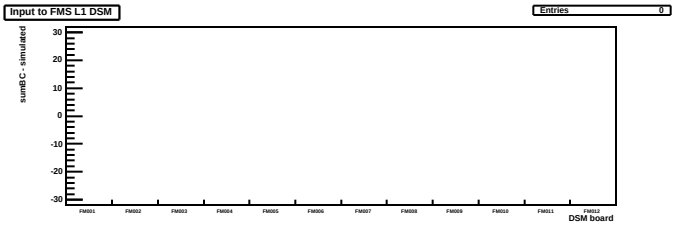

Input to FMS L1 DSM
Entries 605268
sumD
DSM board

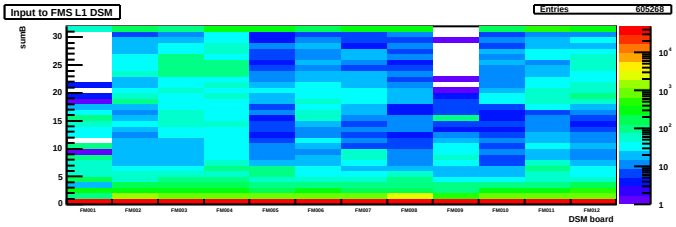

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

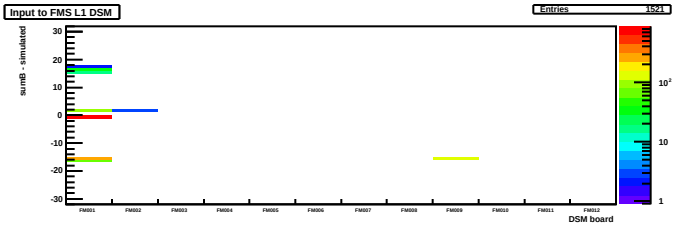

Input to FMS L1 DSM
Entries 605268
sumC
DSM board

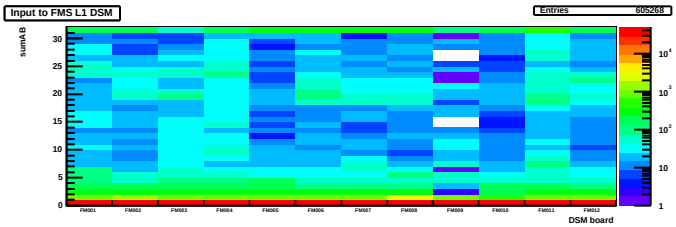

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

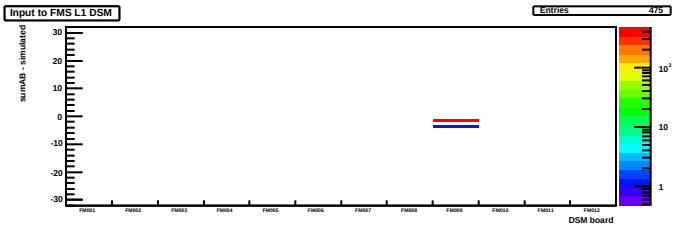

Input to FMS L1 DSM
Entries 605268
sumBC
DSM board

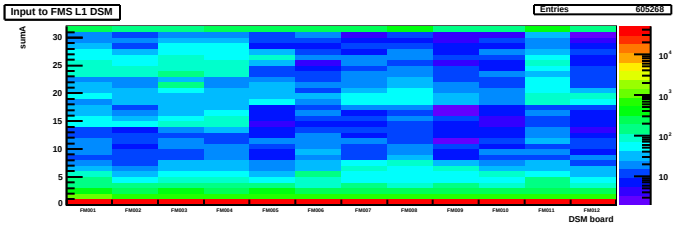

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

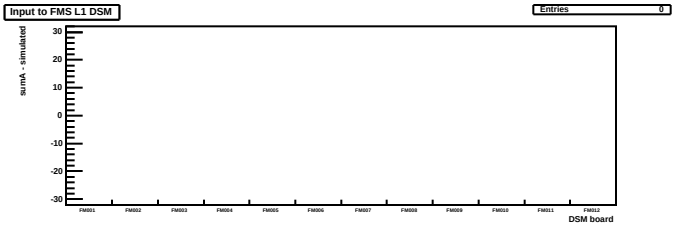

Input to FMS L1 DSM
Entries 605268
sumB
DSM board

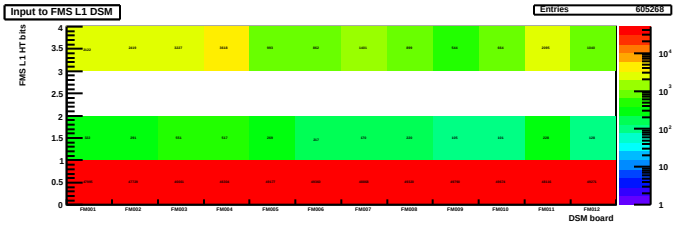

Input to FMS L1 DSM
Entries 1521
sumB - simulated
DSM board

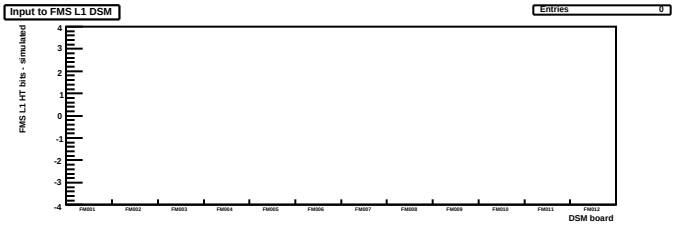

Input to FMS L1 DSM
Entries 605268
sumAB
DSM board

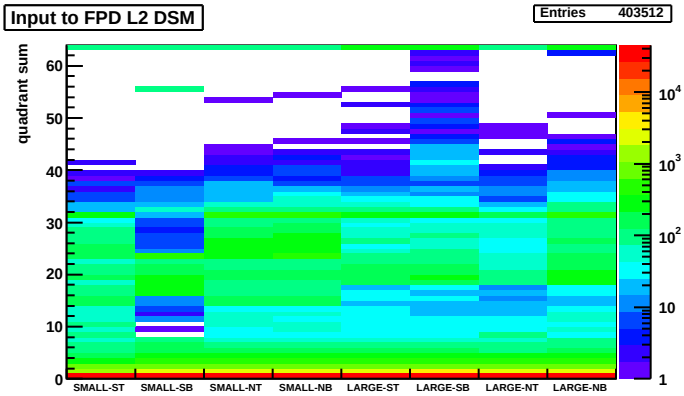

Input to FMS L1 DSM
Entries 475
sumAB - simulated
DSM board

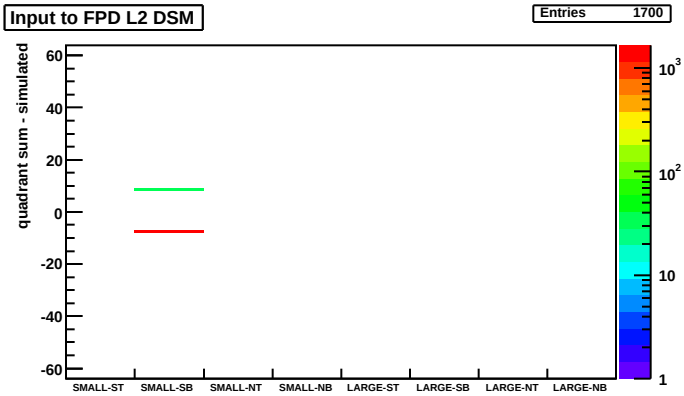

Input to FMS L1 DSM
Entries 605268
sumA
DSM board

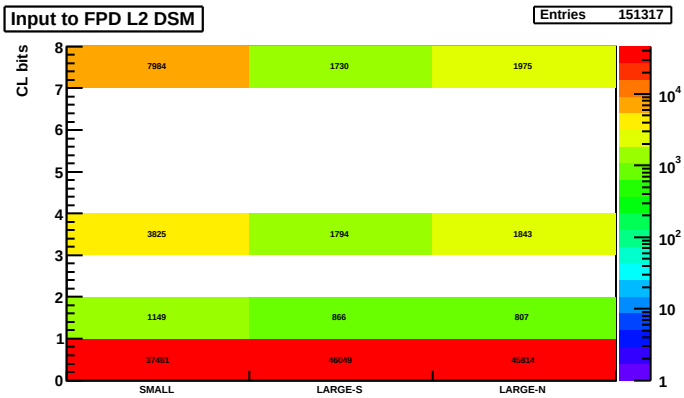

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

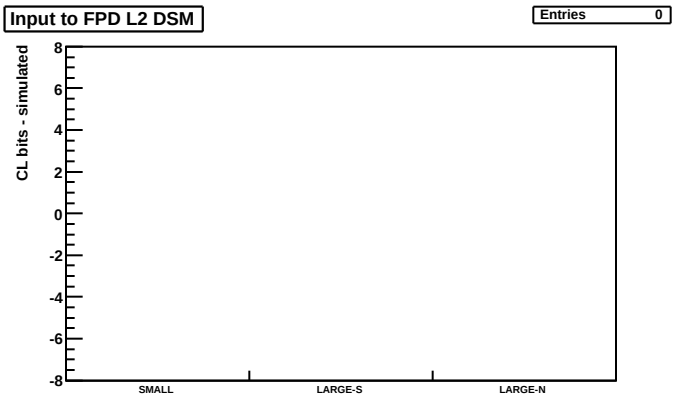

Input to FMS L1 DSM
Entries 605268
FMS L1 HT bits
DSM board

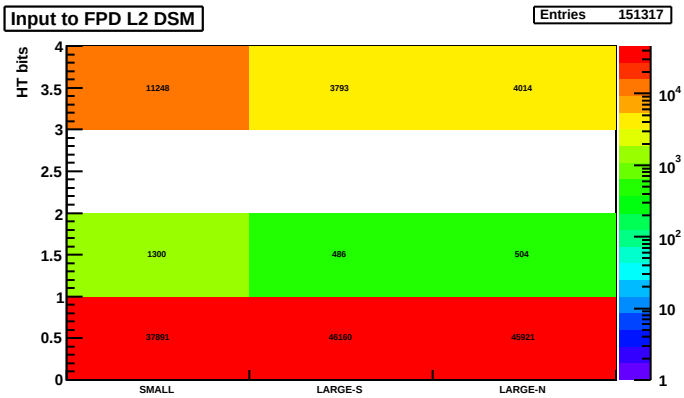

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

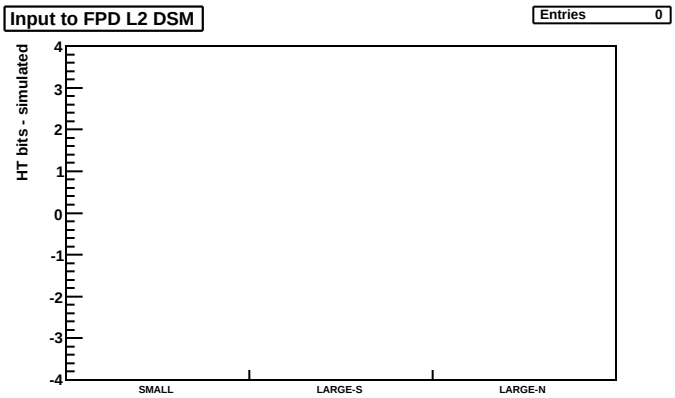

Input to FPD L2 DSM
Entries 403512
quadrant sum
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

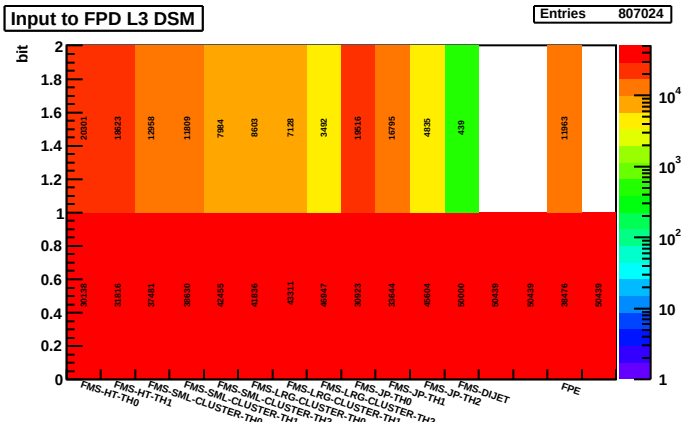

Input to FPD L2 DSM
Entries 1700
quadrant sum - simulated
SMALL-ST SMALL-SB SMALL-NT SMALL-NB LARGE-ST LARGE-SB LARGE-NT LARGE-NB

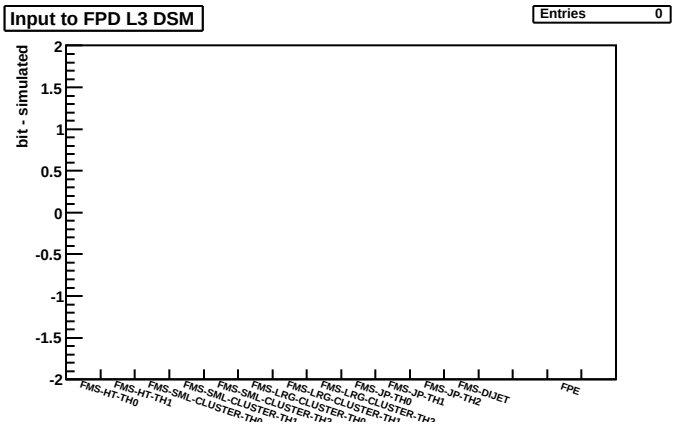

Input to FPD L2 DSM
Entries 151317
CL bits
7984 1730 1975
3825 1794 1843
1149 866 807
37481 46049 45814
SMALL LARGE-S LARGE-N

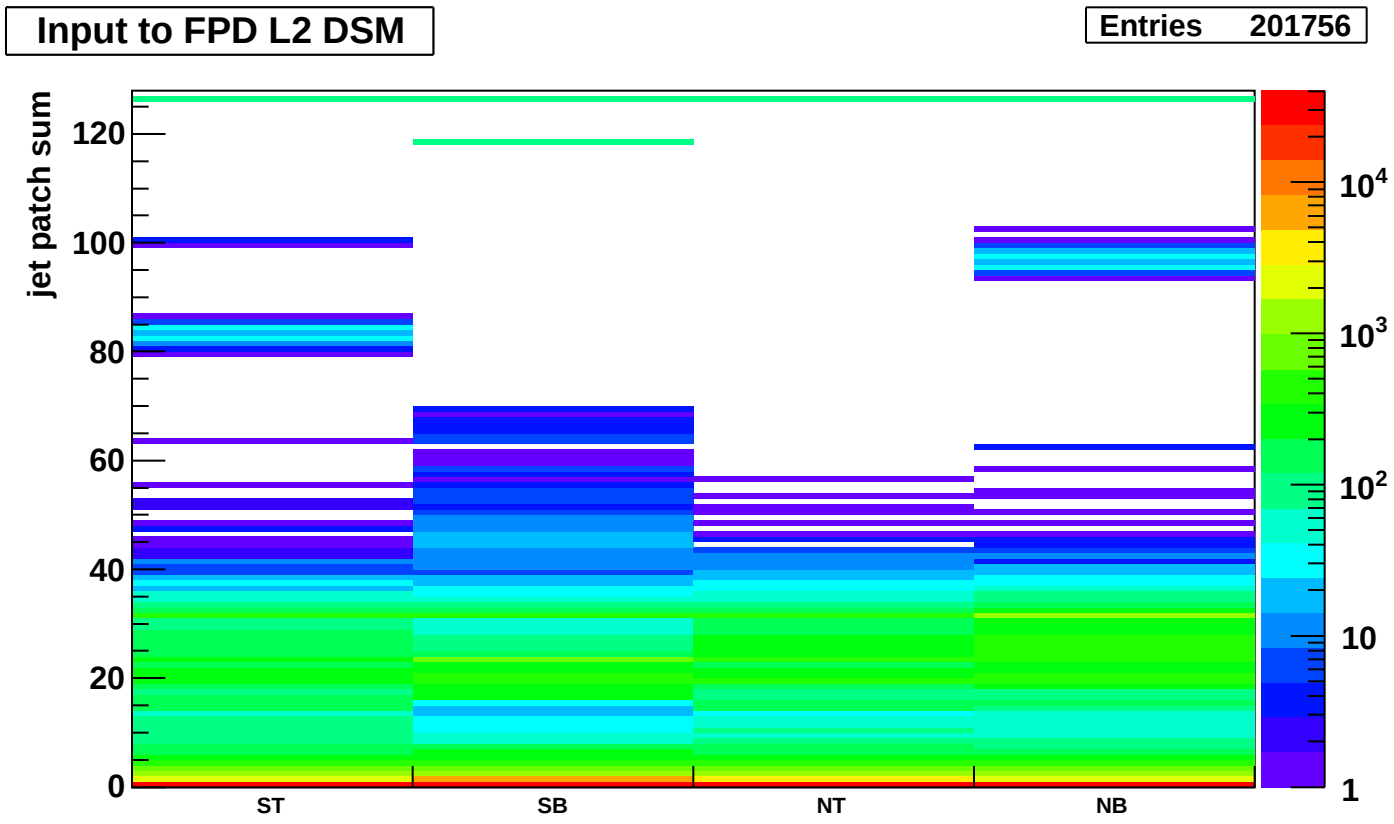

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL LARGE-S LARGE-N

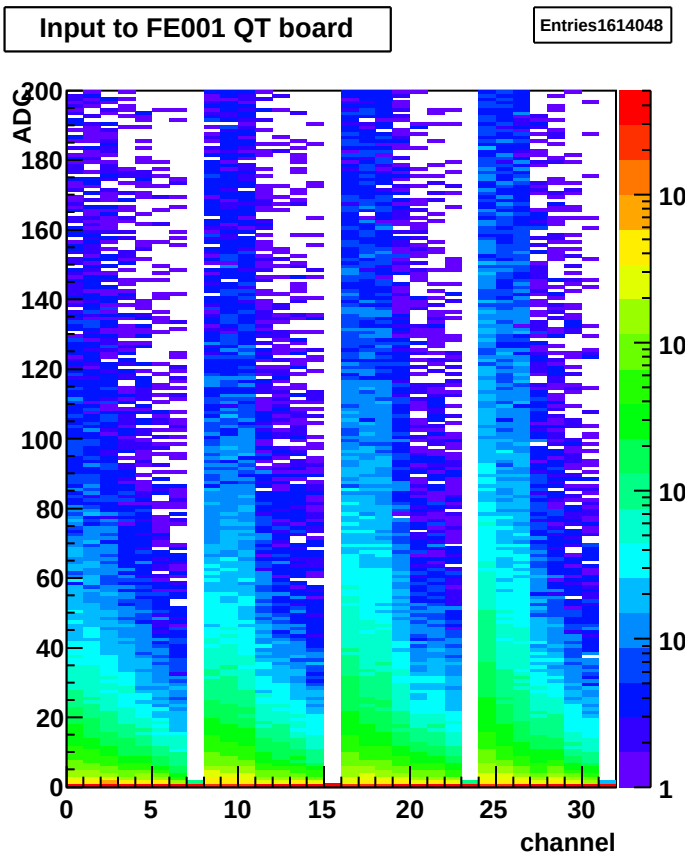

Input to FPD L2 DSM
Entries 151317
HT bits
11248 3793 4014
1300 486 504
37891 46160 45921
SMALL LARGE-S LARGE-N

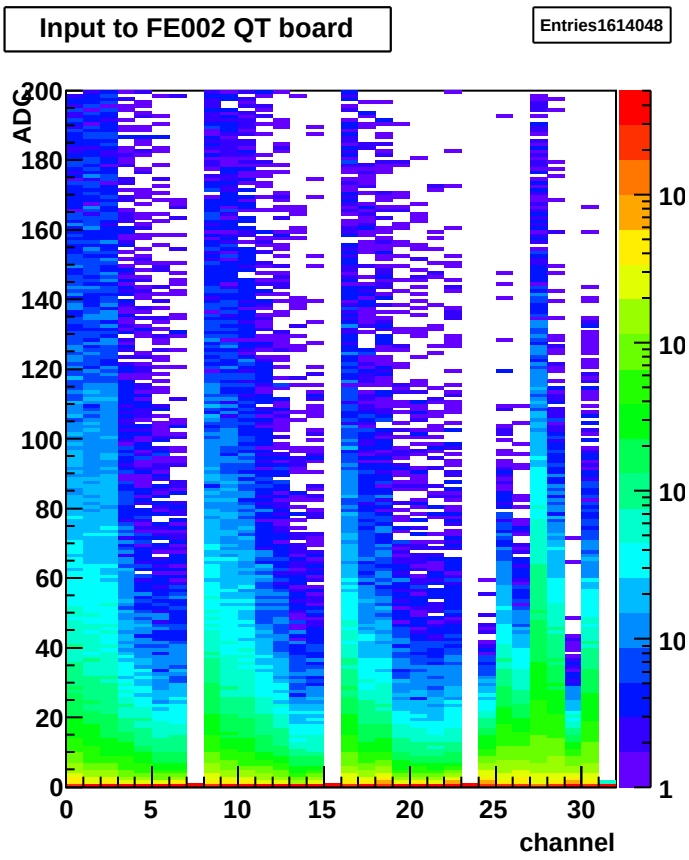

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL LARGE-S LARGE-N

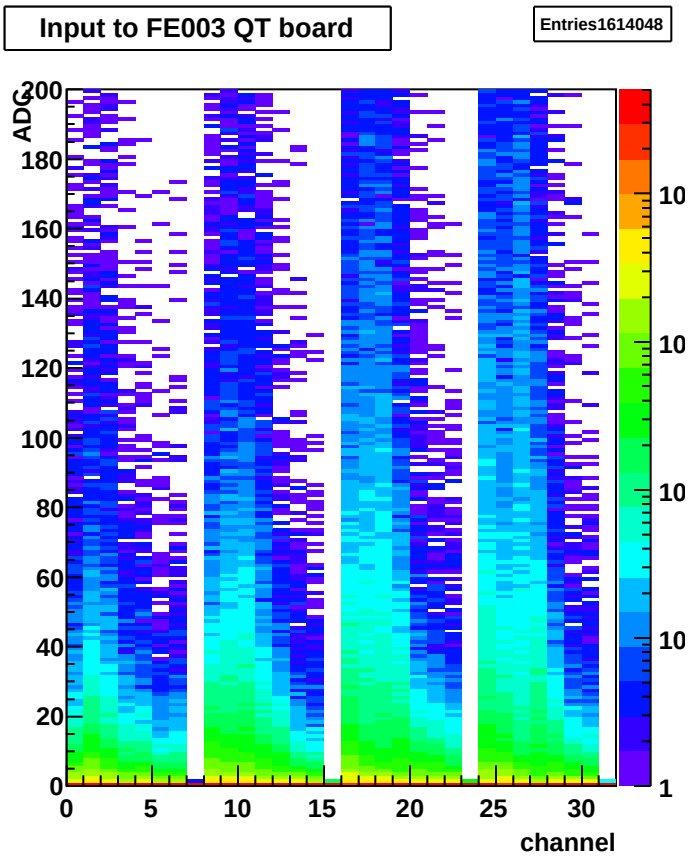

Input to FPD L3 DSM
Entries 807024
bit
23301 18623 12958 11809 7984 8603 7128 3482 18516 16795 4835 439 11963
33138 31816 37481 38630 42455 41836 43311 46947 30923 33644 45604 50000 50439 50439 38476 50439
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

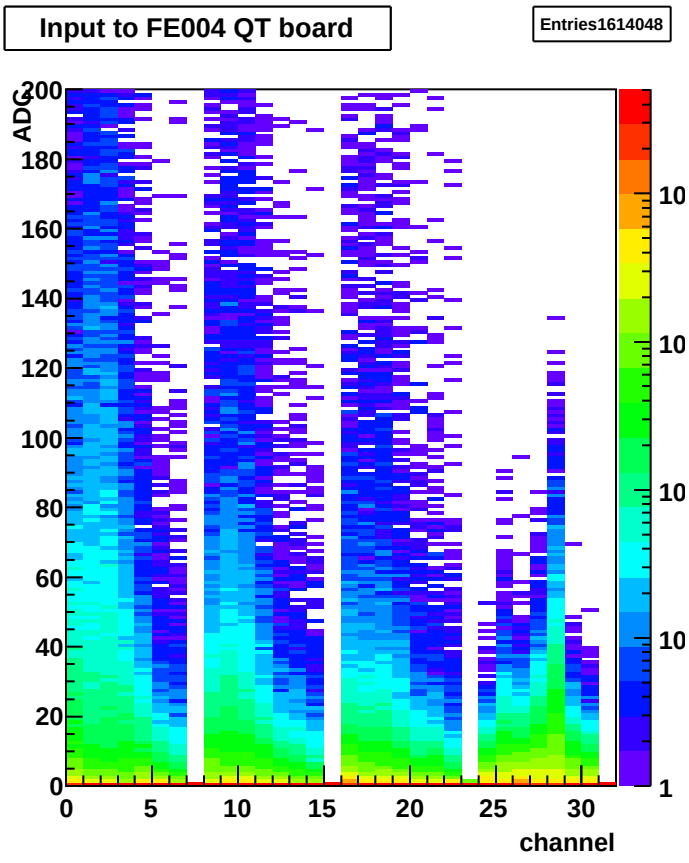

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0 FMS-HT-TH1 FMS-SML-CLUSTER-TH0 FMS-SML-CLUSTER-TH1 FMS-SML-CLUSTER-TH2 FMS-LRG-CLUSTER-TH0 FMS-LRG-CLUSTER-TH1 FMS-LRG-CLUSTER-TH2 FMS-JP-TH0 FMS-JP-TH1 FMS-JP-TH2 FMS-DIJET FPE

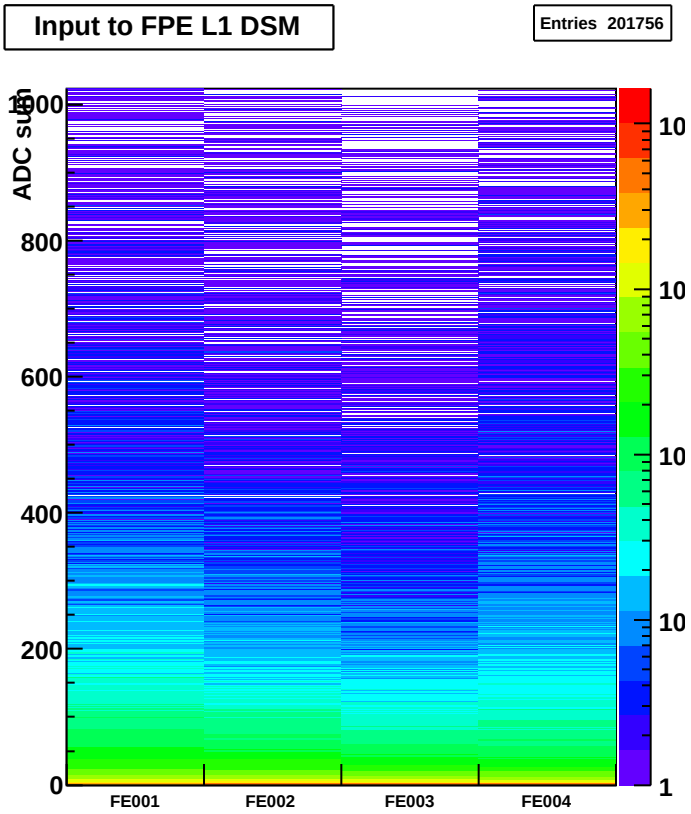

Input to FPD L2 DSM
Entries 201756
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

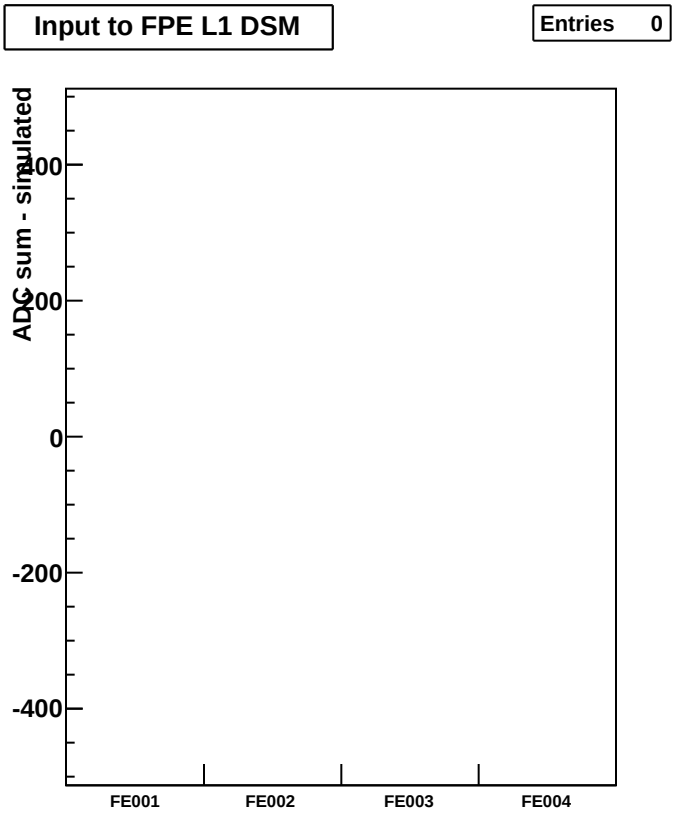

Input to FE001 QT board
Entries1614048
ADC
channel

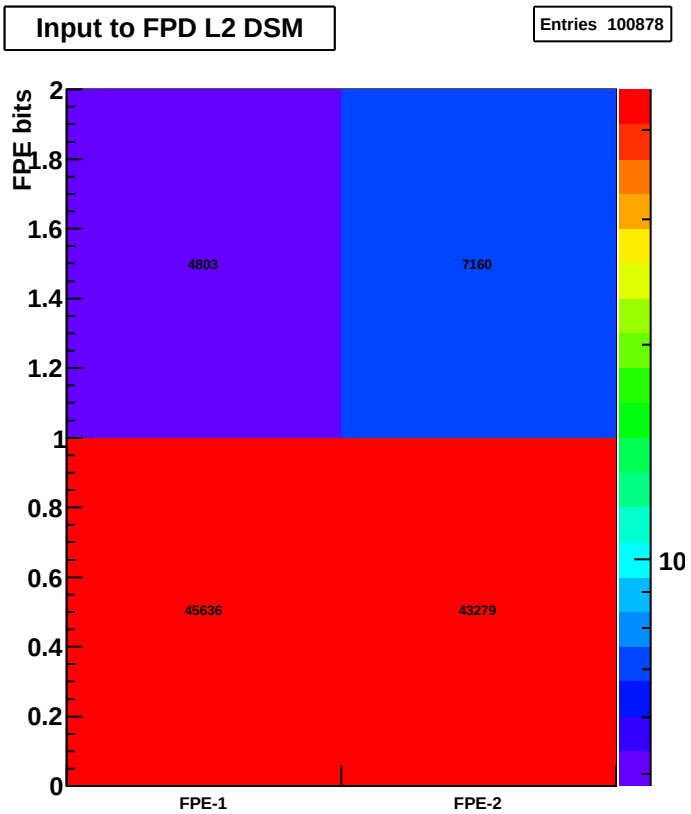

Input to FE002 QT board
Entries1614048
ADC
channel

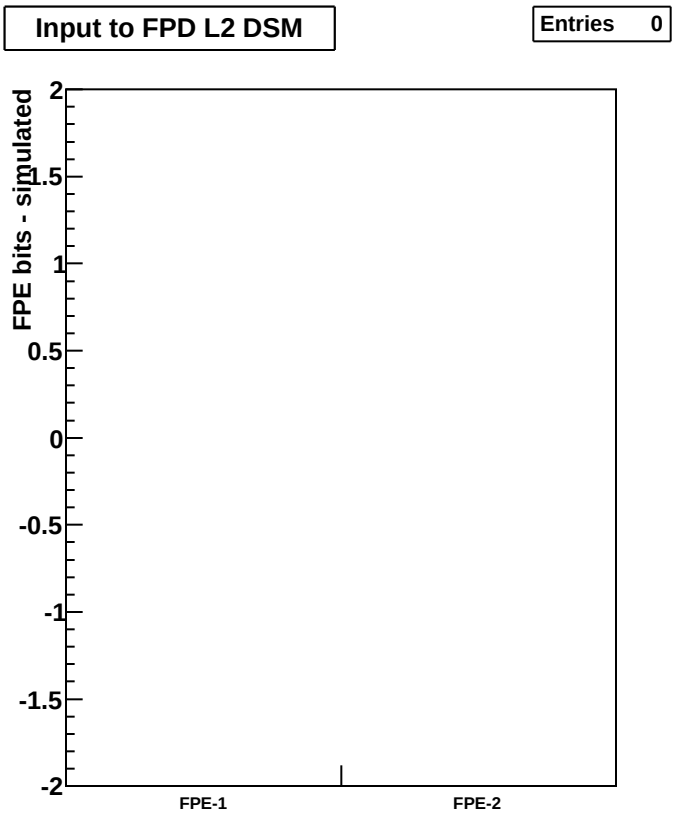

Input to FE003 QT board
Entries1614048
ADC
channel


Input to FE004 QT board
Entries1614048
ADC
channel


Input to FPE L1 DSM
Entries 201756
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 100878
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
4803
7160
45636
43279
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2