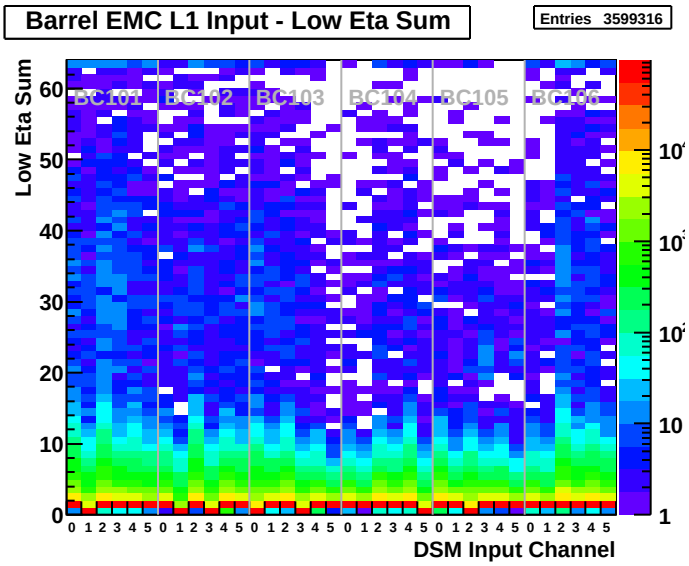
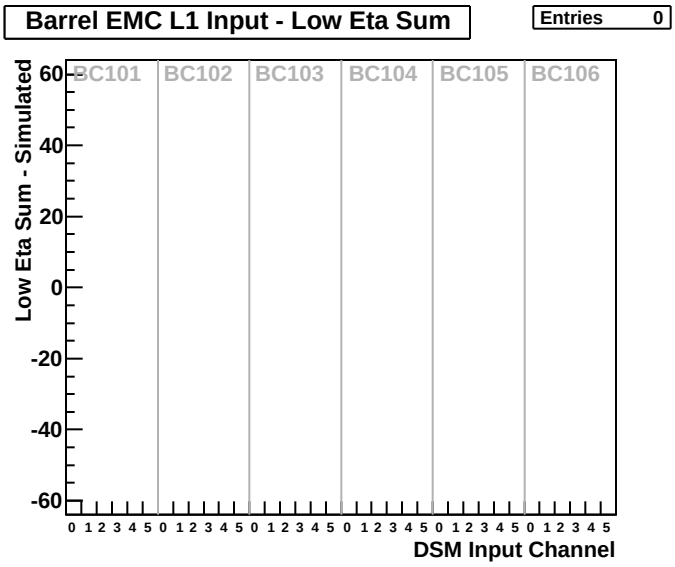
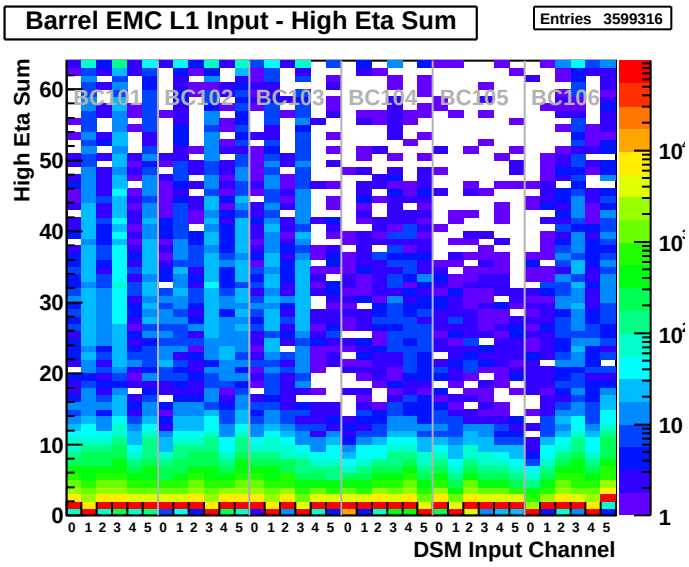
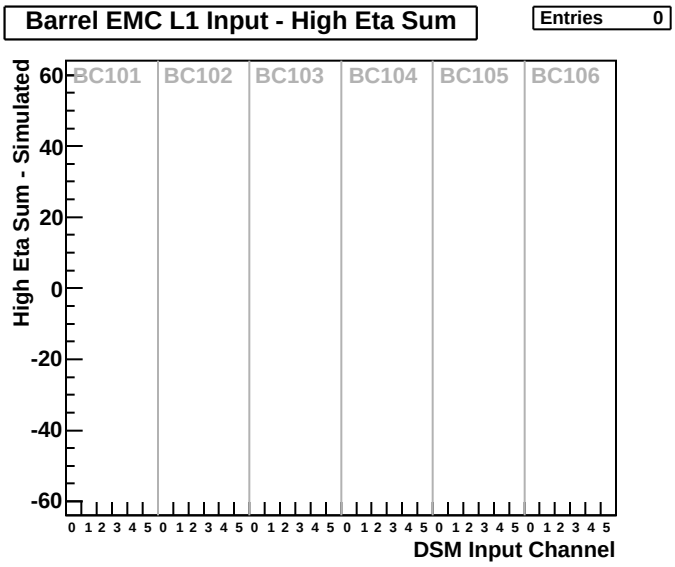
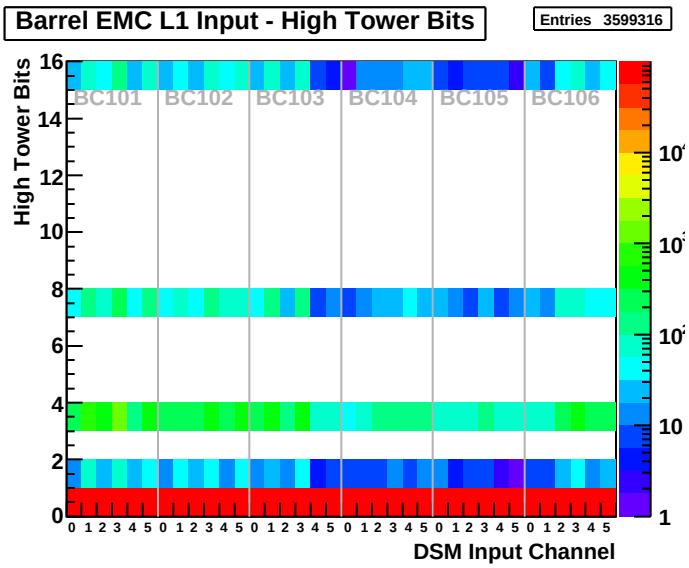
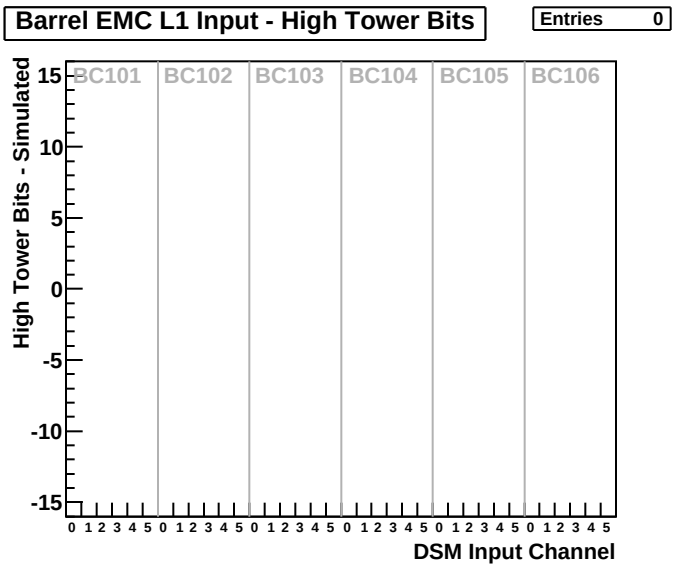

Barrel EMC L1 Input - Low Eta Sum
Entries 3599316
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

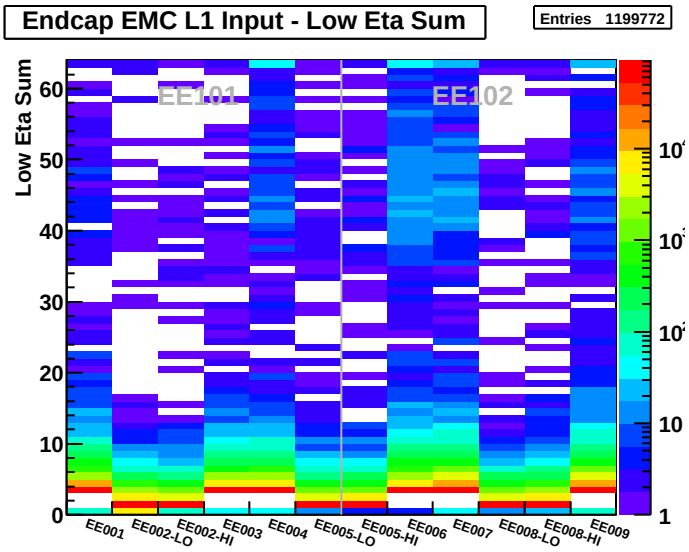

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

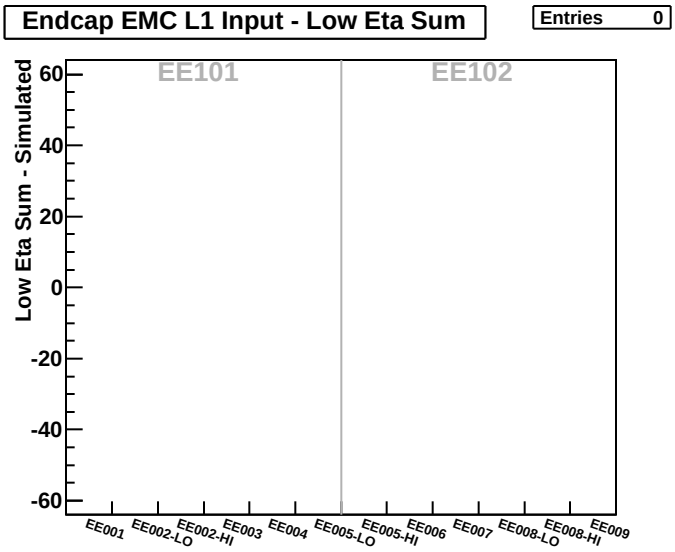

Barrel EMC L1 Input - High Eta Sum
Entries 3599316
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

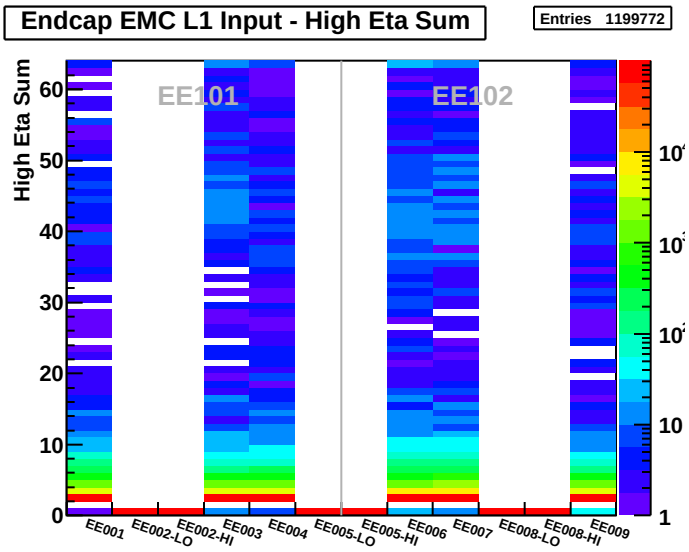

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

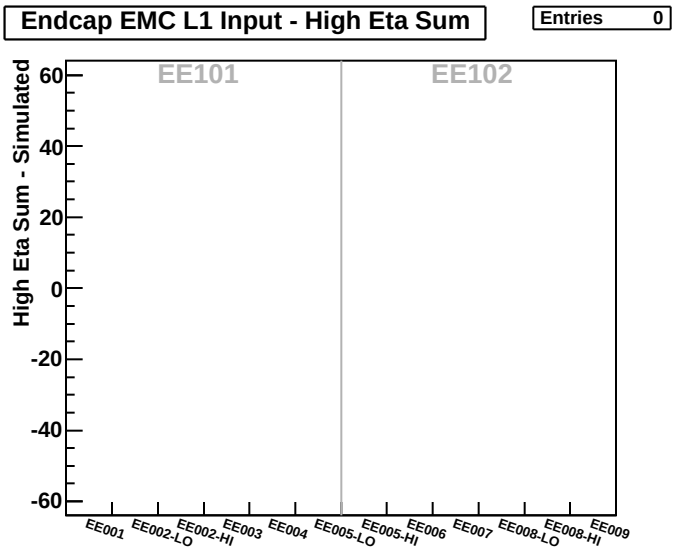

Barrel EMC L1 Input - High Tower Bits
Entries 3599316
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

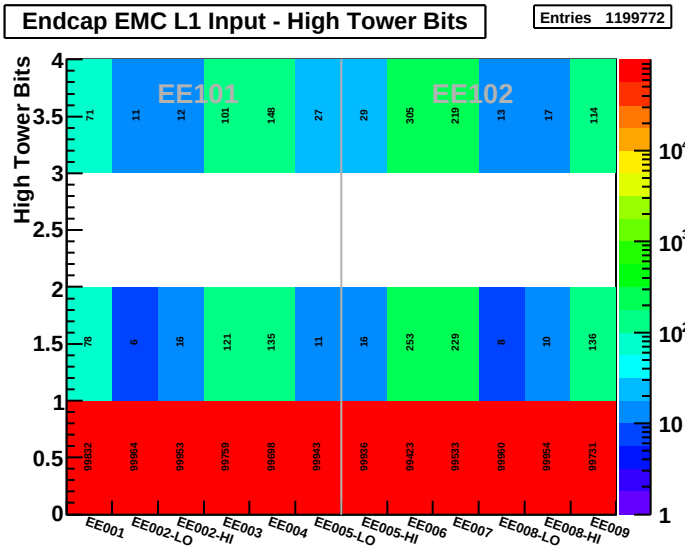

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

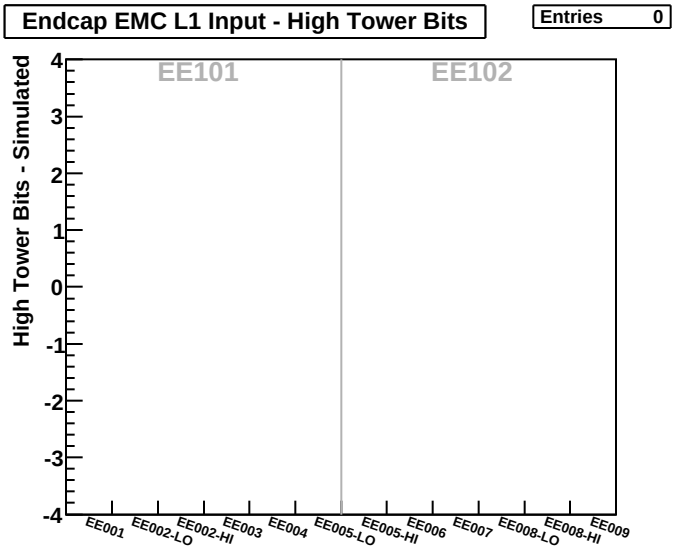

Endcap EMC L1 Input - Low Eta Sum
Entries 1199772
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

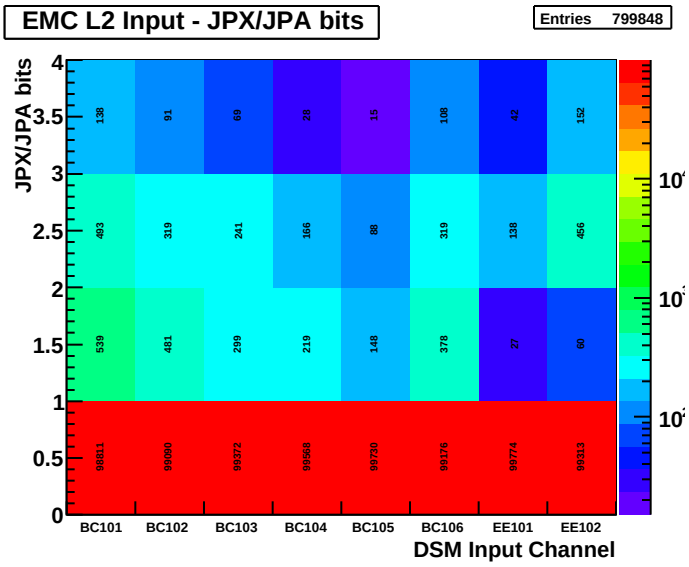

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

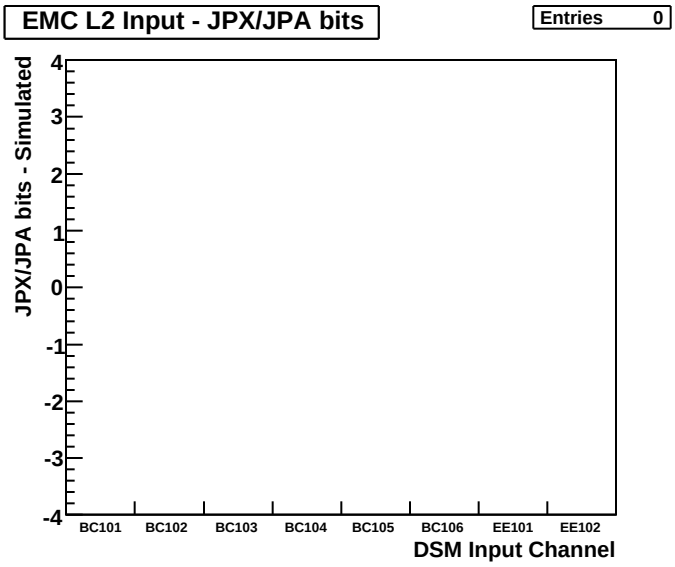

Endcap EMC L1 Input - High Eta Sum
Entries 1199772
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

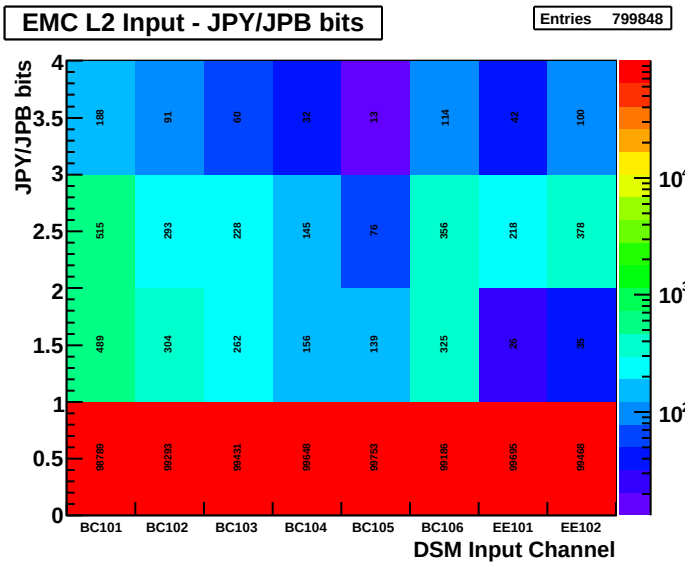

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

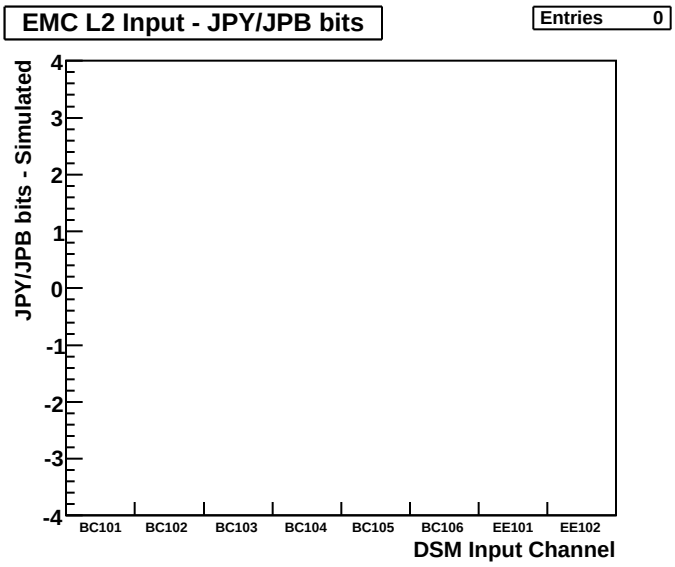

Endcap EMC L1 Input - High Tower Bits
Entries 1199772
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

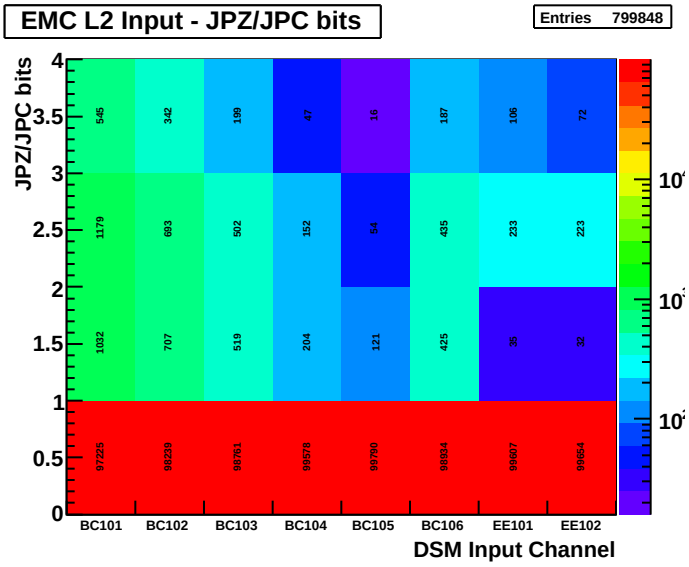

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

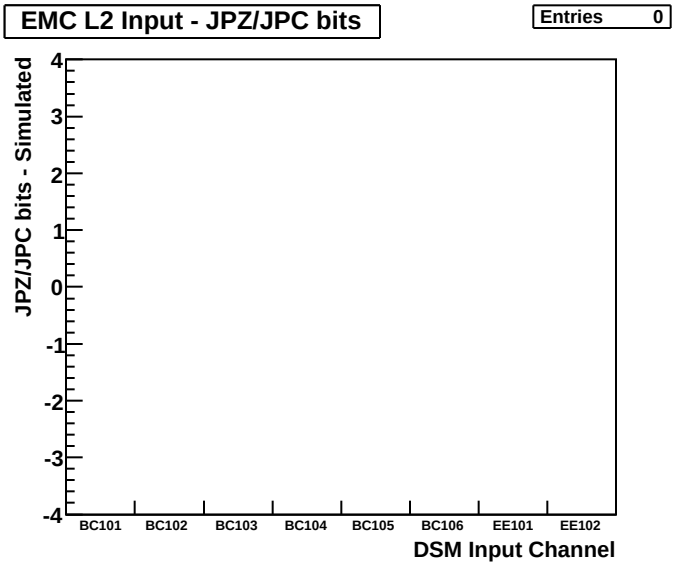

EMC L2 Input - JPX/JPA bits
Entries 799848
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
138 91 69 28 15 108 42 152
493 319 241 166 88 319 138 466
539 481 299 219 148 378 27 60
98611 99080 99372 99568 99730 99176 99774 99313

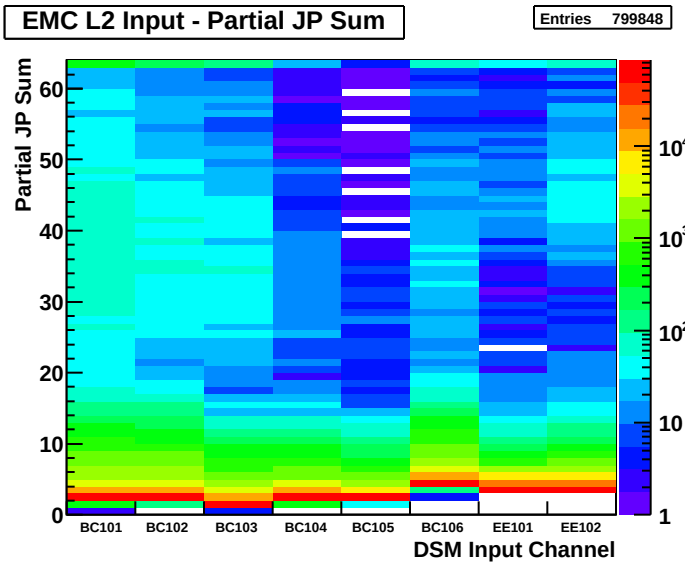

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

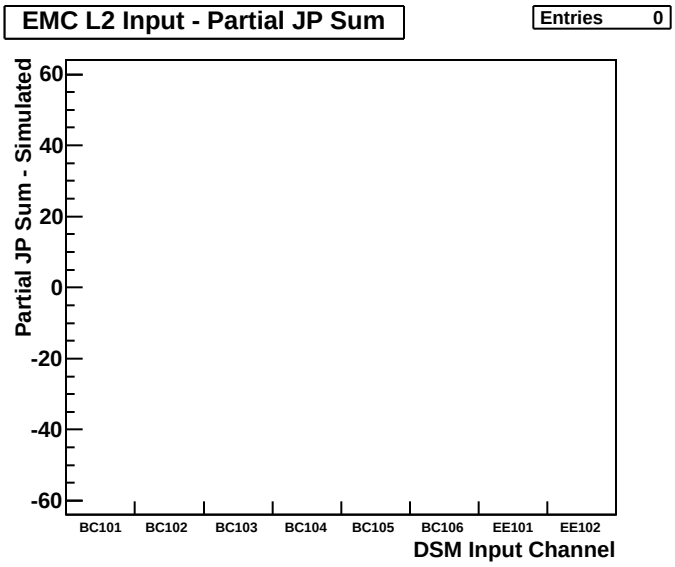

EMC L2 Input - JPY/JPB bits
Entries 799848
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
188 91 60 32 13 114 42 100
515 293 228 145 76 356 218 378
489 304 262 156 139 325 26 35
98789 99293 99431 99648 99753 99186 99695 99468

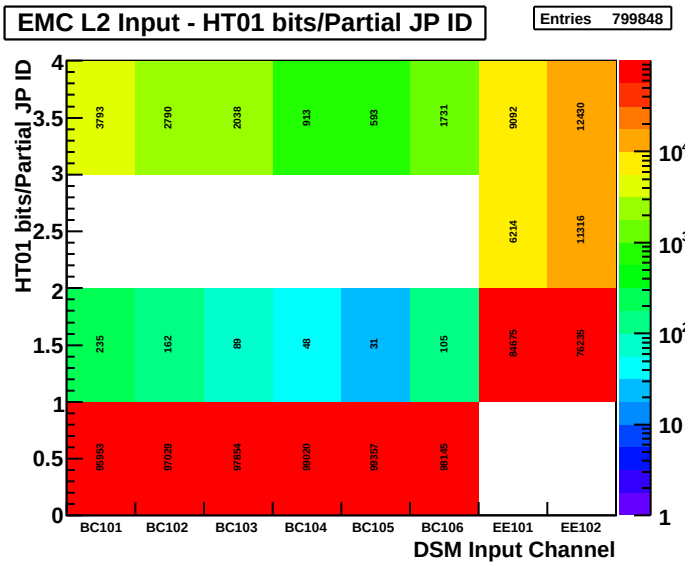

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

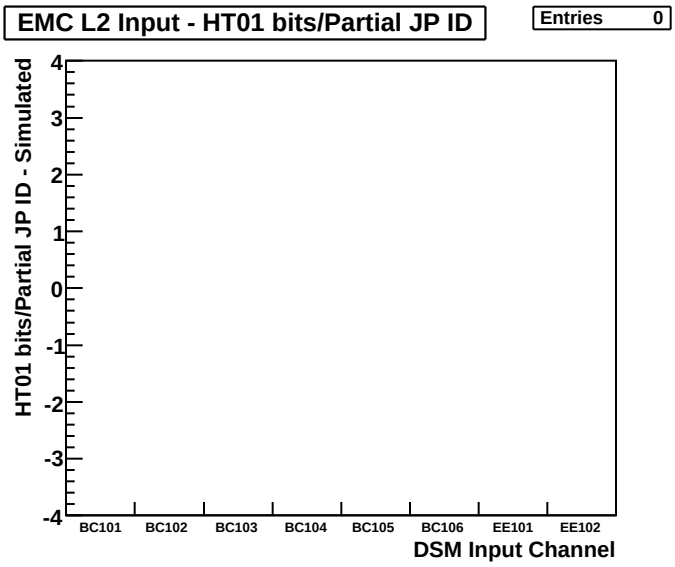

EMC L2 Input - JPZ/JPC bits
Entries 799848
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
545 342 199 47 16 187 106 72
1179 693 502 152 54 435 233 223
1082 707 519 204 121 425 35 32
97225 98229 98761 99578 99790 98934 99607 99654

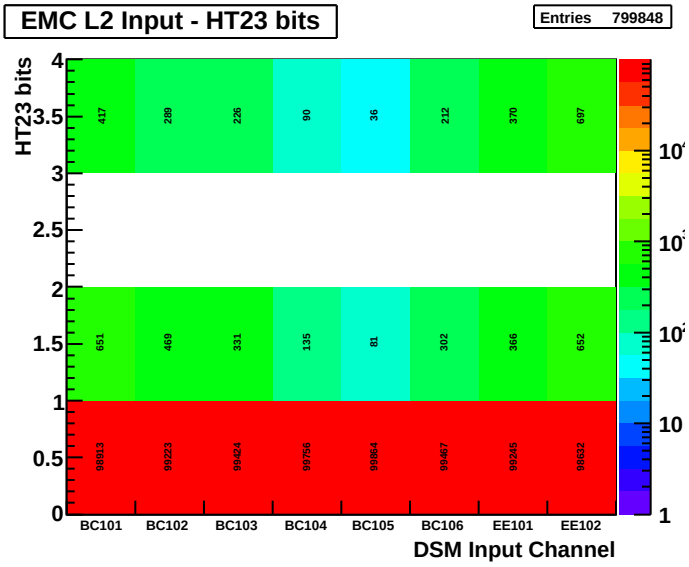

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

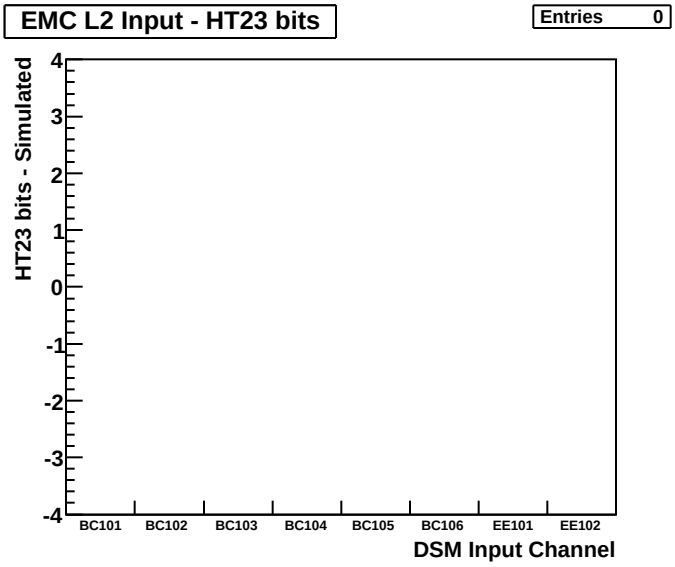

EMC L2 Input - Partial JP Sum
Entries 799848
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

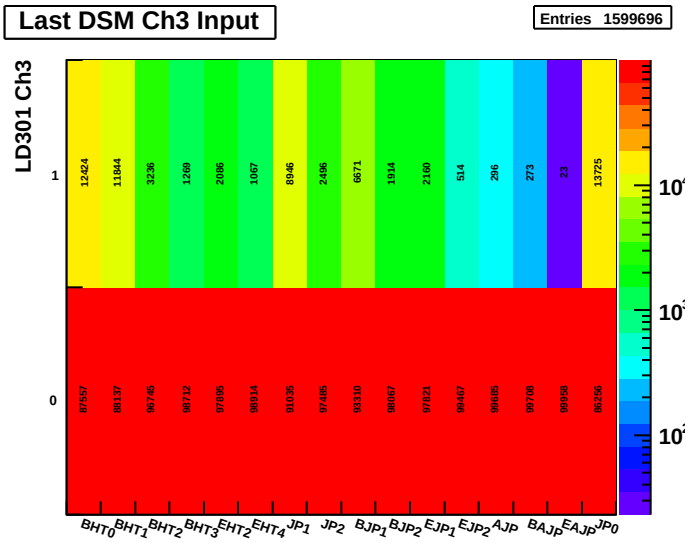

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

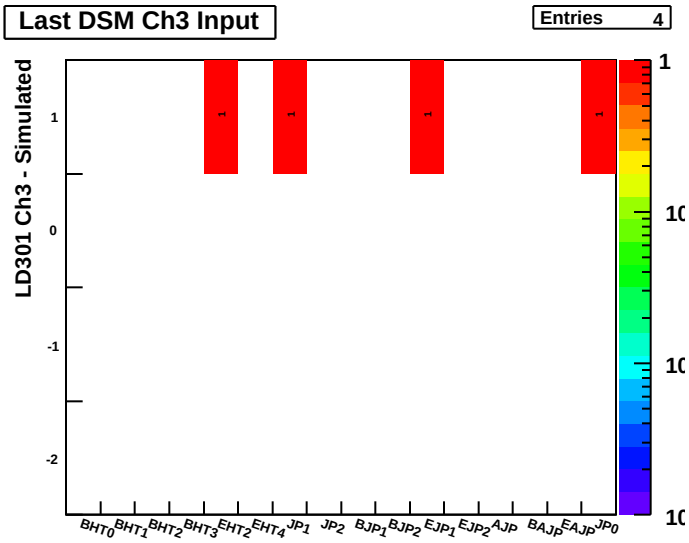

EMC L2 Input - HT01 bits/Partial JP ID
Entries 799848
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

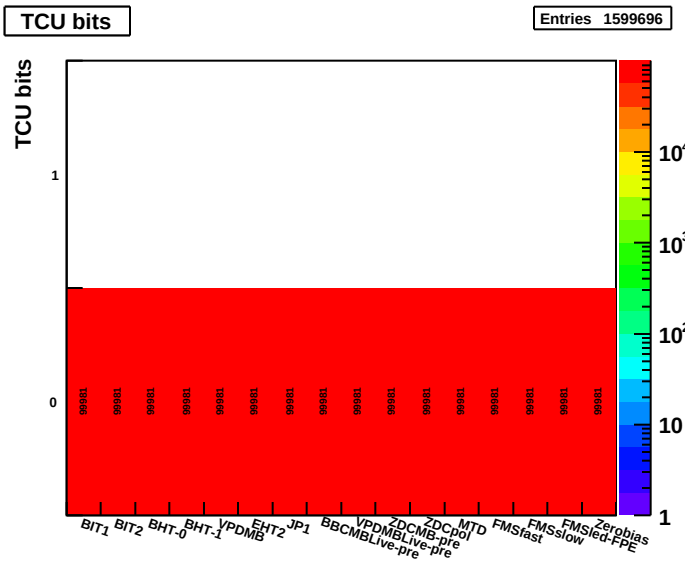

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

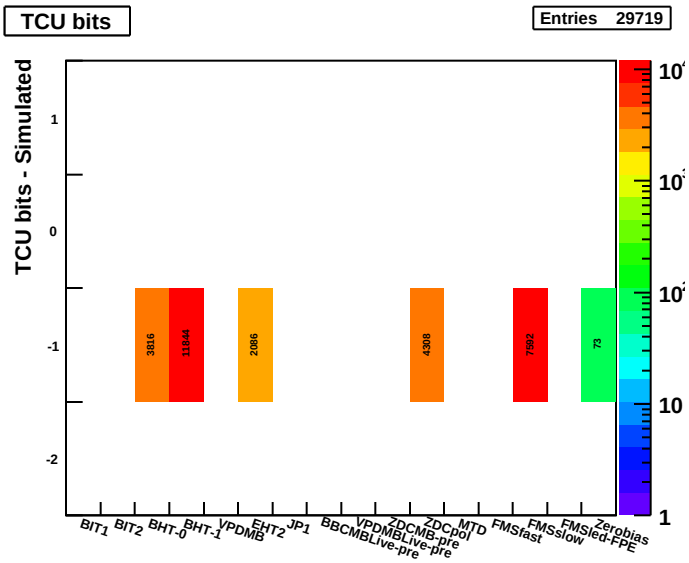

EMC L2 Input - HT23 bits
Entries 799848
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

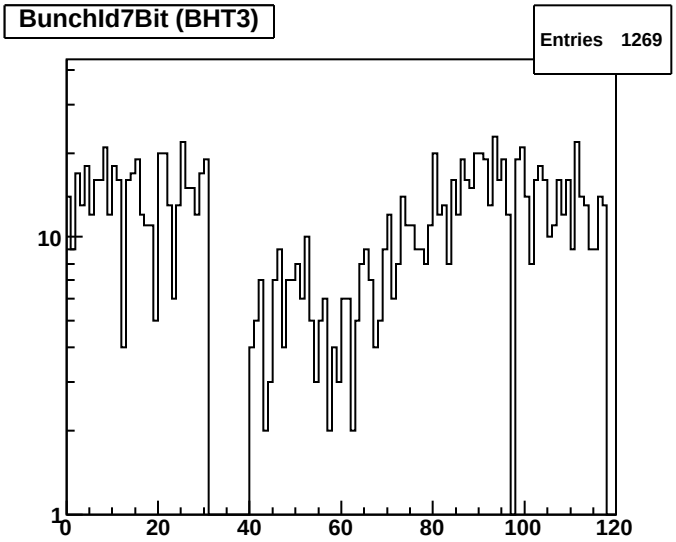

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

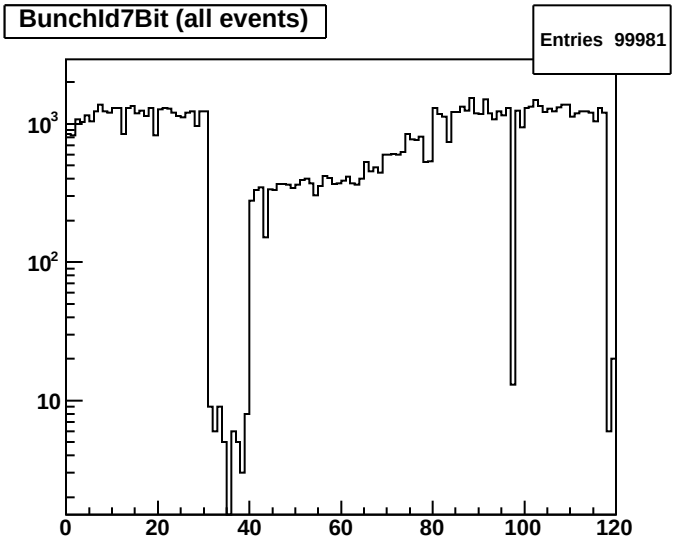

Last DSM Ch3 Input
Entries 1599696
LD301 Ch3
12424 11944 3236 1269 2086 1067 8946 2496 6671 1914 2190 514 296 273 23 13725
87557 88137 95745 98712 97895 98914 91035 97485 93310 98067 97821 98467 99685 98708 99958 86256
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

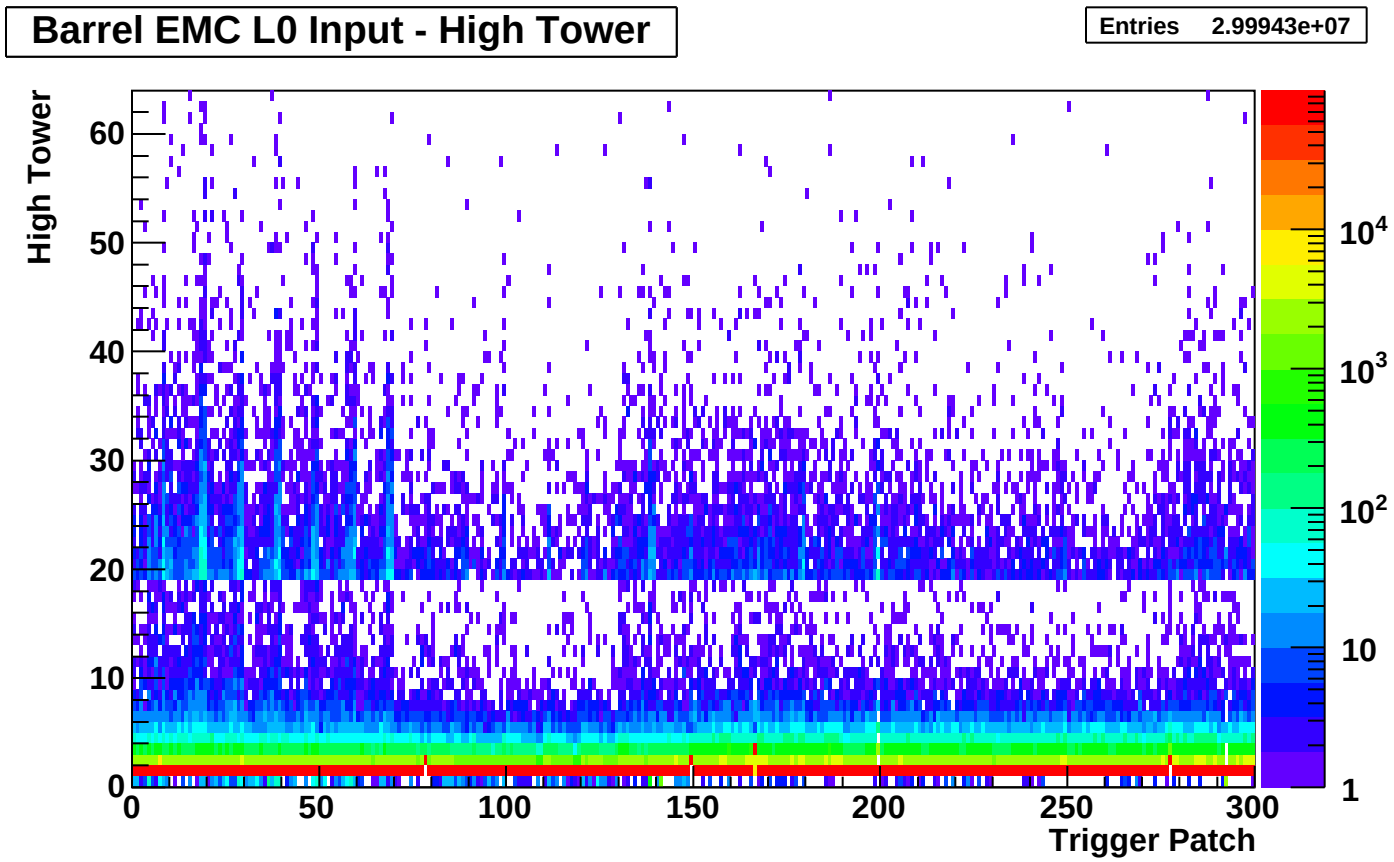

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
1 1 1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

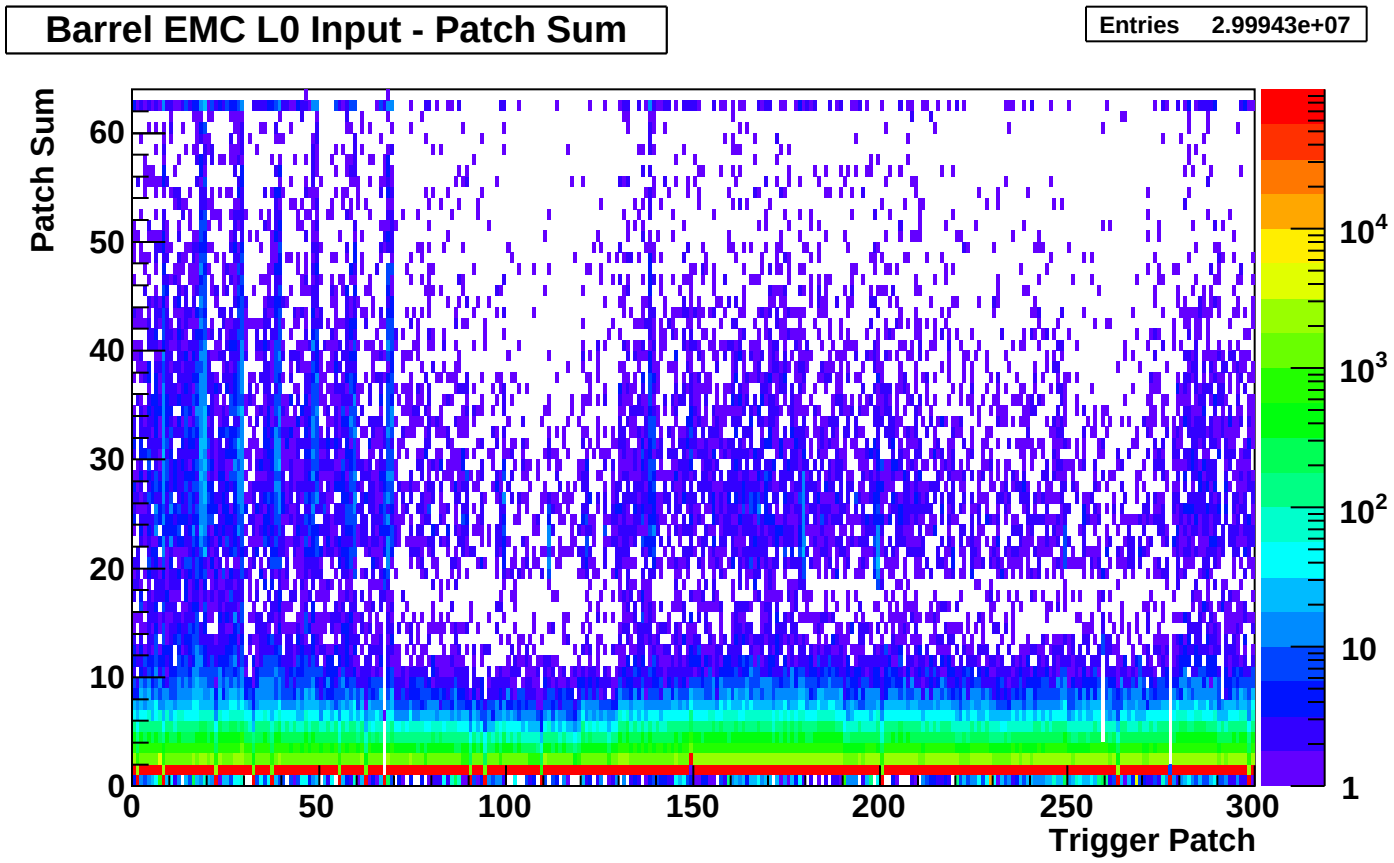

TCU bits
Entries 1599696
TCU bits
99981 99981 99981 99981 99981 99981 99981 99981 99981 99981 99981 99981 99981 99981 99981 99981
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

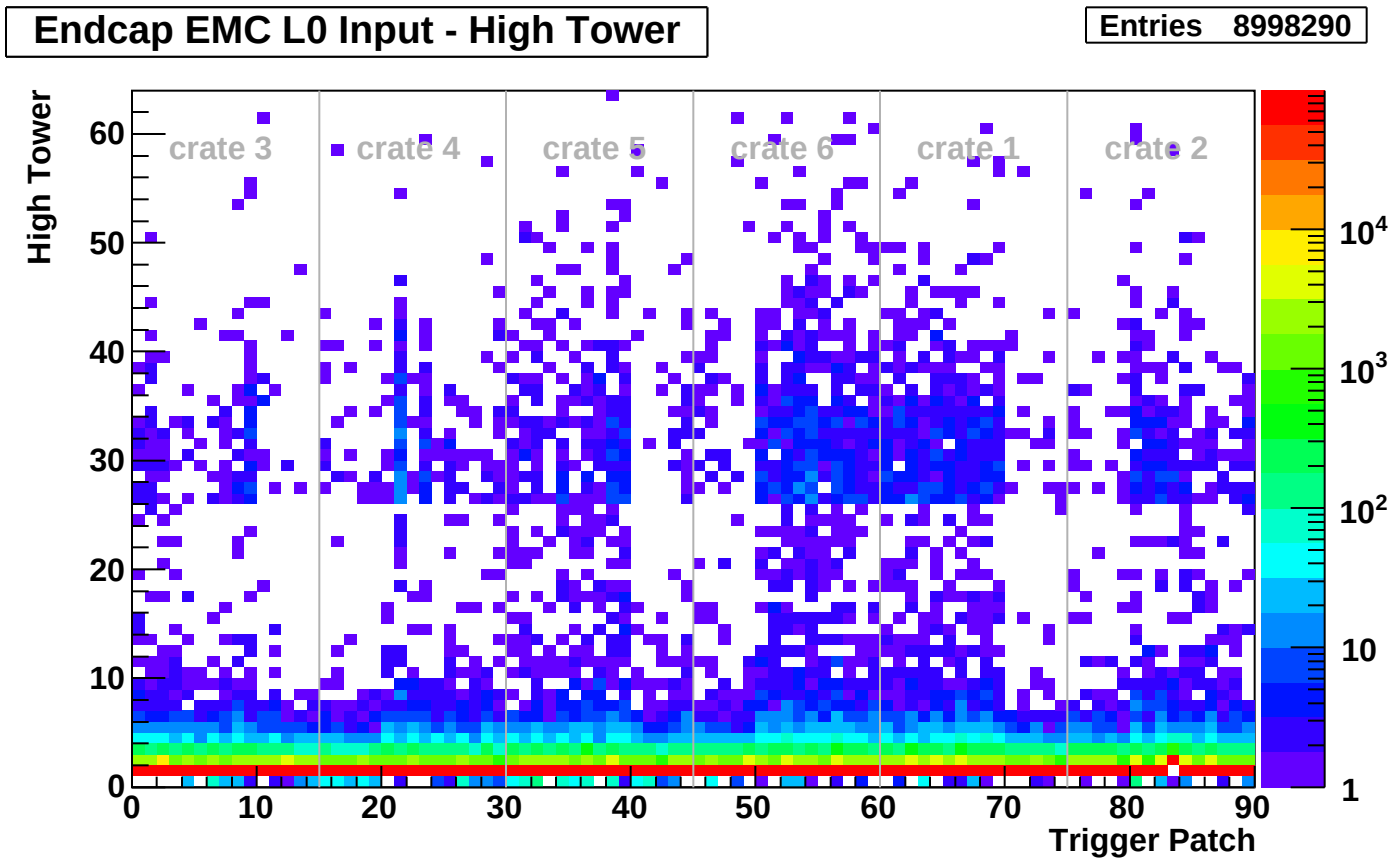

TCU bits
Entries 29719
TCU bits - Simulated
3816 11844 2086 4308 7892 73
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

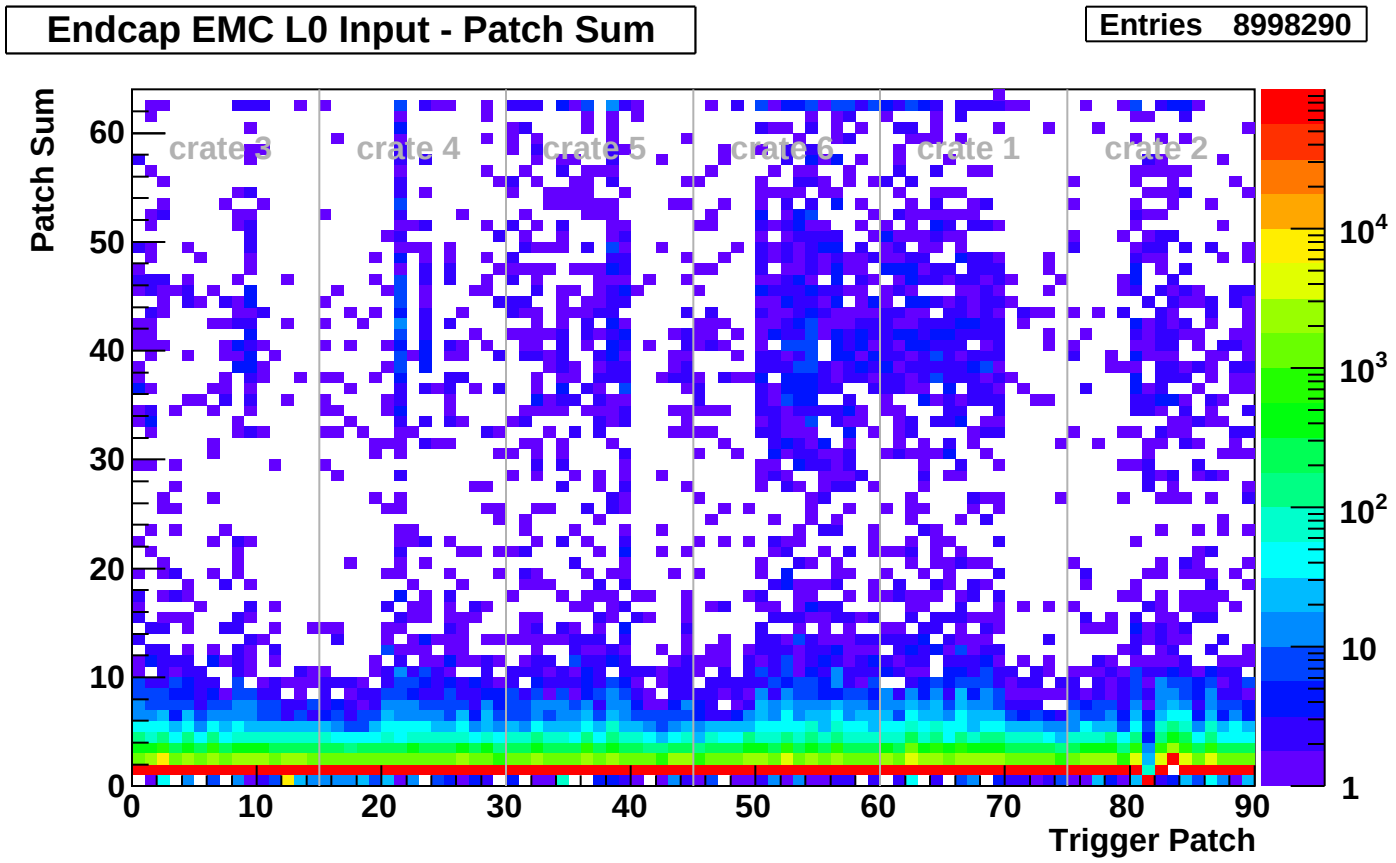

BunchId7Bit (BHT3)
Entries 1269

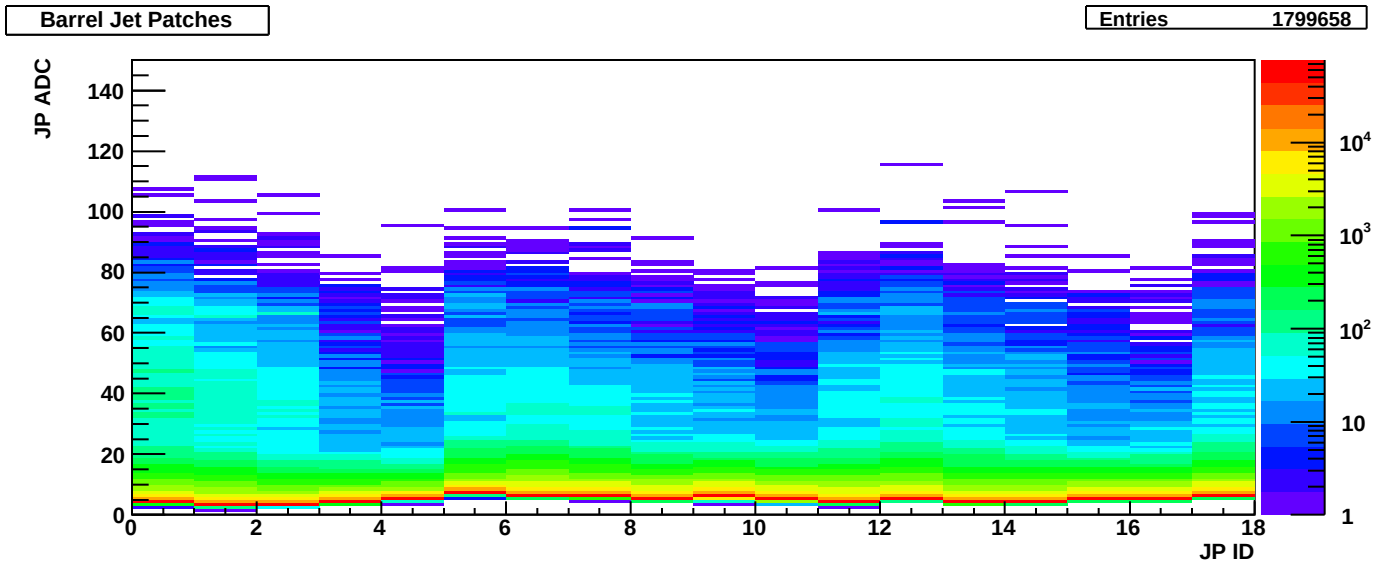

BunchId7Bit (all events)
Entries 99981

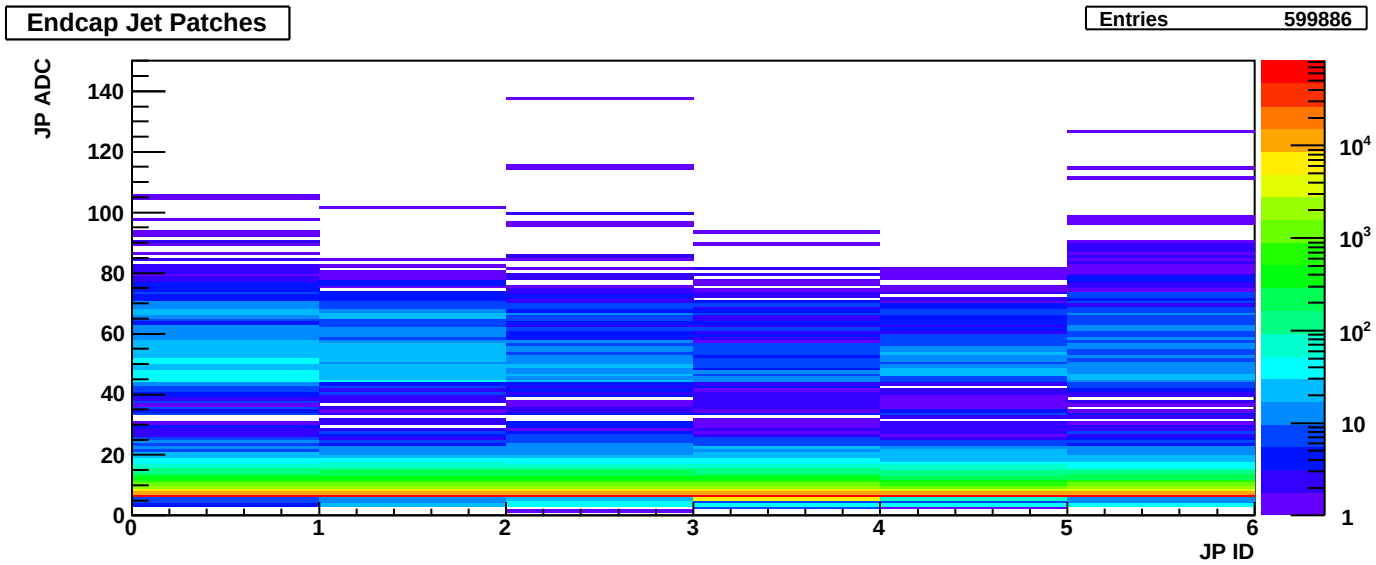

Barrel EMC L0 Input - High Tower
Entries 2.99943e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

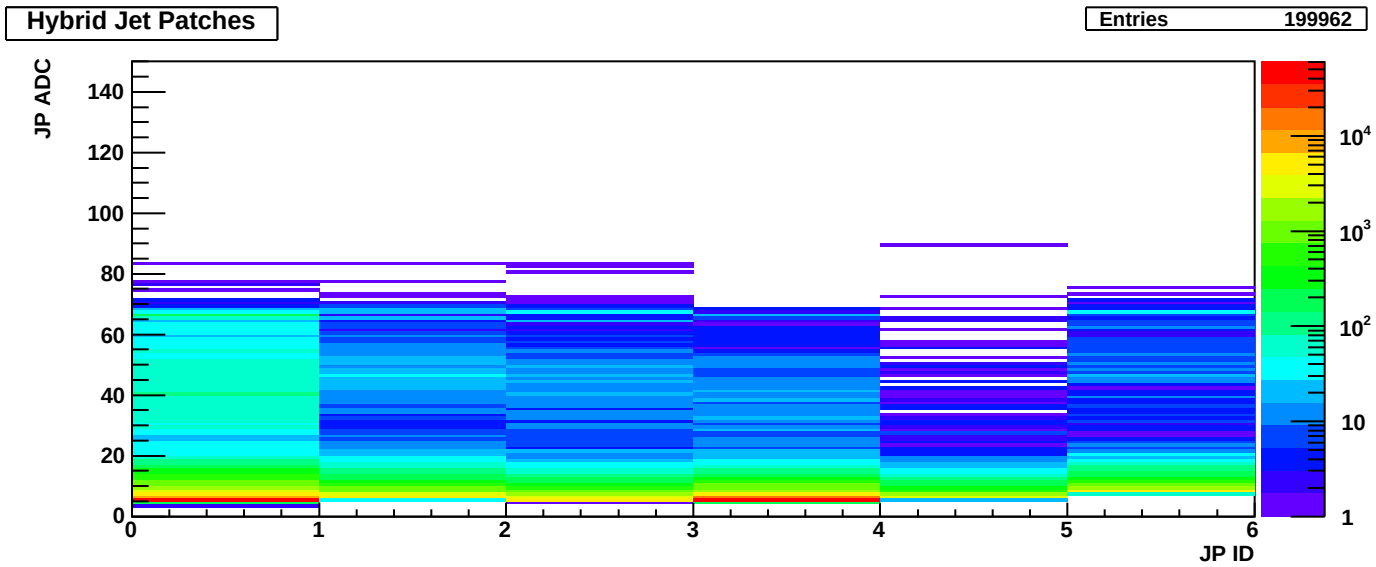

Barrel EMC L0 Input - Patch Sum
Entries 2.99943e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$

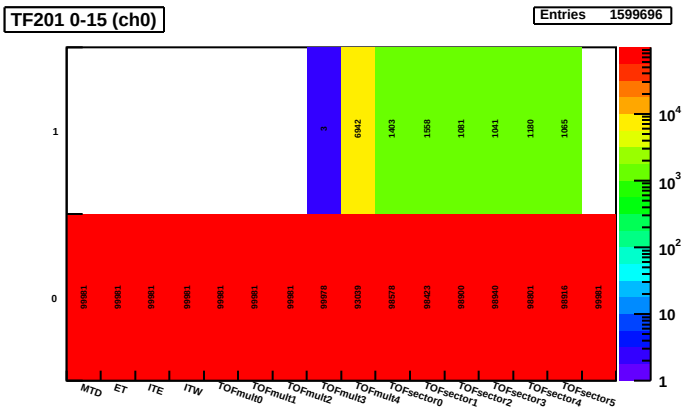

Endcap EMC L0 Input - High Tower
Entries 8998290
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

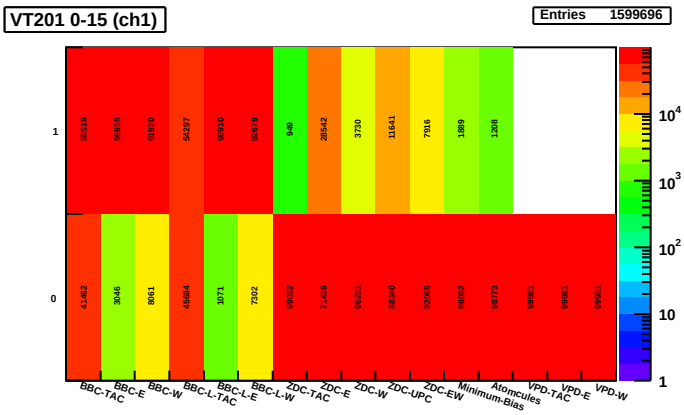

Endcap EMC L0 Input - Patch Sum
Entries 8998290
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

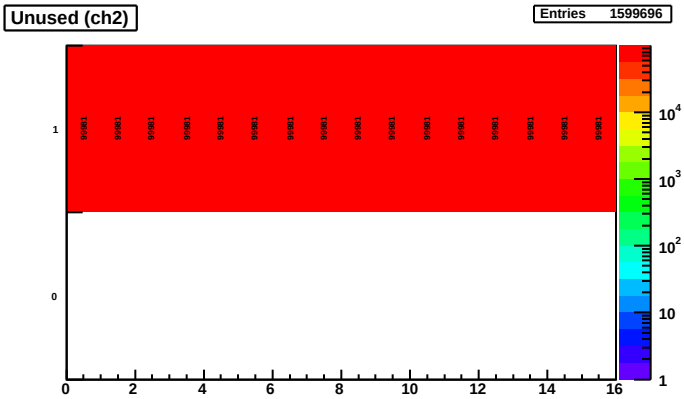

Barrel Jet Patches
Entries 1799658
JP ADC
JP ID

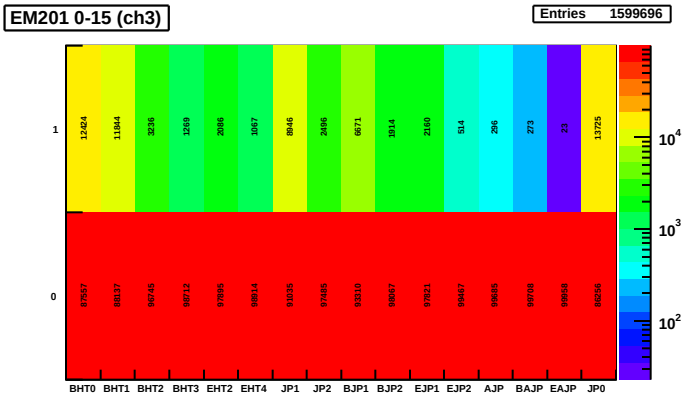

Endcap Jet Patches
Entries 599886
JP ADC
JP ID

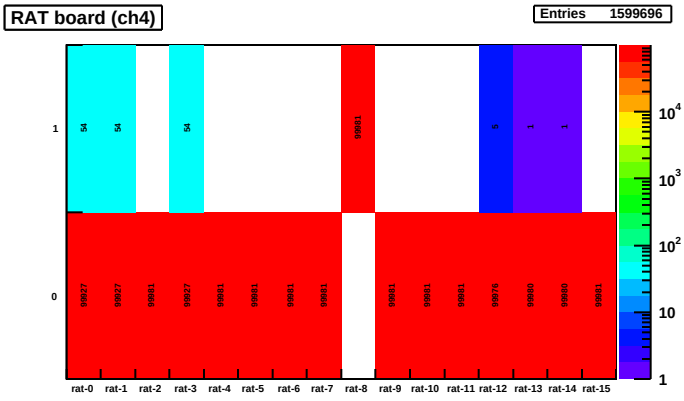

Hybrid Jet Patches
Entries 199962
JP ADC
JP ID

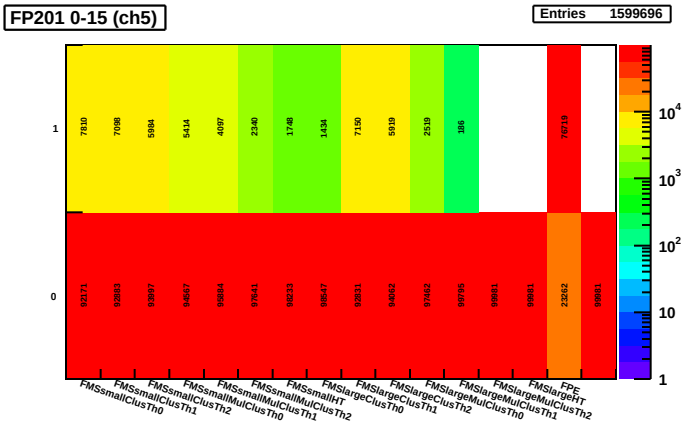

TF201 0-15 (ch0)
Entries 1599696
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

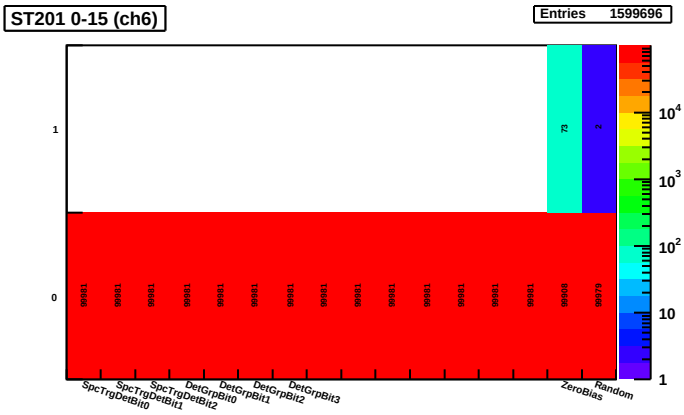

VT201 0-15 (ch1)
Entries 1599696
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

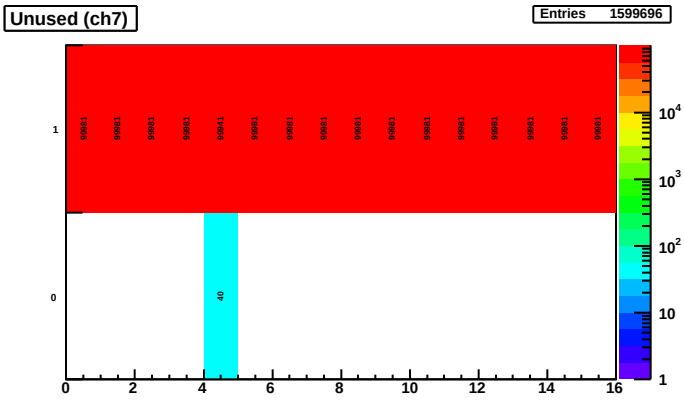

Unused (ch2)
Entries 1599696

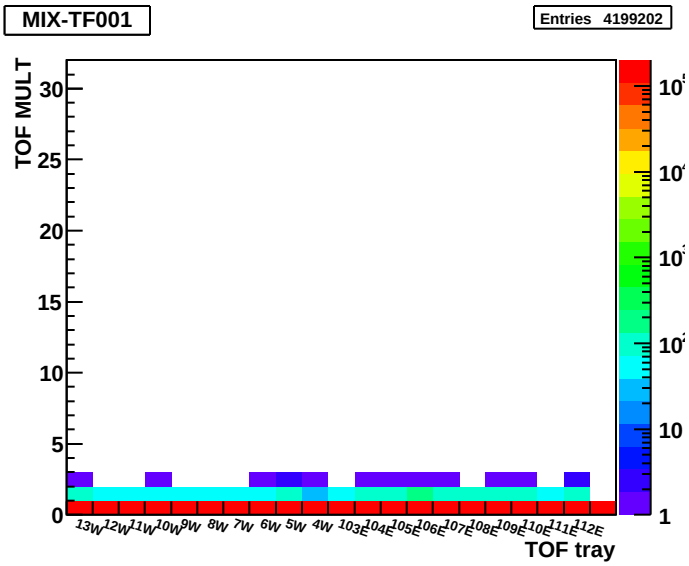

EM201 0-15 (ch3)
Entries 1599696
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

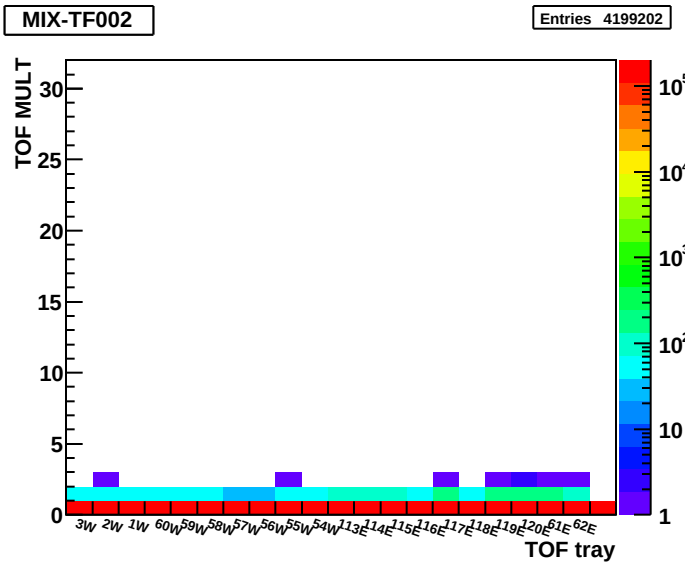

RAT board (ch4)
Entries 1599696
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

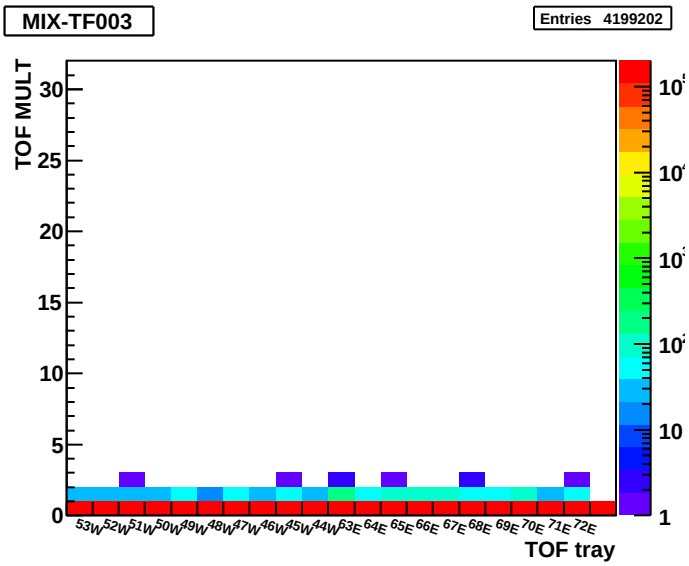

FP201 0-15 (ch5)
Entries 1599696
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

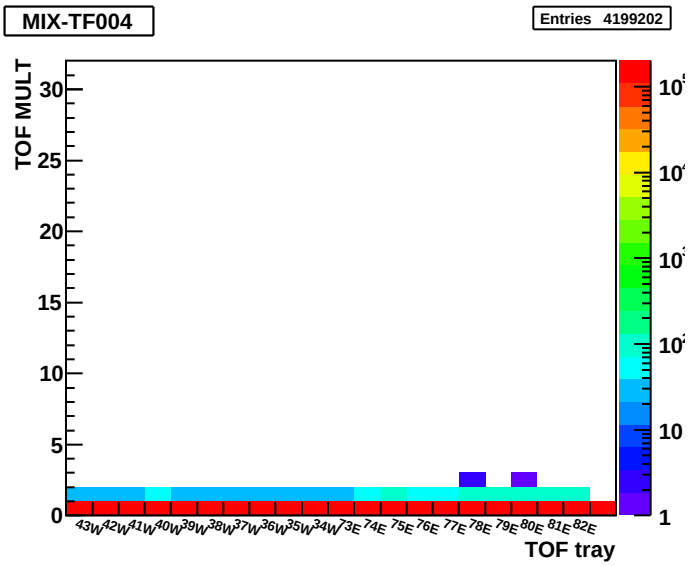

ST201 0-15 (ch6)
Entries 1599696
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

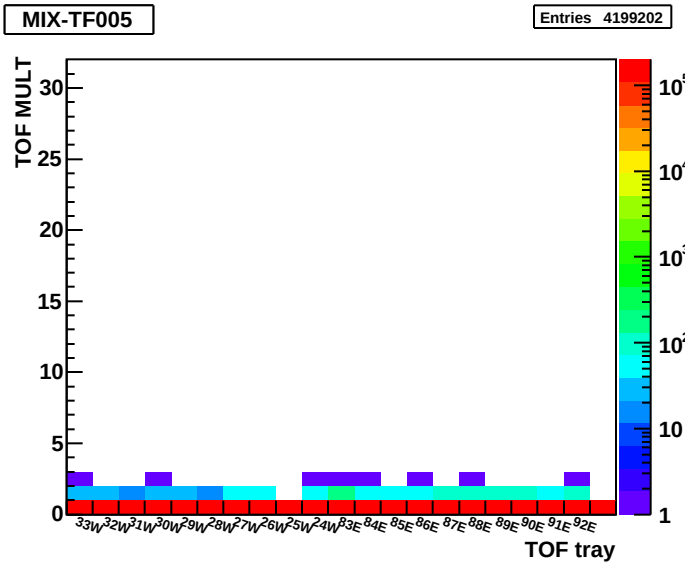

Unused (ch7)
Entries 1599696

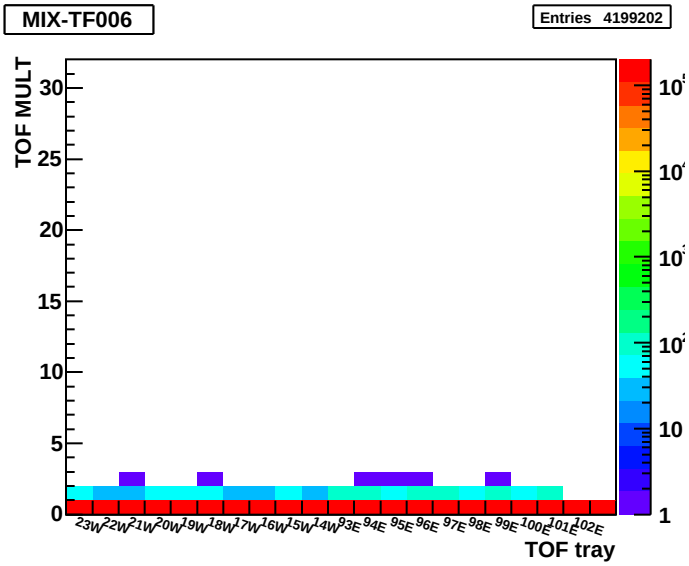

MIX-TF001
Entries 4199202
TOF MULT
TOF tray

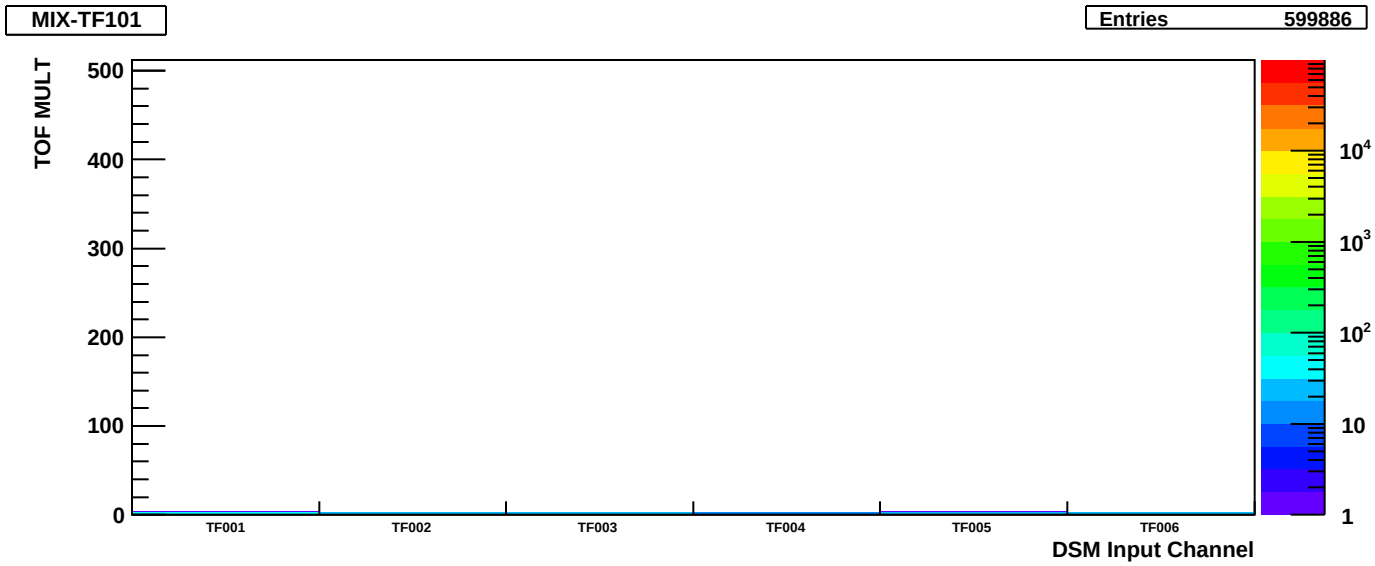

MIX-TF002
Entries 4199202
TOF MULT
TOF tray

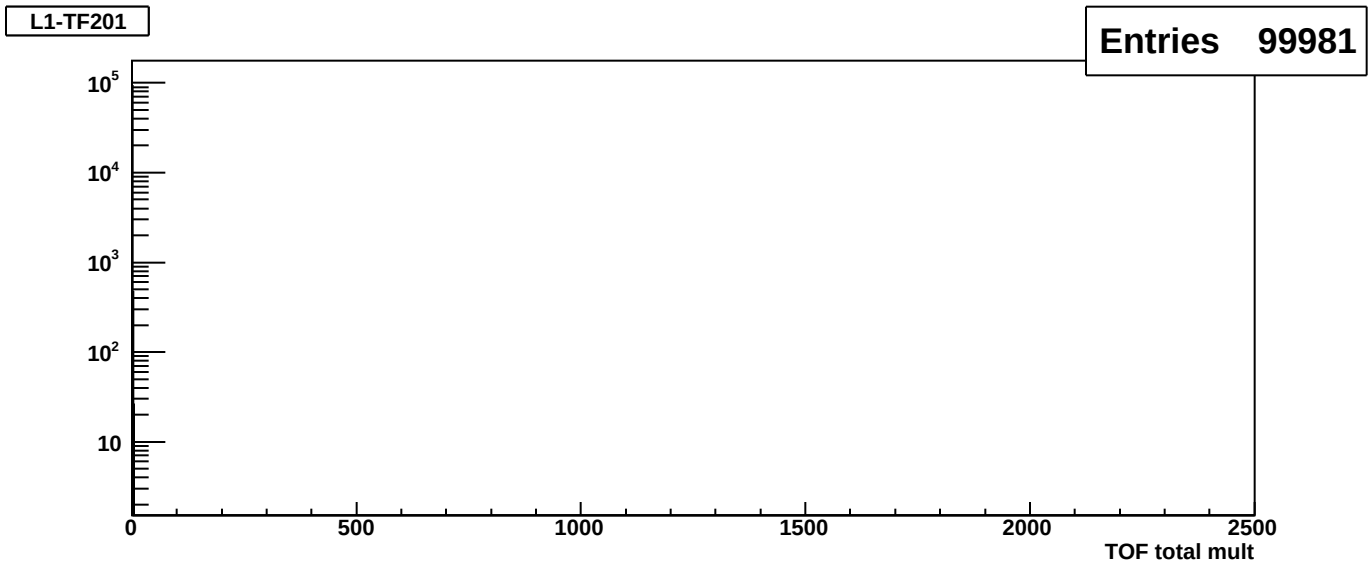

MIX-TF003
Entries 4199202
TOF MULT
TOF tray

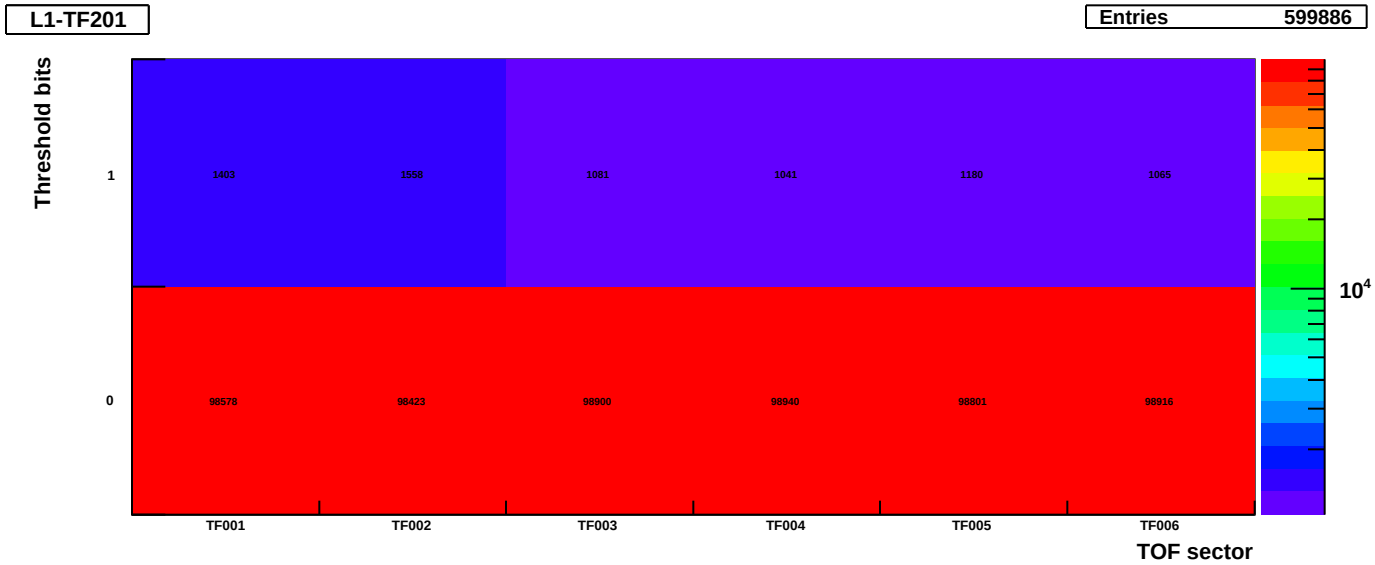

MIX-TF004
Entries 4199202
TOF MULT
TOF tray

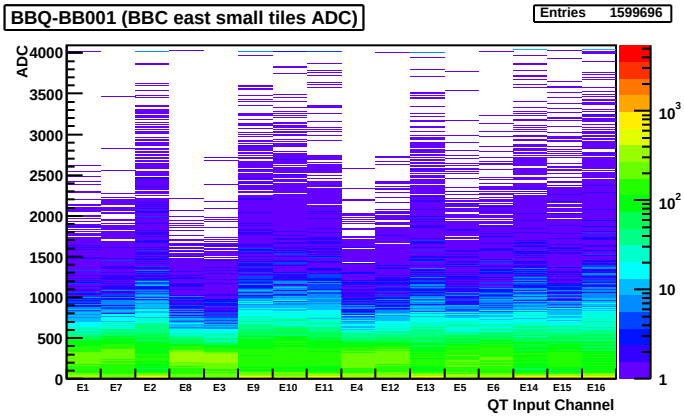

MIX-TF005
Entries 4199202
TOF MULT
TOF tray

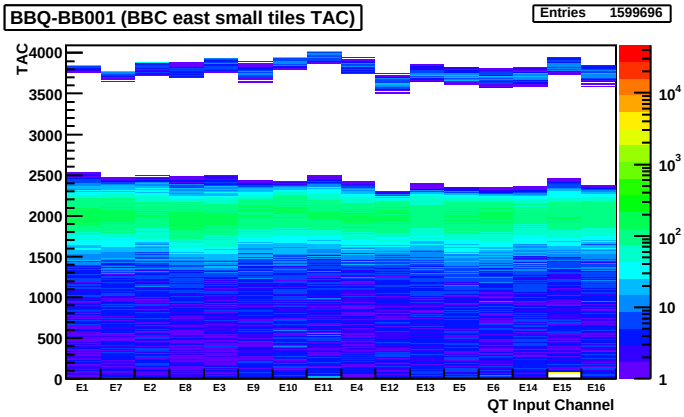

MIX-TF006
Entries 4199202
TOF MULT
TOF tray

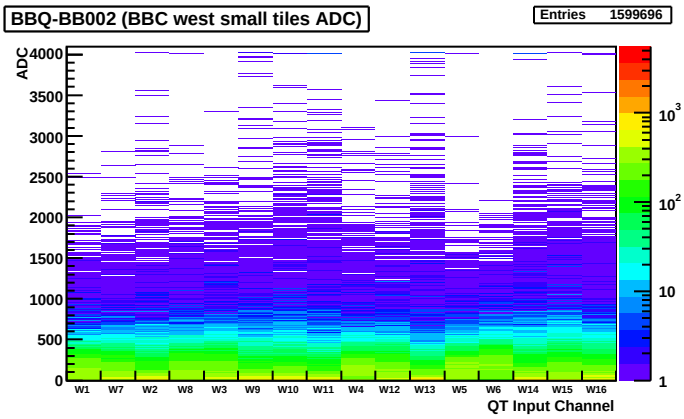

MIX-TF101
Entries 599886
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^4$
$10^3$
$10^2$
10
1

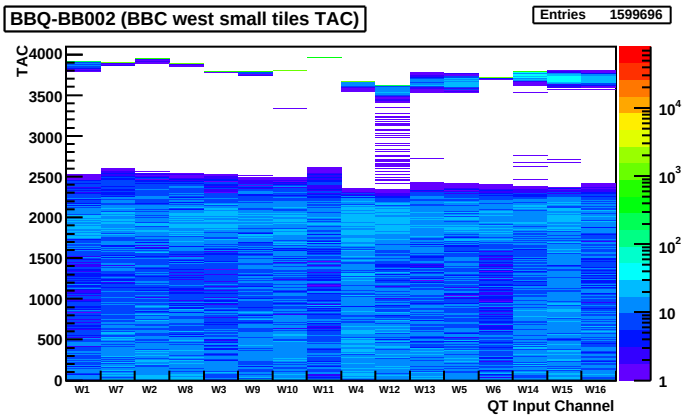

L1-TF201
Entries 99981
$10^5$
$10^4$
$10^3$
$10^2$
10
0
500
1000
1500
2000
2500
TOF total mult

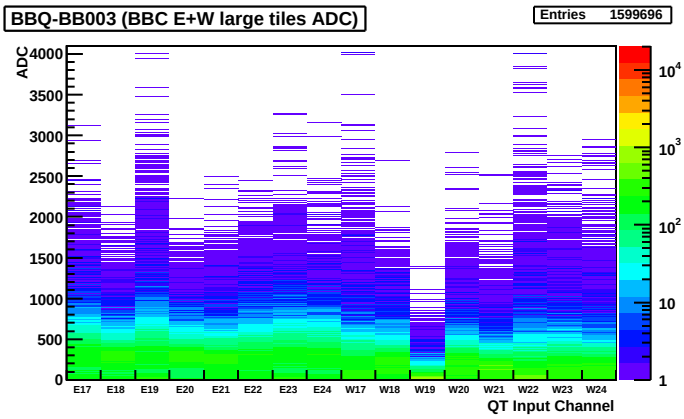

L1-TF201
Entries 599886
Threshold bits
1
0
1403
1558
1081
1041
1180
1065
98578
98423
98900
98940
98801
98916
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
$10^4$

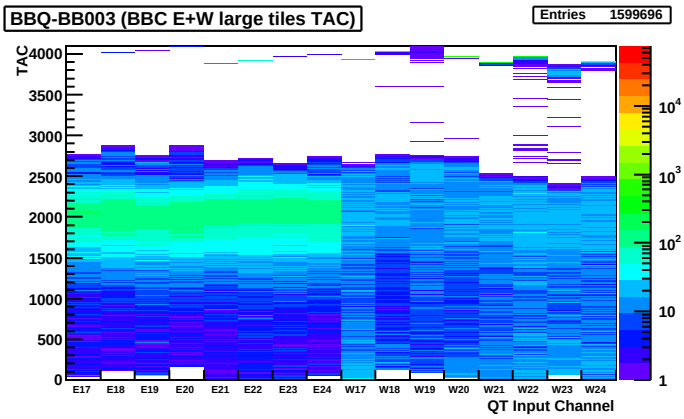

BBQ-BB001 (BBC east small tiles ADC)
Entries 1599696
ADC
QT Input Channel

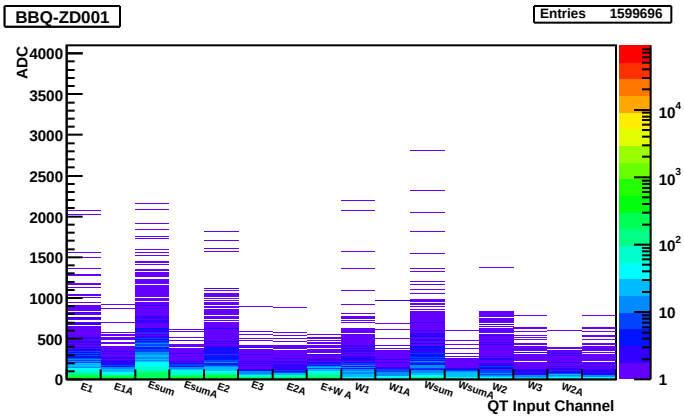

BBQ-BB001 (BBC east small tiles TAC)
Entries 1599696
TAC
QT Input Channel

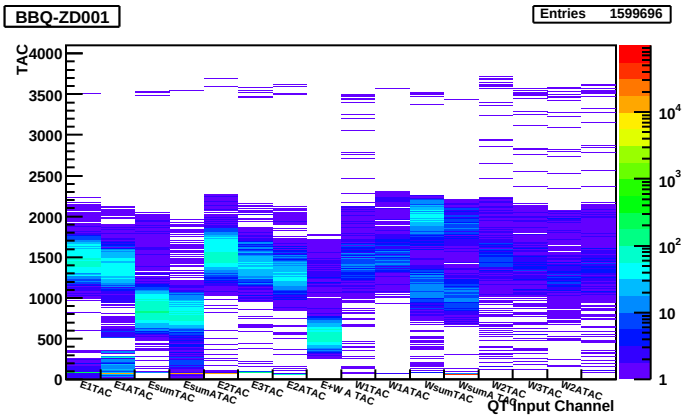

BBQ-BB002 (BBC west small tiles ADC)
Entries 1599696
ADC
QT Input Channel

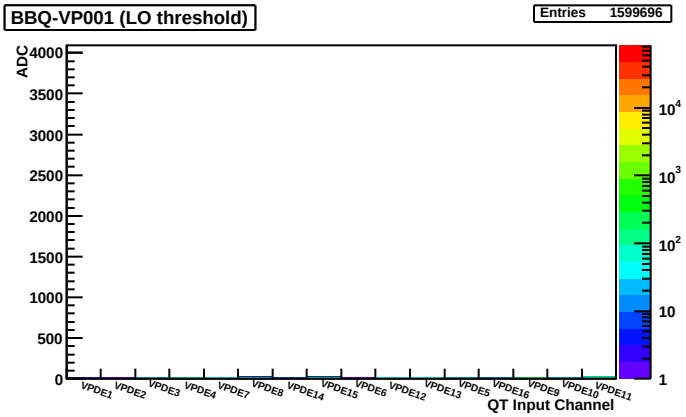

BBQ-BB002 (BBC west small tiles TAC)
Entries 1599696
TAC
QT Input Channel

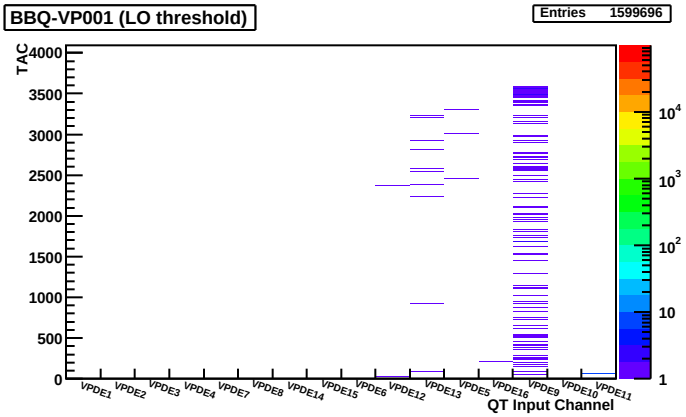

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1599696
ADC
QT Input Channel

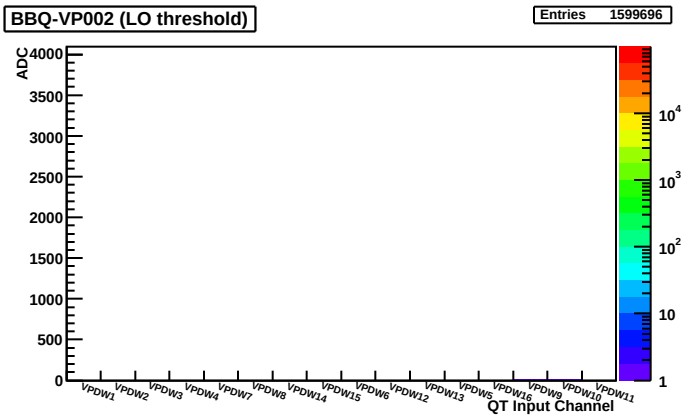

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1599696
TAC
QT Input Channel

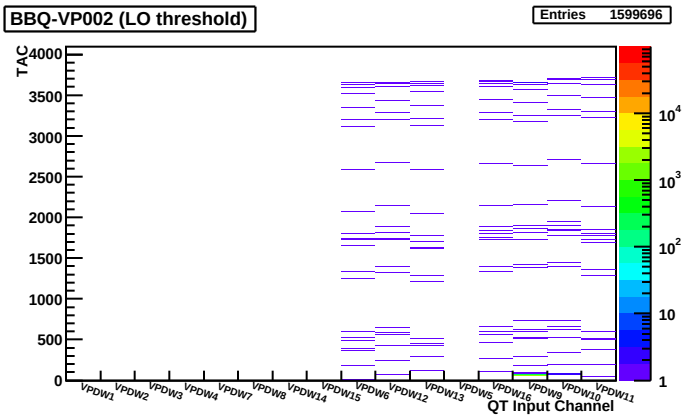

BBQ-ZD001
Entries 1599696
ADC
QT Input Channel

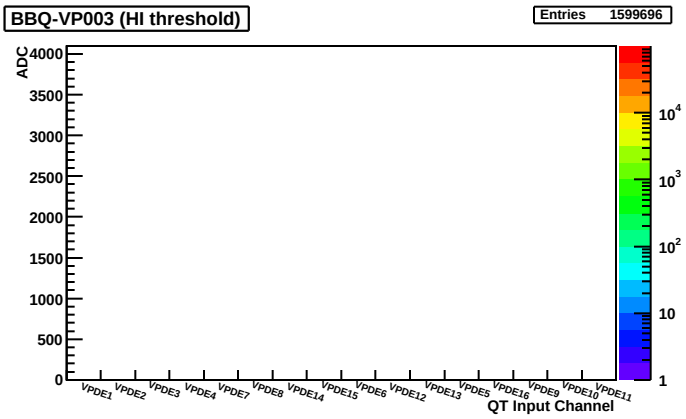

BBQ-ZD001
Entries 1599696
TAC
QT Input Channel

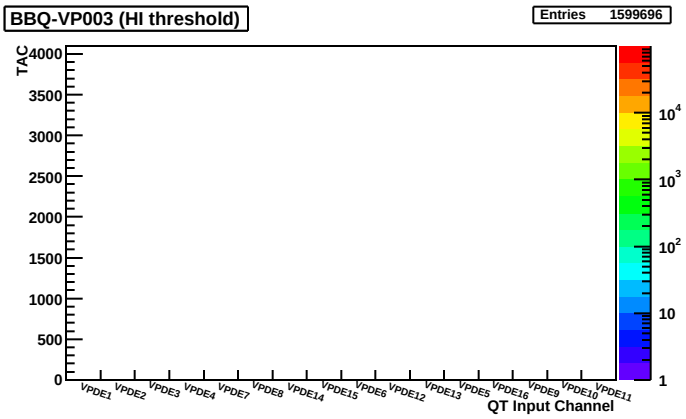

BBQ-VP001 (LO threshold)
Entries 1599696
ADC
QT Input Channel

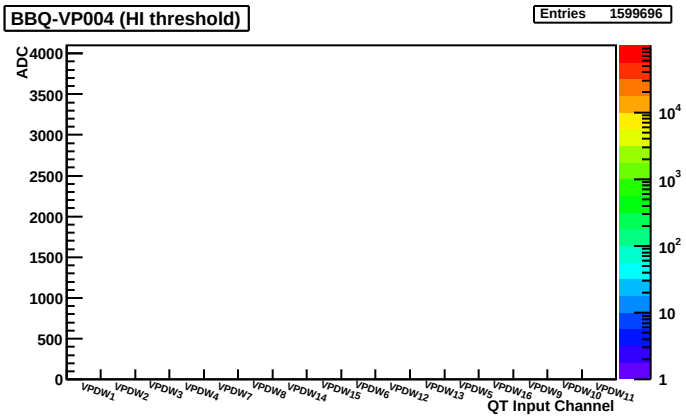

BBQ-VP001 (LO threshold)
Entries 1599696
TAC
QT Input Channel

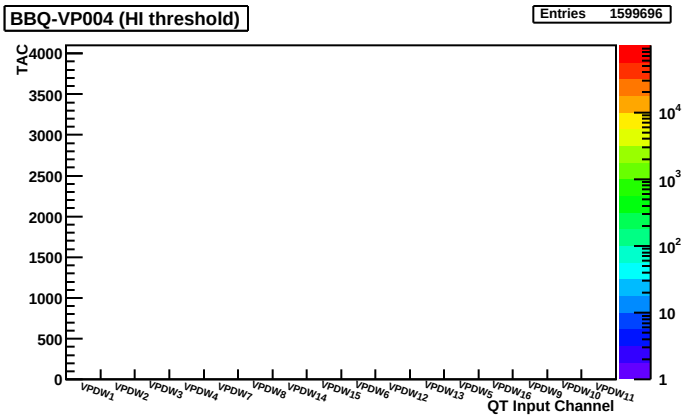

BBQ-VP002 (LO threshold)
Entries 1599696
ADC
QT Input Channel

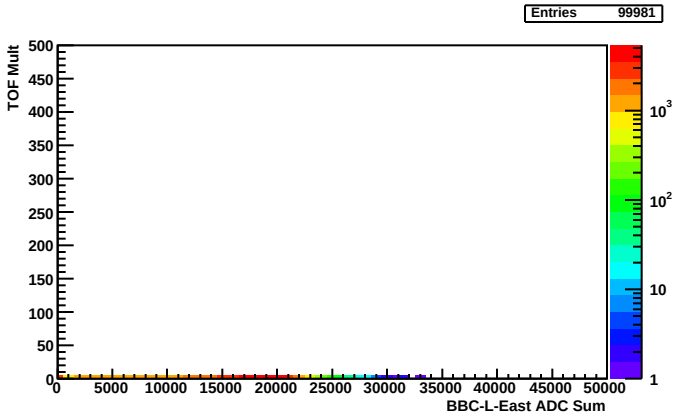

BBQ-VP002 (LO threshold)
Entries 1599696
TAC
QT Input Channel

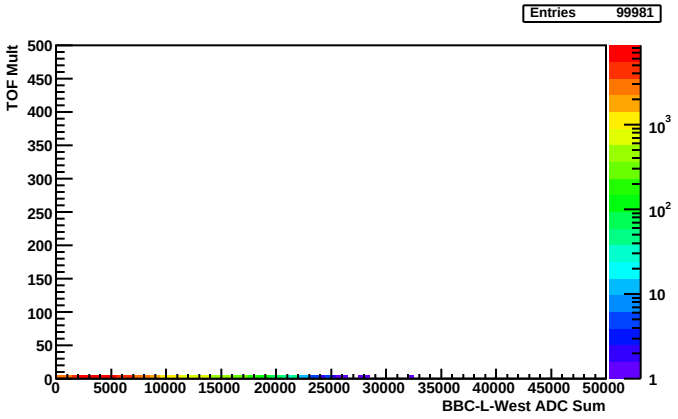

BBQ-VP003 (HI threshold)
Entries 1599696
ADC
QT Input Channel

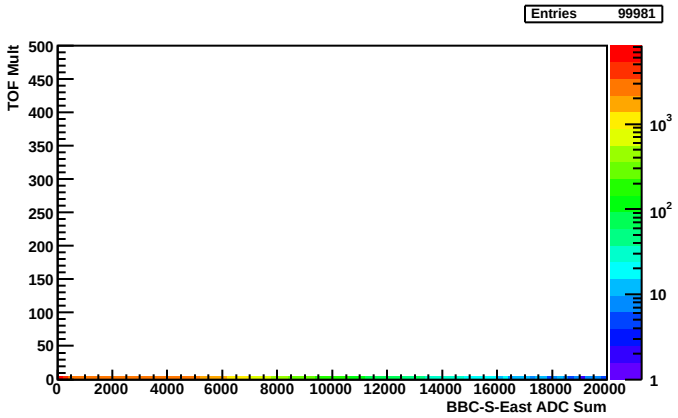

BBQ-VP003 (HI threshold)
Entries 1599696
TAC
QT Input Channel

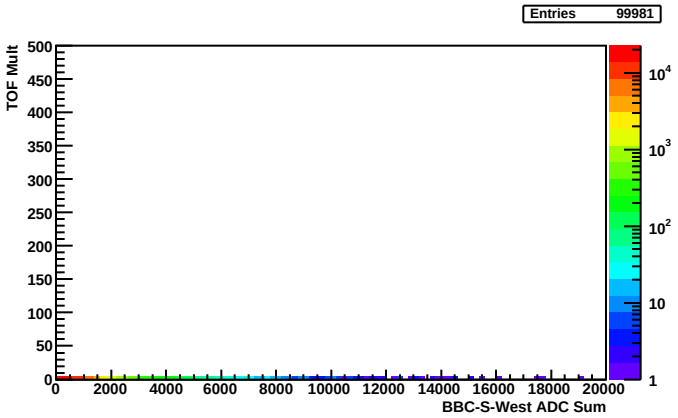

BBQ-VP004 (HI threshold)
Entries 1599696
ADC
QT Input Channel

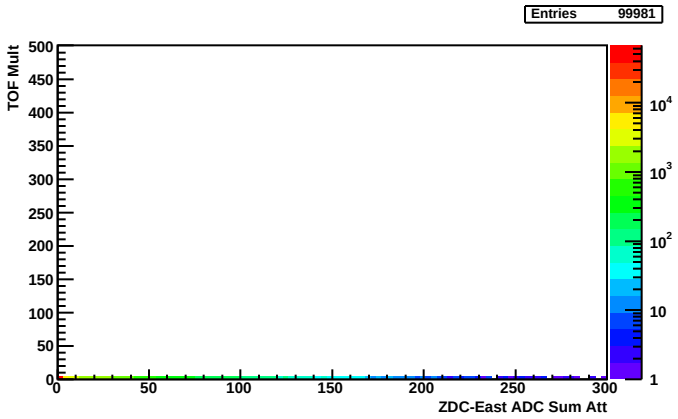

BBQ-VP004 (HI threshold)
Entries 1599696
TAC
QT Input Channel

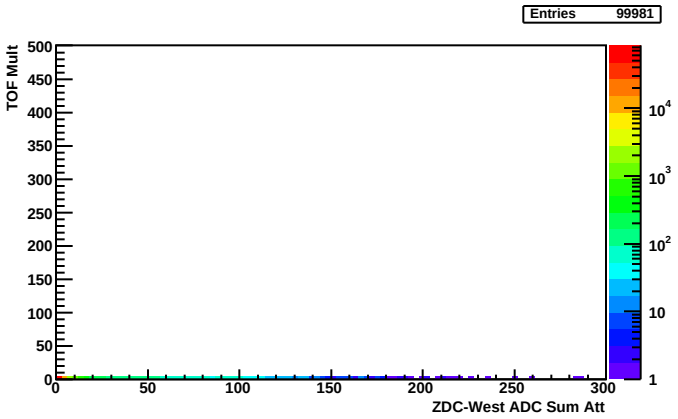

Entries 99981
TOF Mult
BBC-L-East ADC Sum

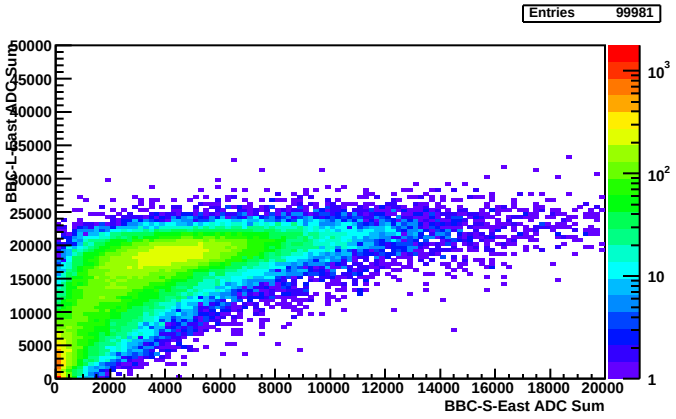

Entries 99981
TOF Mult
BBC-L-West ADC Sum

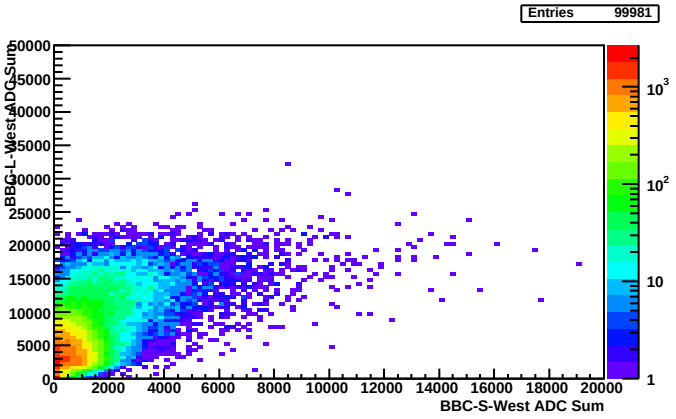

Entries 99981
TOF Mult
BBC-S-East ADC Sum

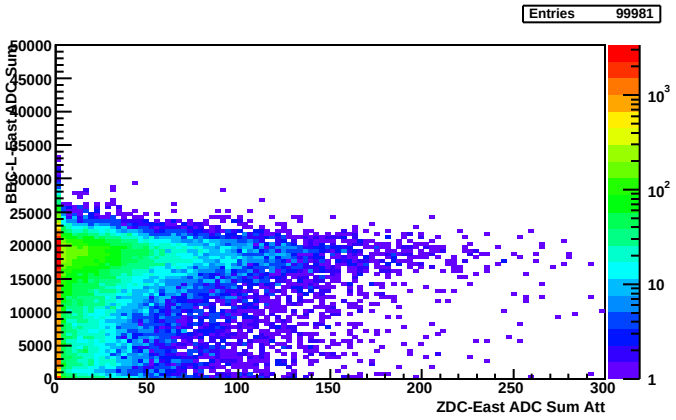

Entries 99981
TOF Mult
BBC-S-West ADC Sum

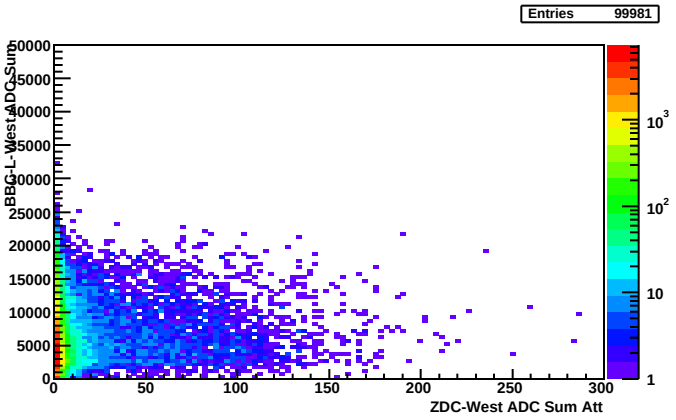

Entries 99981
TOF Mult
ZDC-East ADC Sum Att

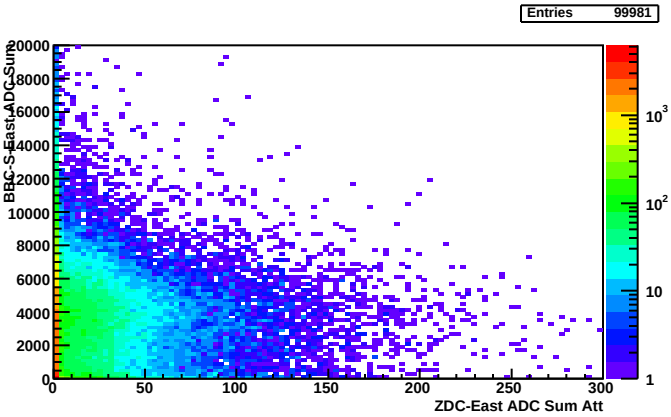

Entries 99981
TOF Mult
ZDC-West ADC Sum Att

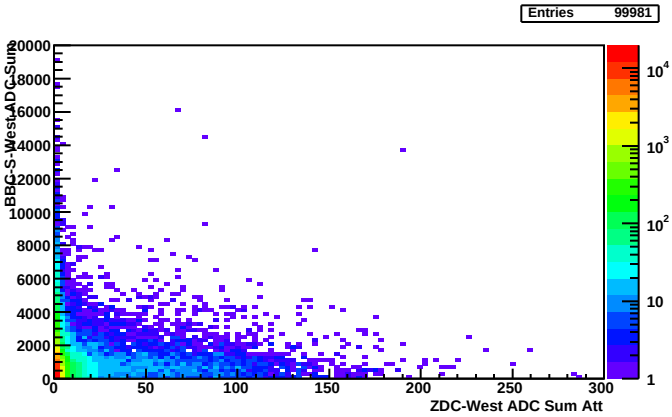

Entries 99981
BBC-L-East ADC Sum
BBC-S-East ADC Sum

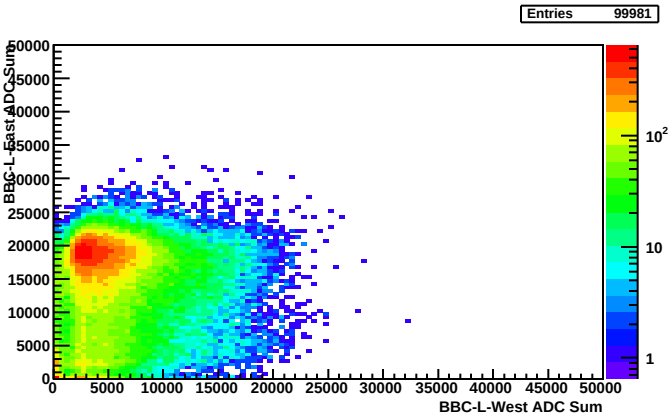

Entries 99981
BBC-L-West ADC Sum
BBC-S-West ADC Sum

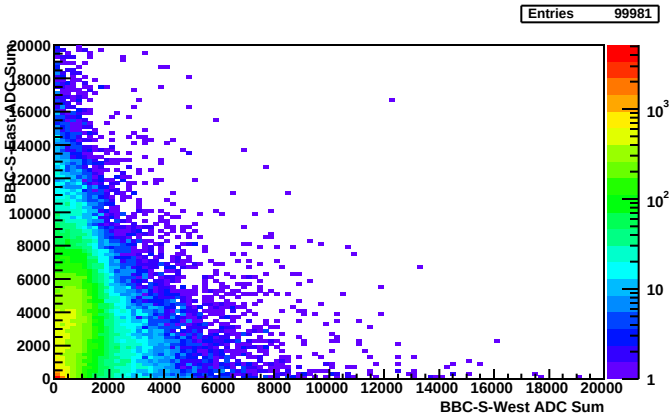

Entries 99981
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

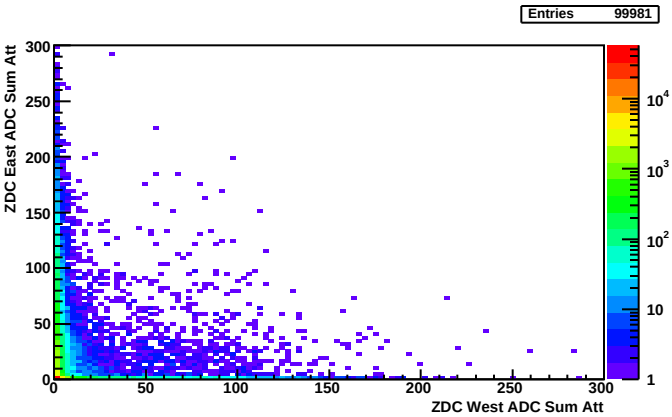

Entries 99981
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

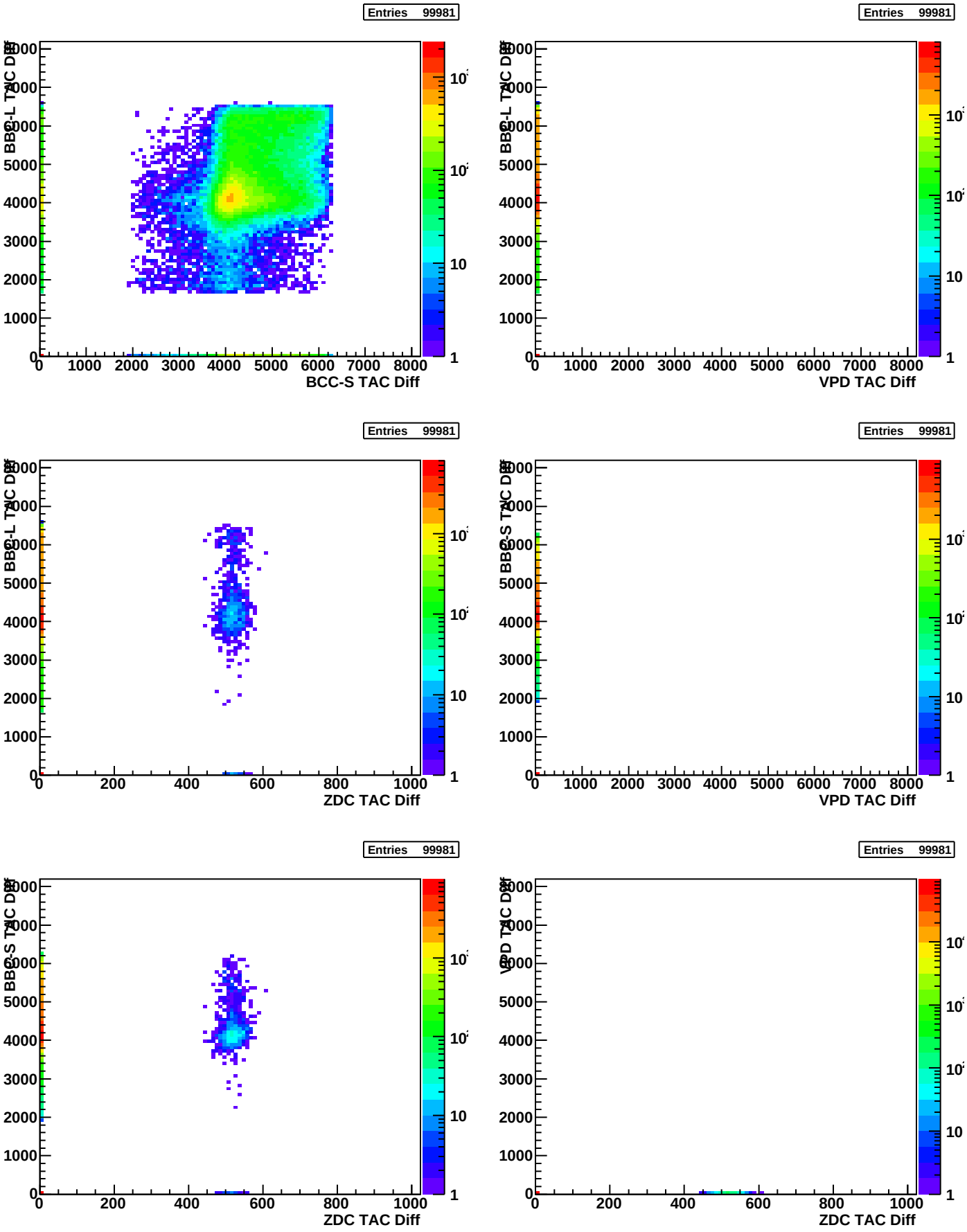

Entries 99981
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

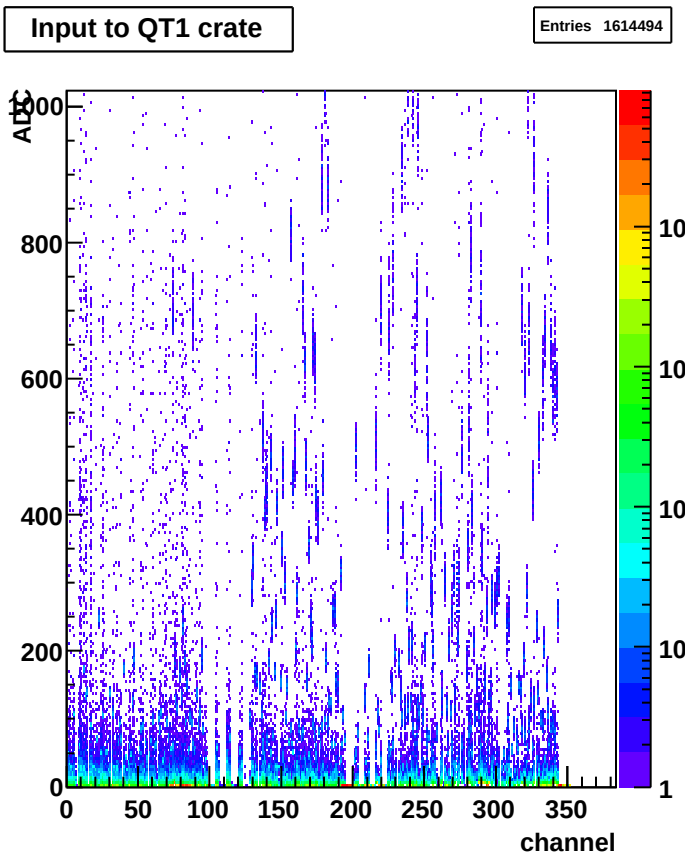

Entries 99981
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

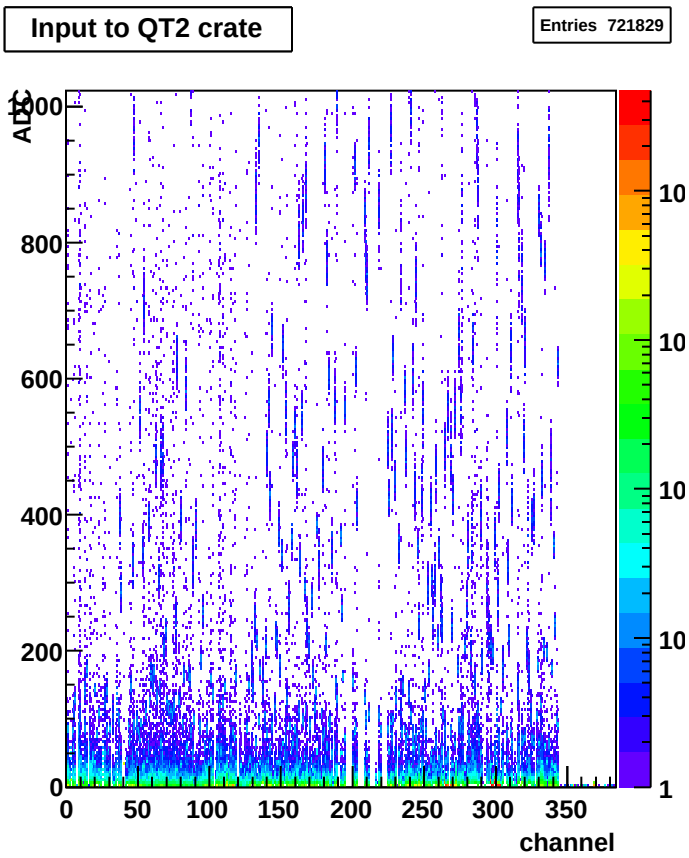

Entries 99981
BBC-L-East ADC Sum
BBC-L-West ADC Sum

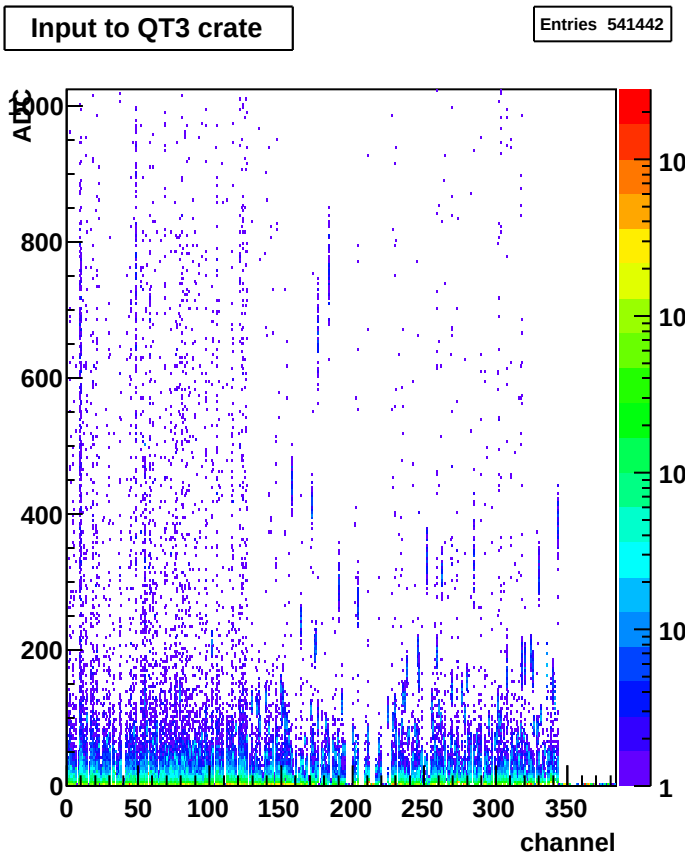

Entries 99981
BBC-S-East ADC Sum
BBC-S-West ADC Sum

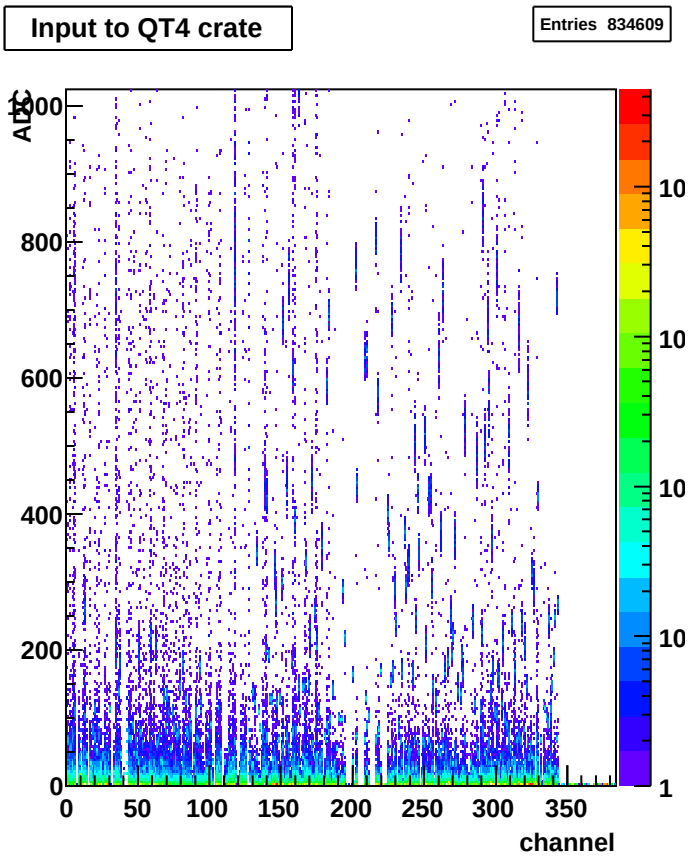

Entries 99981
ZDC East ADC Sum Att
ZDC West ADC Sum Att

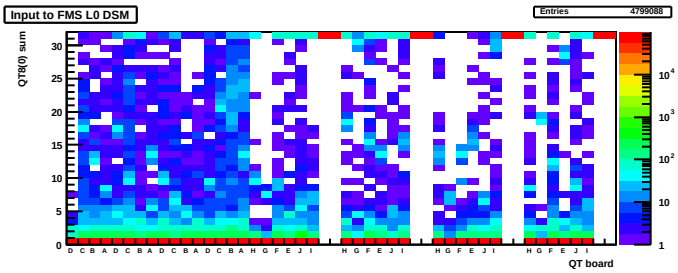

Entries 99981
BBC-L TAC Diff
BCC-S TAC Diff
Entries 99981
BBC-L TAC Diff
VPD TAC Diff
Entries 99981
BBC-L TAC Diff
ZDC TAC Diff
Entries 99981
BBC-S TAC Diff
VPD TAC Diff
Entries 99981
BBC-S TAC Diff
ZDC TAC Diff
Entries 99981
VPD TAC Diff
ZDC TAC Diff

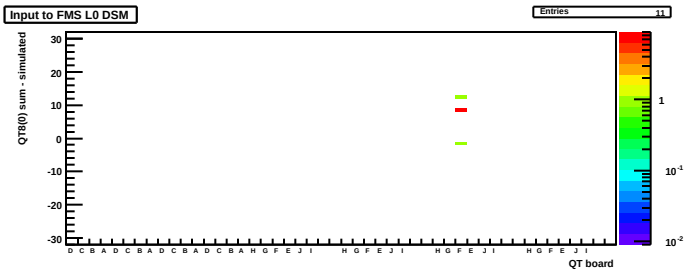

Input to QT1 crate
Entries 1614494
ADC
channel

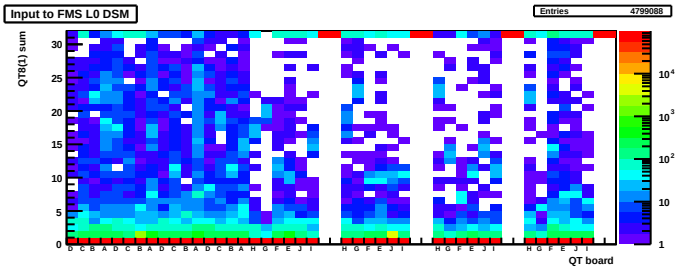

Input to QT2 crate
Entries 721829
ADC
channel

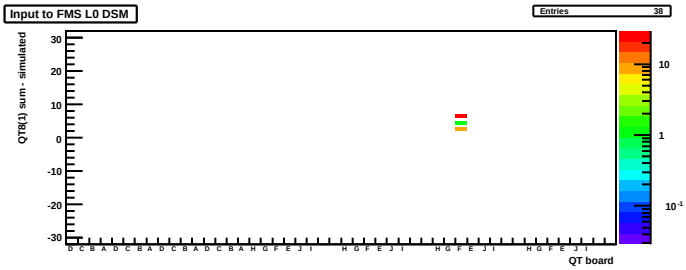

Input to QT3 crate
Entries 541442
ADC
channel

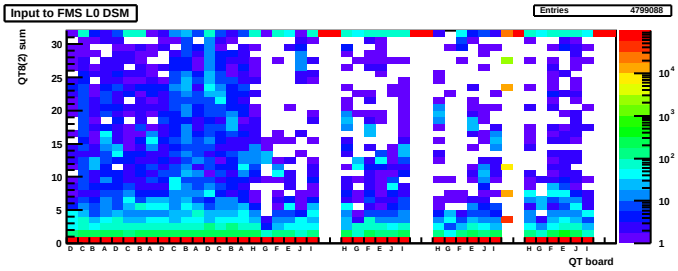

Input to QT4 crate
Entries 834609
ADC
channel

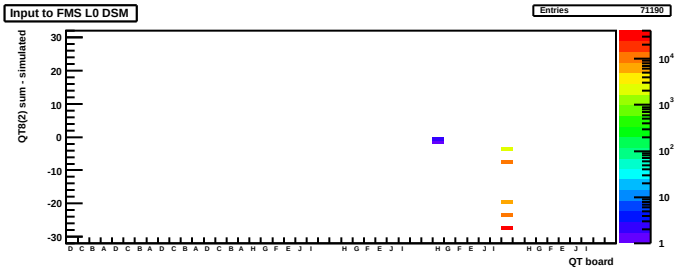

Input to FMS L0 DSM
Entries 4799088
QT8(0) sum
QT board

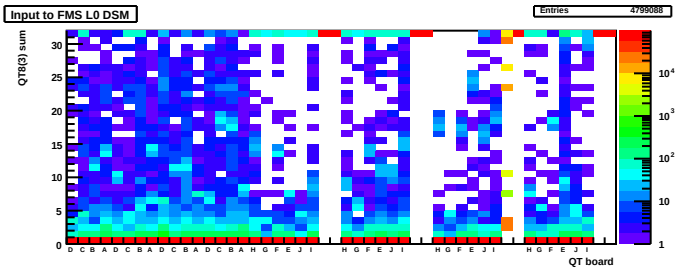

Input to FMS L0 DSM
Entries 11
QT8(0) sum - simulated
QT board

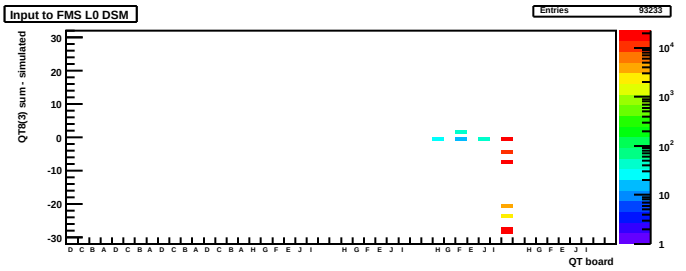

Input to FMS L0 DSM
Entries 4799088
QT8(1) sum
QT board

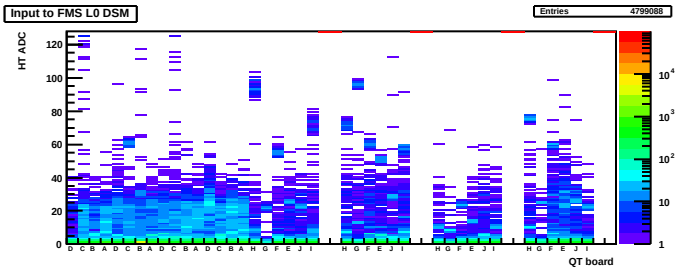

Input to FMS L0 DSM
Entries 38
QT8(1) sum - simulated
QT board

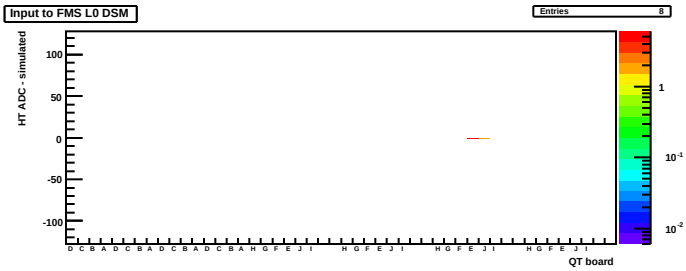

Input to FMS L0 DSM
Entries 4799088
QT8(2) sum
QT board

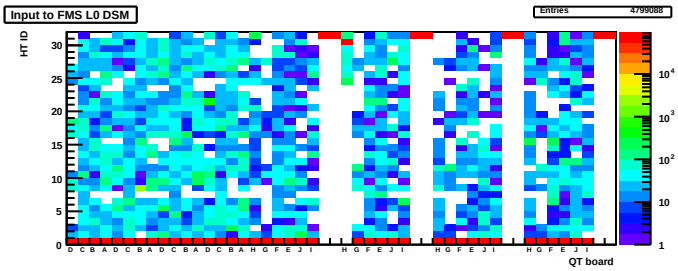

Input to FMS L0 DSM
Entries 71190
QT8(2) sum - simulated
QT board

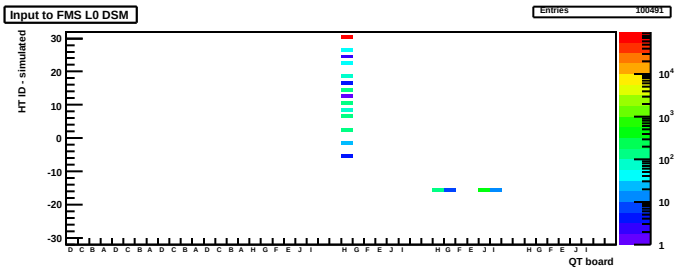

Input to FMS L0 DSM
Entries 4799088
QT8(3) sum
QT board

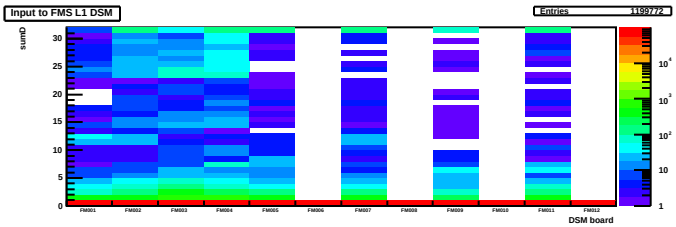

Input to FMS L0 DSM
Entries 93233
QT8(3) sum - simulated
QT board

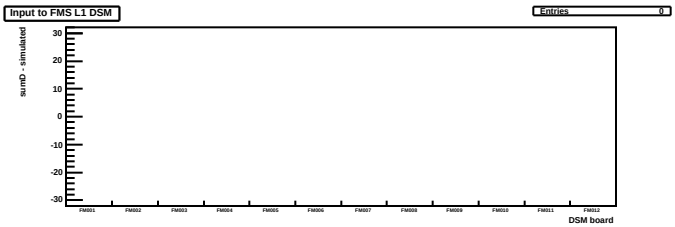

Input to FMS L0 DSM
Entries 4799088
HT ADC
QT board

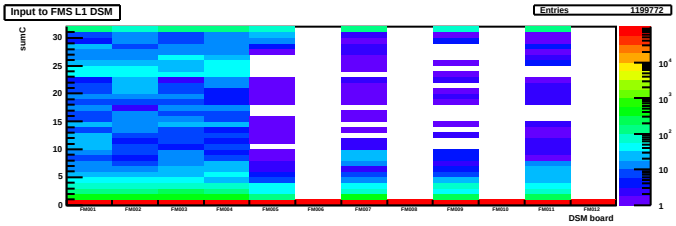

Input to FMS L0 DSM
Entries 8
HT ADC - simulated
QT board

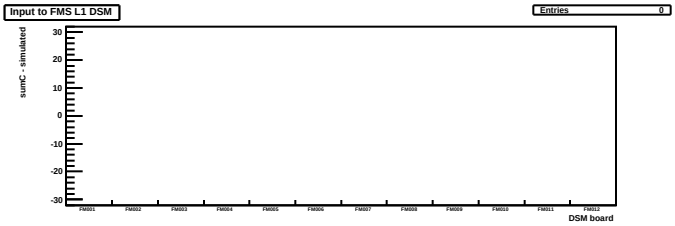

Input to FMS L0 DSM
Entries 4799088
HT ID
QT board

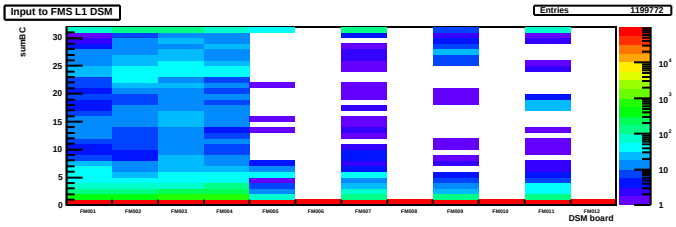

Input to FMS L0 DSM
Entries 100491
HT ID - simulated
QT board

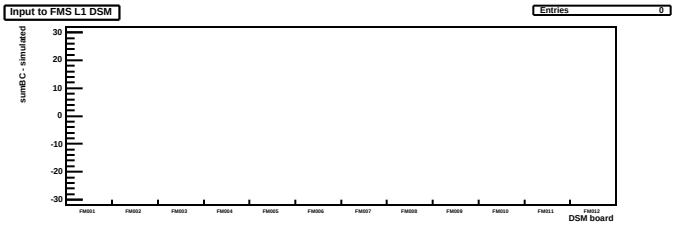

Input to FMS L1 DSM
Entries 1199772
sumD
DSM board

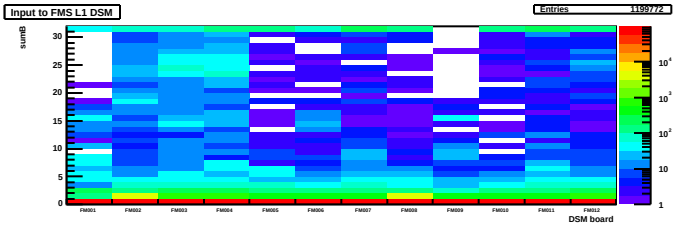

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

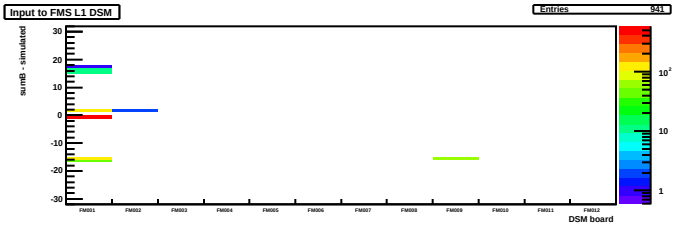

Input to FMS L1 DSM
Entries 1199772
sumC
DSM board

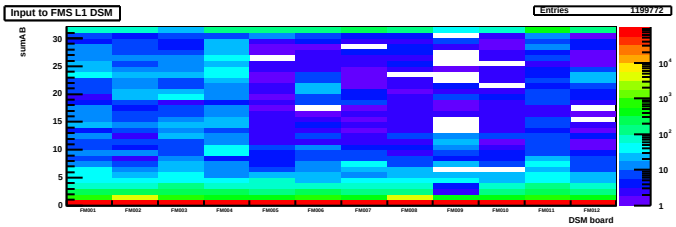

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

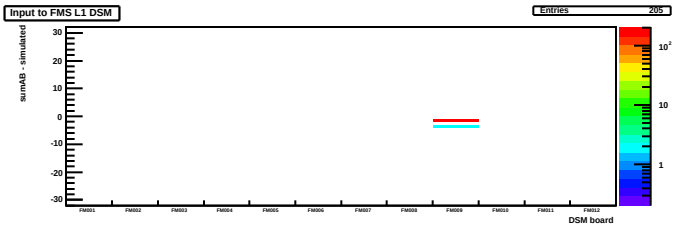

Input to FMS L1 DSM
Entries 1199772
sumBC
DSM board

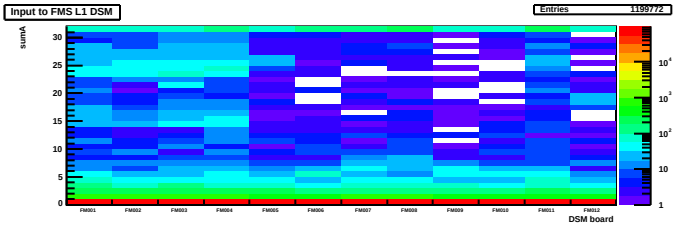

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

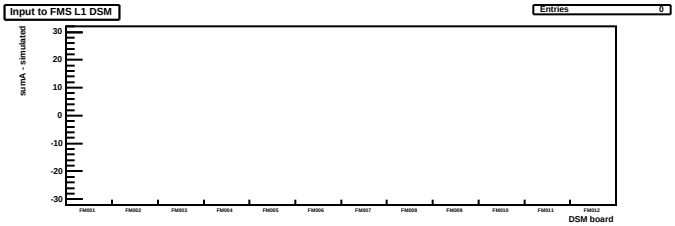

Input to FMS L1 DSM
Entries 1199772
sumB
DSM board

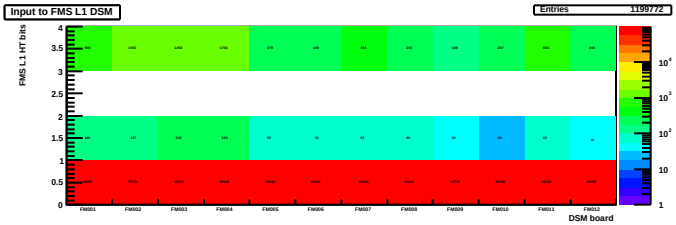

Input to FMS L1 DSM
Entries 941
sumB - simulated
DSM board

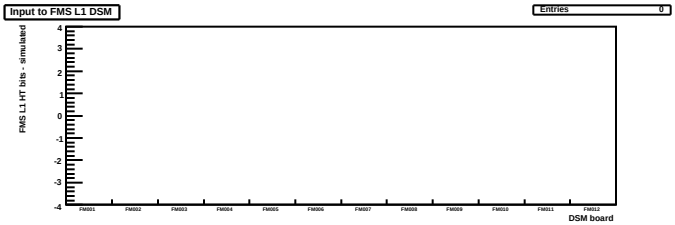

Input to FMS L1 DSM
Entries 1199772
sumAB
DSM board

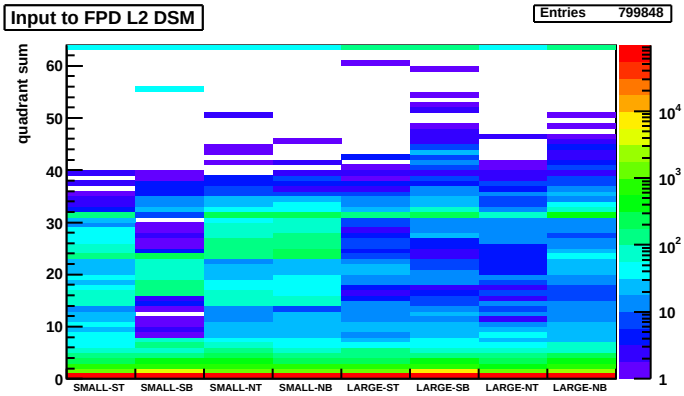

Input to FMS L1 DSM
Entries 205
sumAB - simulated
DSM board

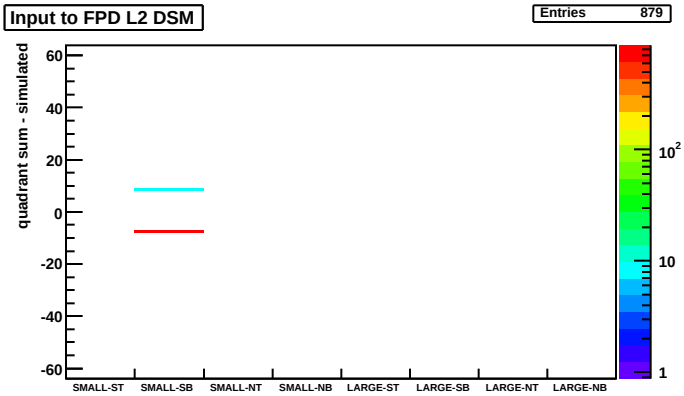

Input to FMS L1 DSM
Entries 1199772
sumA
DSM board

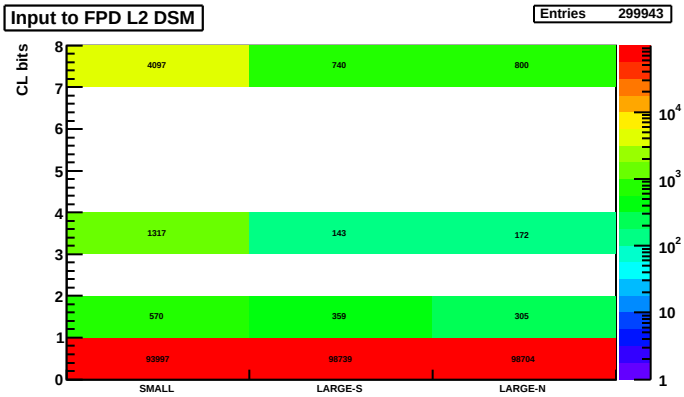

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

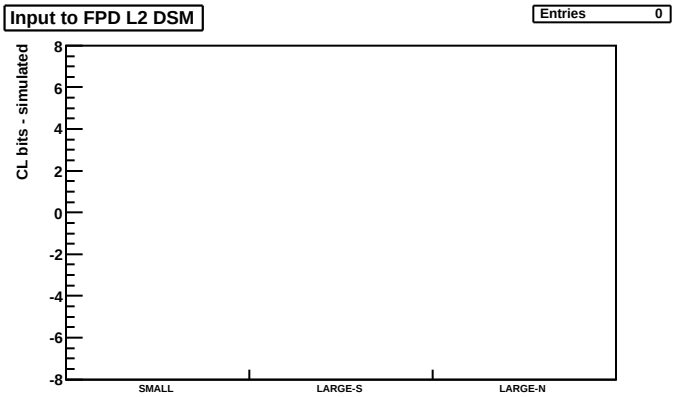

Input to FMS L1 DSM
Entries 1199772
FMS L1 HT bits
DSM board

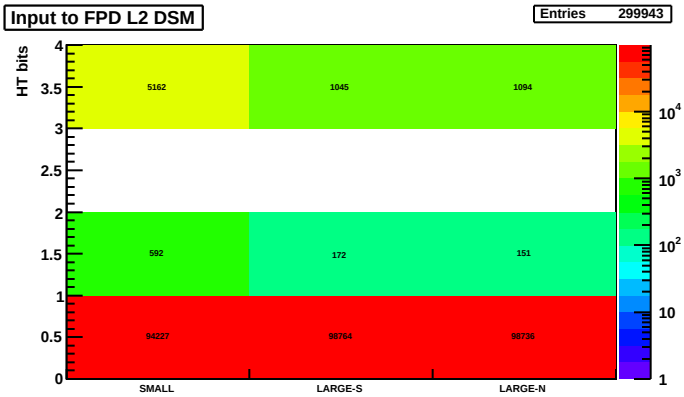

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

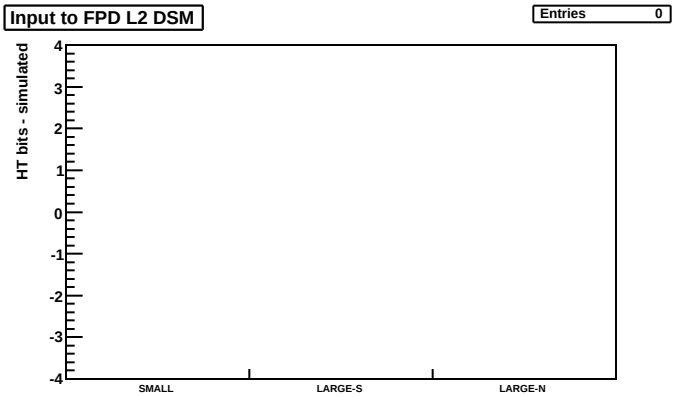

Input to FPD L2 DSM
Entries 799848
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

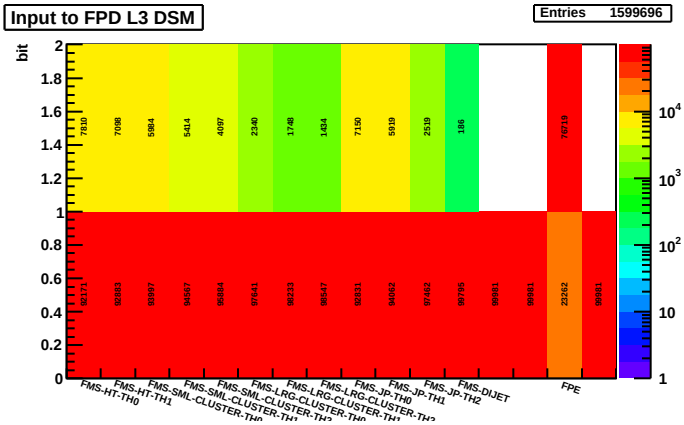

Input to FPD L2 DSM
Entries 879
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

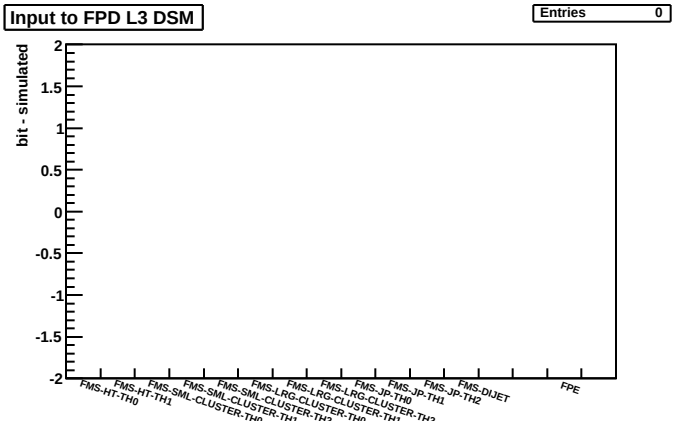

Input to FPD L2 DSM
Entries 299943
CL bits
4097
740
800
1317
143
172
570
359
305
93997
98739
98704
SMALL
LARGE-S
LARGE-N

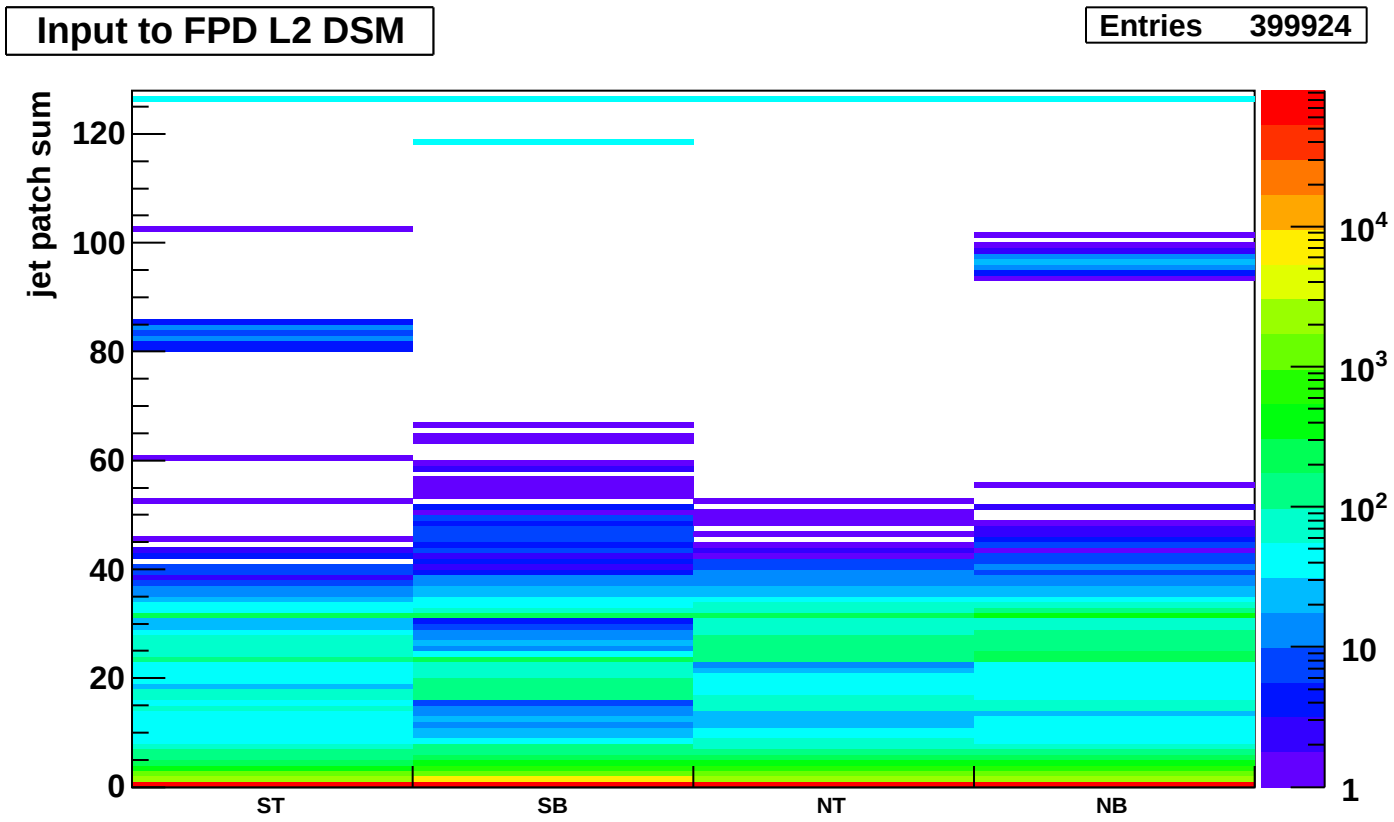

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

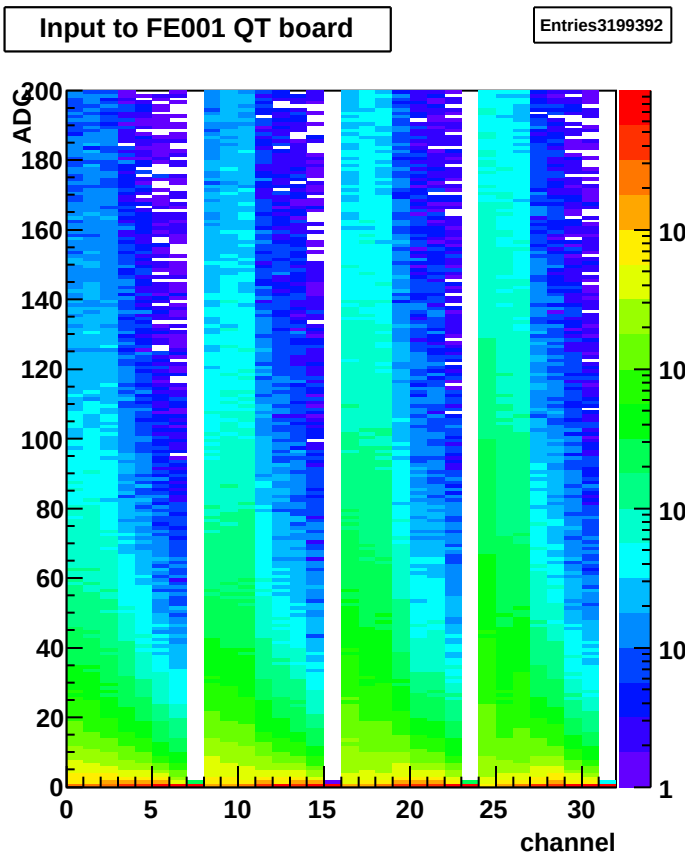

Input to FPD L2 DSM
Entries 299943
HT bits
5162
1045
1094
592
172
151
94227
98764
98736
SMALL
LARGE-S
LARGE-N

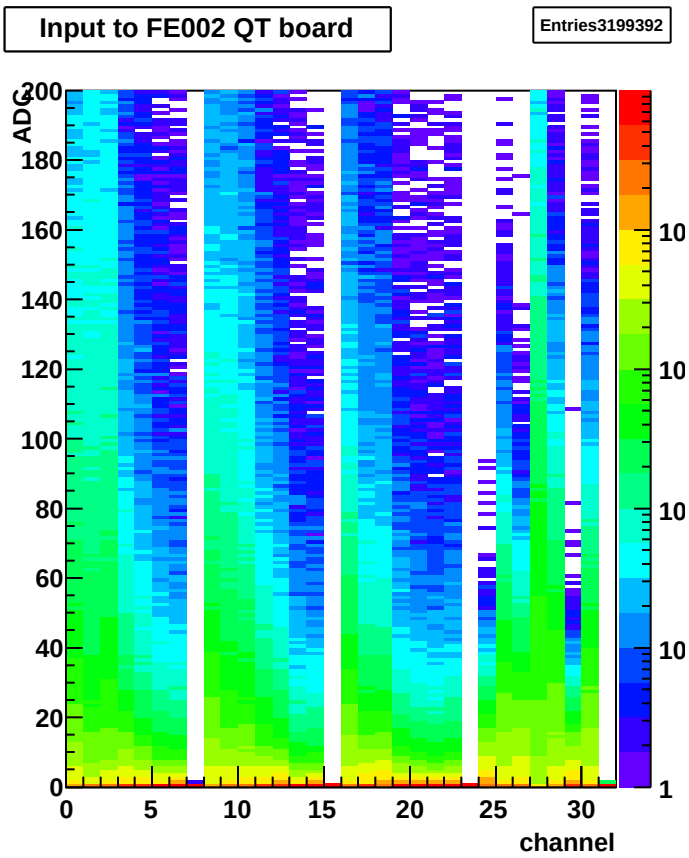

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

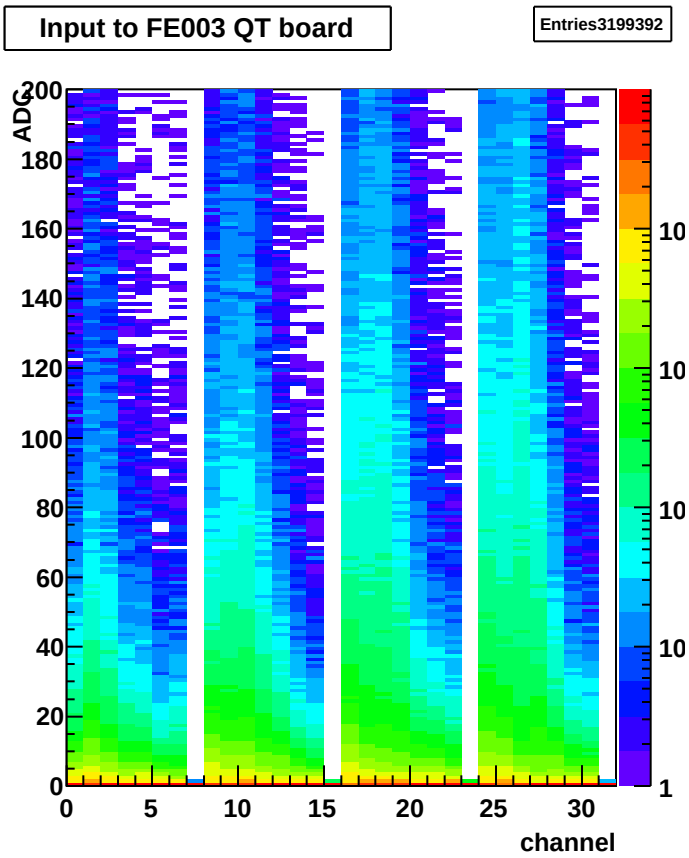

Input to FPD L3 DSM
Entries 1599696
bit
7810
7098
5984
5414
4097
2340
1748
1424
7150
5919
2519
186
7679
92171
92883
93997
94567
95884
97641
99233
98547
92831
94062
97462
99795
99931
99931
99931
23252
99931
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

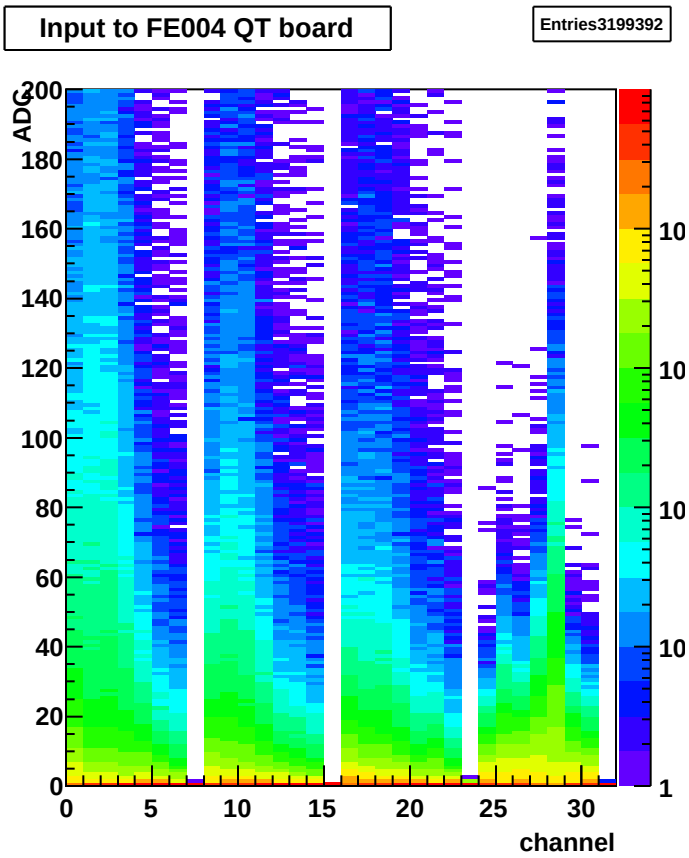

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

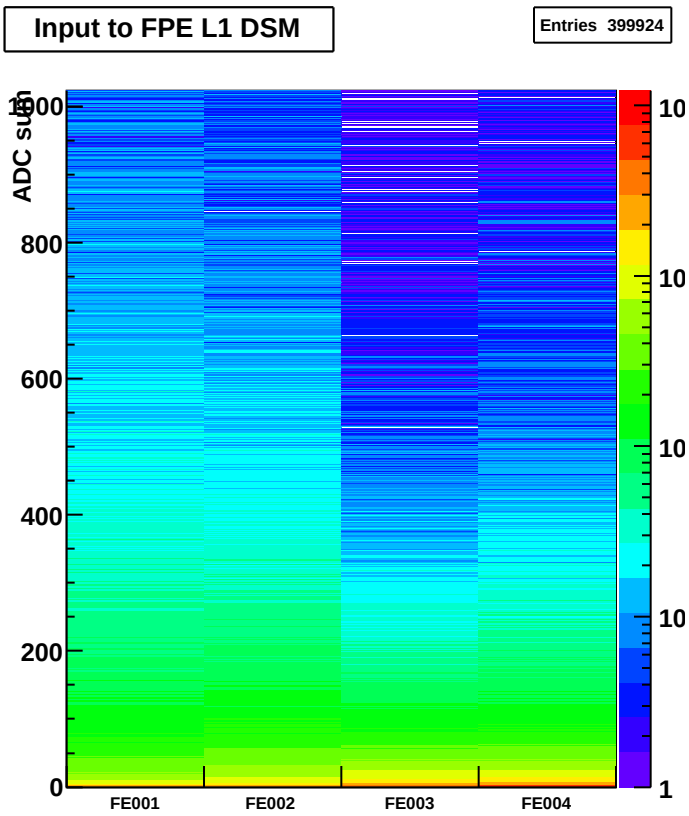

Input to FPD L2 DSM
Entries 399924
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$

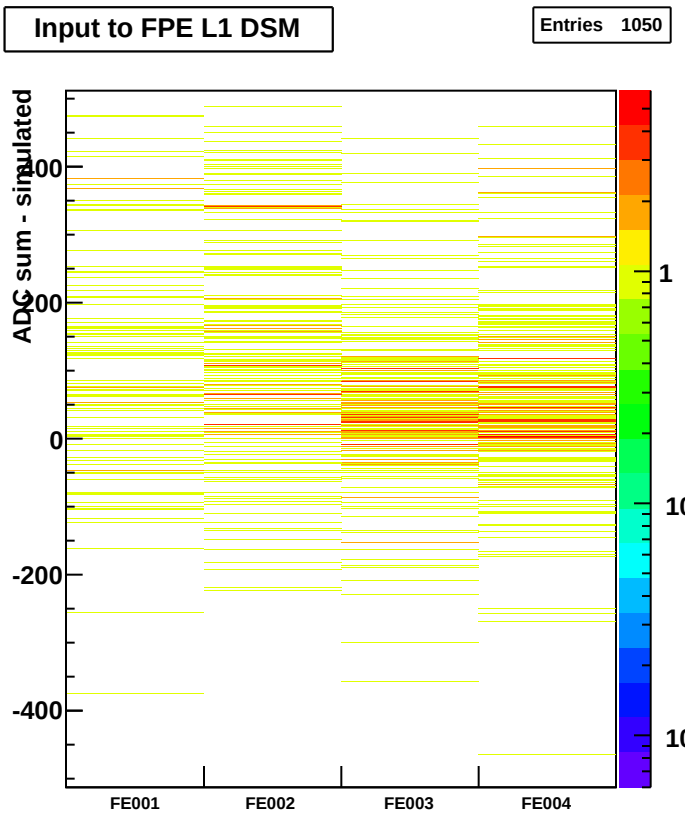

Input to FE001 QT board
Entries3199392
ADC
channel

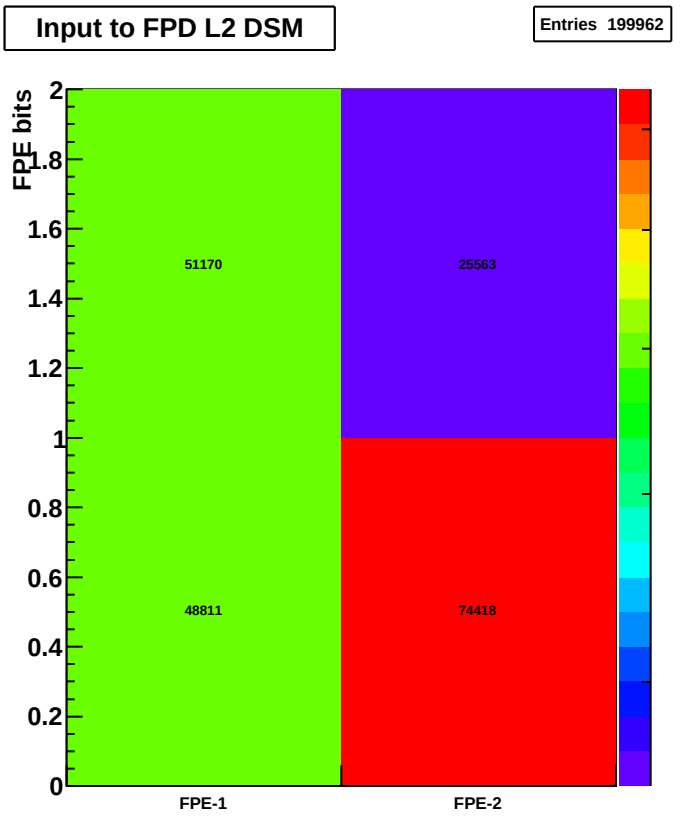

Input to FE002 QT board
Entries3199392
ADC
channel

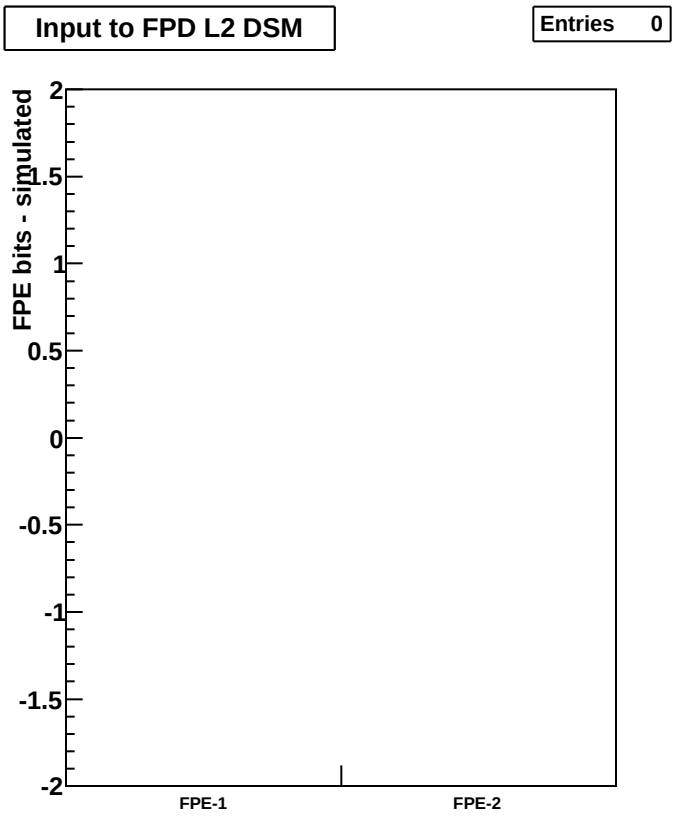

Input to FE003 QT board
Entries3199392
ADC
channel


Input to FE004 QT board
Entries3199392
ADC
channel


Input to FPE L1 DSM
Entries 399924
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 1050
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 199962
FPE bits
51170
25563
48811
74418
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2