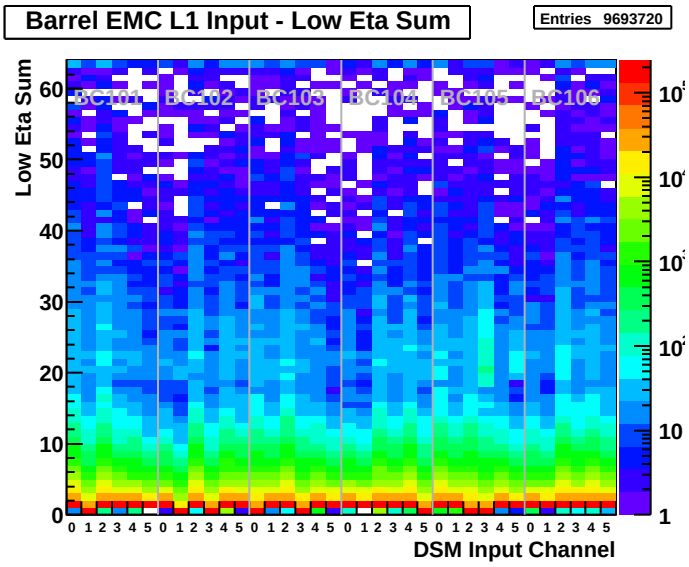
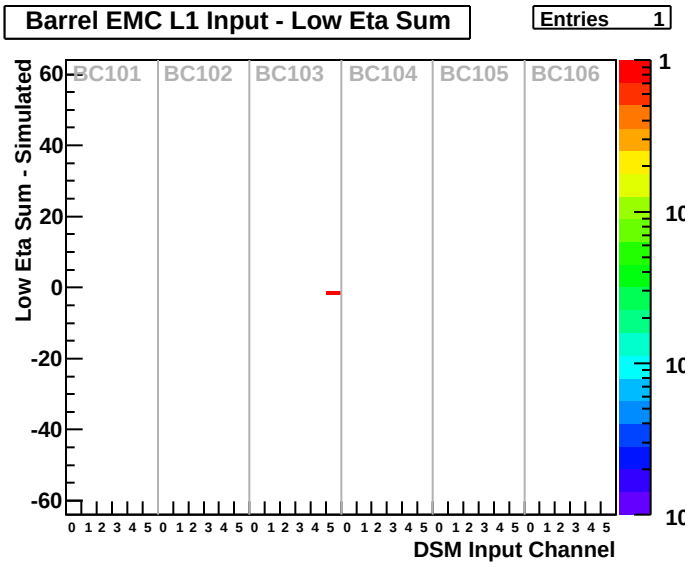
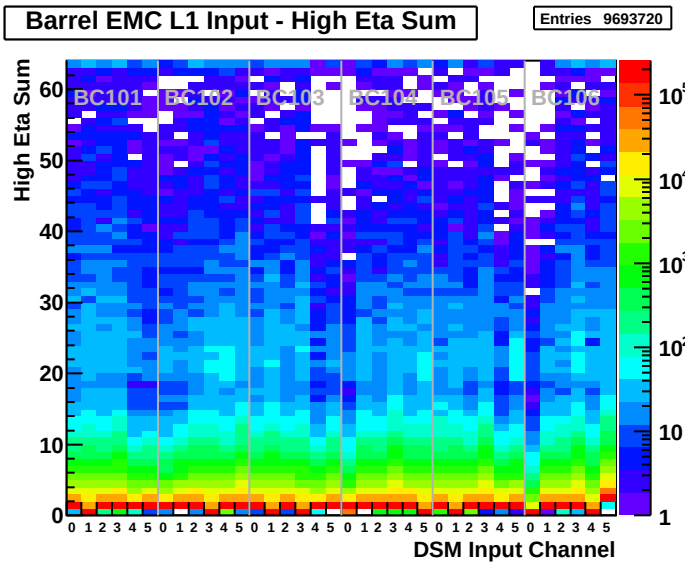
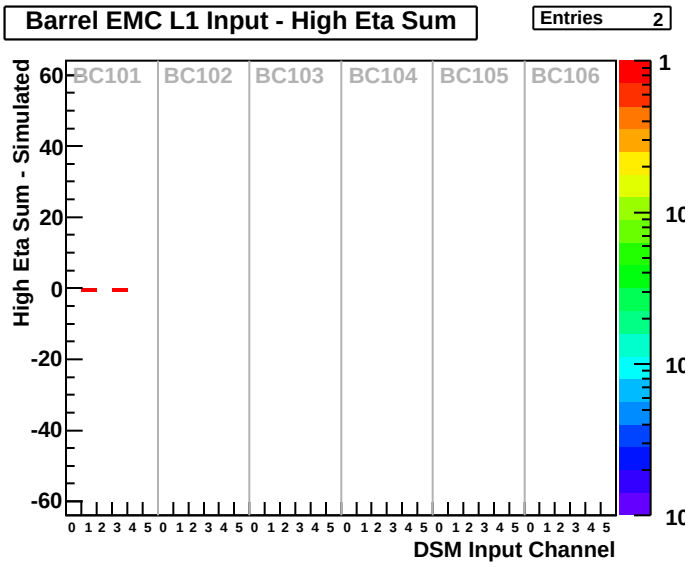
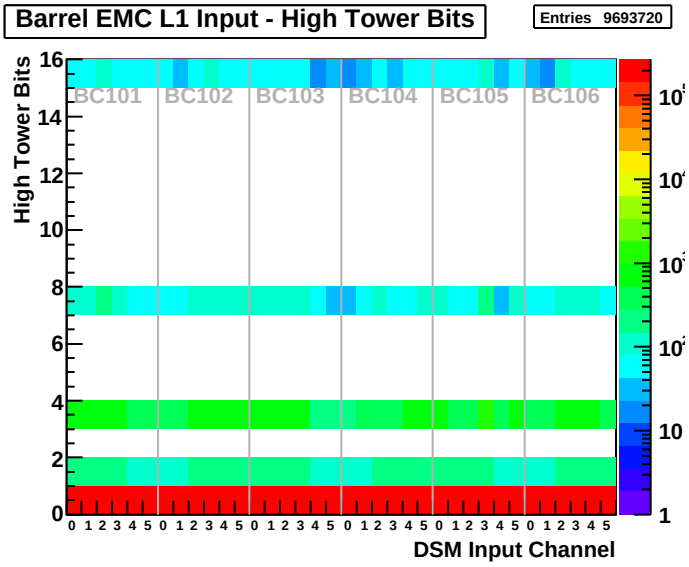
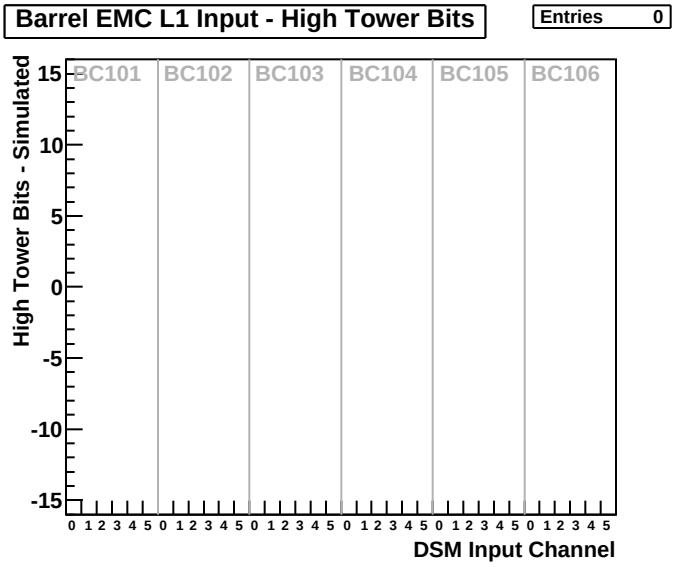

Barrel EMC L1 Input - Low Eta Sum
Entries 9693720
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

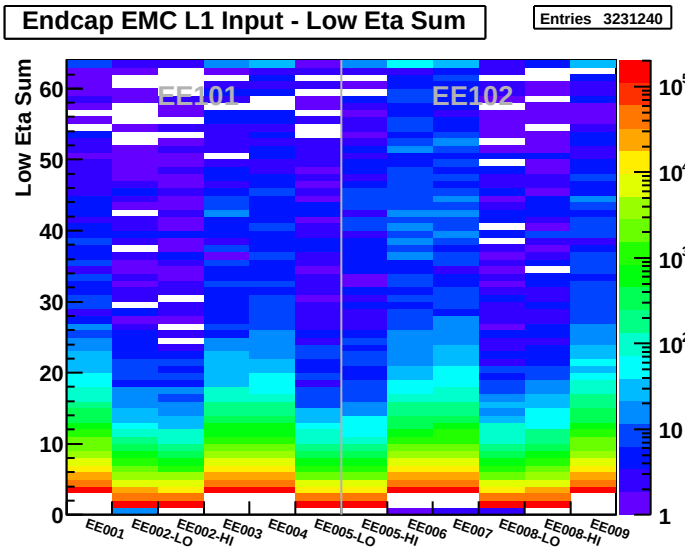

Barrel EMC L1 Input - Low Eta Sum
Entries 1
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

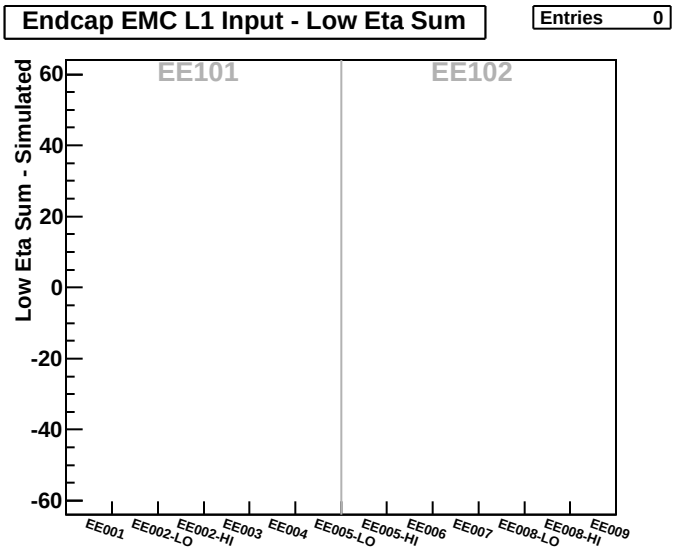

Barrel EMC L1 Input - High Eta Sum
Entries 9693720
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

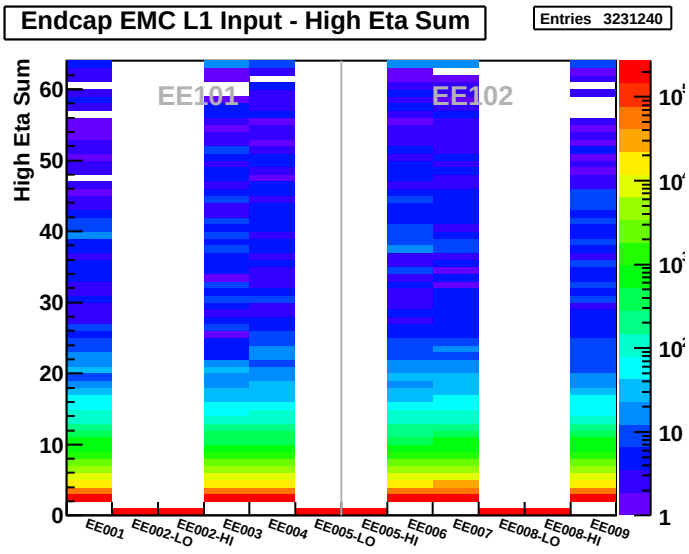

Barrel EMC L1 Input - High Eta Sum
Entries 2
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

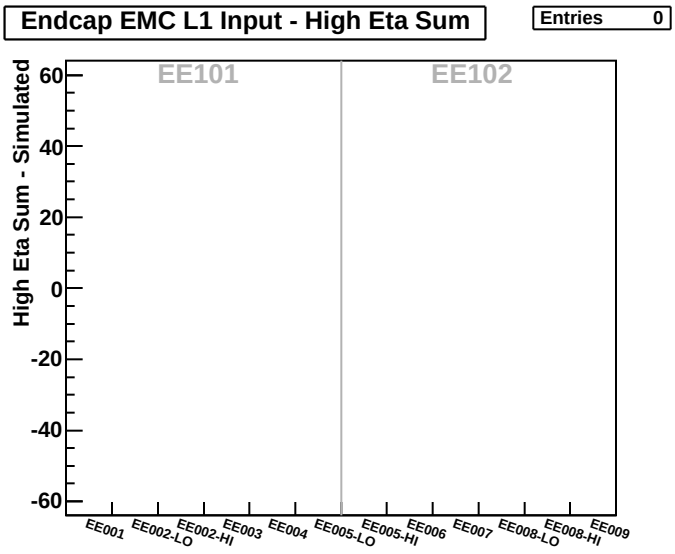

Barrel EMC L1 Input - High Tower Bits
Entries 9693720
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

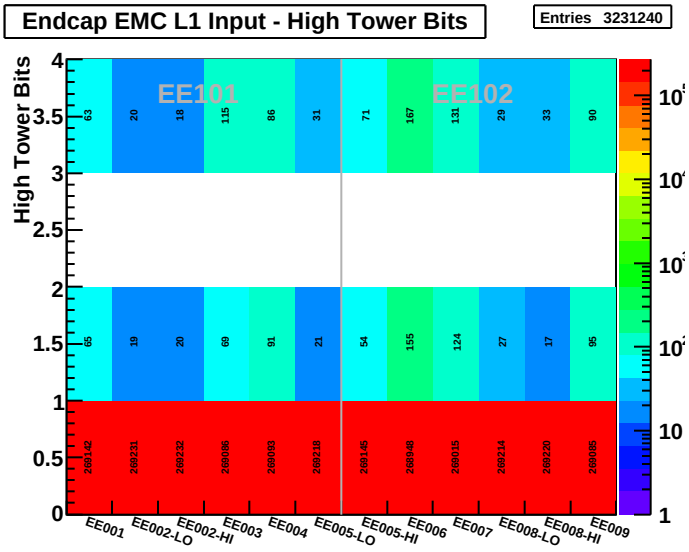

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

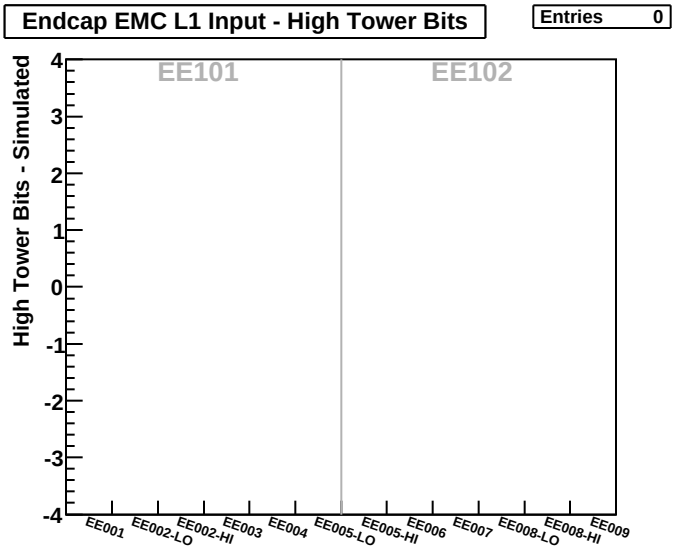

Endcap EMC L1 Input - Low Eta Sum
Entries 3231240
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

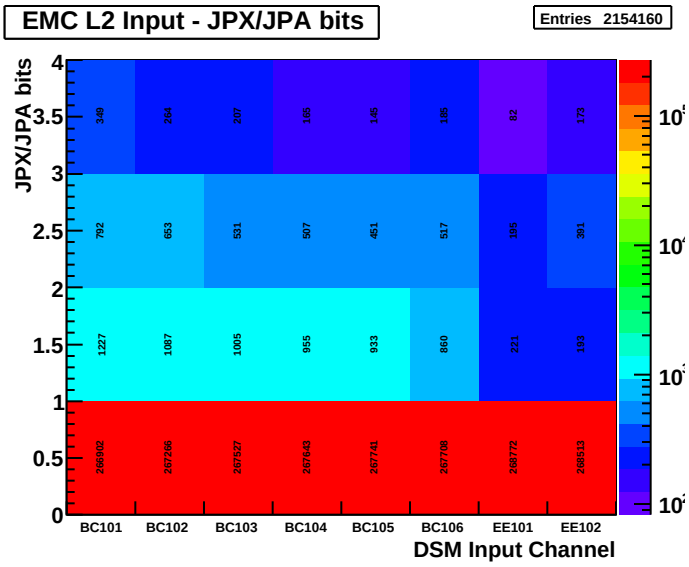

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

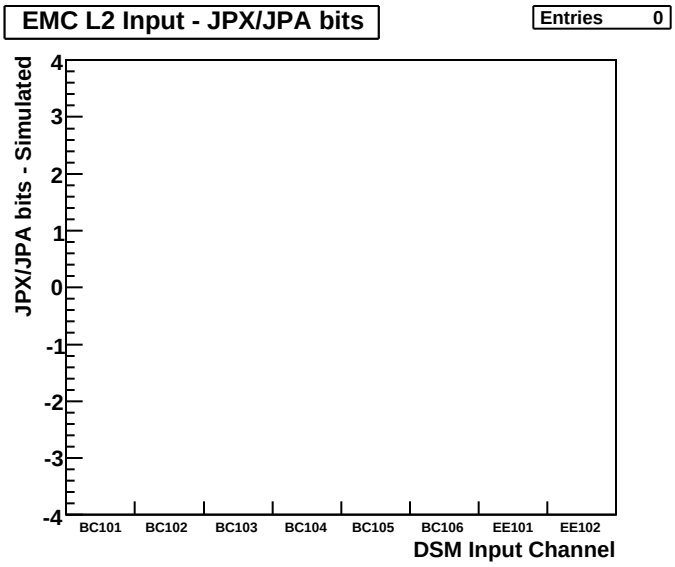

Endcap EMC L1 Input - High Eta Sum
Entries 3231240
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

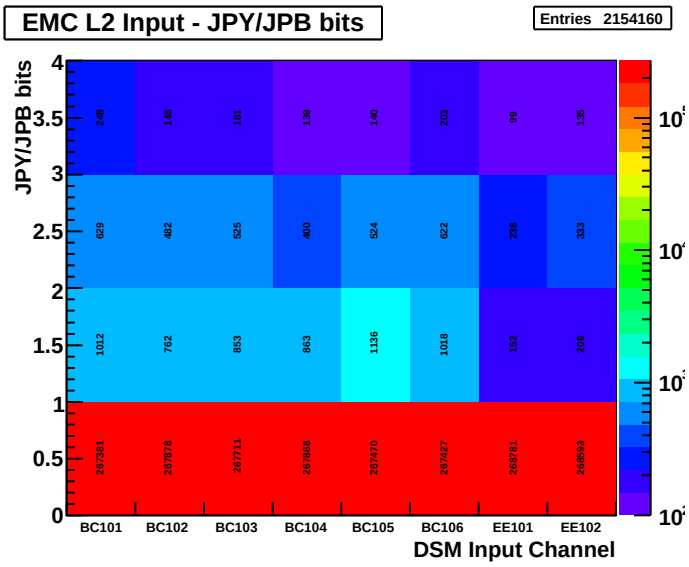

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

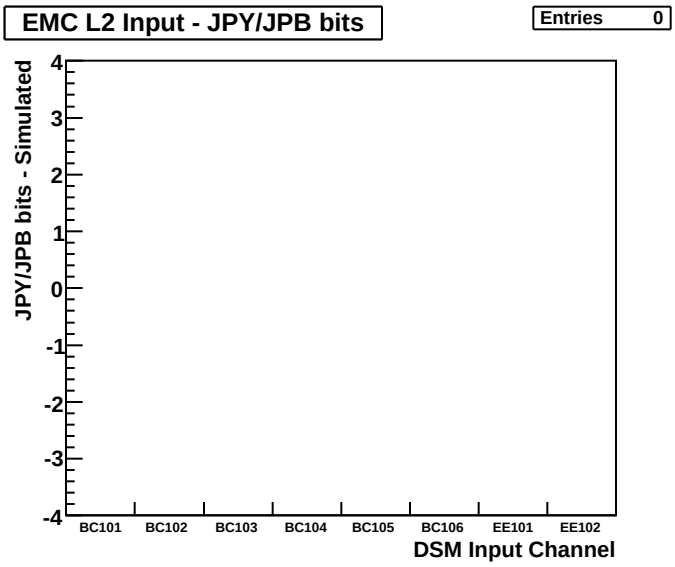

Endcap EMC L1 Input - High Tower Bits
Entries 3231240
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

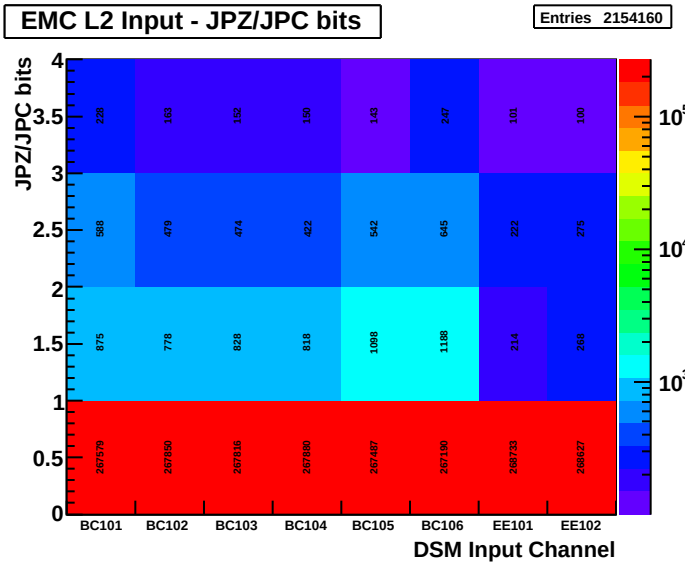

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

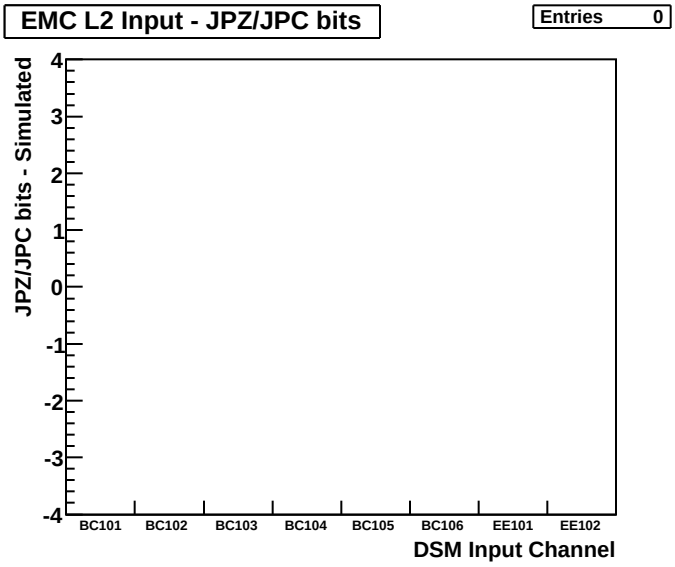

EMC L2 Input - JPX/JPA bits
Entries 2154160
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
349 264 207 165 145 185 82 173
792 653 531 507 451 517 195 391
1227 1087 1005 955 933 860 221 193
266902 267266 267527 267643 267741 267708 268772 268513

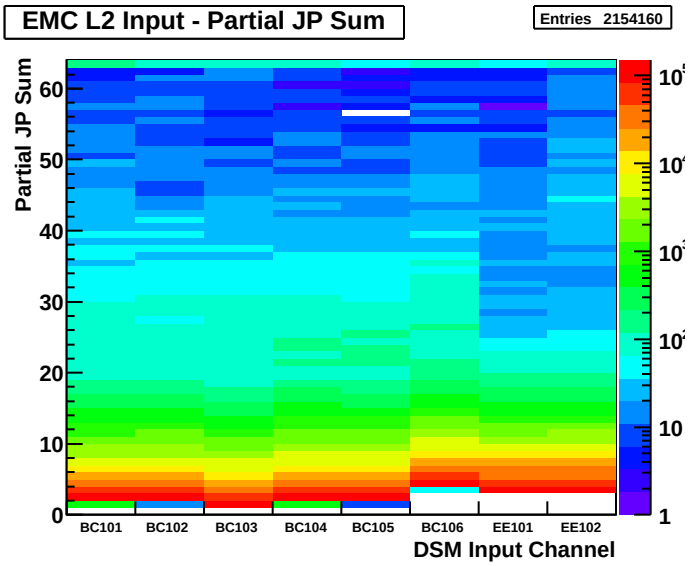

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

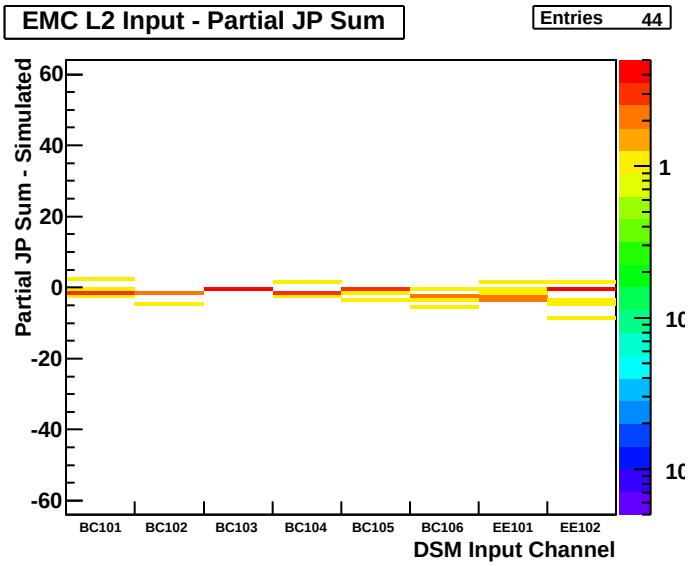

EMC L2 Input - JPY/JPB bits
Entries 2154160
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
246 148 101 139 140 203 99 135
629 482 525 400 524 622 238 333
1012 762 853 863 1136 1018 152 209
267381 267378 267711 267868 267470 267427 268781 268593

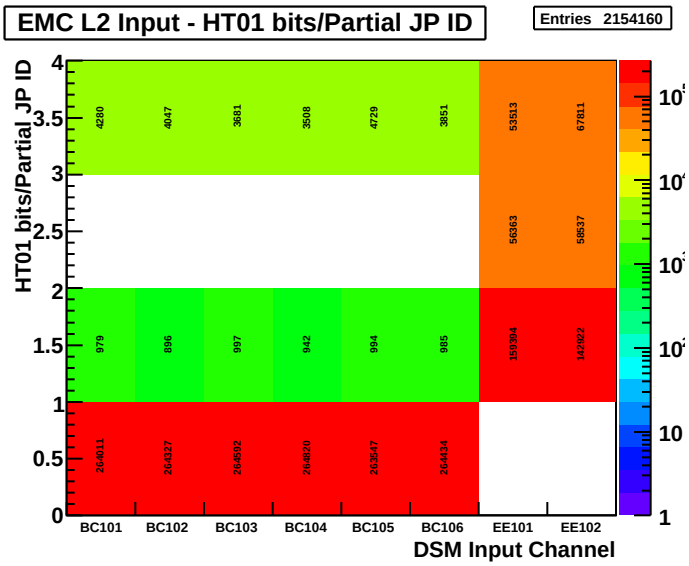

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

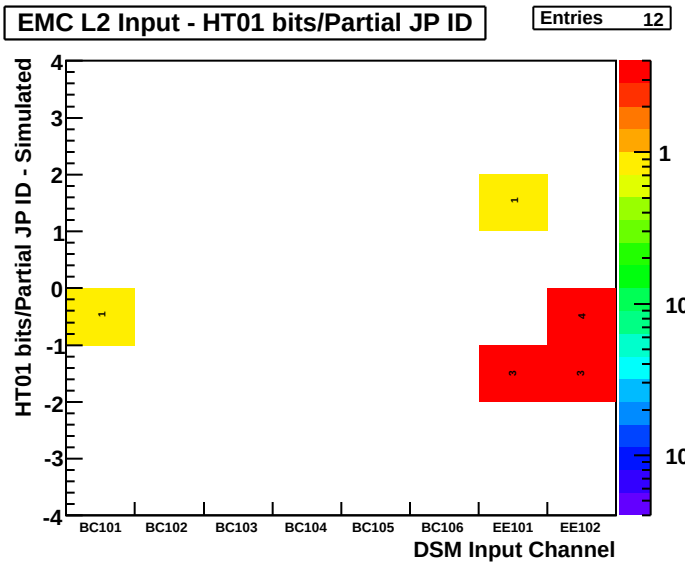

EMC L2 Input - JPZ/JPC bits
Entries 2154160
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
228 163 152 150 143 247 101 100
588 479 474 422 542 645 222 275
875 778 828 818 1098 1188 214 268
267579 267850 267816 267880 267487 267190 268733 268627

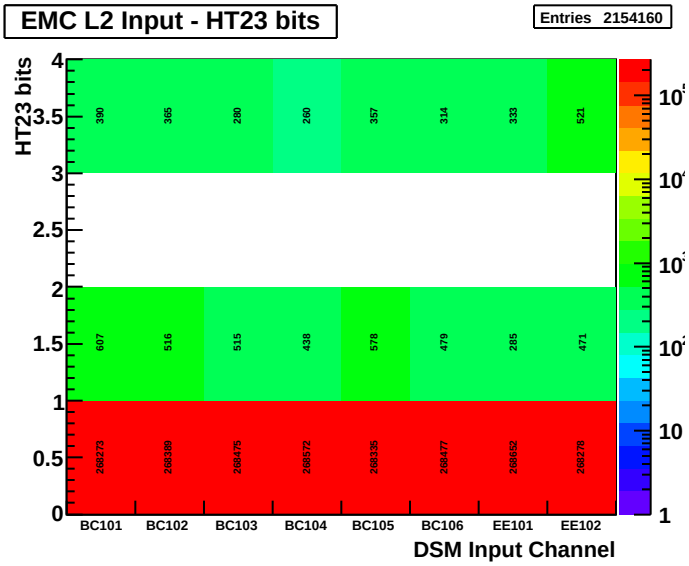

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

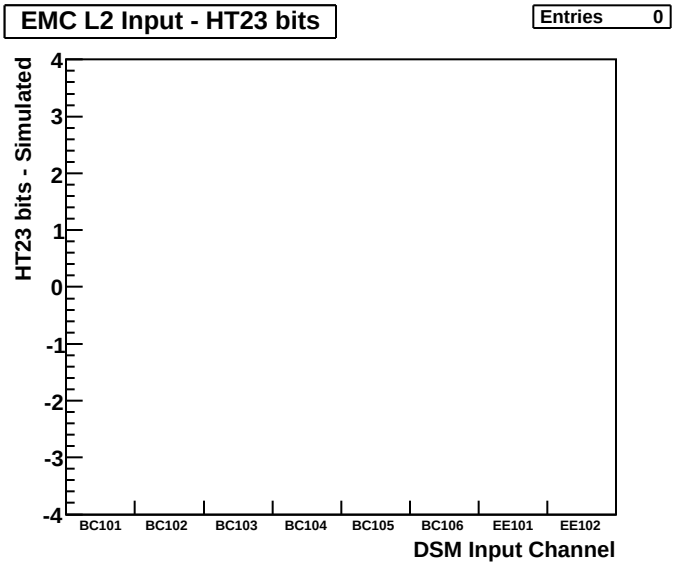

EMC L2 Input - Partial JP Sum
Entries 2154160
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

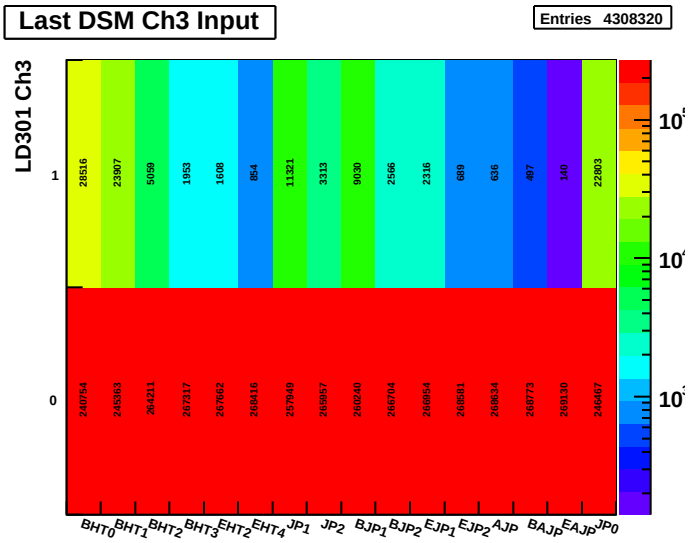

EMC L2 Input - Partial JP Sum
Entries 44
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

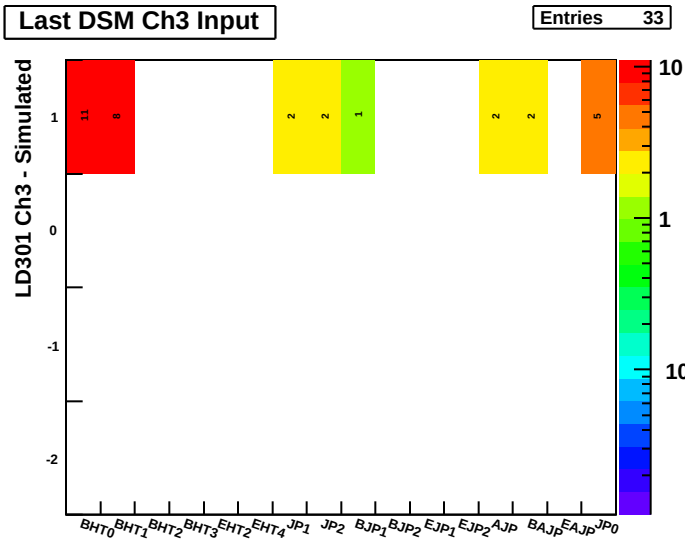

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2154160
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

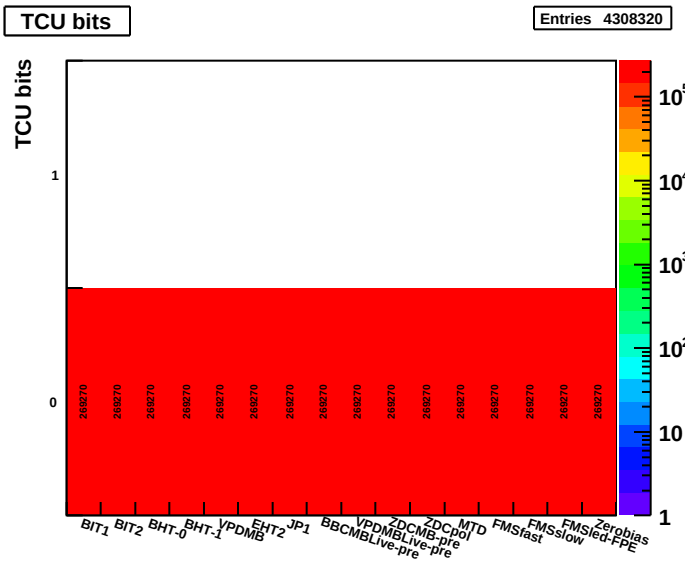

EMC L2 Input - HT01 bits/Partial JP ID
Entries 12
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

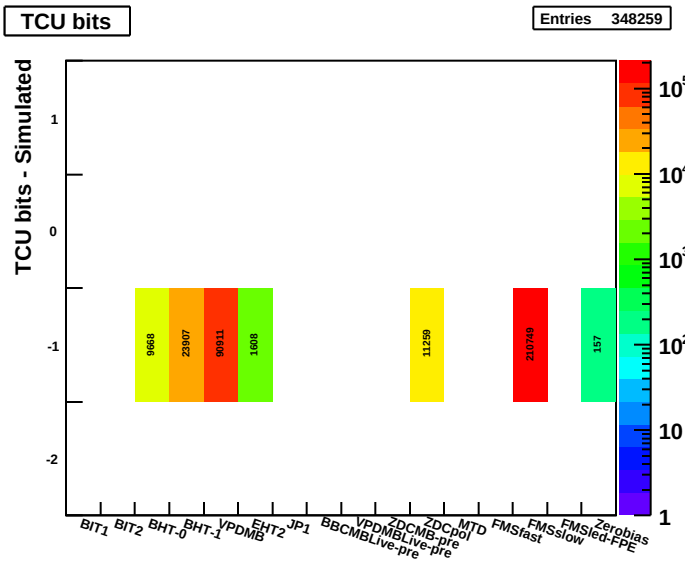

EMC L2 Input - HT23 bits
Entries 2154160
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

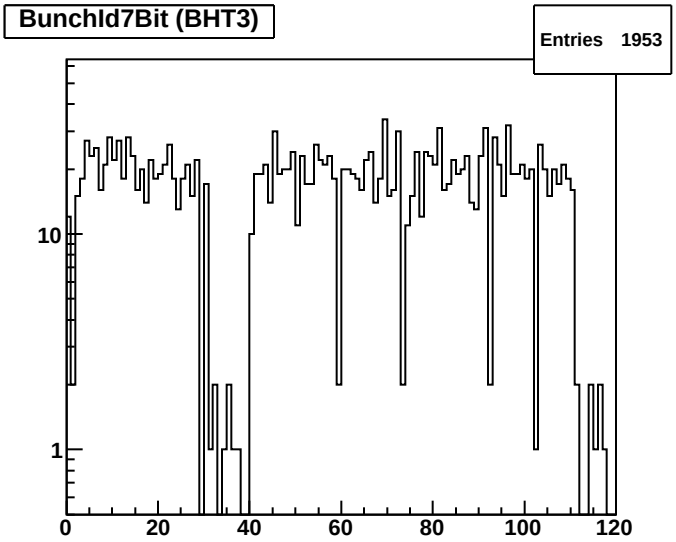

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

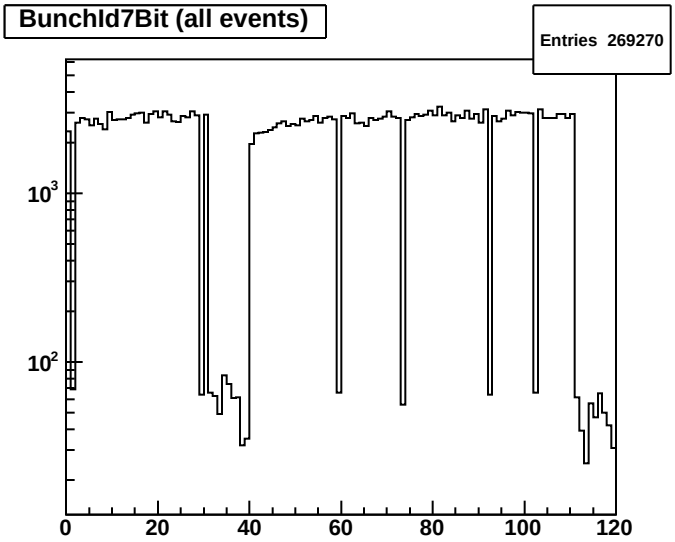

Last DSM Ch3 Input
Entries 4308320
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

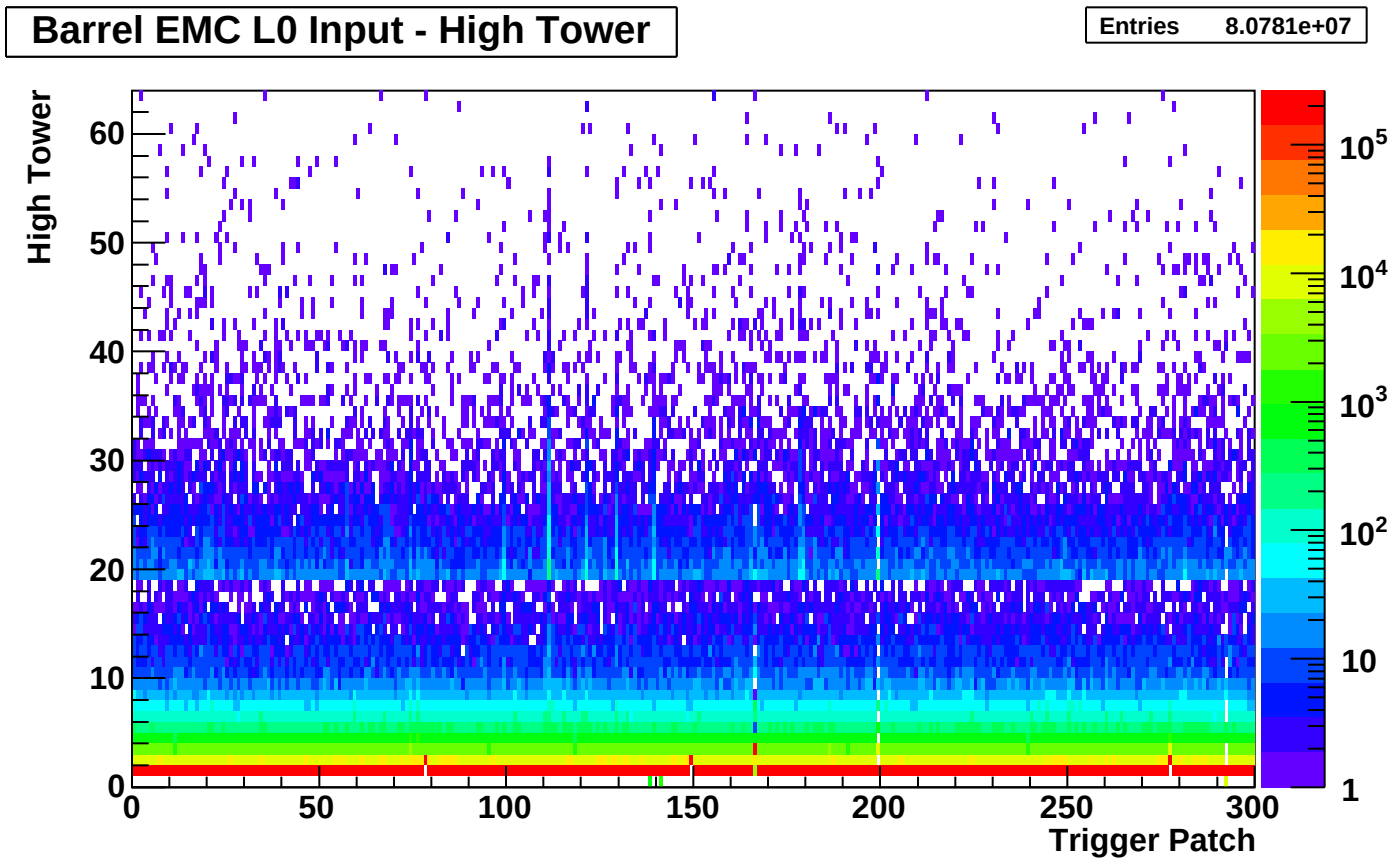

Last DSM Ch3 Input
Entries 33
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

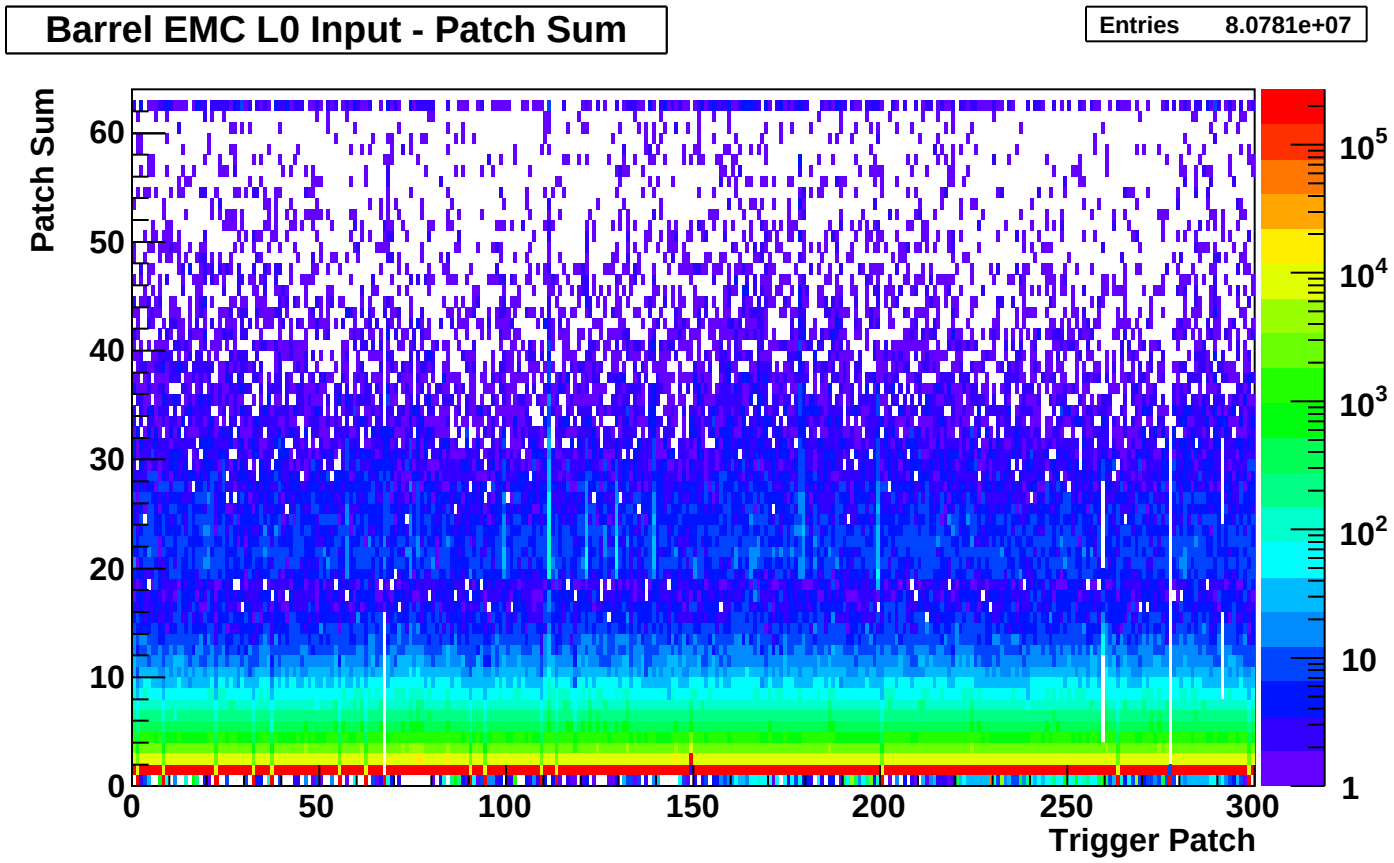

TCU bits
Entries 4308320
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

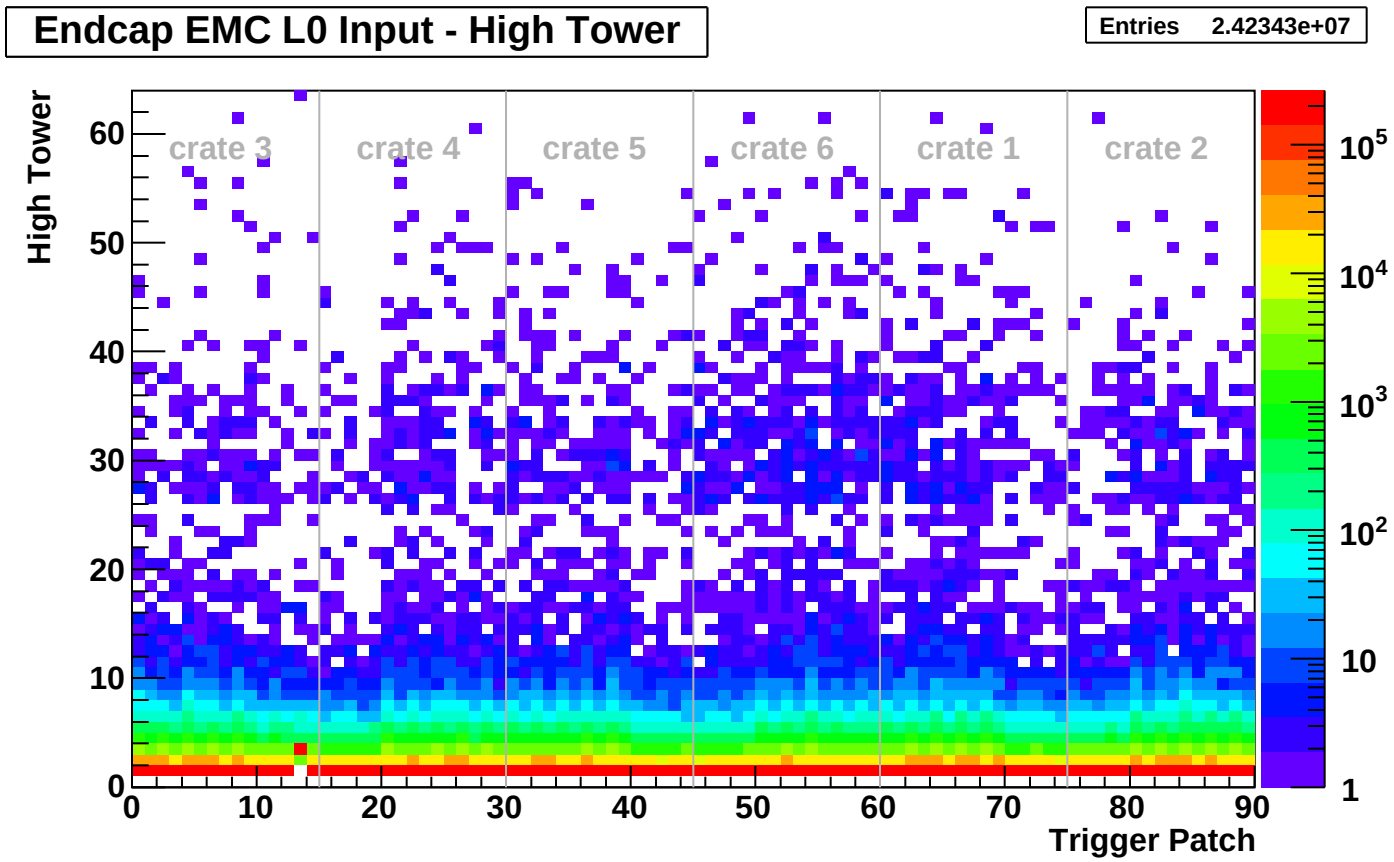

TCU bits
Entries 348259
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

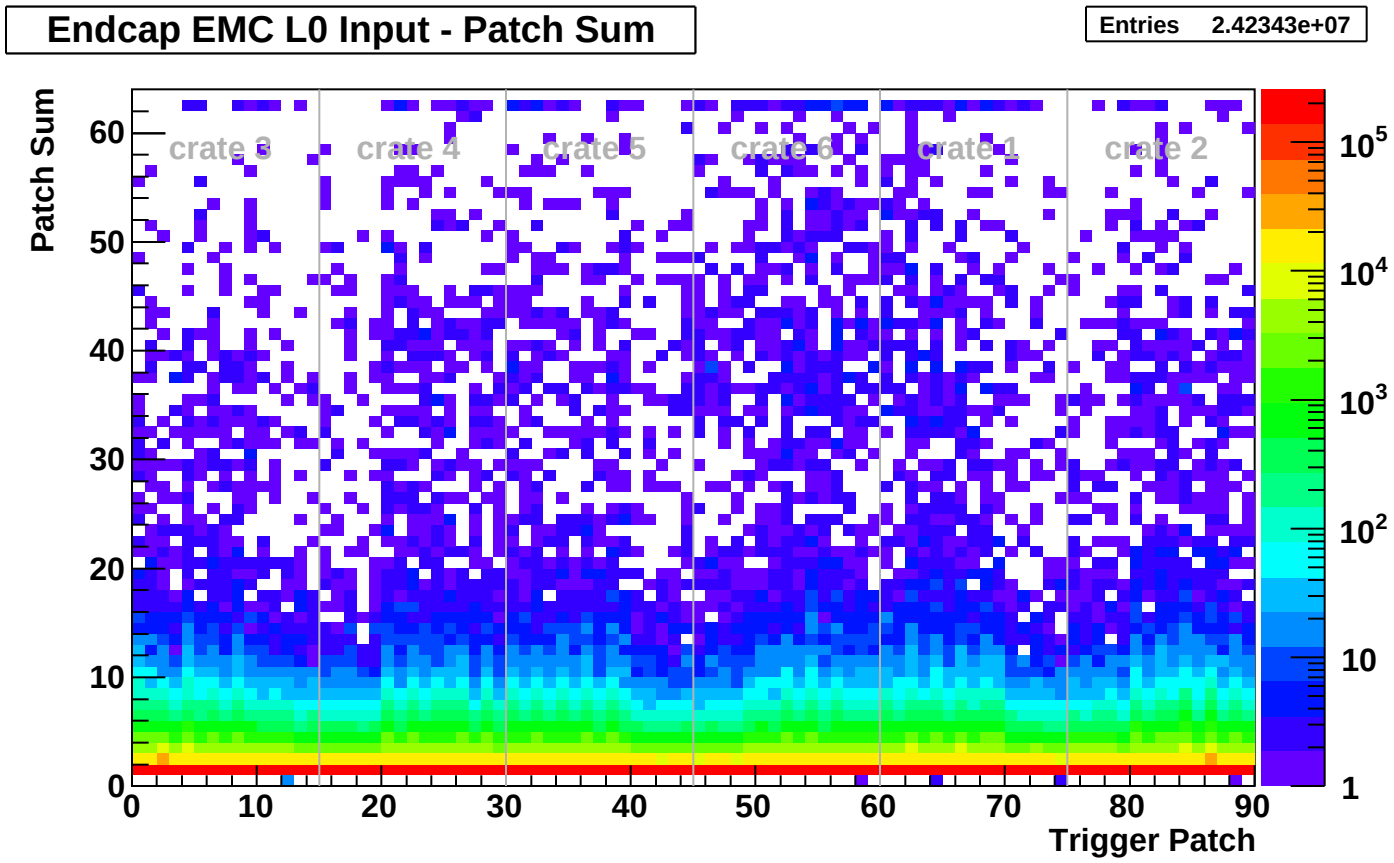

BunchId7Bit (BHT3)
Entries 1953

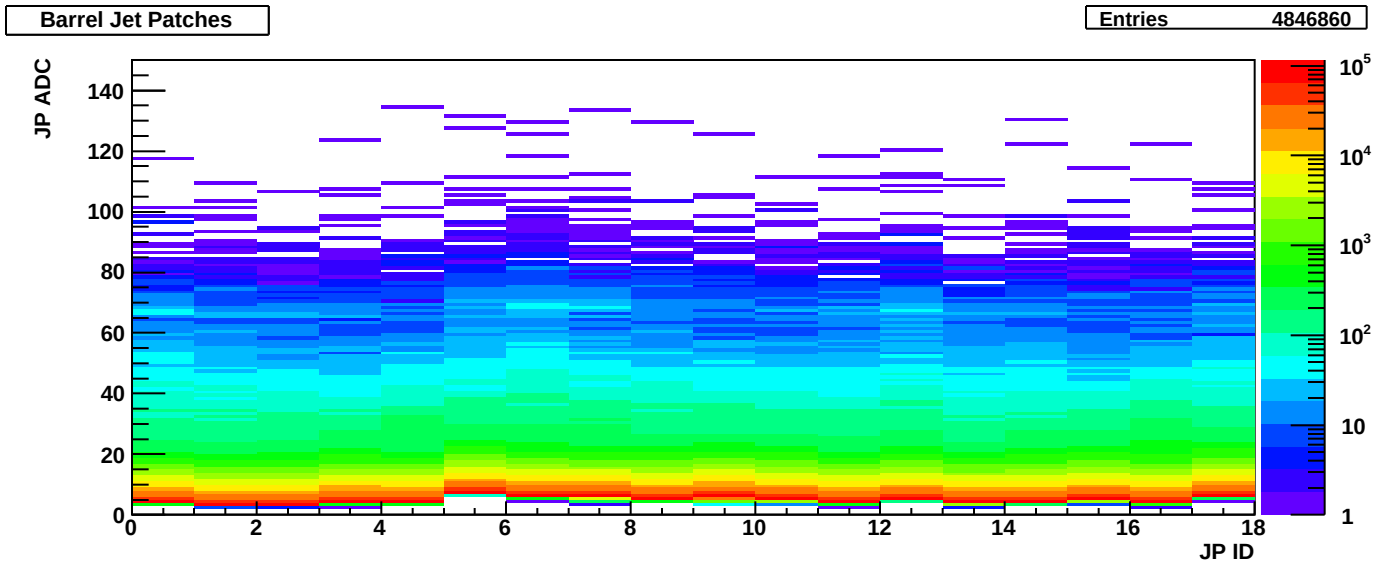

BunchId7Bit (all events)
Entries 269270

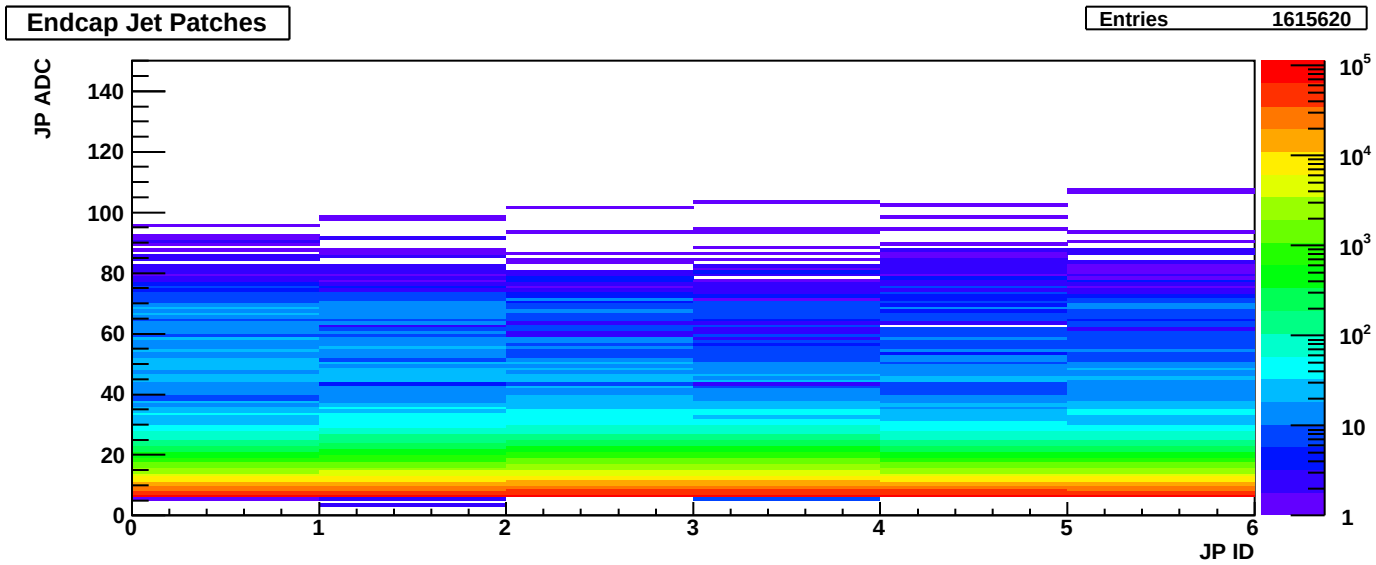

Barrel EMC L0 Input - High Tower
Entries 8.0781e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

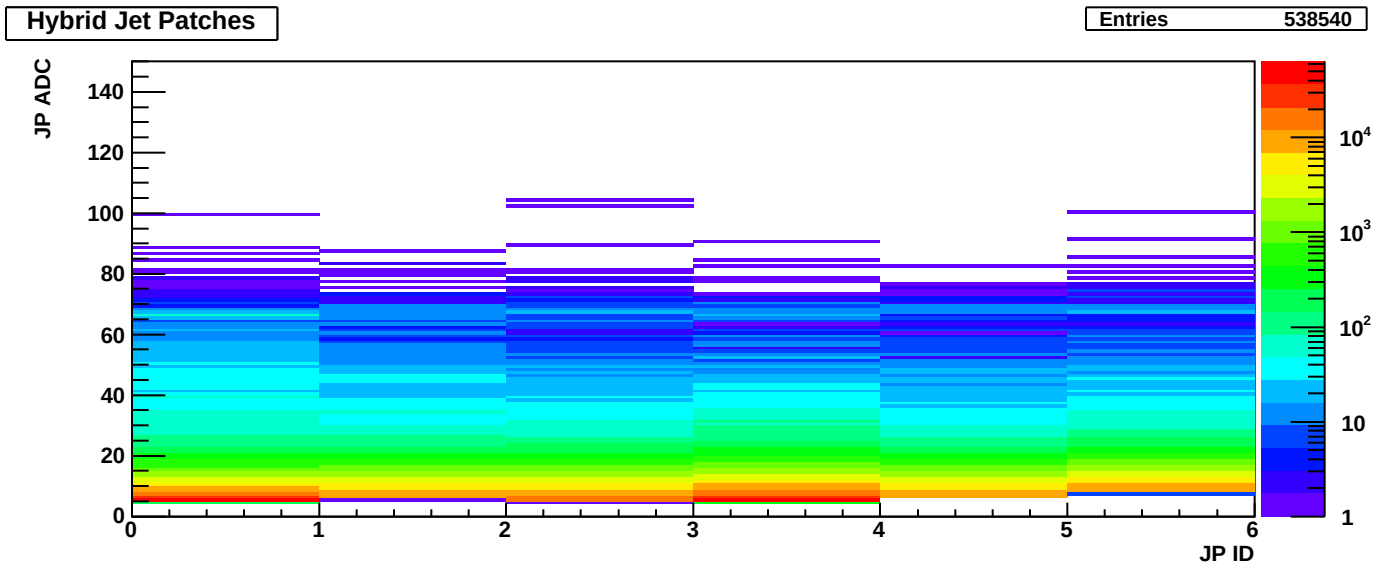

Barrel EMC L0 Input - Patch Sum
Entries 8.0781e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

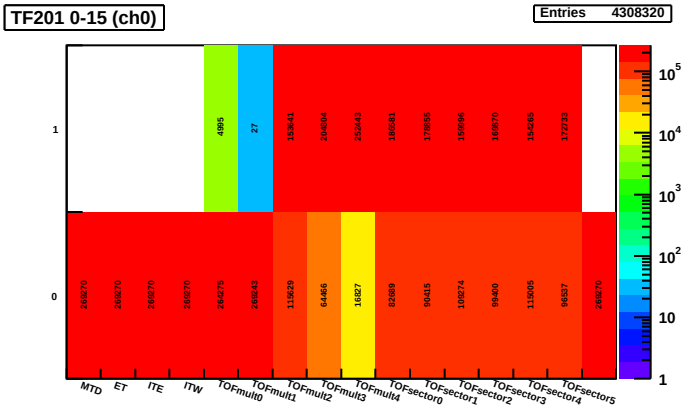

Endcap EMC L0 Input - High Tower
Entries 2.42343e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

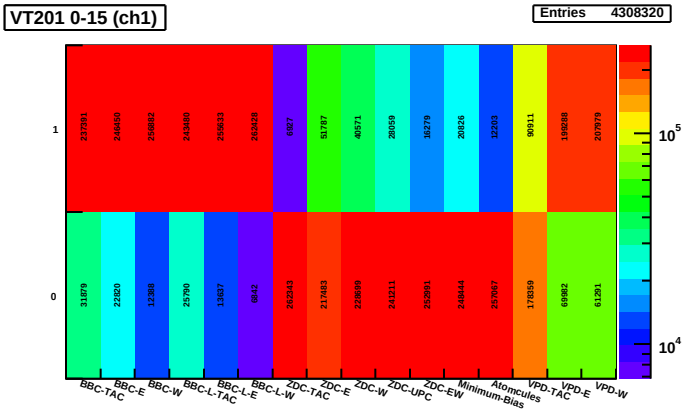

Endcap EMC L0 Input - Patch Sum
Entries 2.42343e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

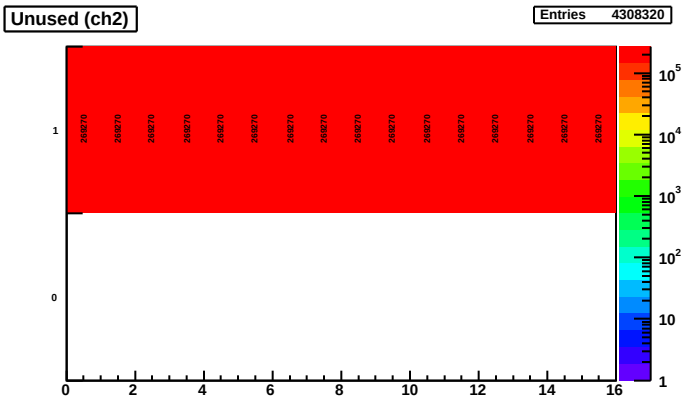

Barrel Jet Patches
Entries 4846860
JP ADC
JP ID

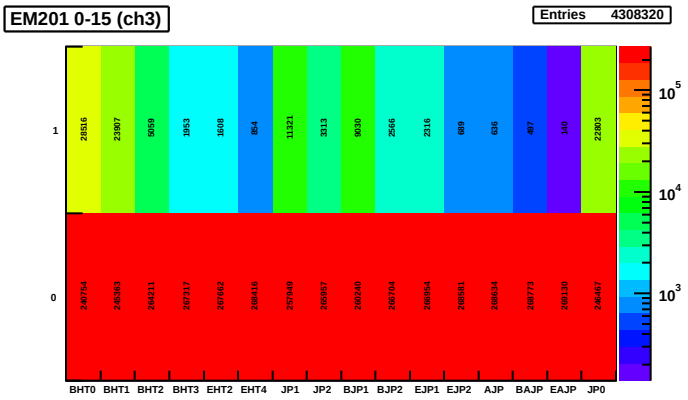

Endcap Jet Patches
Entries 1615620
JP ADC
JP ID

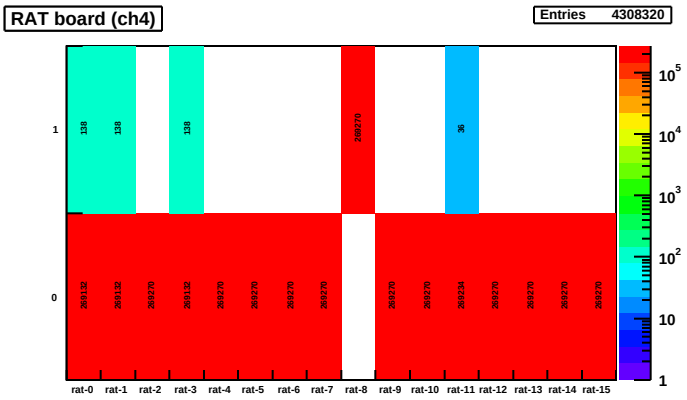

Hybrid Jet Patches
Entries 538540
JP ADC
JP ID

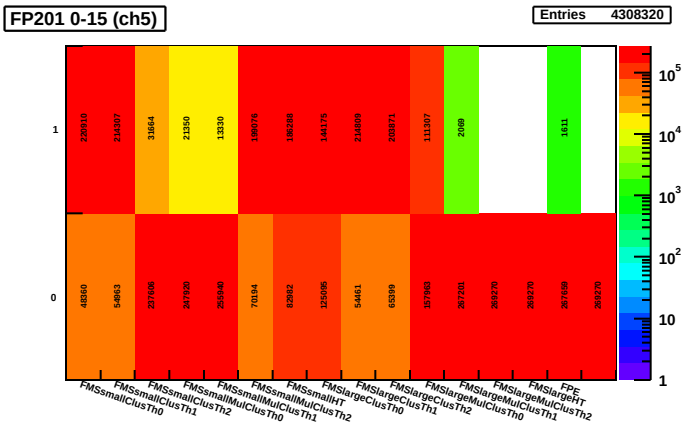

TF201 0-15 (ch0)
Entries 4308320
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

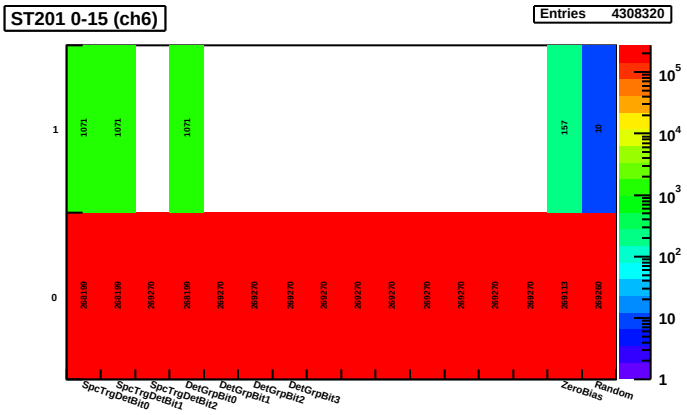

VT201 0-15 (ch1)
Entries 4308320
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

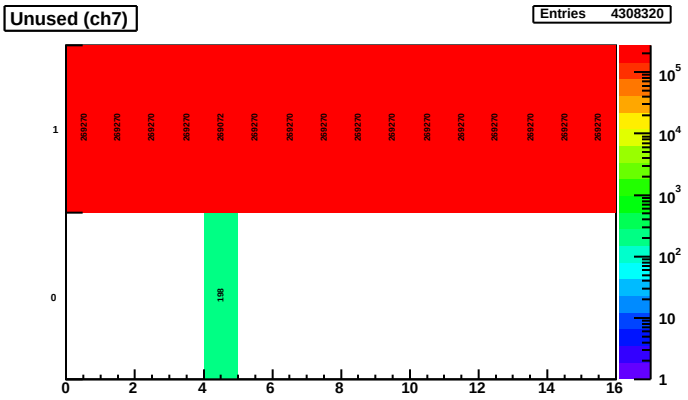

Unused (ch2)
Entries 4308320

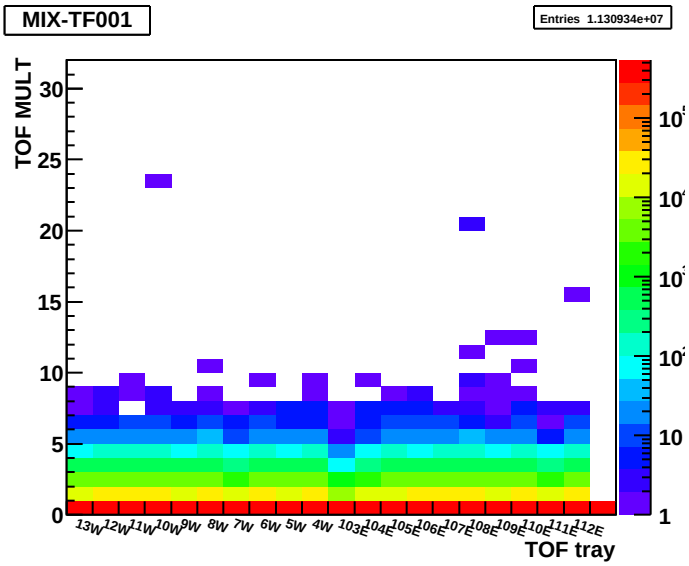

EM201 0-15 (ch3)
Entries 4308320
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

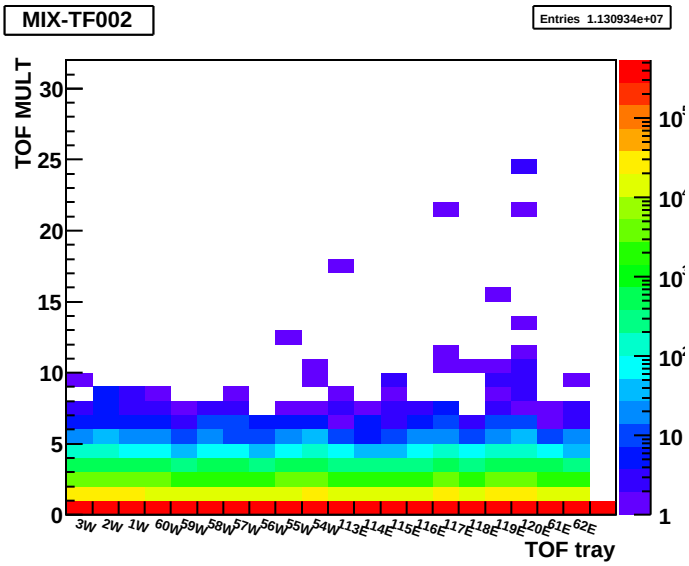

RAT board (ch4)
Entries 4308320
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

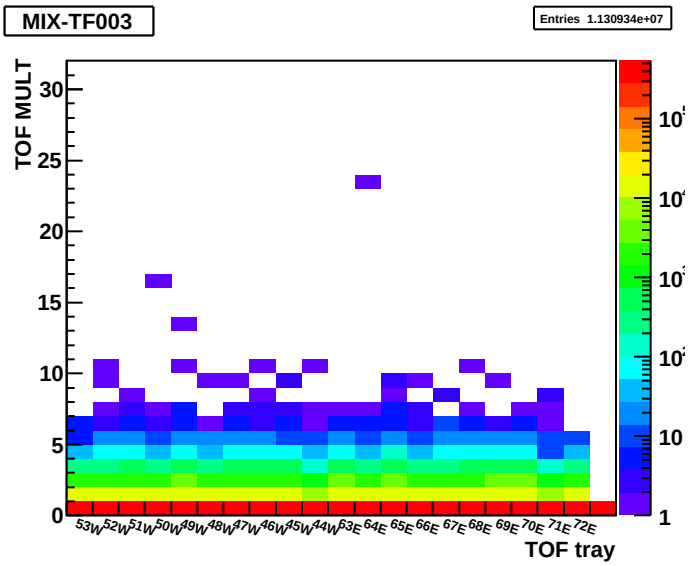

FP201 0-15 (ch5)
Entries 4308320
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

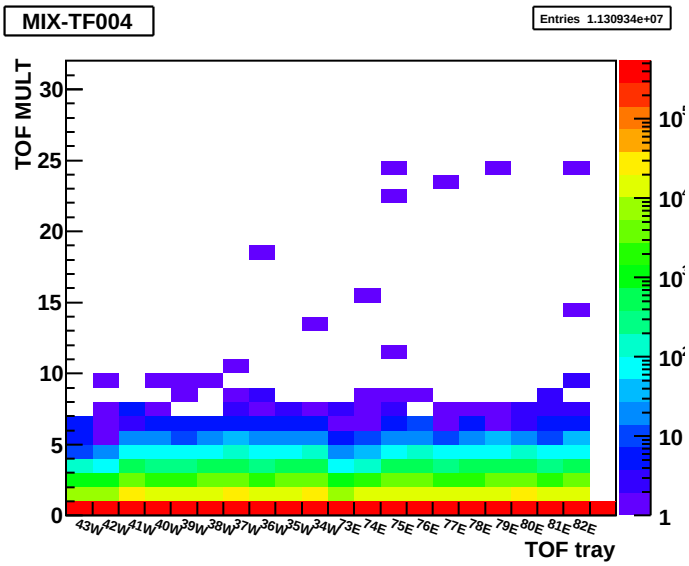

ST201 0-15 (ch6)
Entries 4308320
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

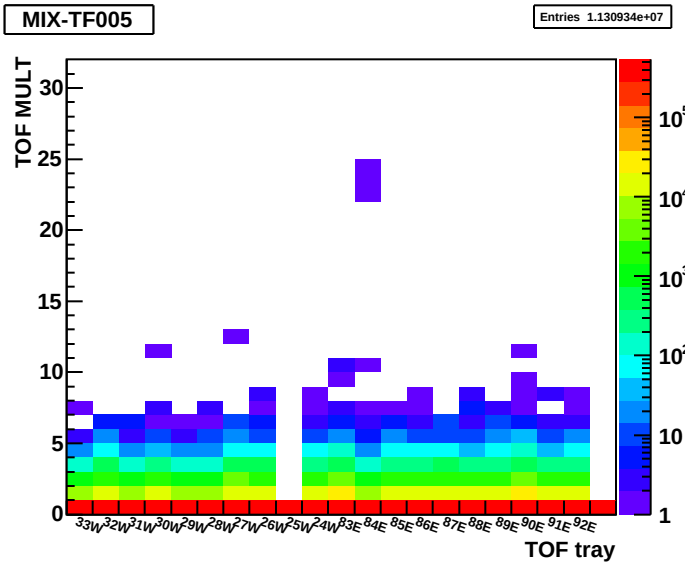

Unused (ch7)
Entries 4308320

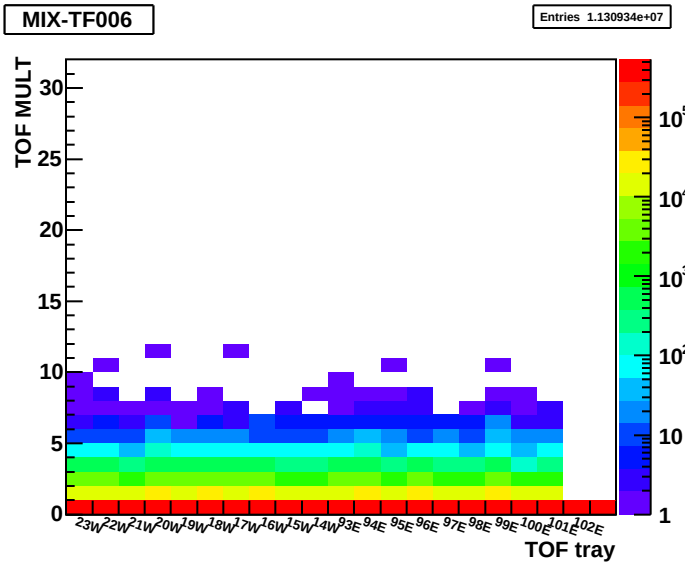

MIX-TF001
Entries 1.130934e+07
TOF MULT
TOF tray

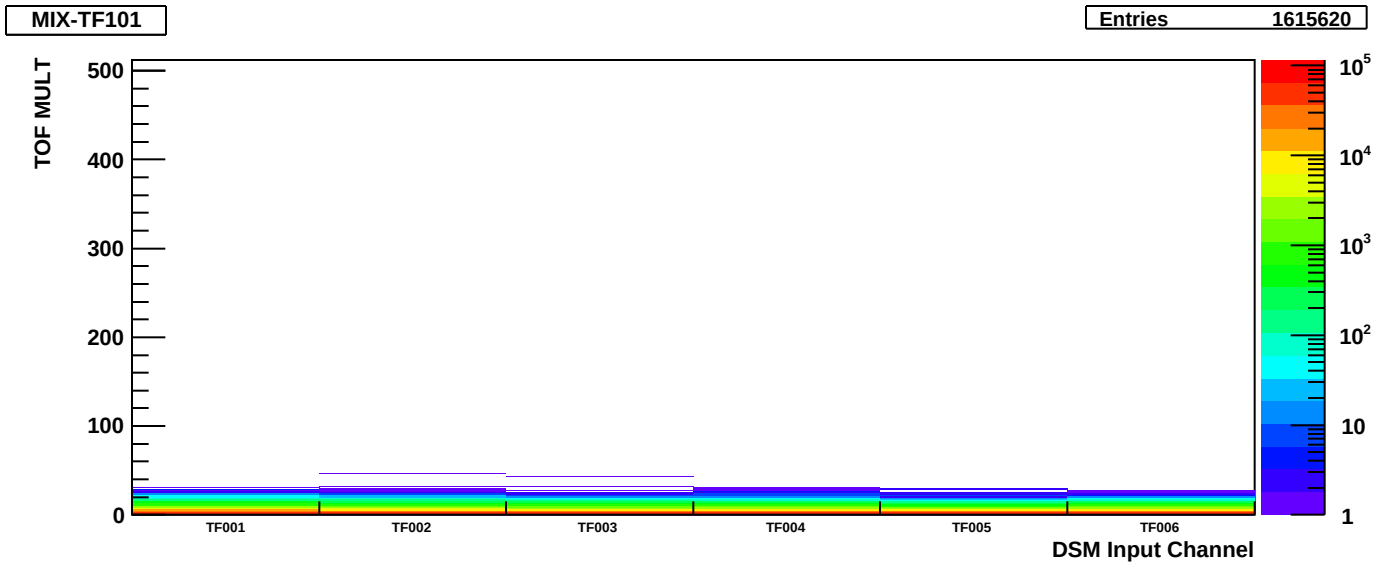

MIX-TF002
Entries 1.130934e+07
TOF MULT
TOF tray

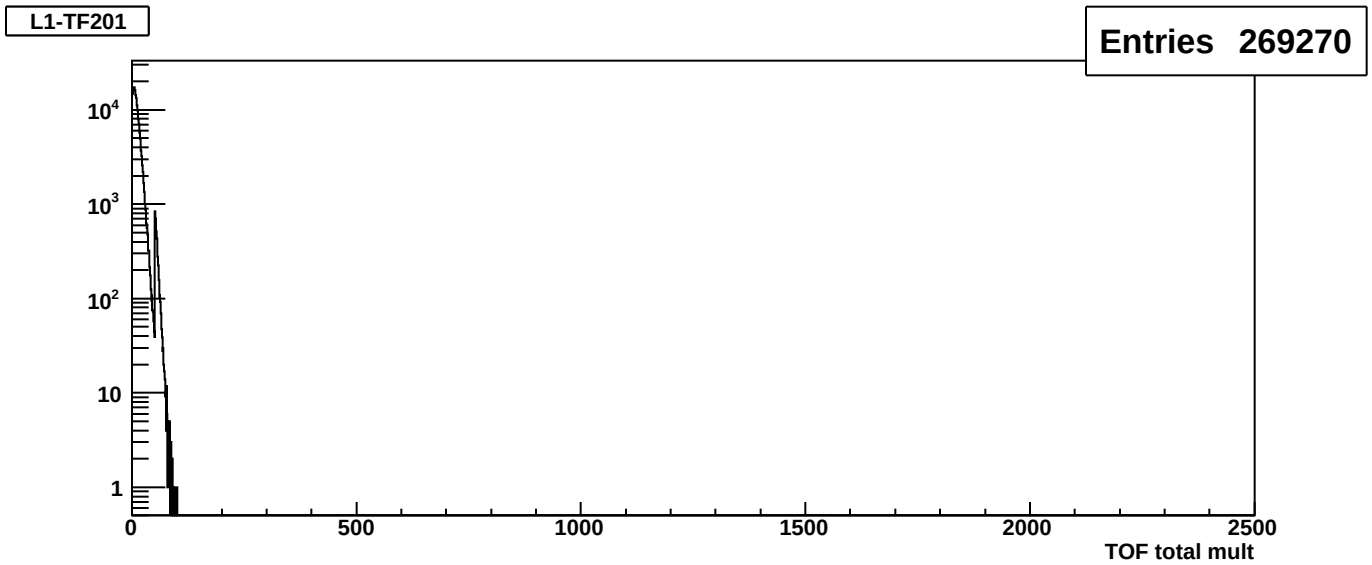

MIX-TF003
Entries 1.130934e+07
TOF MULT
TOF tray

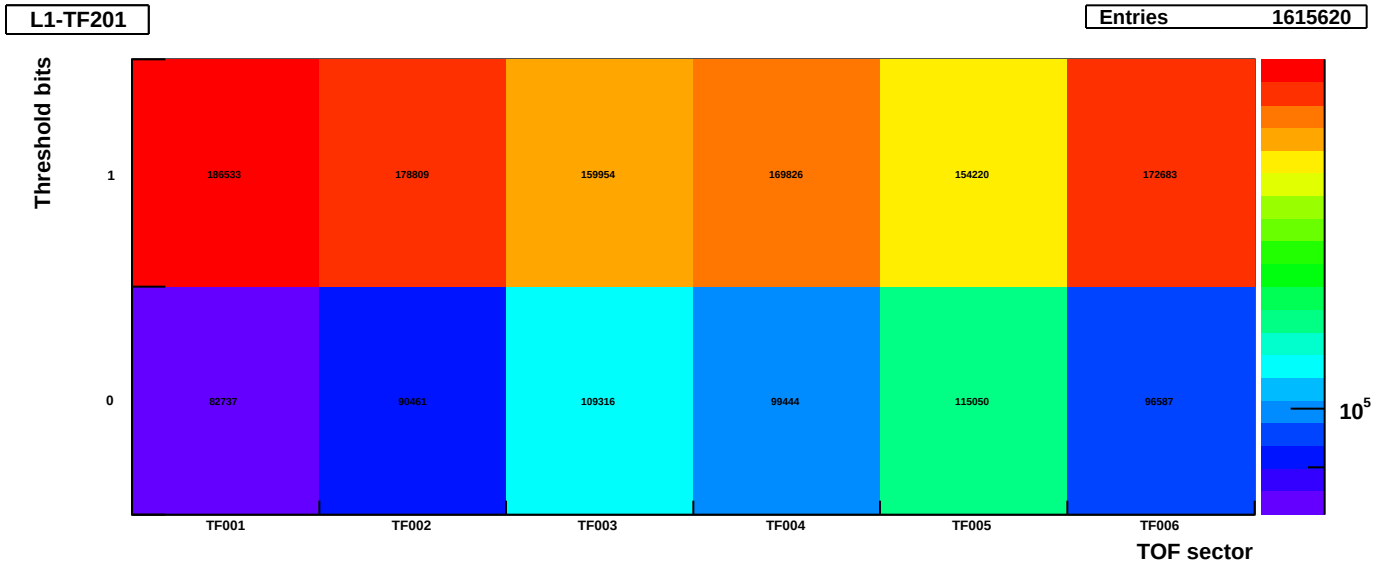

MIX-TF004
Entries 1.130934e+07
TOF MULT
TOF tray

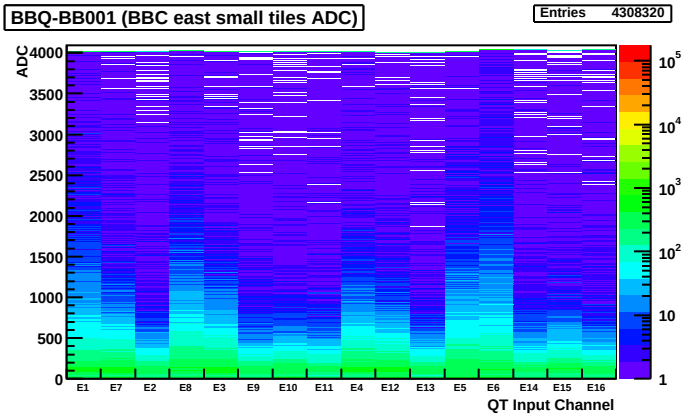

MIX-TF005
Entries 1.130934e+07
TOF MULT
TOF tray

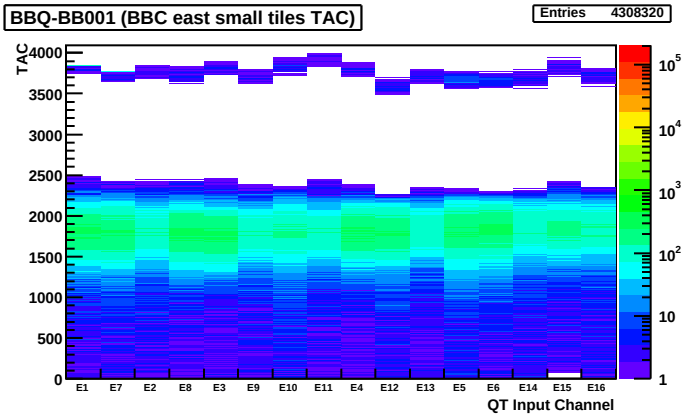

MIX-TF006
Entries 1.130934e+07
TOF MULT
TOF tray

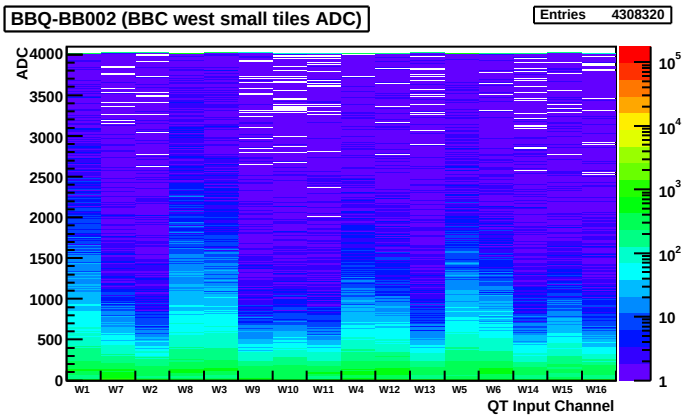

MIX-TF101
Entries 1615620
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

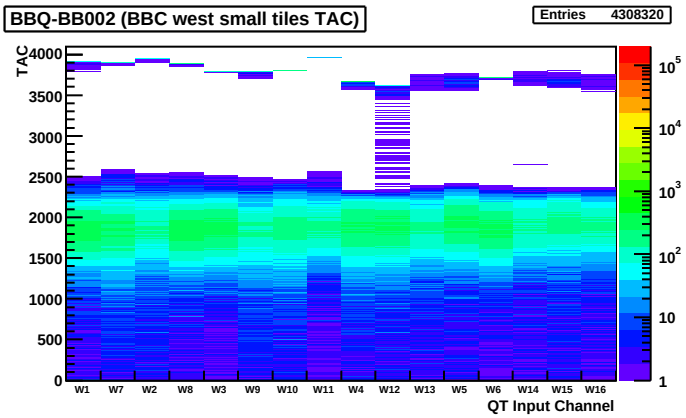

L1-TF201
Entries 269270
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

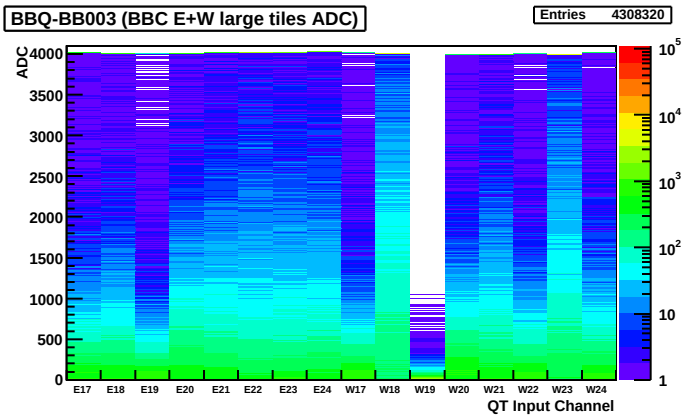

L1-TF201
Entries 1615620
Threshold bits
1
0
186533
178809
159954
169826
154220
172683
82737
90461
109316
99444
115050
96587
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

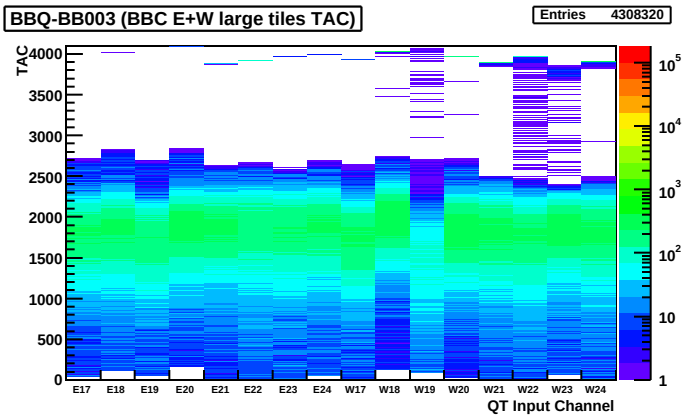

BBQ-BB001 (BBC east small tiles ADC)
Entries 4308320
ADC
QT Input Channel

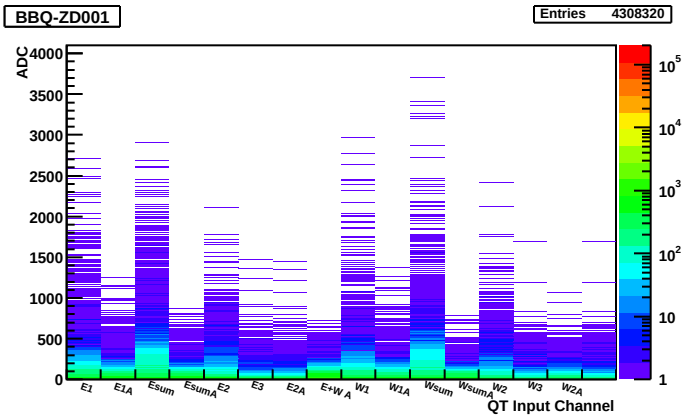

BBQ-BB001 (BBC east small tiles TAC)
Entries 4308320
TAC
QT Input Channel

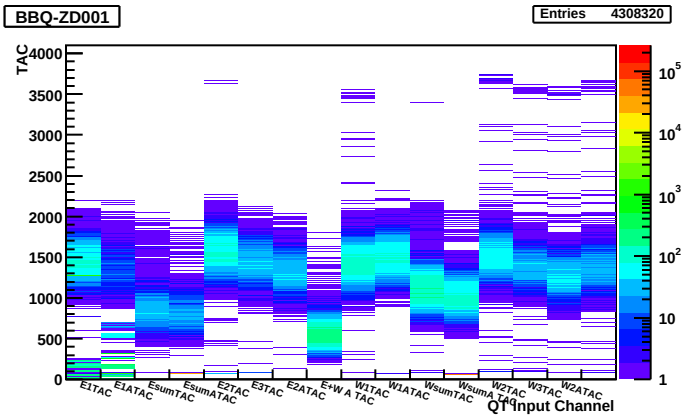

BBQ-BB002 (BBC west small tiles ADC)
Entries 4308320
ADC
QT Input Channel

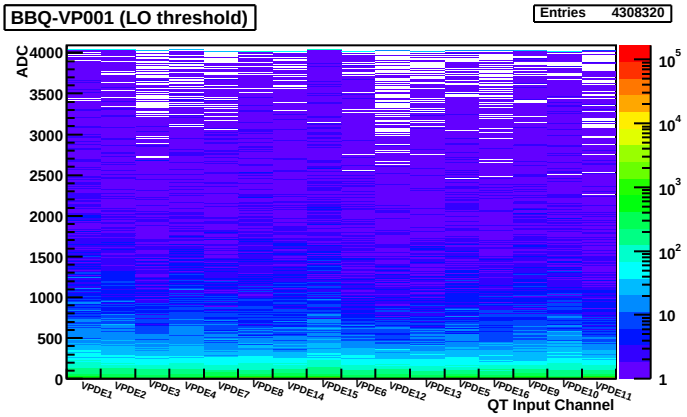

BBQ-BB002 (BBC west small tiles TAC)
Entries 4308320
TAC
QT Input Channel

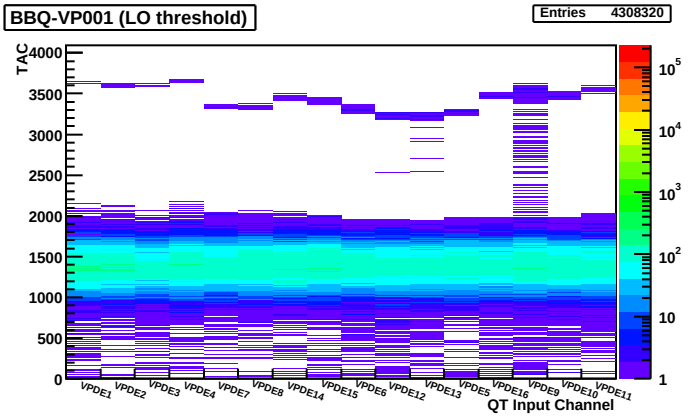

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4308320
ADC
QT Input Channel

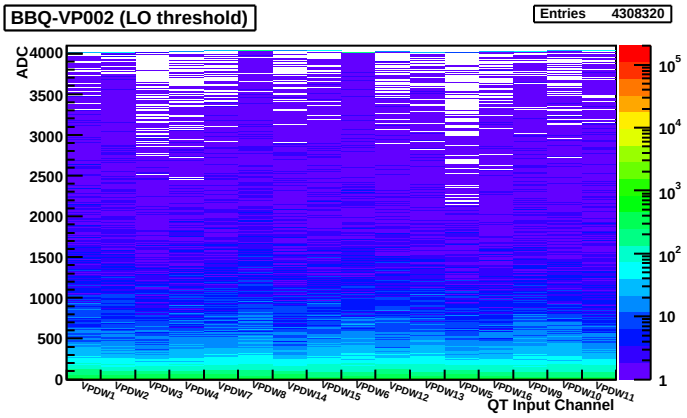

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4308320
TAC
QT Input Channel

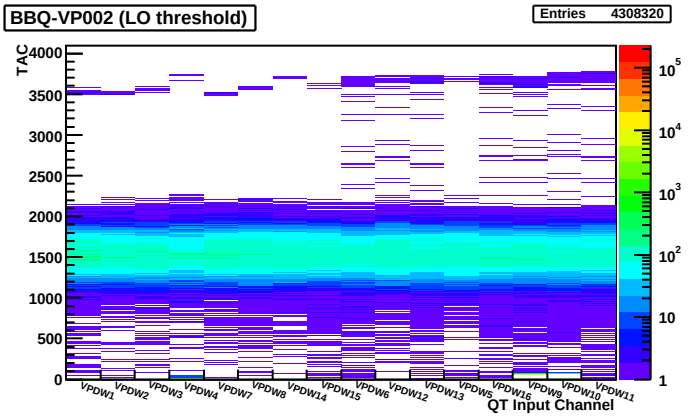

BBQ-ZD001
Entries 4308320
ADC
QT Input Channel

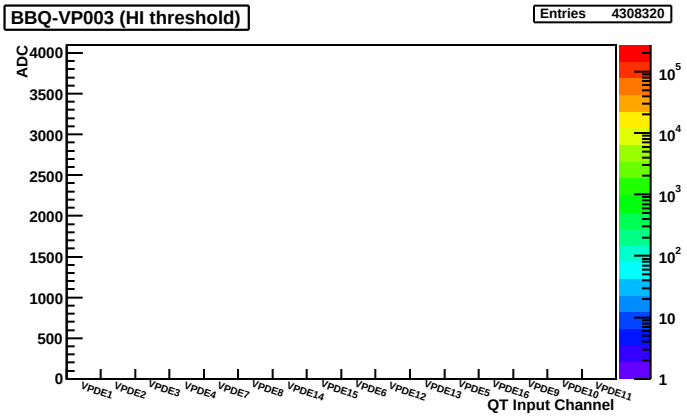

BBQ-ZD001
Entries 4308320
TAC
QT Input Channel

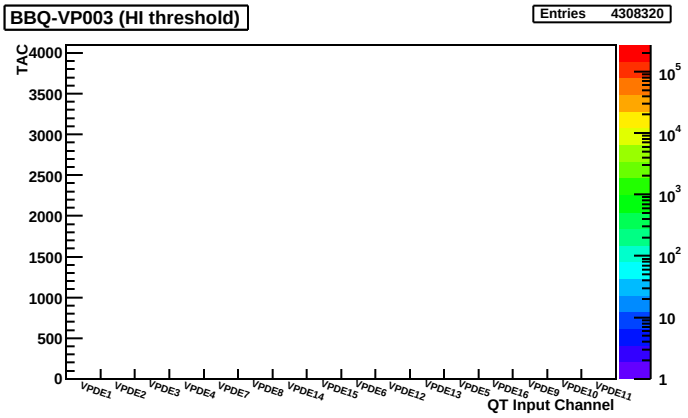

BBQ-VP001 (LO threshold)
Entries 4308320
ADC
QT Input Channel

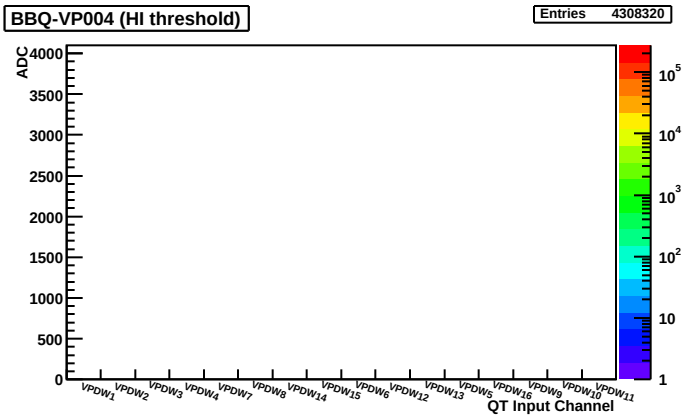

BBQ-VP001 (LO threshold)
Entries 4308320
TAC
QT Input Channel

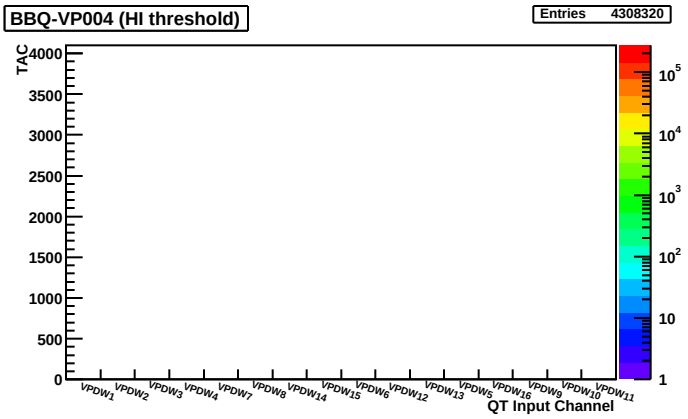

BBQ-VP002 (LO threshold)
Entries 4308320
ADC
QT Input Channel

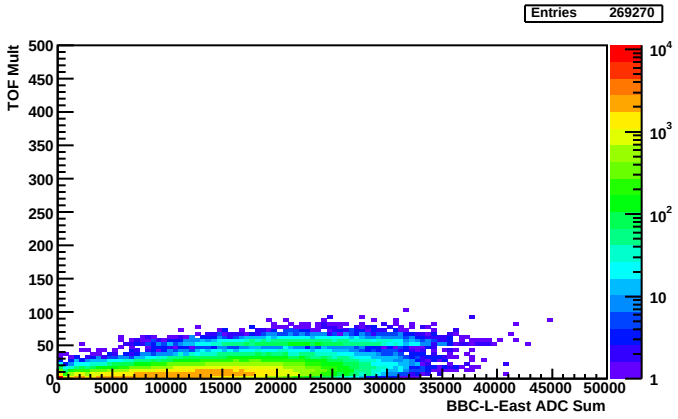

BBQ-VP002 (LO threshold)
Entries 4308320
TAC
QT Input Channel

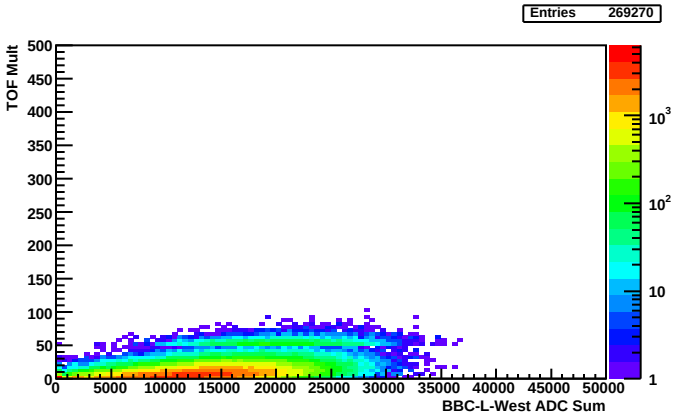

BBQ-VP003 (HI threshold)
Entries 4308320
ADC
QT Input Channel

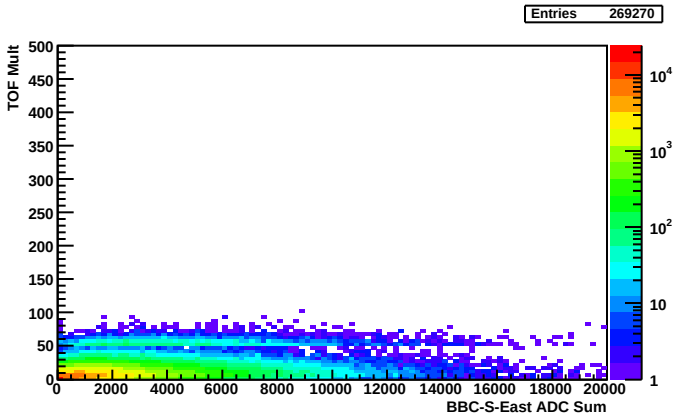

BBQ-VP003 (HI threshold)
Entries 4308320
TAC
QT Input Channel

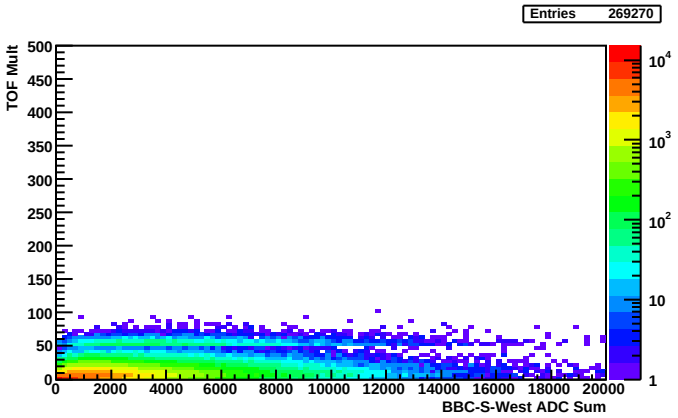

BBQ-VP004 (HI threshold)
Entries 4308320
ADC
QT Input Channel

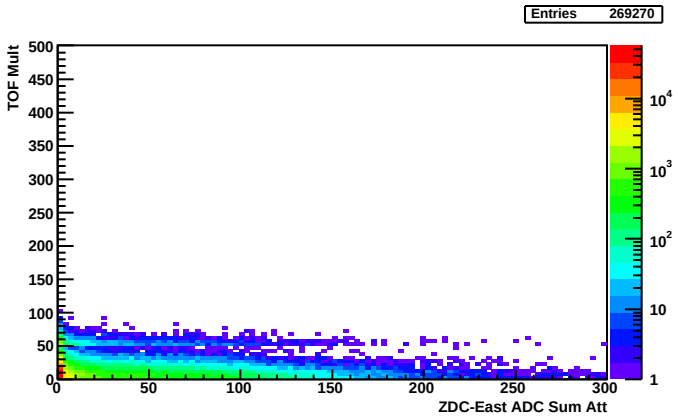

BBQ-VP004 (HI threshold)
Entries 4308320
TAC
QT Input Channel

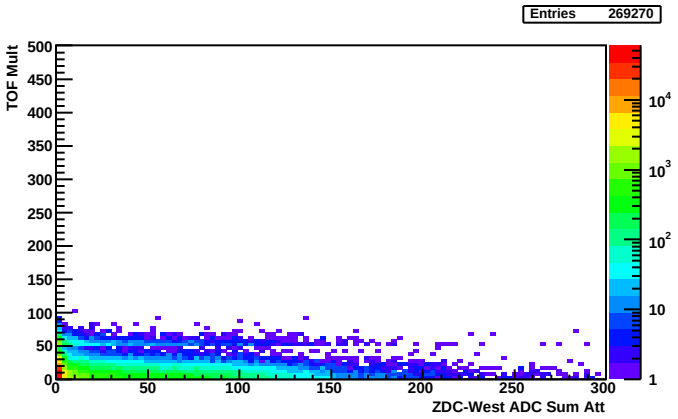

Entries 269270
TOF Mult
BBC-L-East ADC Sum

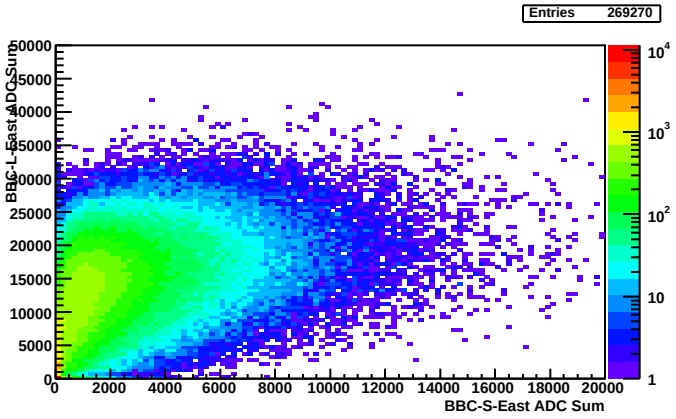

Entries 269270
TOF Mult
BBC-L-West ADC Sum

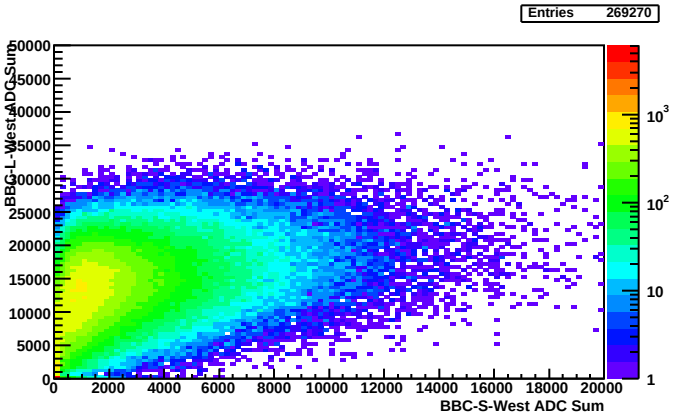

Entries 269270
TOF Mult
BBC-S-East ADC Sum

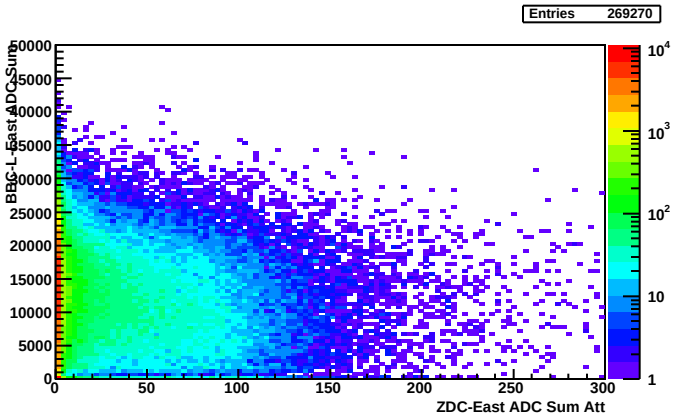

Entries 269270
TOF Mult
BBC-S-West ADC Sum

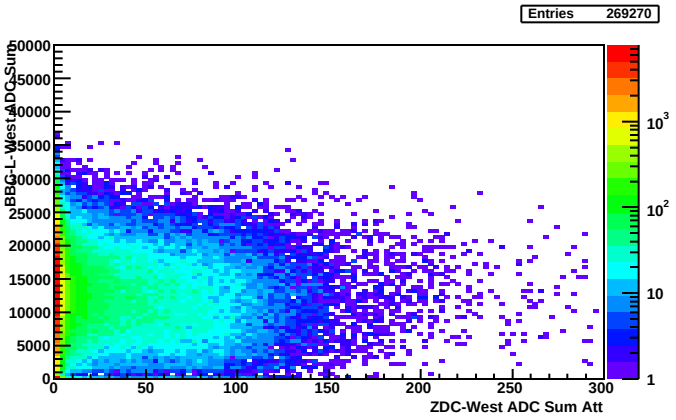

Entries 269270
TOF Mult
ZDC-East ADC Sum Att

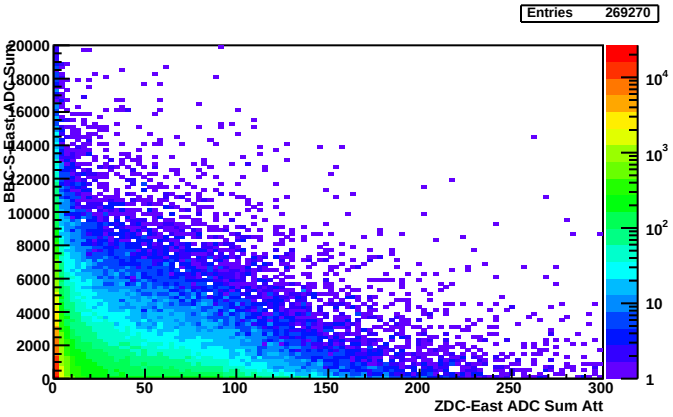

Entries 269270
TOF Mult
ZDC-West ADC Sum Att

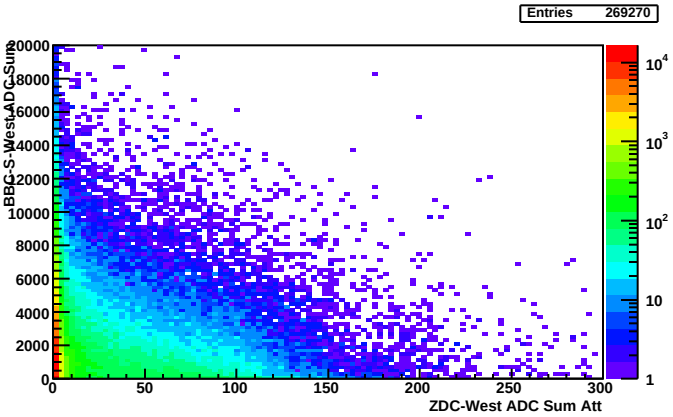

Entries 269270
BBC-L-East ADC Sum
BBC-S-East ADC Sum

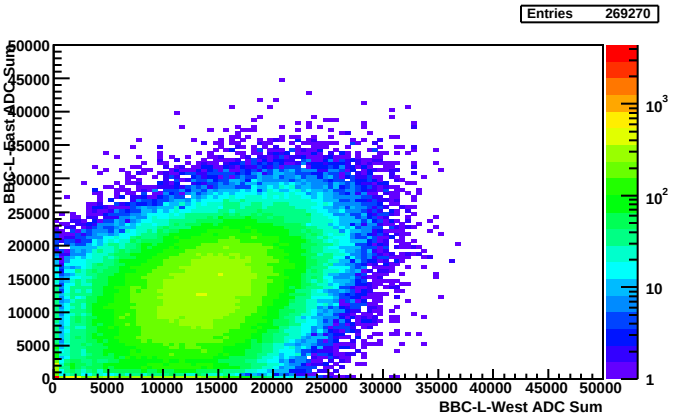

Entries 269270
BBC-L-West ADC Sum
BBC-S-West ADC Sum

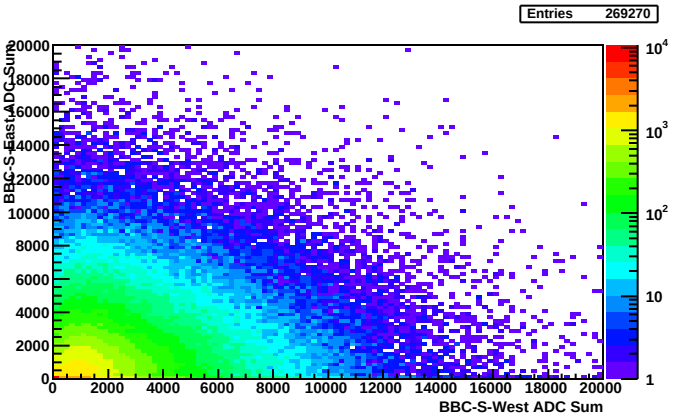

Entries 269270
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

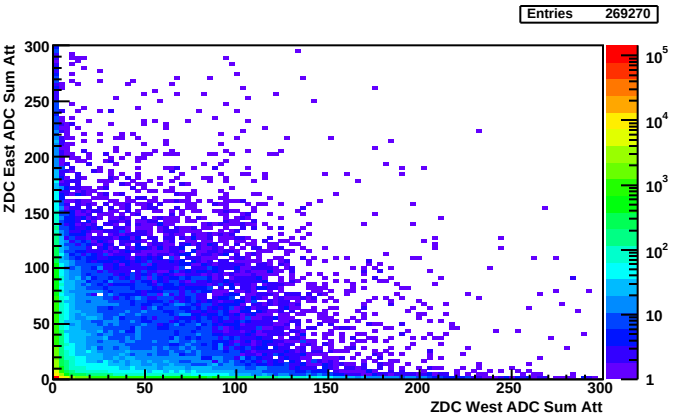

Entries 269270
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

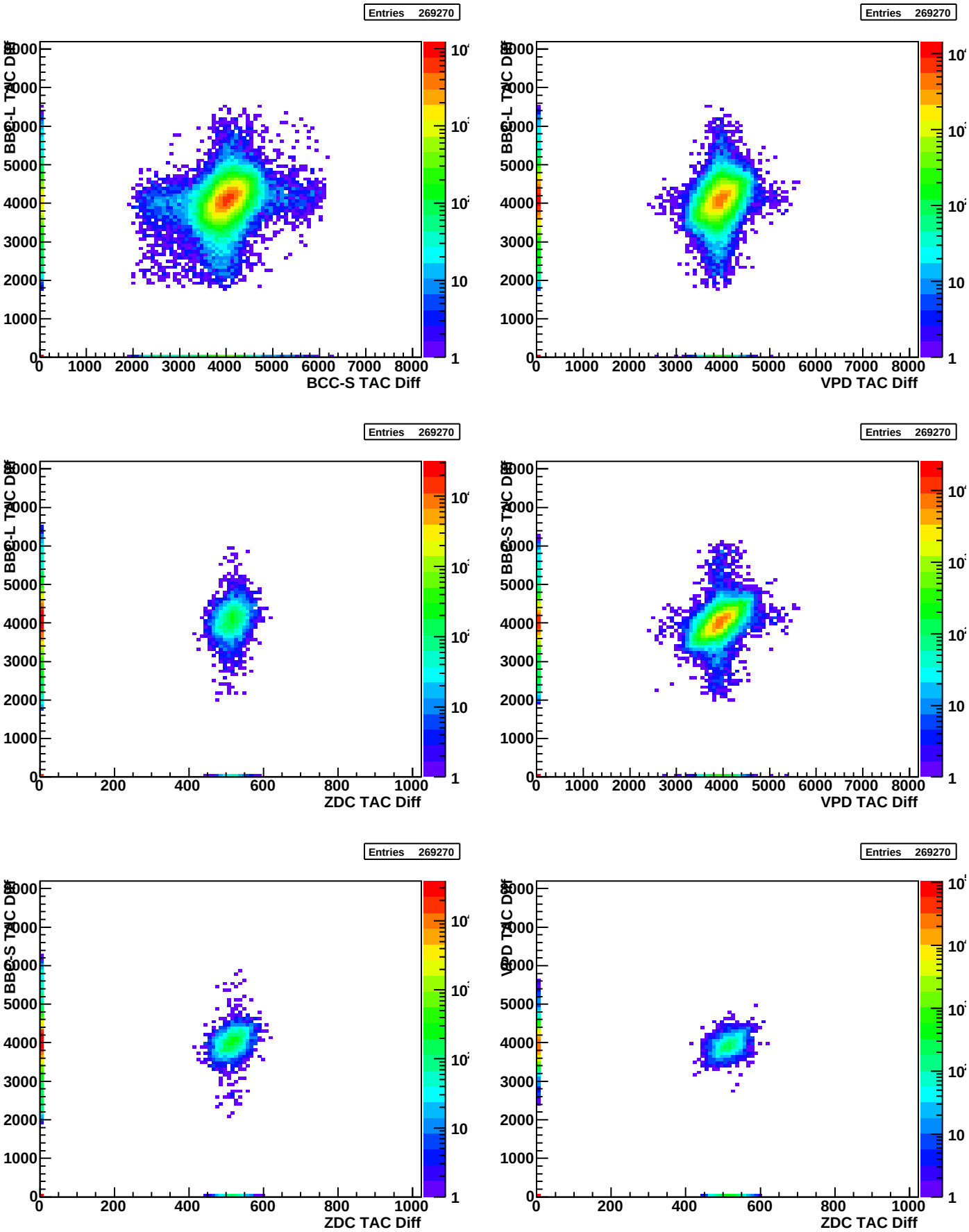

Entries 269270
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

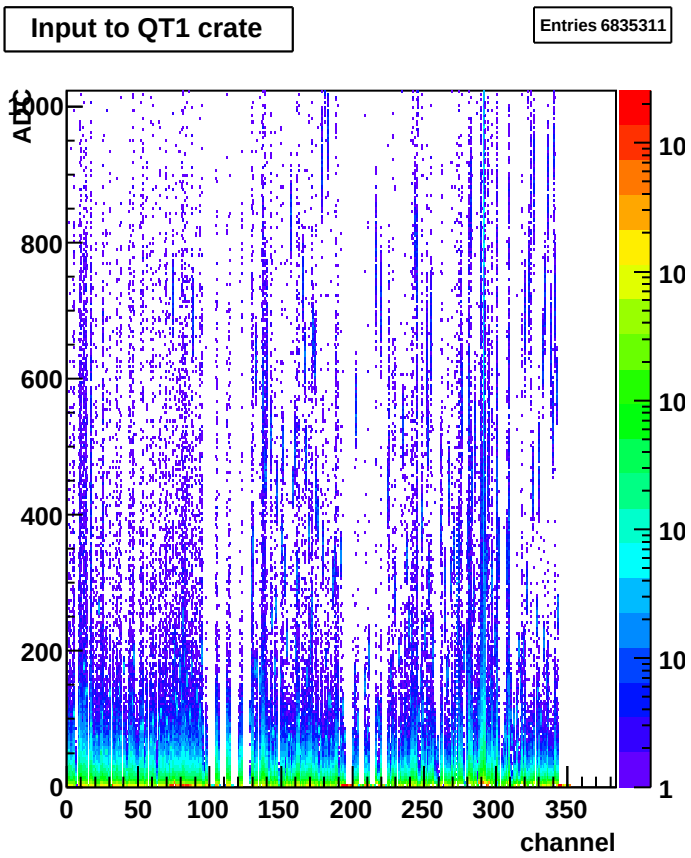

Entries 269270
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

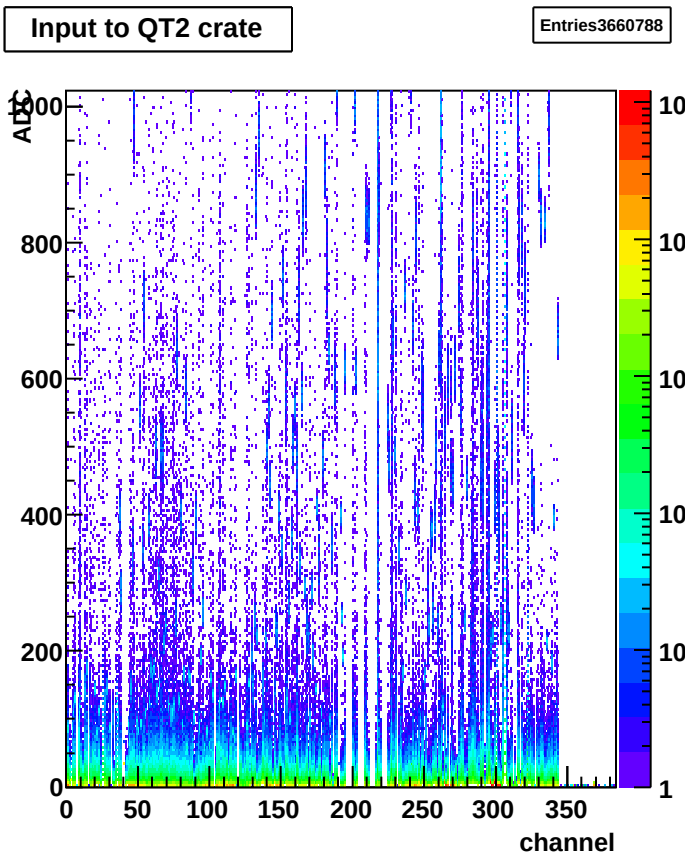

Entries 269270
BBC-L-East ADC Sum
BBC-L-West ADC Sum

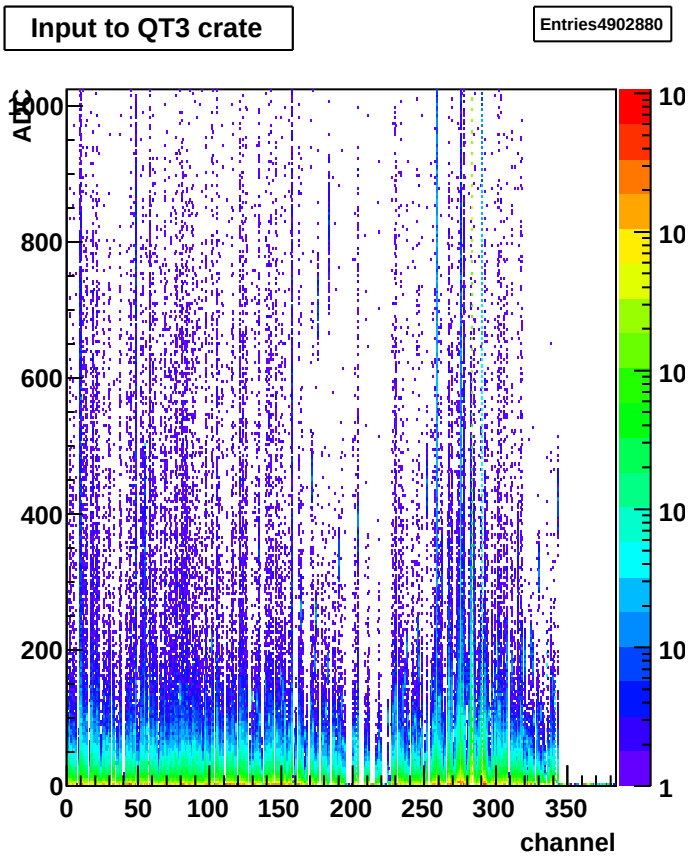

Entries 269270
BBC-S-East ADC Sum
BBC-S-West ADC Sum

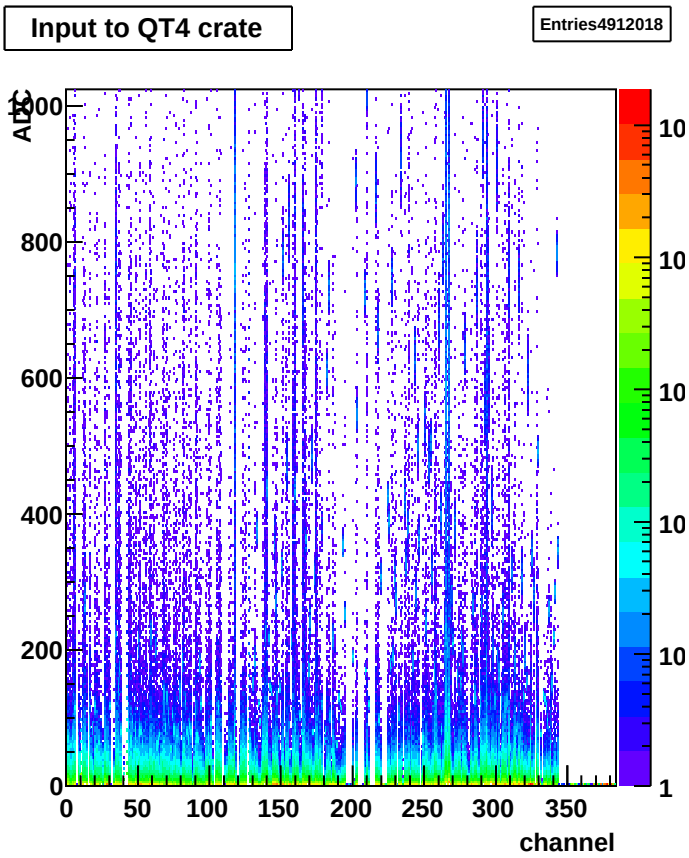

Entries 269270
ZDC East ADC Sum Att
ZDC West ADC Sum Att

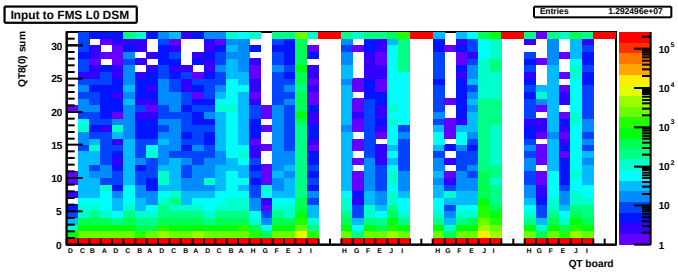

Entries 269270
BBC-L TAC Diff
BCC-S TAC Diff
Entries 269270
BBC-L TAC Diff
VPD TAC Diff
Entries 269270
BBC-L TAC Diff
ZDC TAC Diff
Entries 269270
BBC-S TAC Diff
VPD TAC Diff
Entries 269270
BBC-S TAC Diff
ZDC TAC Diff
Entries 269270
VPD TAC Diff
ZDC TAC Diff

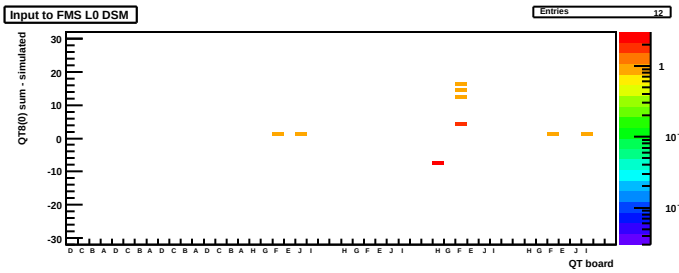

Input to QT1 crate
Entries 6835311
ADC
channel

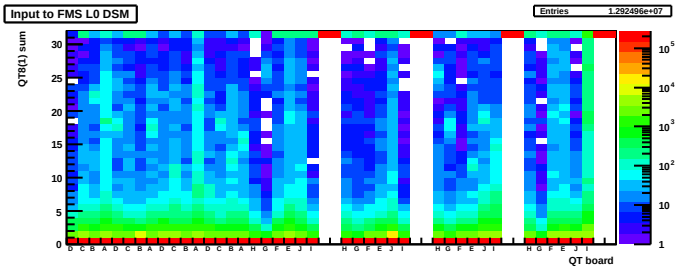

Input to QT2 crate
Entries3660788
ADC
channel

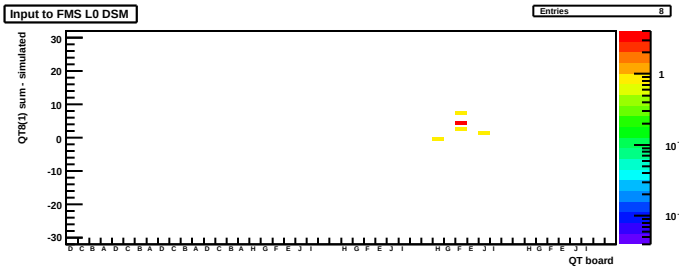

Input to QT3 crate
Entries4902880
ADC
channel

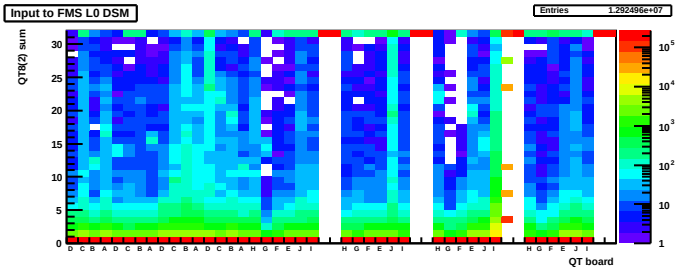

Input to QT4 crate
Entries4912018
ADC
channel

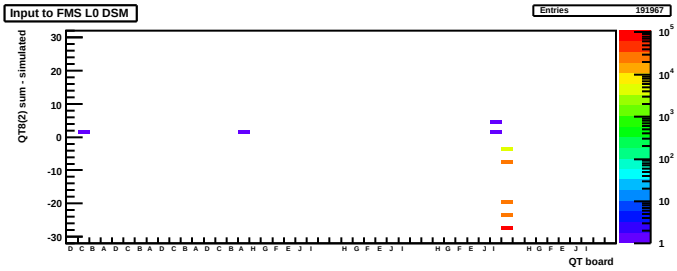

Input to FMS L0 DSM
Entries 1.292496e+07
QT8(0) sum
QT board

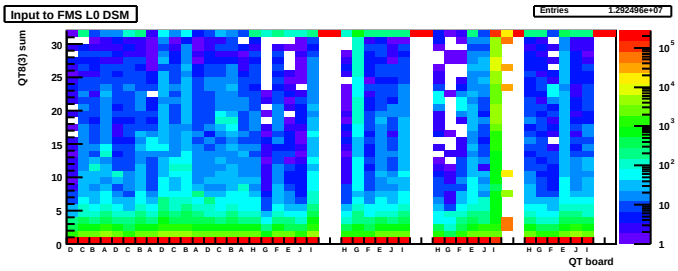

Input to FMS L0 DSM
Entries 12
QT8(0) sum - simulated
QT board

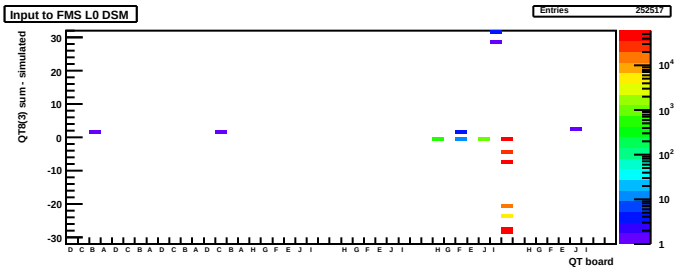

Input to FMS L0 DSM
Entries 1.292496e+07
QT8(1) sum
QT board

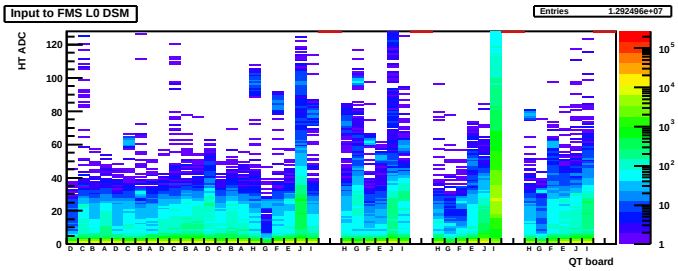

Input to FMS L0 DSM
Entries 8
QT8(1) sum - simulated
QT board

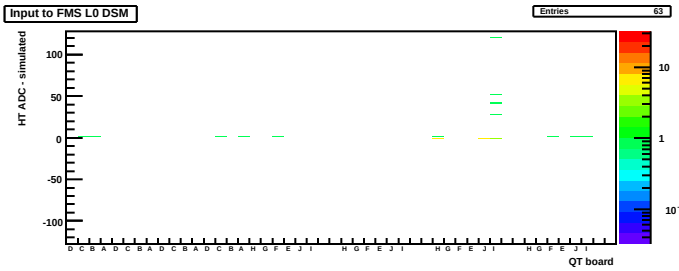

Input to FMS L0 DSM
Entries 1.292496e+07
QT8(2) sum
QT board

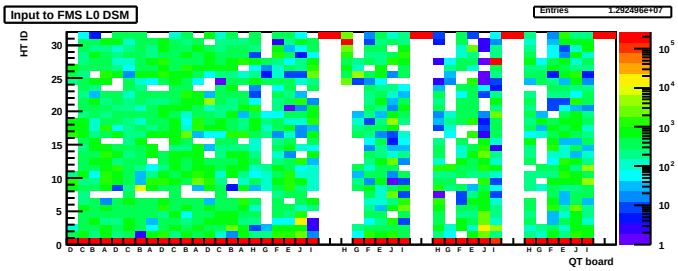

Input to FMS L0 DSM
Entries 191967
QT8(2) sum - simulated
QT board

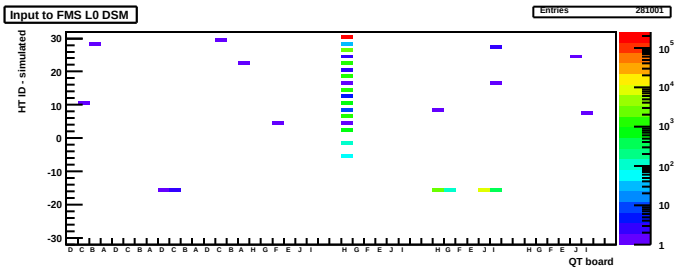

Input to FMS L0 DSM
Entries 1.292496e+07
QT8(3) sum
QT board

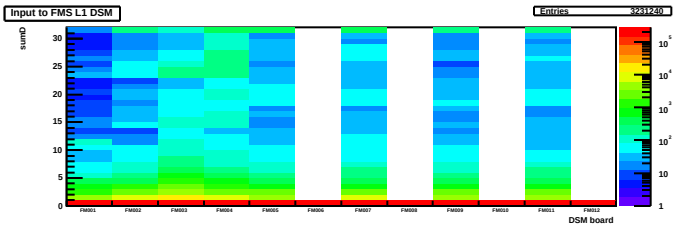

Input to FMS L0 DSM
Entries 252517
QT8(3) sum - simulated
QT board

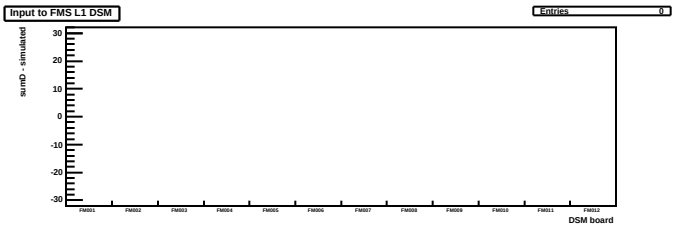

Input to FMS L0 DSM
Entries 1.292496e+07
HT ADC
QT board

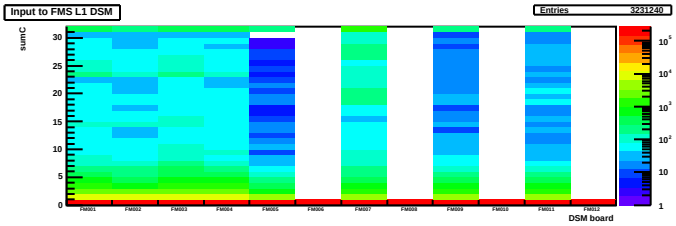

Input to FMS L0 DSM
Entries 63
HT ADC - simulated
QT board

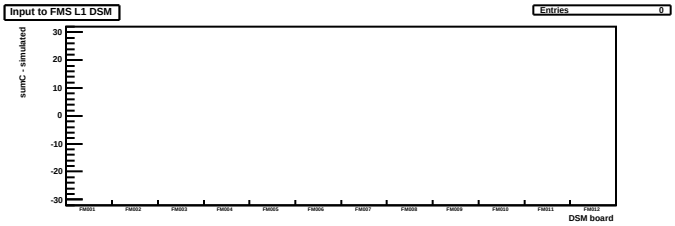

Input to FMS L0 DSM
Entries 1.292496e+07
HT ID
QT board

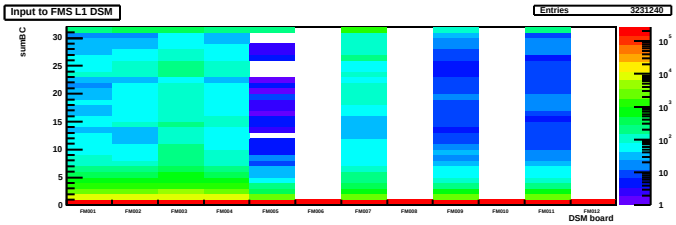

Input to FMS L0 DSM
Entries 281001
HT ID - simulated
QT board

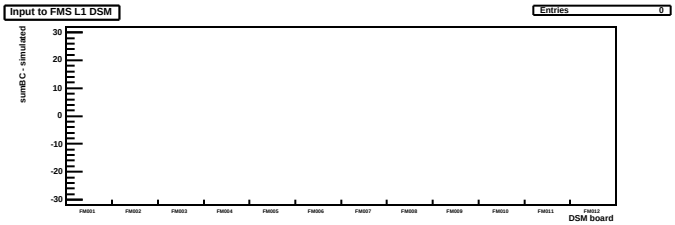

Input to FMS L1 DSM
Entries 3231240
sumD
DSM board

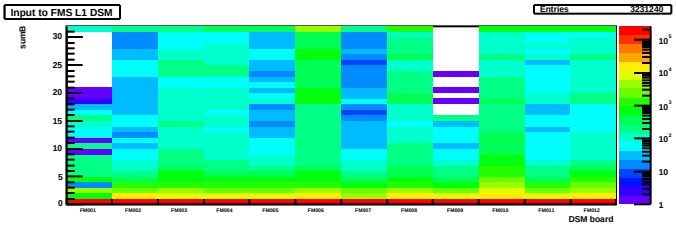

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

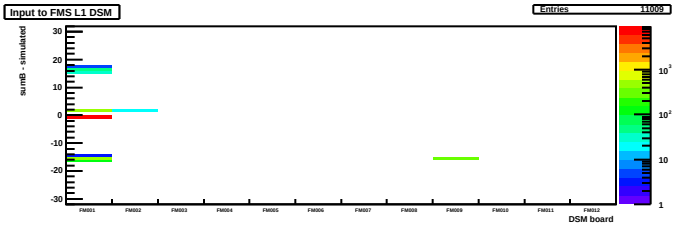

Input to FMS L1 DSM
Entries 3231240
sumC
DSM board

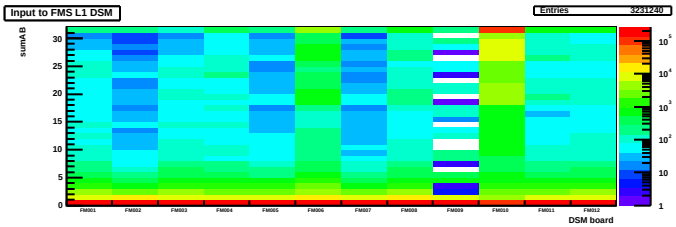

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

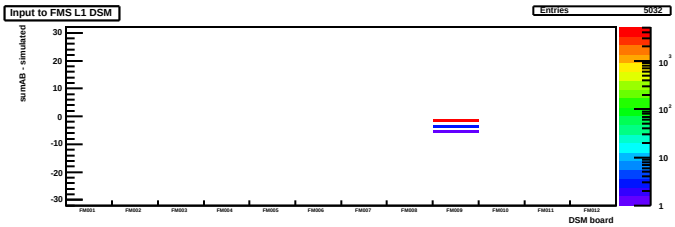

Input to FMS L1 DSM
Entries 3231240
sumBC
DSM board

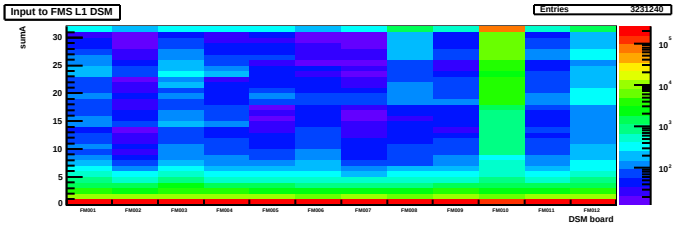

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

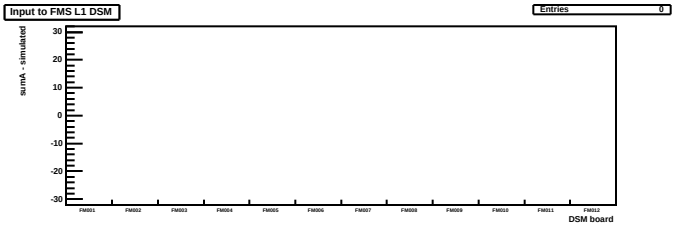

Input to FMS L1 DSM
Entries 3231240
sumB
DSM board

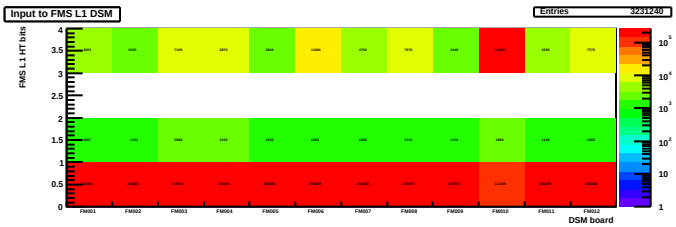

Input to FMS L1 DSM
Entries 11009
sumB - simulated
DSM board

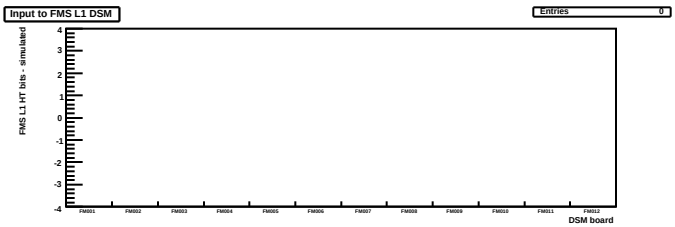

Input to FMS L1 DSM
Entries 3231240
sumAB
DSM board

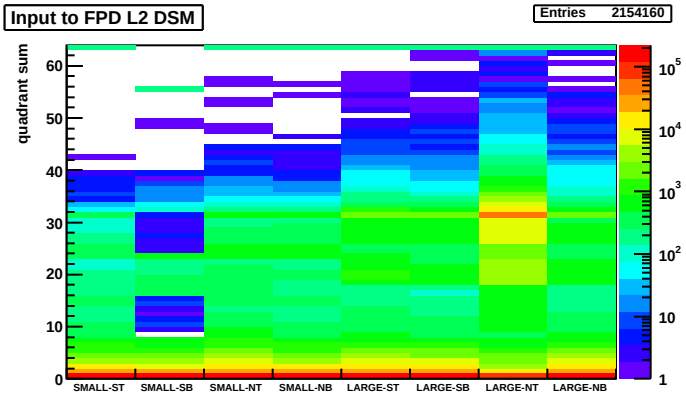

Input to FMS L1 DSM
Entries 5032
sumAB - simulated
DSM board

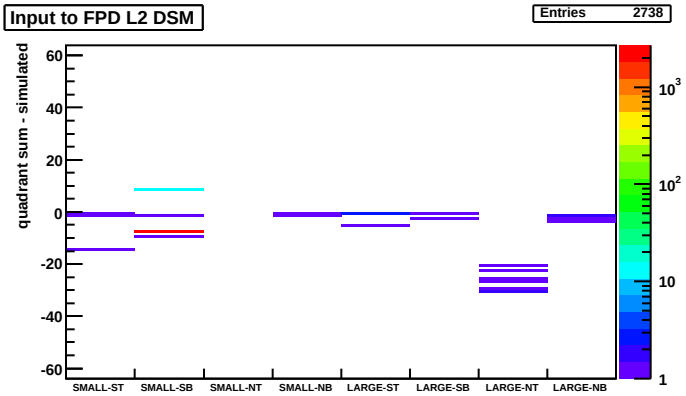

Input to FMS L1 DSM
Entries 3231240
sumA
DSM board

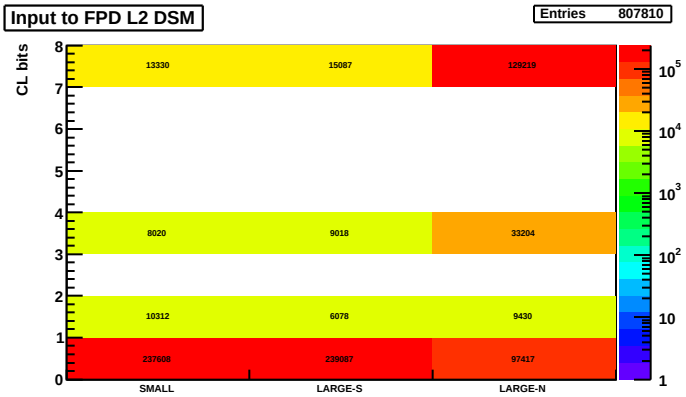

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

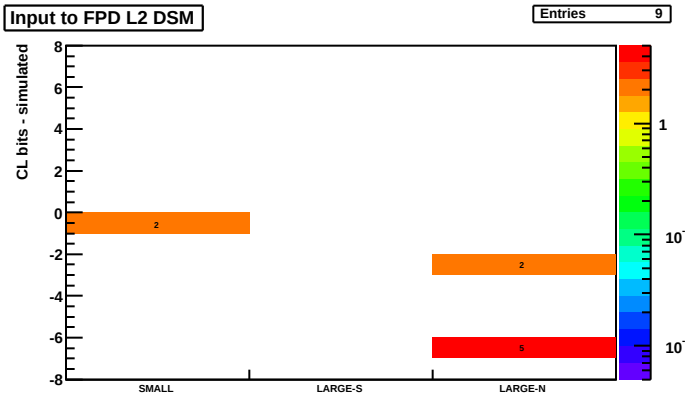

Input to FMS L1 DSM
Entries 3231240
FMS L1 HT bits
DSM board

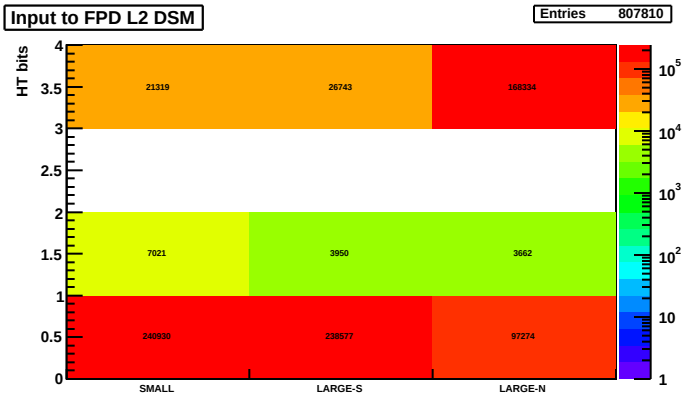

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

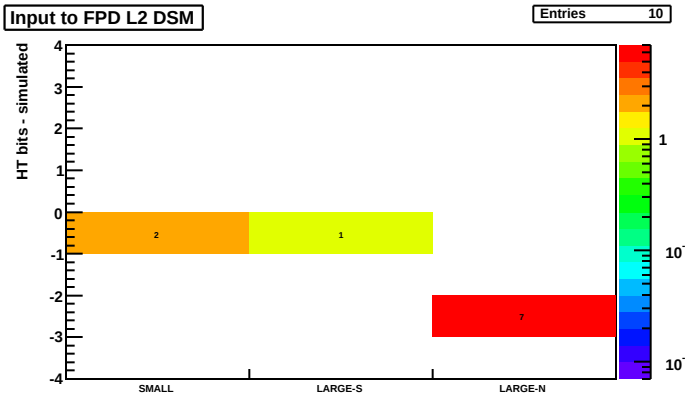

Input to FPD L2 DSM
Entries 2154160
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

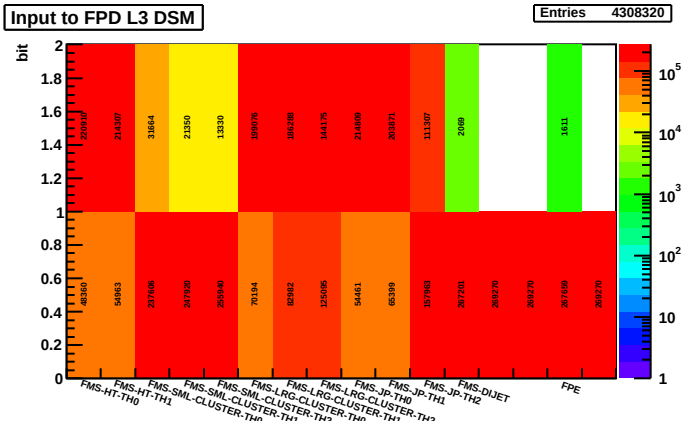

Input to FPD L2 DSM
Entries 2738
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

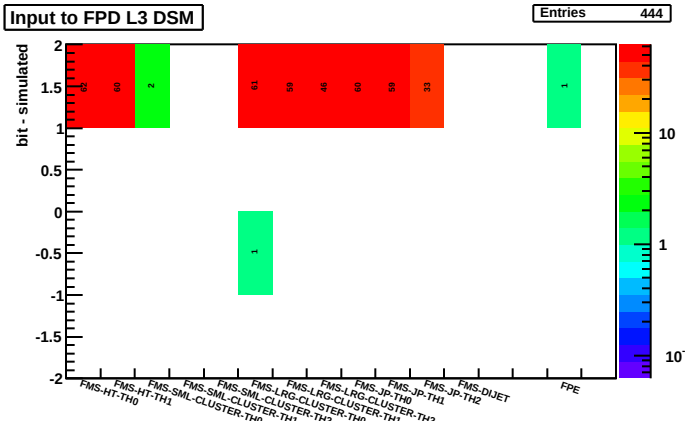

Input to FPD L2 DSM
Entries 807810
CL bits
13330
15087
129219
8020
9018
33204
10312
6078
9430
237608
239087
97417
SMALL
LARGE-S
LARGE-N

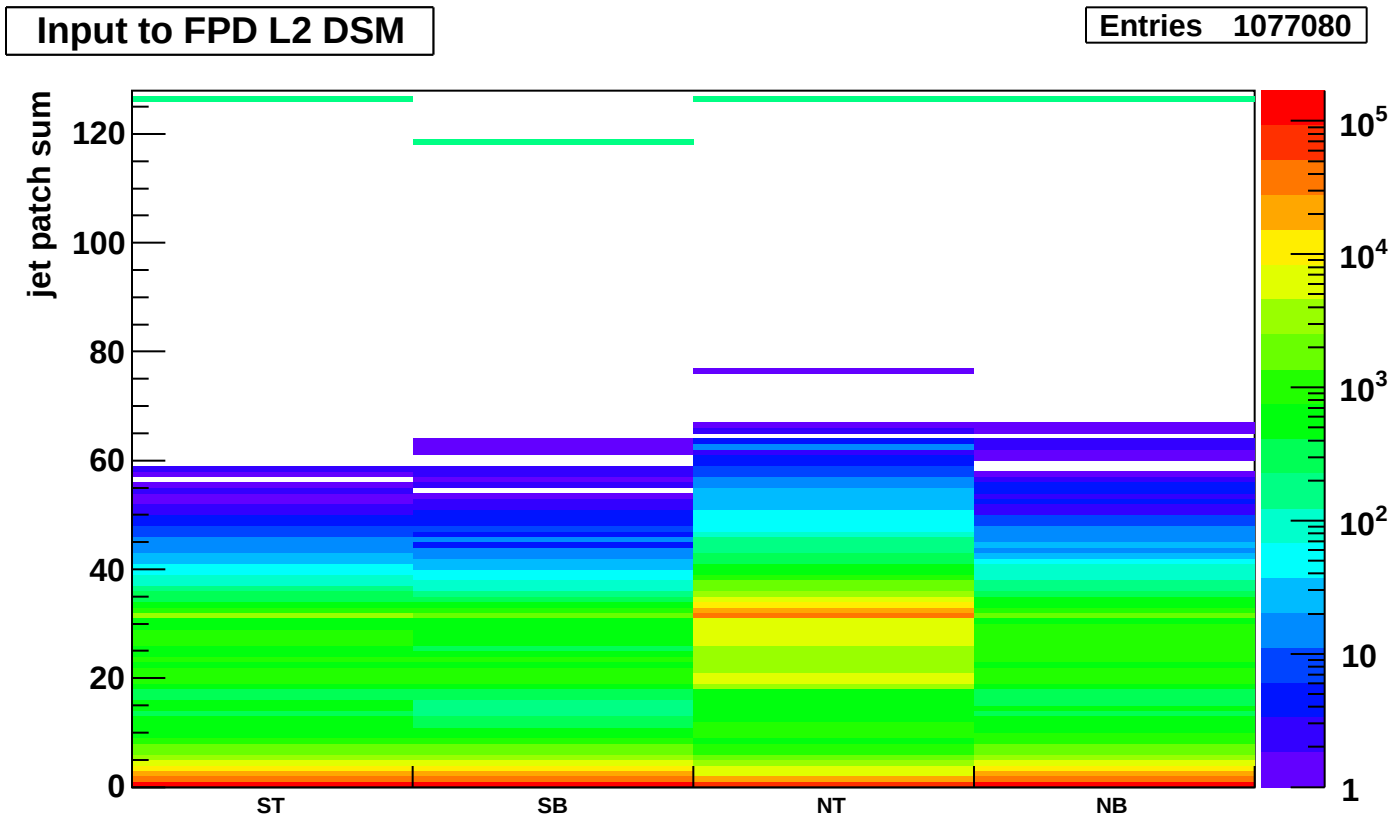

Input to FPD L2 DSM
Entries 9
CL bits - simulated
2
2
5
SMALL
LARGE-S
LARGE-N

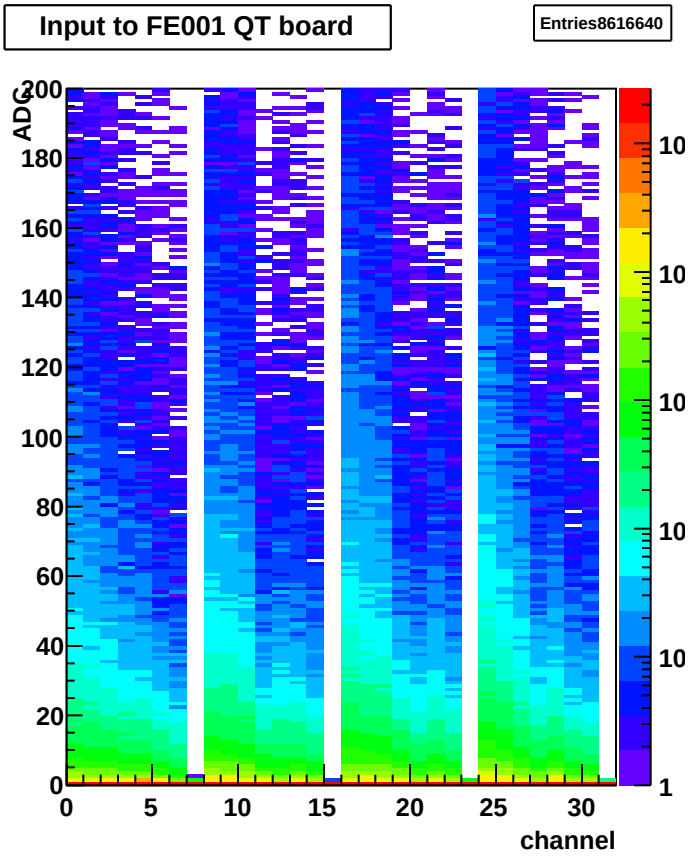

Input to FPD L2 DSM
Entries 807810
HT bits
21319
26743
168334
7021
3950
3662
240930
238577
97274
SMALL
LARGE-S
LARGE-N

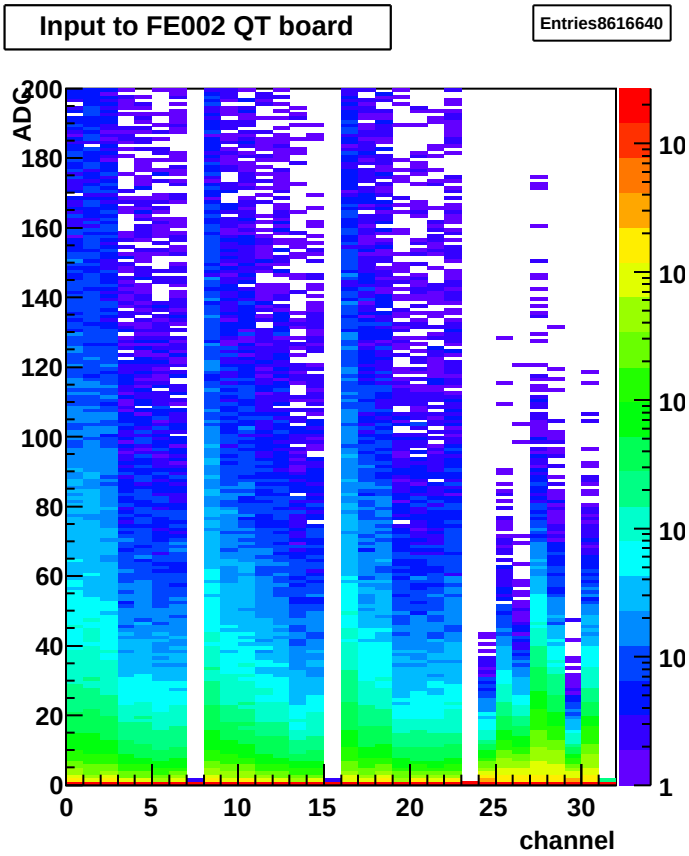

Input to FPD L2 DSM
Entries 10
HT bits - simulated
2
1
7
SMALL
LARGE-S
LARGE-N

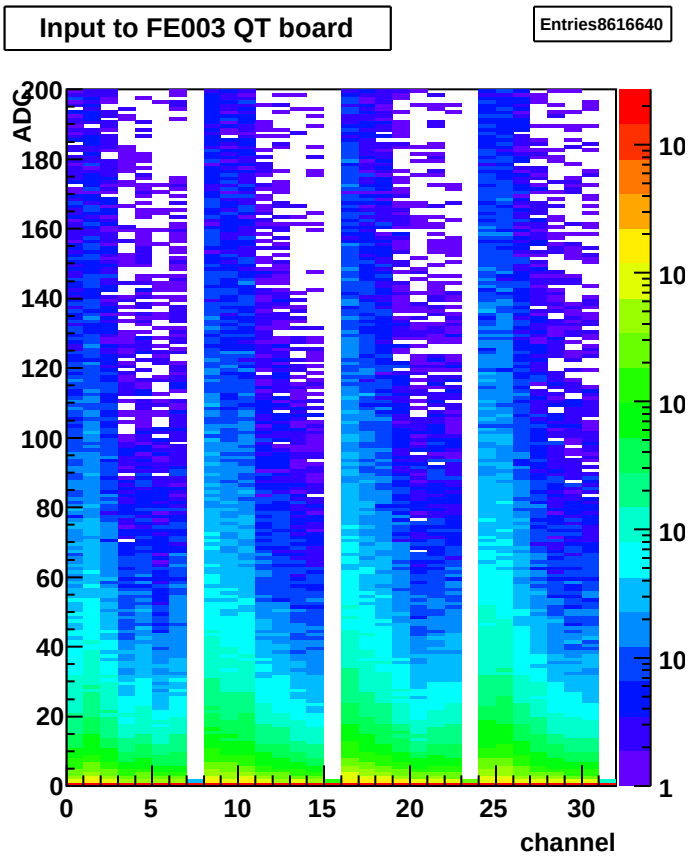

Input to FPD L3 DSM
Entries 4308320
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

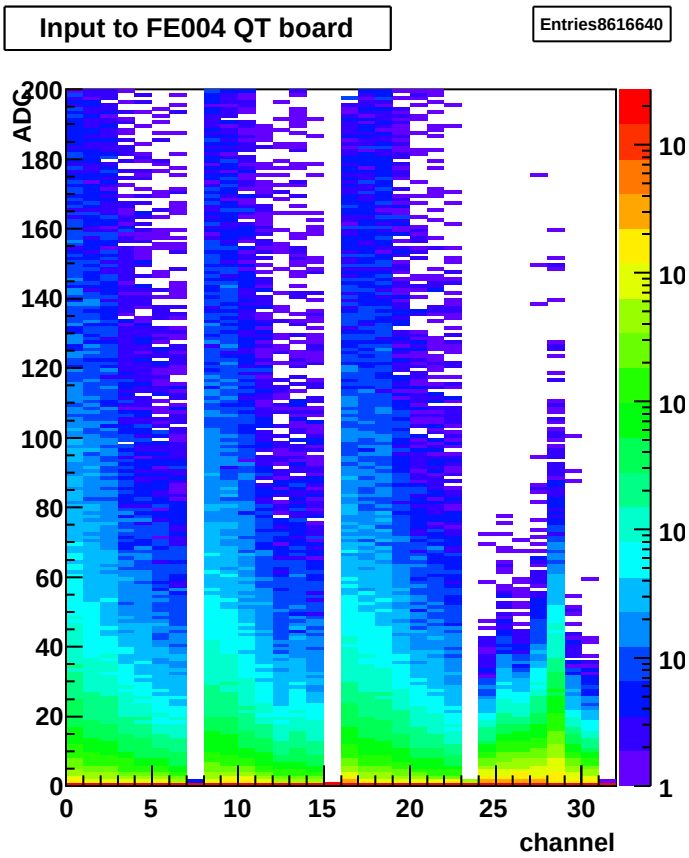

Input to FPD L3 DSM
Entries 444
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

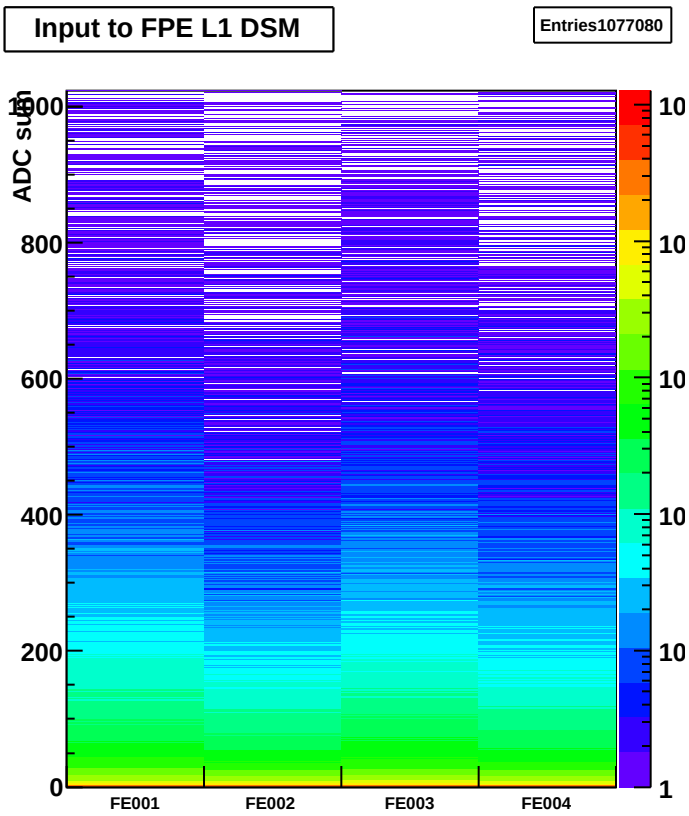

Input to FPD L2 DSM
Entries 1077080
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

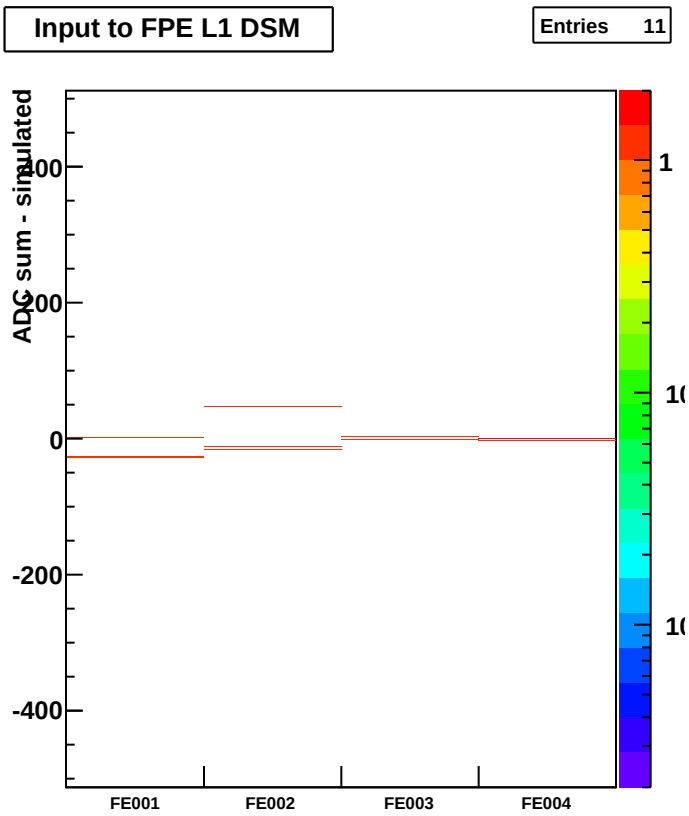

Input to FE001 QT board
Entries8616640
ADC
channel

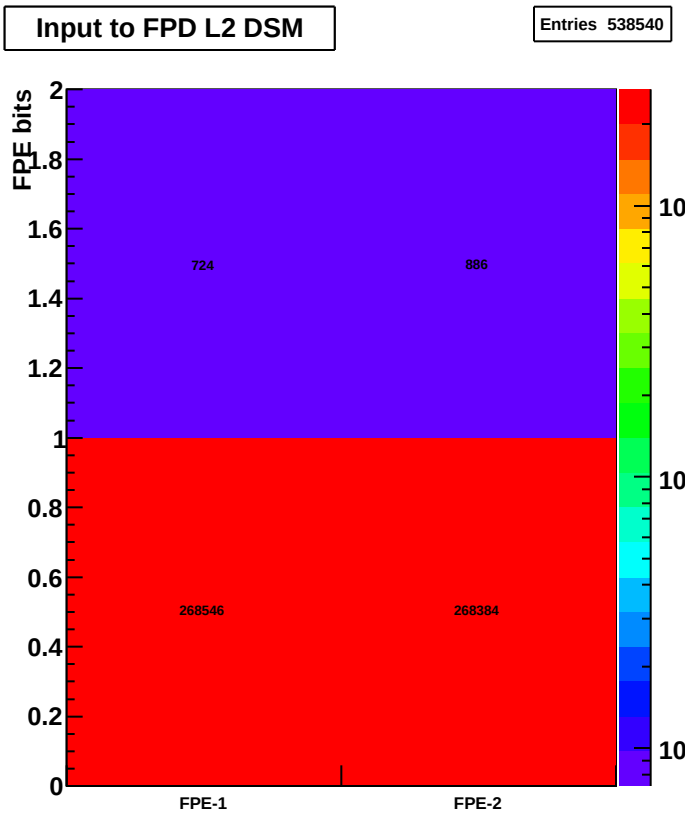

Input to FE002 QT board
Entries8616640
ADC
channel

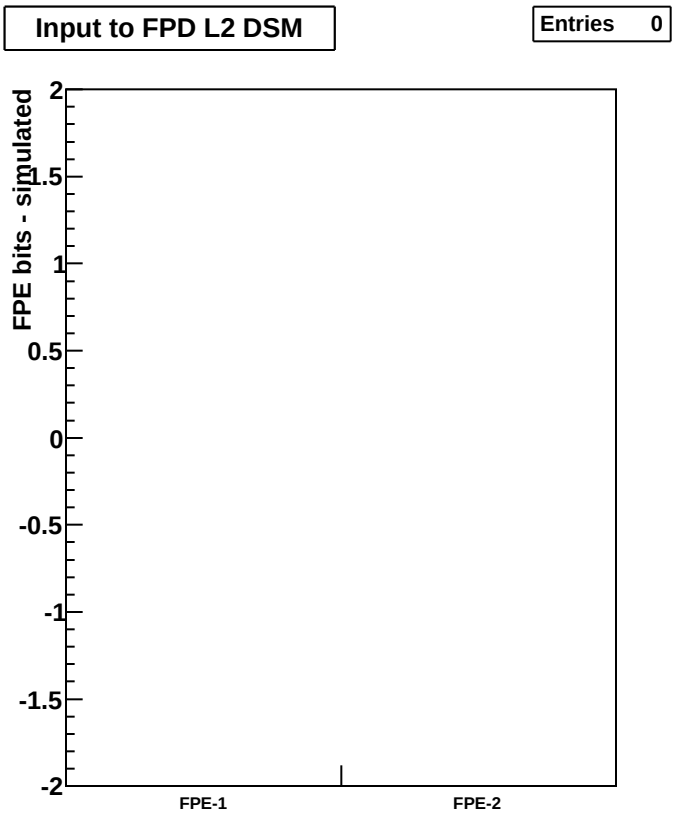

Input to FE003 QT board
Entries8616640
ADC
channel


Input to FE004 QT board
Entries8616640
ADC
channel


Input to FPE L1 DSM
Entries1077080
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 11
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 538540
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
724
886
268546
268384
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2