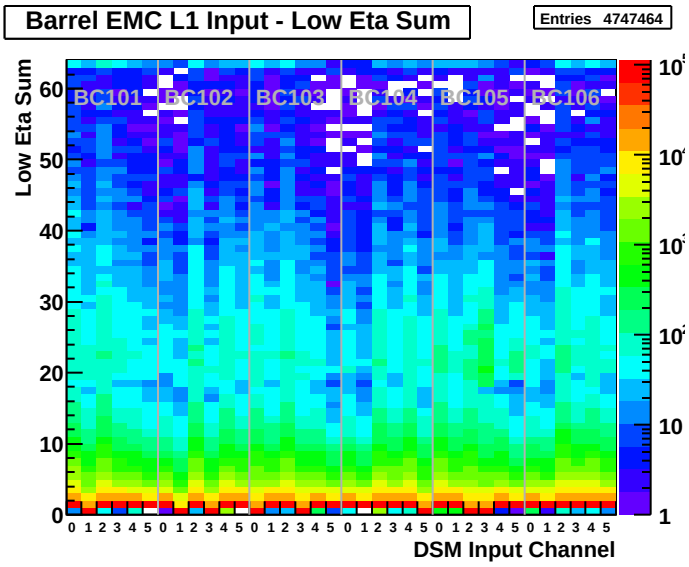
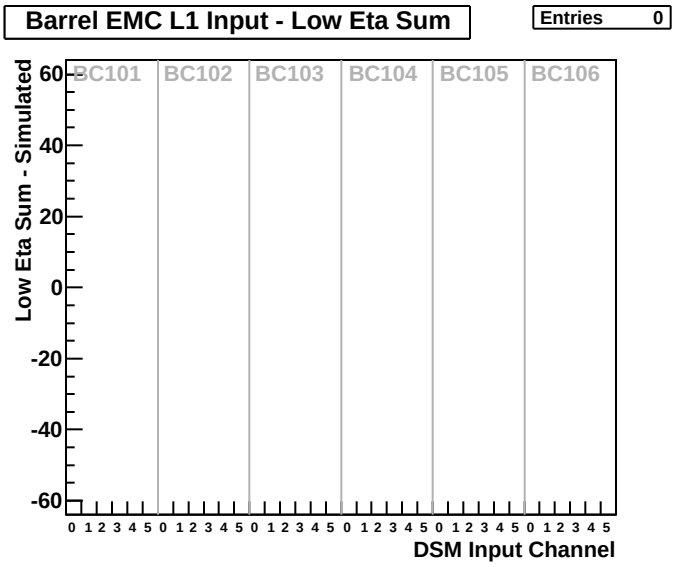
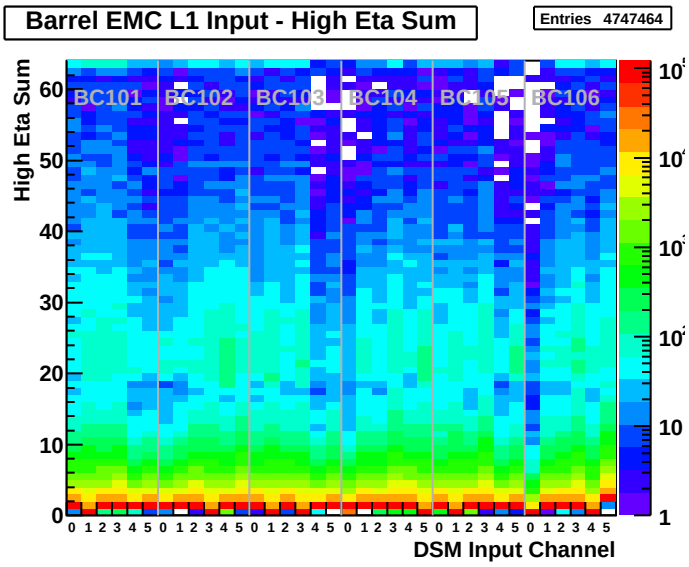
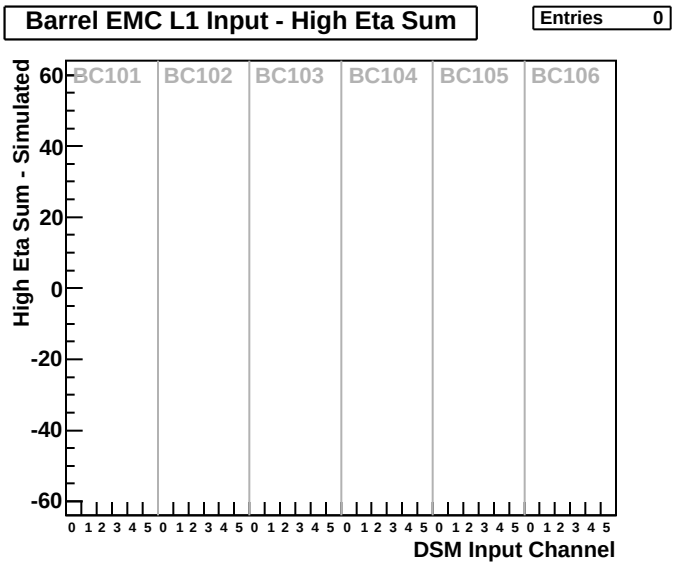
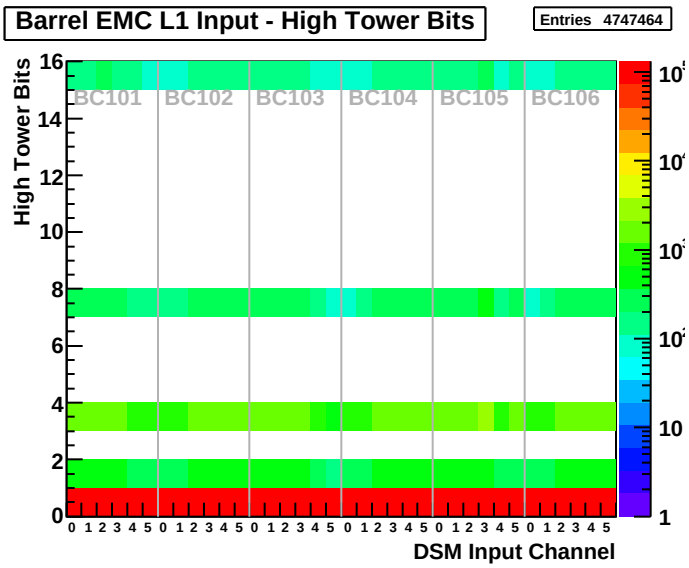
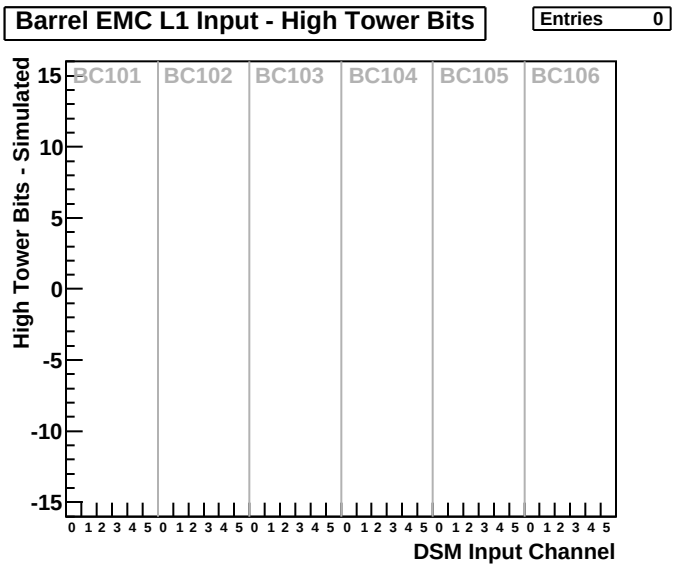

Barrel EMC L1 Input - Low Eta Sum
Entries 4747464
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

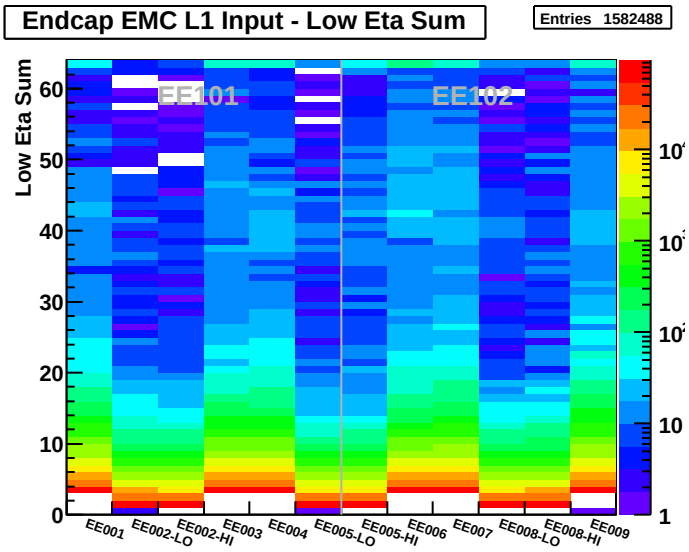

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

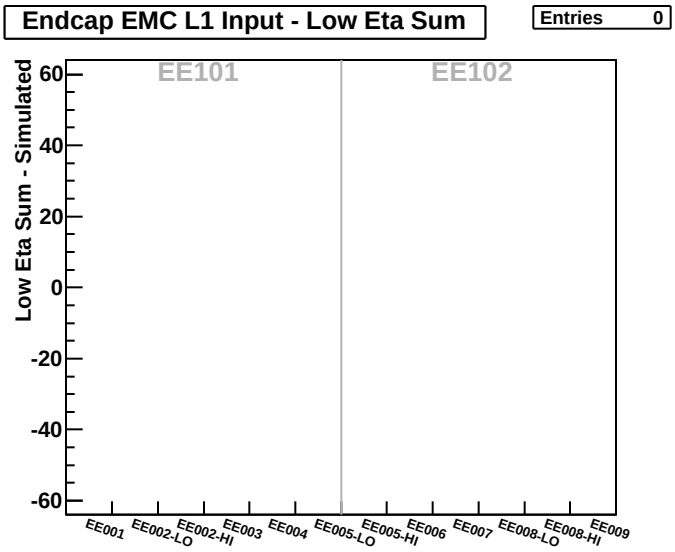

Barrel EMC L1 Input - High Eta Sum
Entries 4747464
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

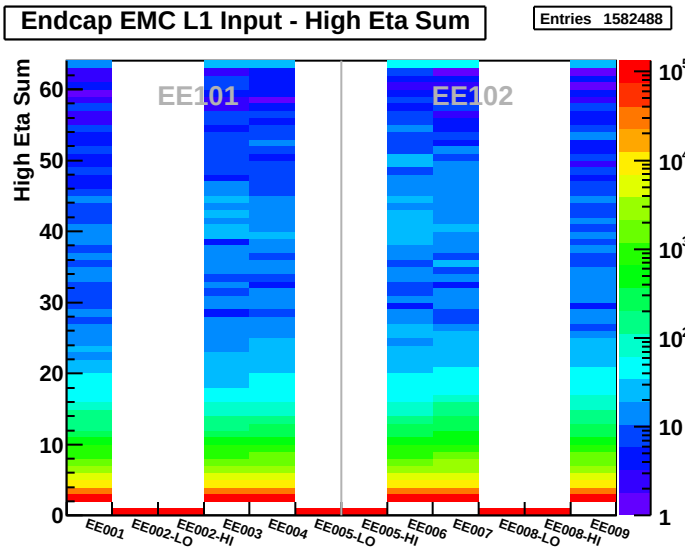

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

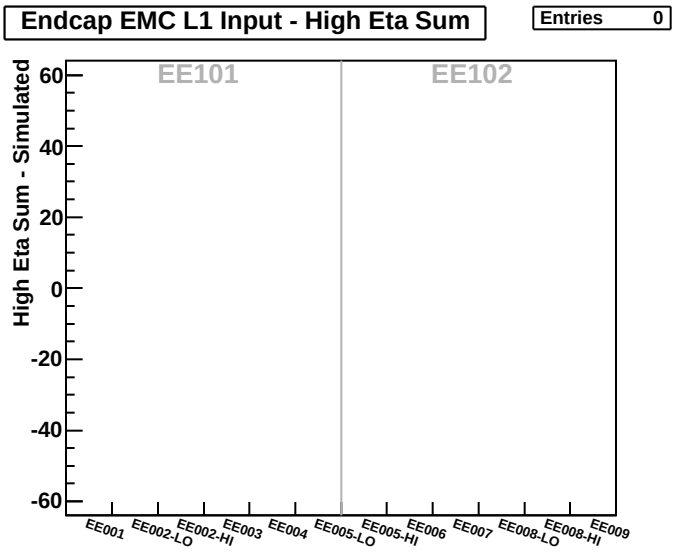

Barrel EMC L1 Input - High Tower Bits
Entries 4747464
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

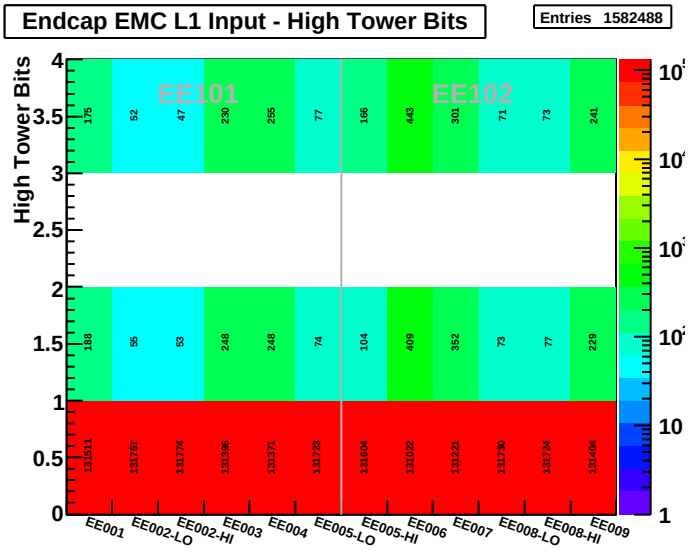

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

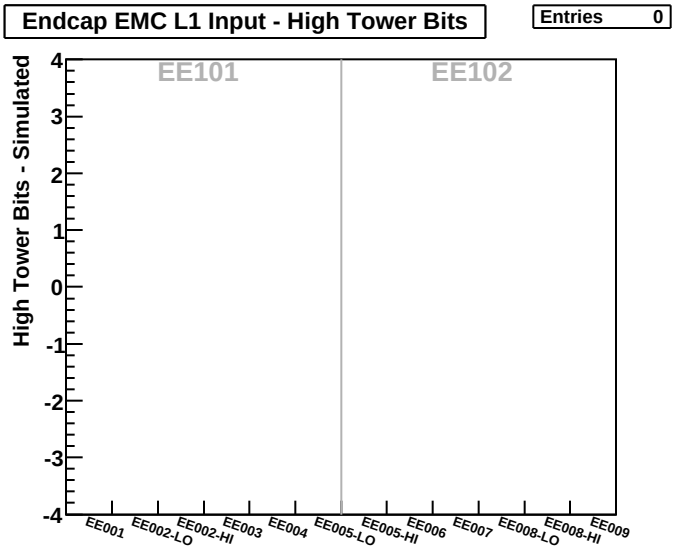

Endcap EMC L1 Input - Low Eta Sum
Entries 1582488
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

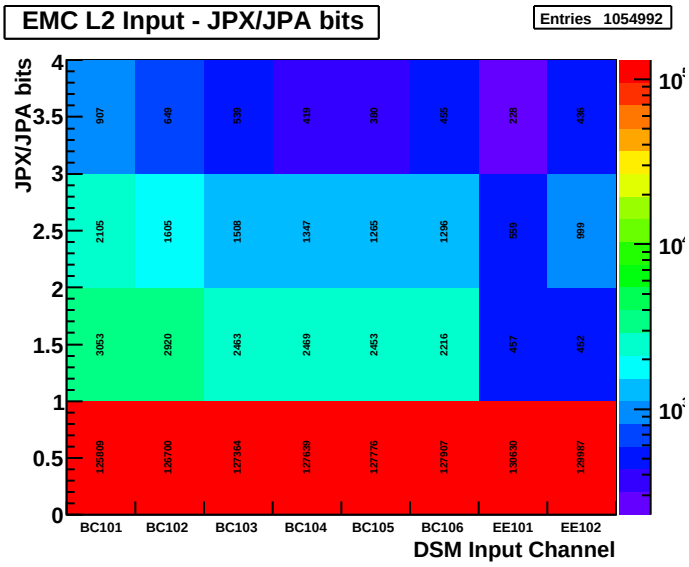

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

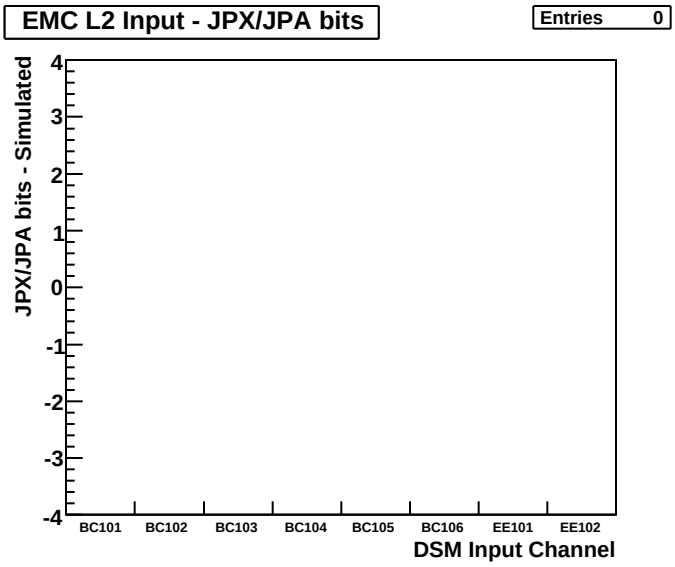

Endcap EMC L1 Input - High Eta Sum
Entries 1582488
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

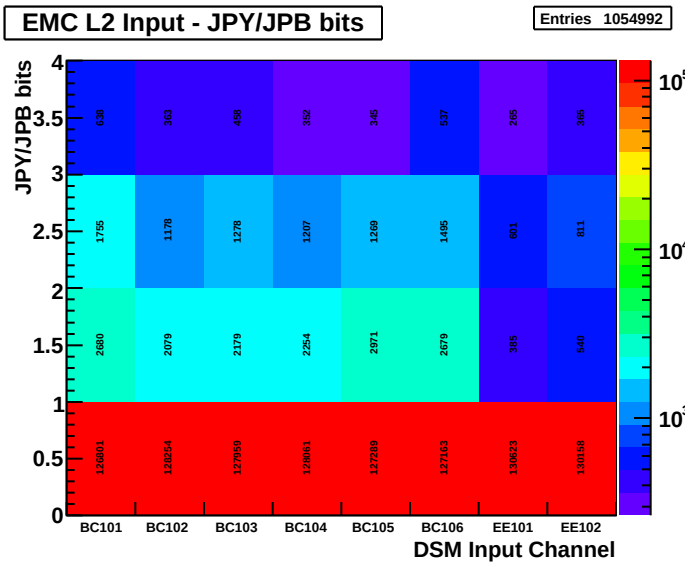

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

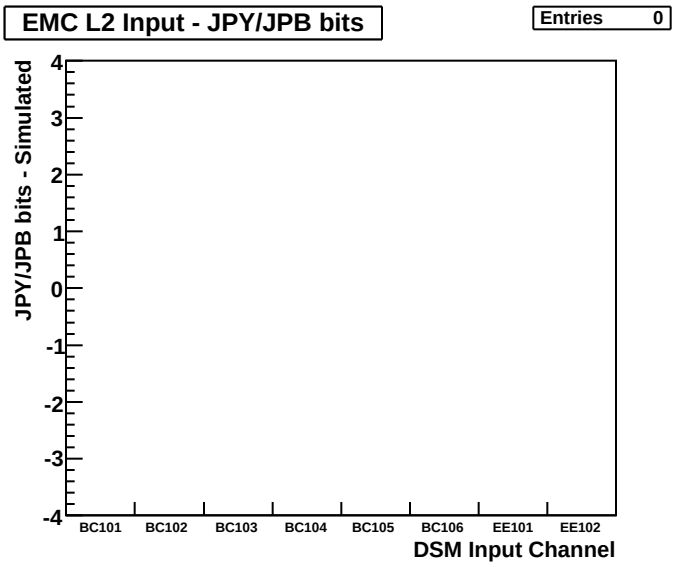

Endcap EMC L1 Input - High Tower Bits
Entries 1582488
High Tower Bits
EE101
EE102
175 52 47 230 255 77 166 443 301 71 73 241
188 55 53 248 248 74 104 409 352 73 77 229
131511 131767 131774 131396 131371 131723 131604 131022 131221 131730 131724 131404
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

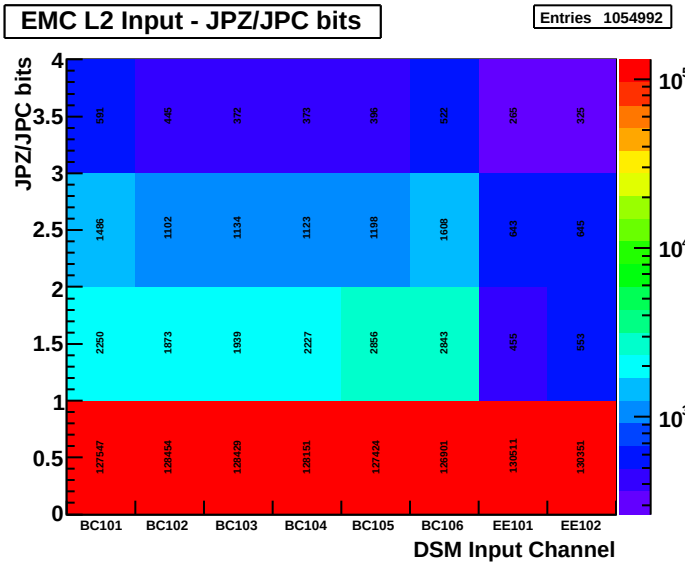

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

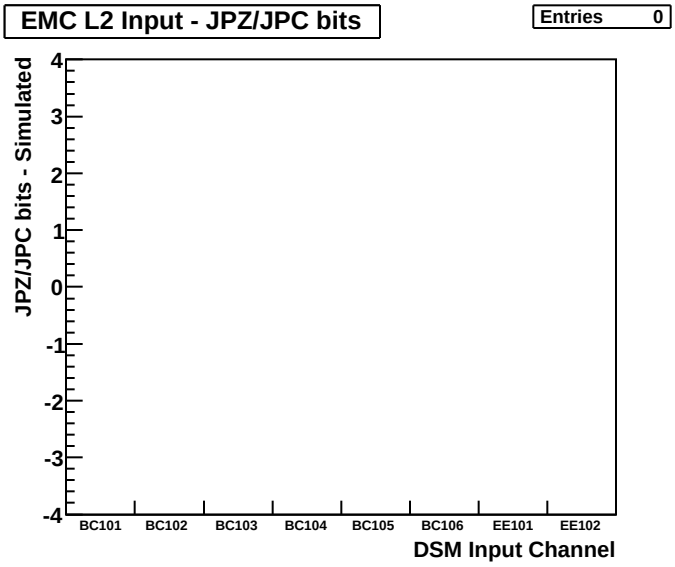

EMC L2 Input - JPX/JPA bits
Entries 1054992
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
907 649 539 419 380 455 228 436
2105 1605 1508 1347 1265 1296 559 999
3053 2920 2463 2469 2453 2216 457 452
125809 126700 127364 127639 127776 127907 130630 129987

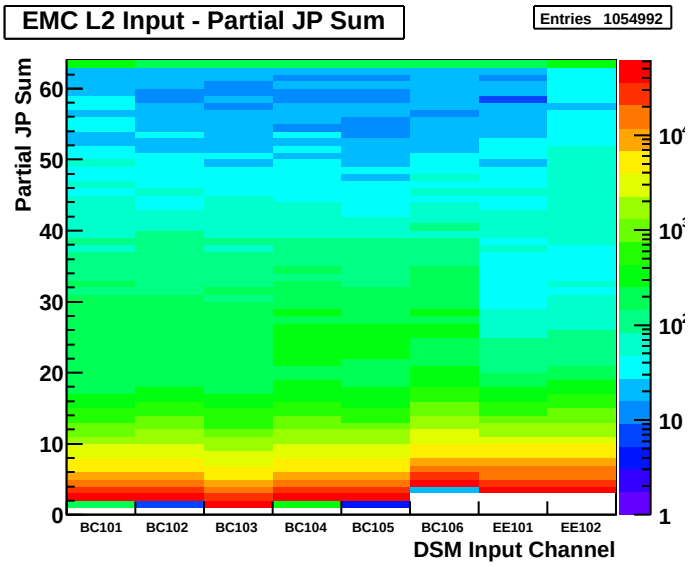

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

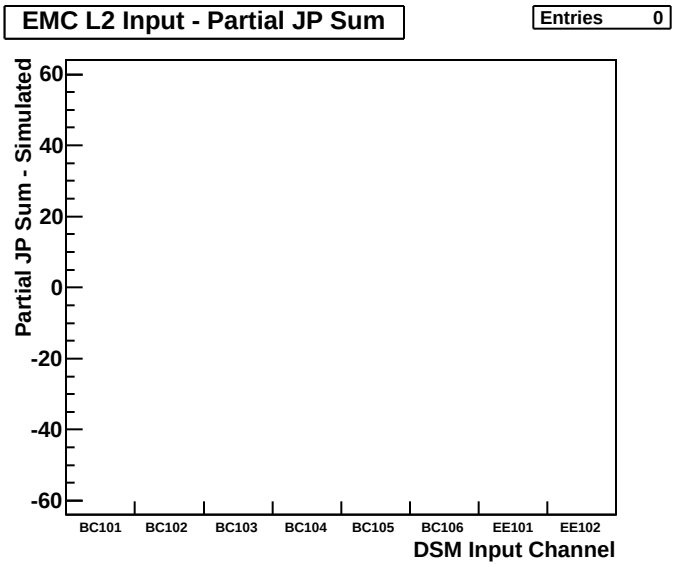

EMC L2 Input - JPY/JPB bits
Entries 1054992
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
638 368 458 352 345 537 265 365
1755 1178 1278 1207 1269 1495 601 811
2680 2079 2179 2254 2971 2679 385 540
126801 128254 127959 128061 127289 127163 130623 130158

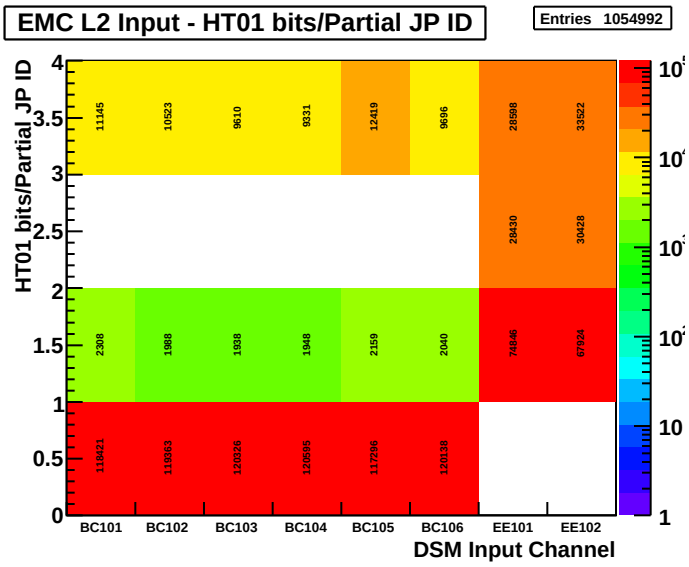

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

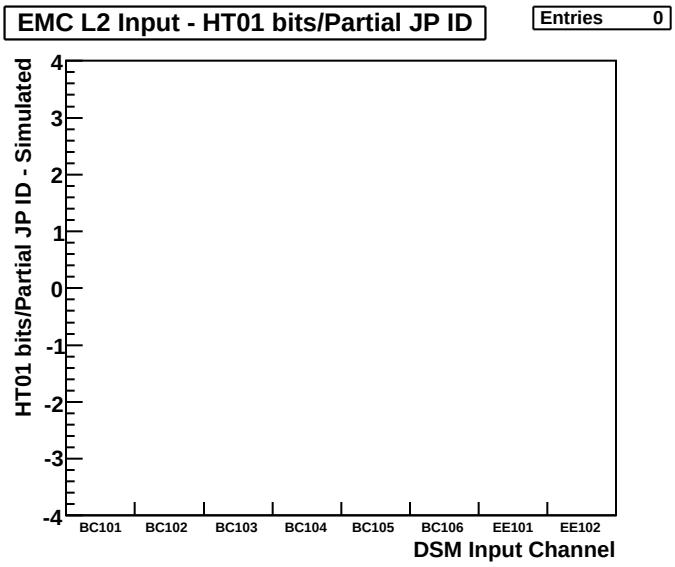

EMC L2 Input - JPZ/JPC bits
Entries 1054992
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
591 445 372 373 396 522 265 325
1486 1102 1134 1123 1198 1608 643 645
2250 1873 1939 2227 2856 2843 455 553
127547 128454 128429 128151 127424 126901 130511 130351

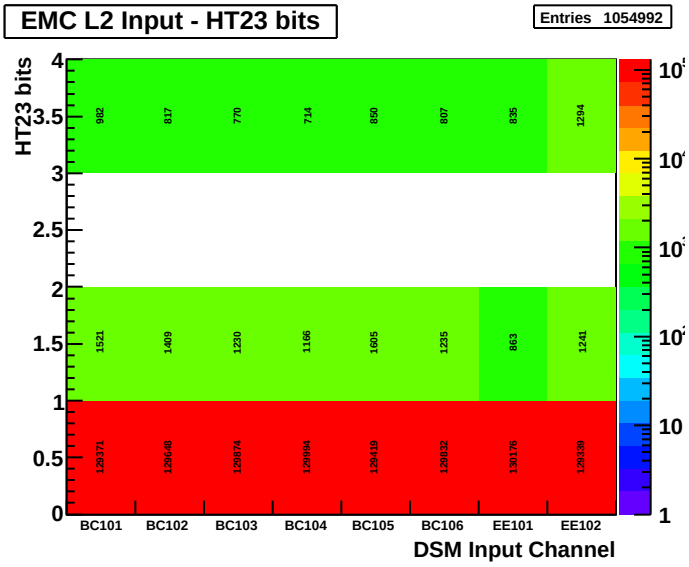

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

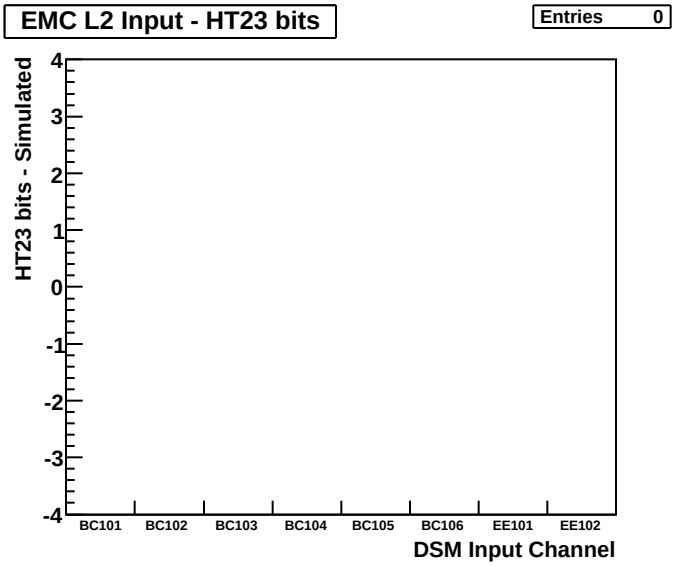

EMC L2 Input - Partial JP Sum
Entries 1054992
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

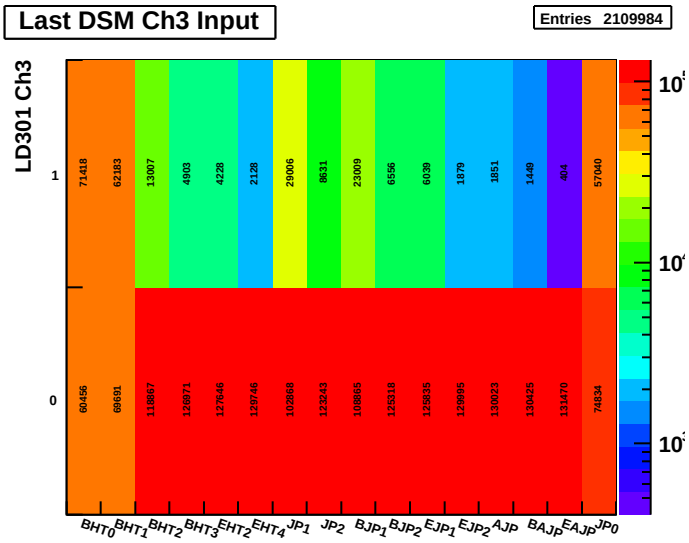

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

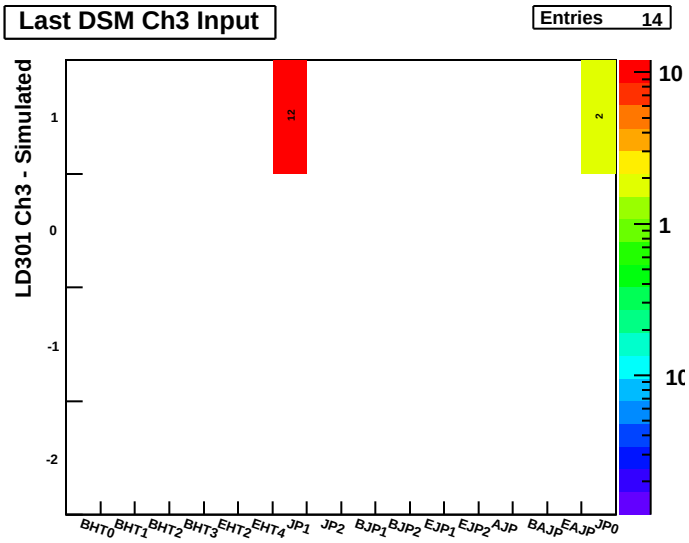

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1054992
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

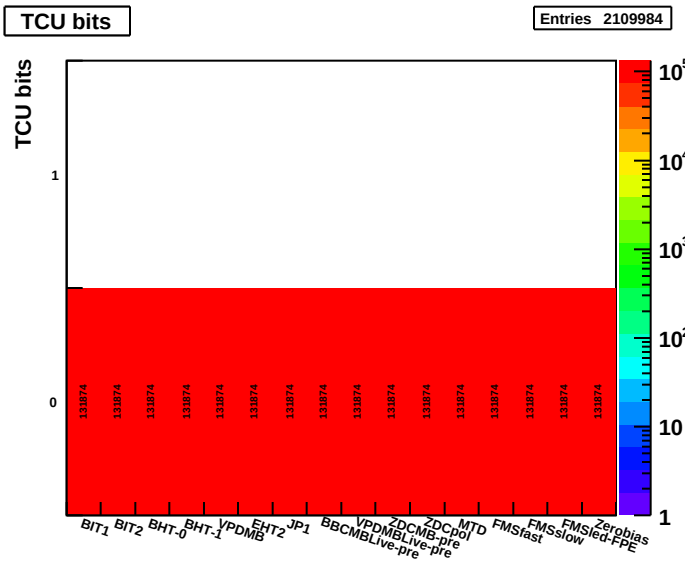

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

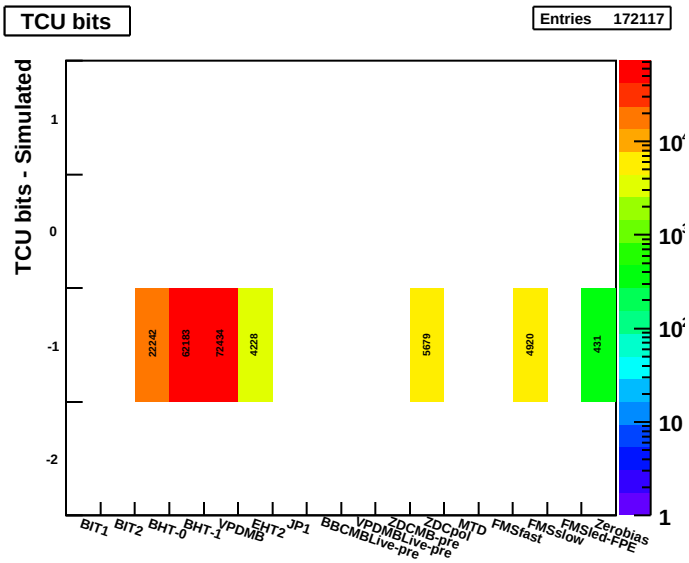

EMC L2 Input - HT23 bits
Entries 1054992
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

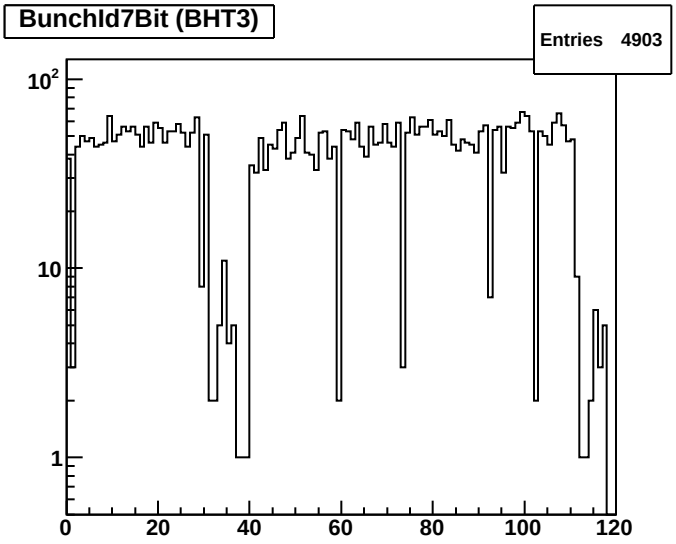

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

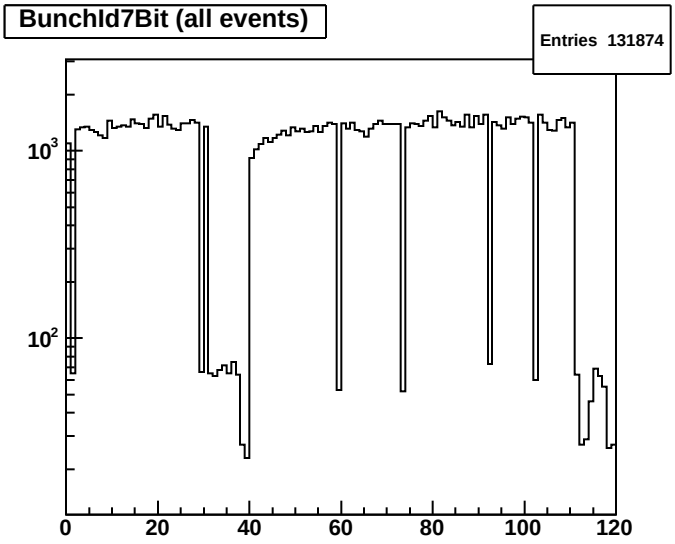

Last DSM Ch3 Input
Entries 2109984
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

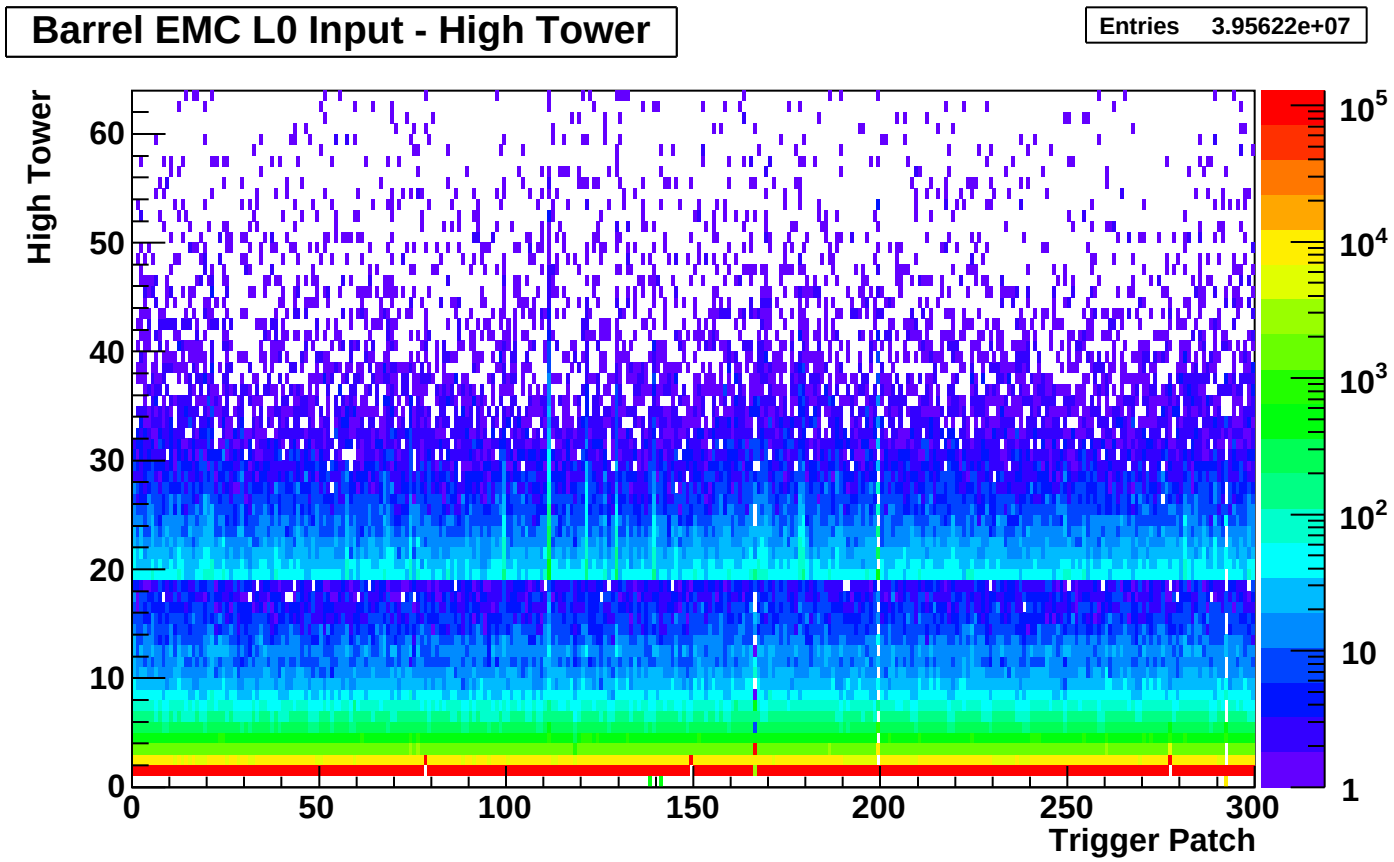

Last DSM Ch3 Input
Entries 14
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

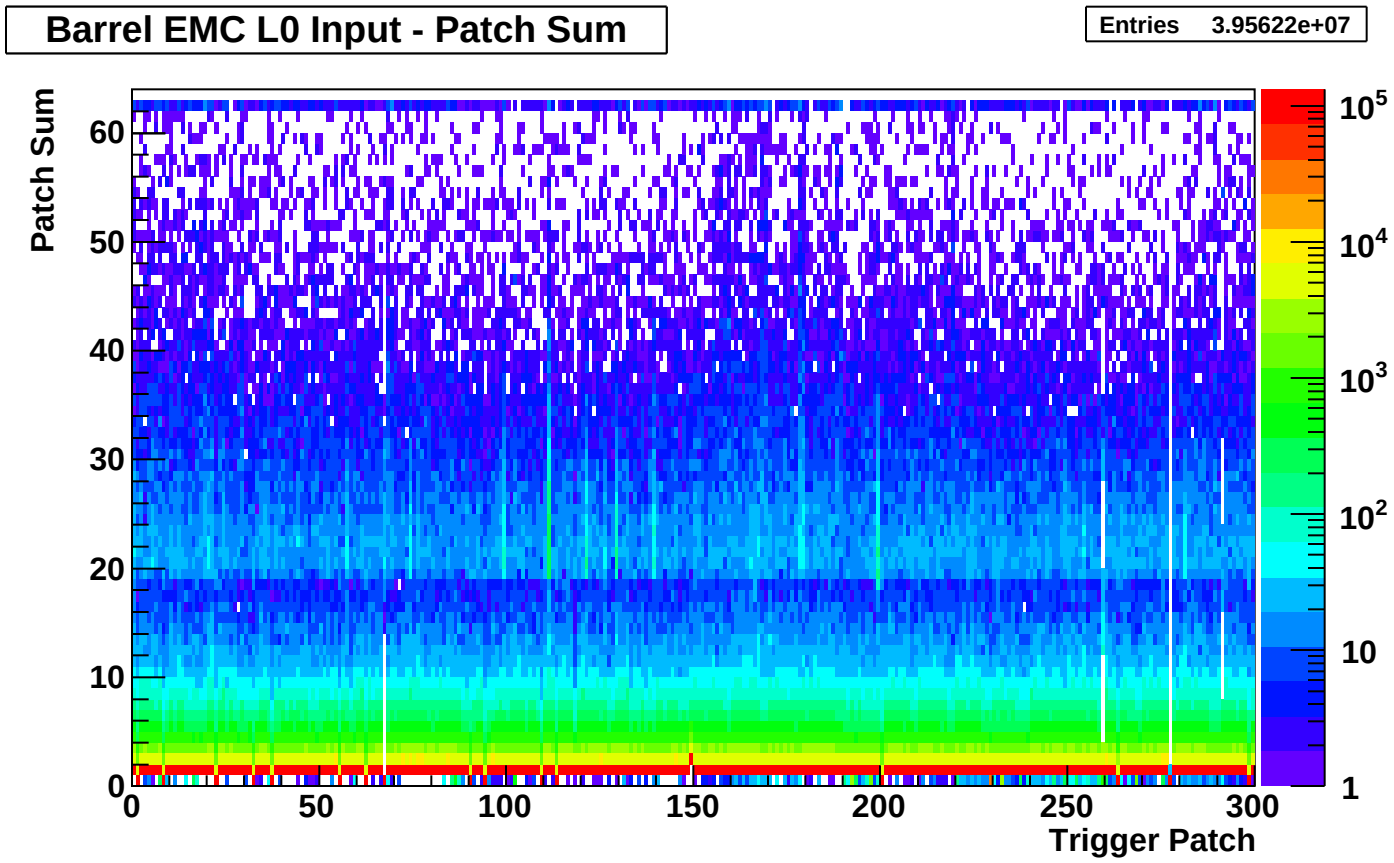

TCU bits
Entries 2109984
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

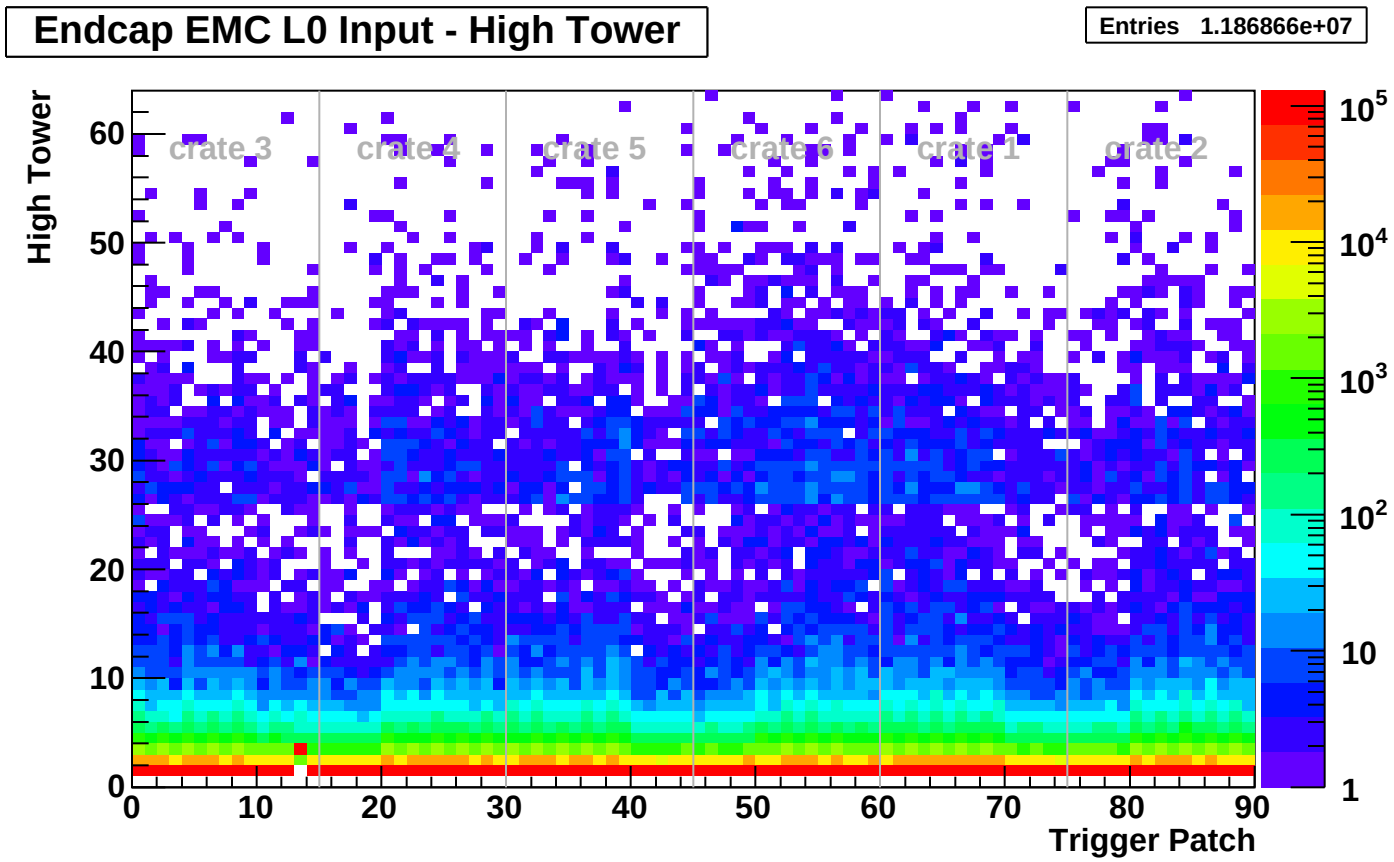

TCU bits
Entries 172117
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

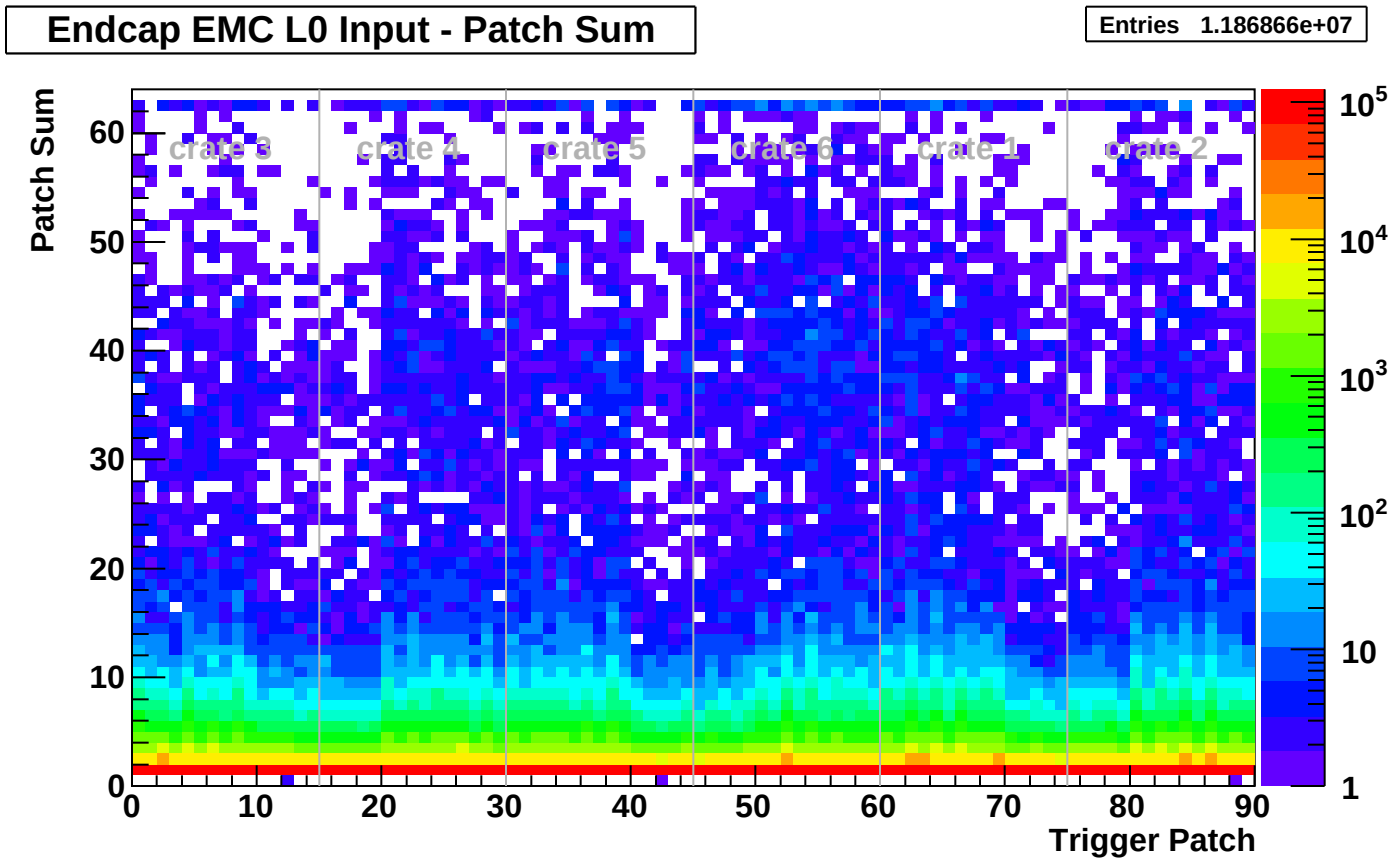

BunchId7Bit (BHT3)
Entries 4903

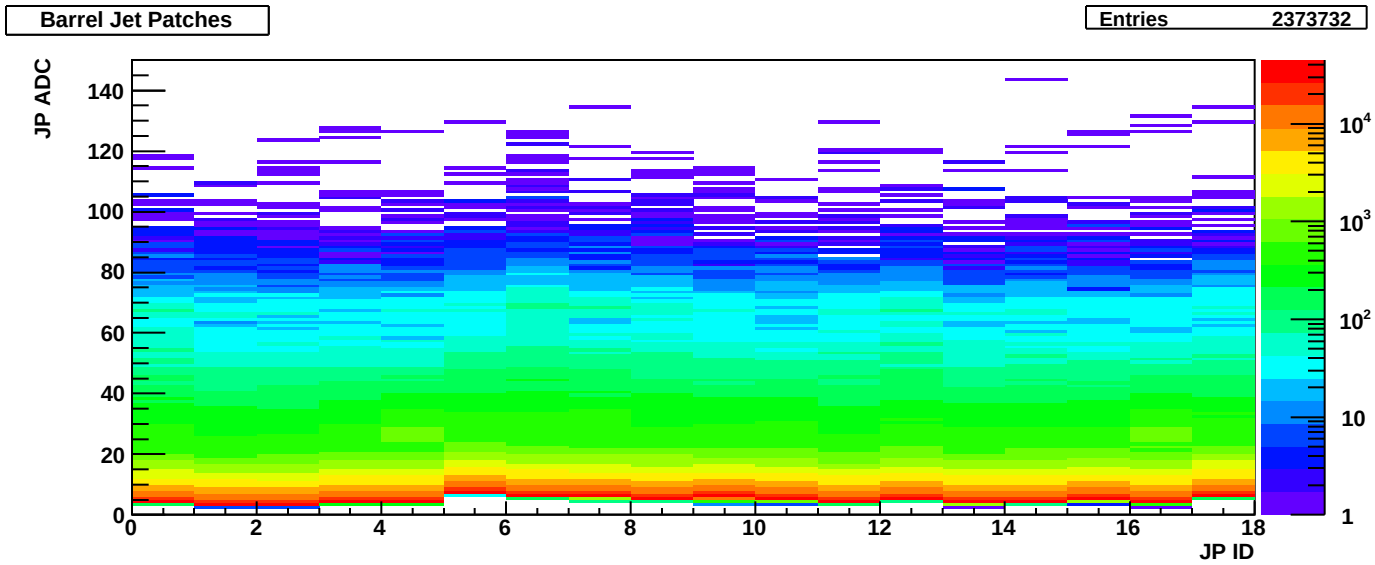

BunchId7Bit (all events)
Entries 131874

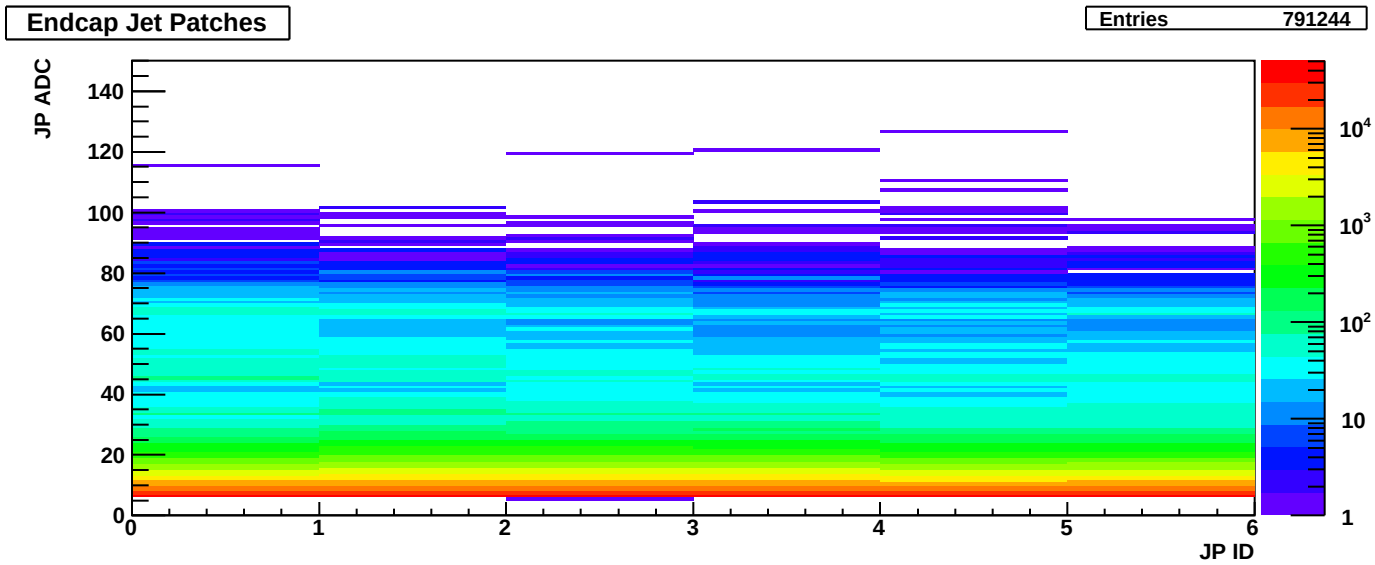

Barrel EMC L0 Input - High Tower
Entries 3.95622e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

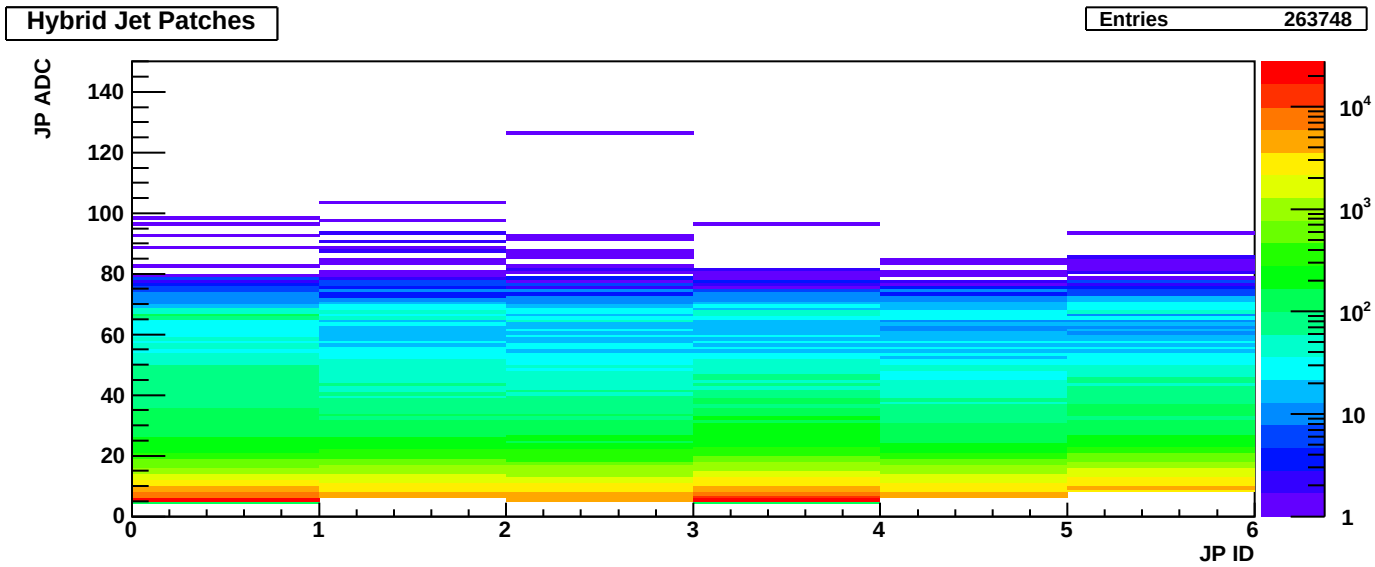

Barrel EMC L0 Input - Patch Sum
Entries 3.95622e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

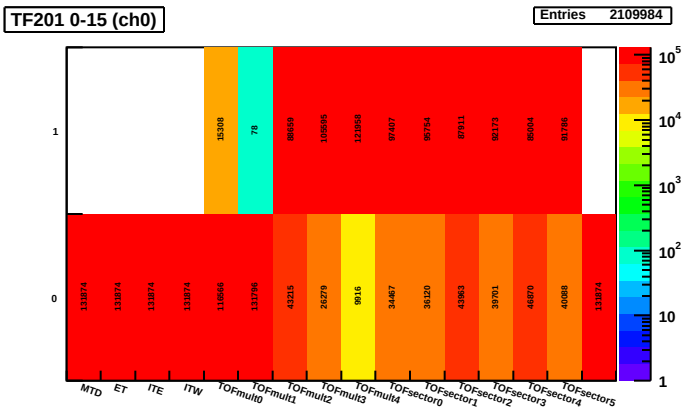

Endcap EMC L0 Input - High Tower
Entries 1.186866e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

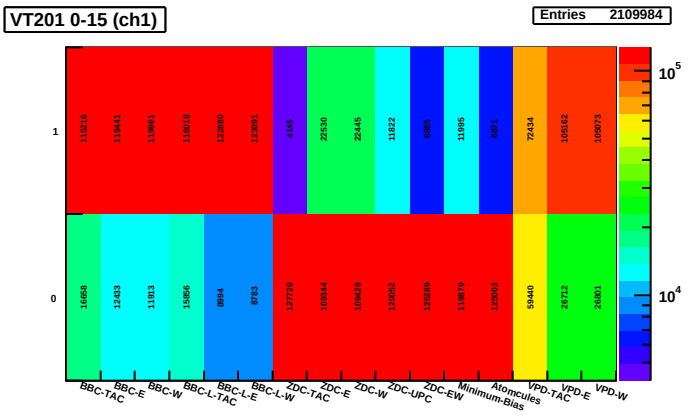

Endcap EMC L0 Input - Patch Sum
Entries 1.186866e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

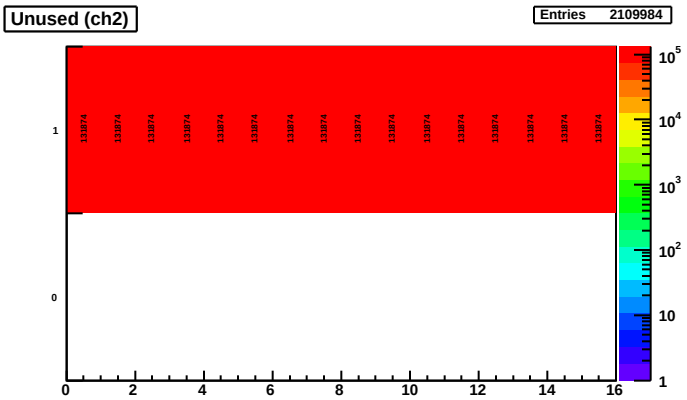

Barrel Jet Patches
Entries 2373732
JP ADC
JP ID

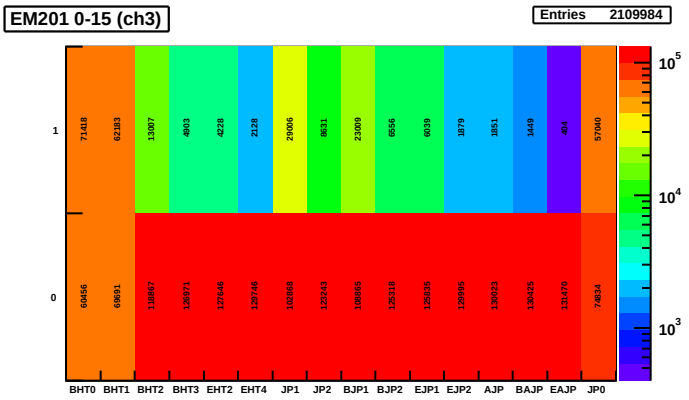

Endcap Jet Patches
Entries 791244
JP ADC
JP ID

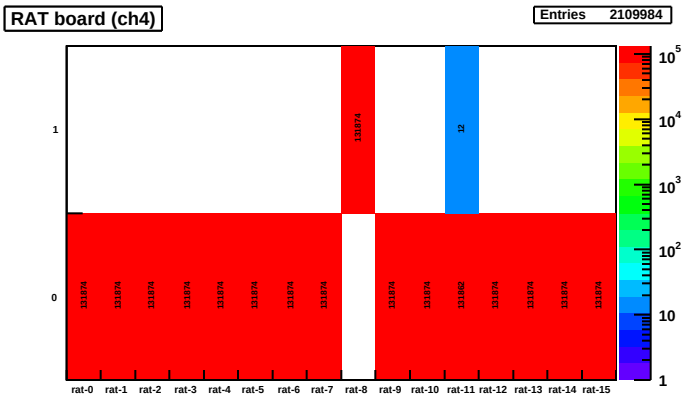

Hybrid Jet Patches
Entries 263748
JP ADC
JP ID

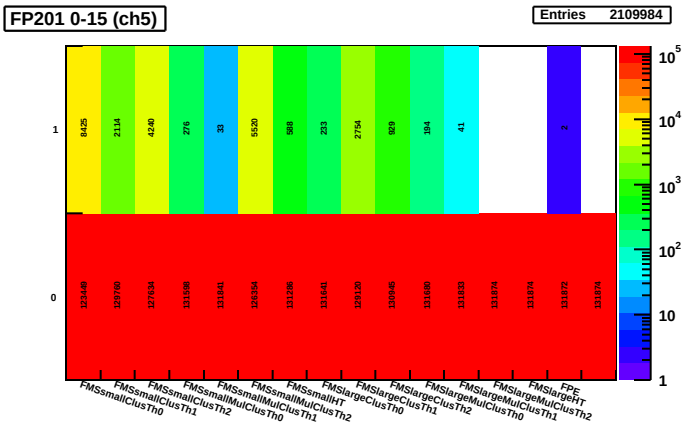

TF201 0-15 (ch0)
Entries 2109984
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

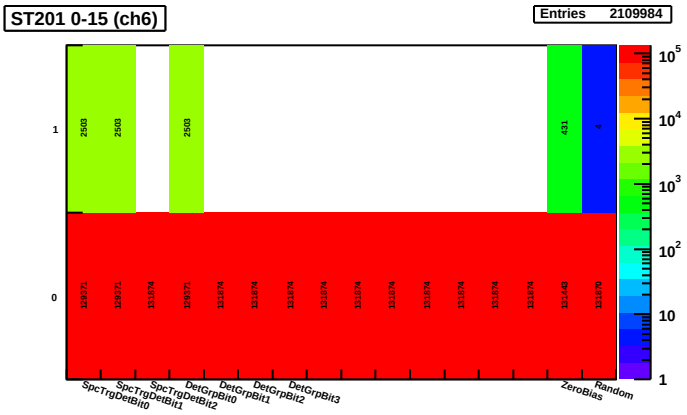

VT201 0-15 (ch1)
Entries 2109984
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

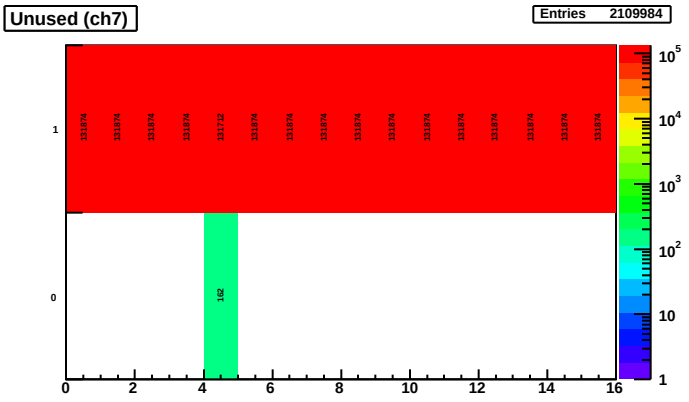

Unused (ch2)
Entries 2109984

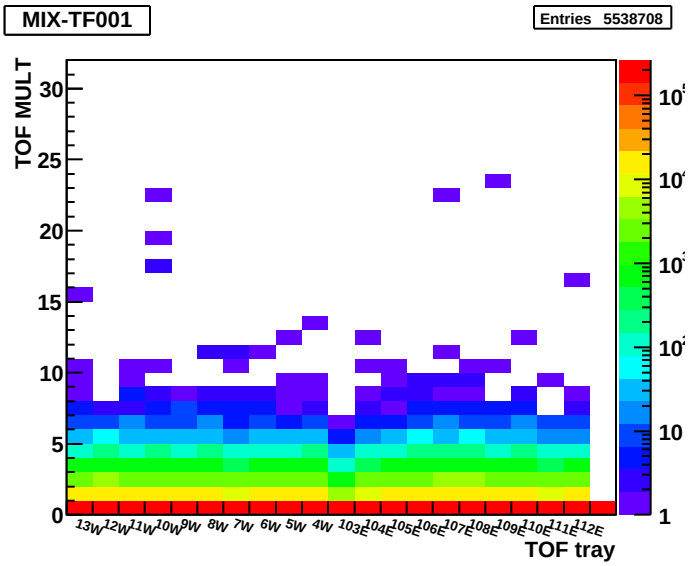

EM201 0-15 (ch3)
Entries 2109984
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

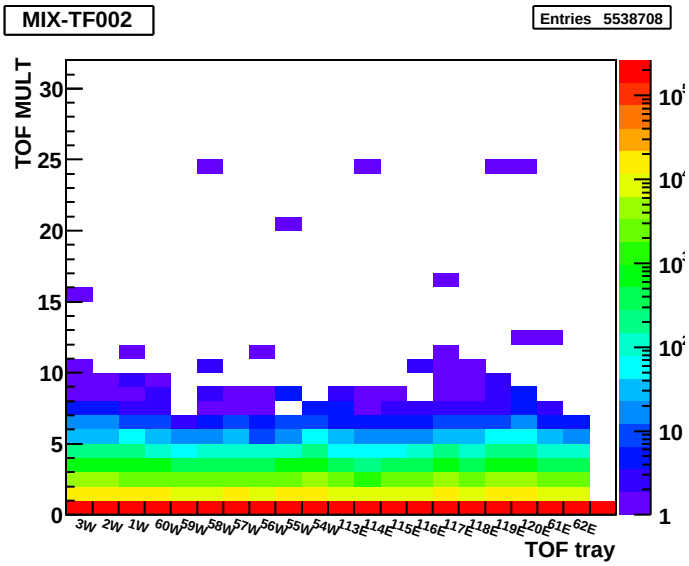

RAT board (ch4)
Entries 2109984
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

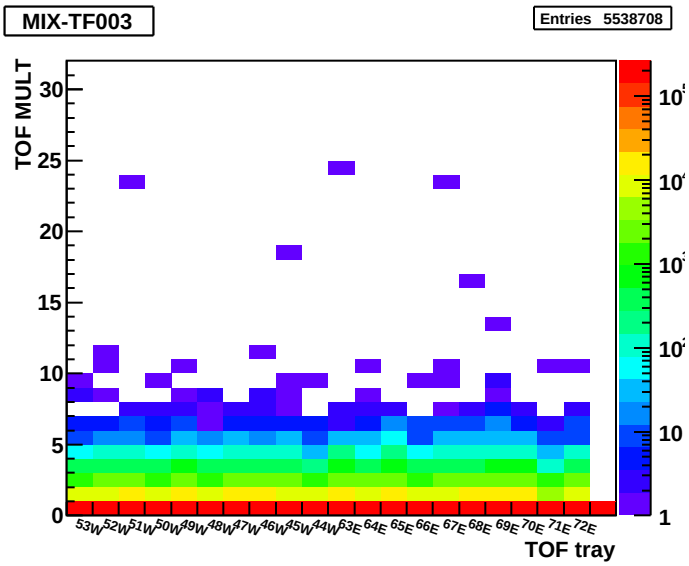

FP201 0-15 (ch5)
Entries 2109984
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

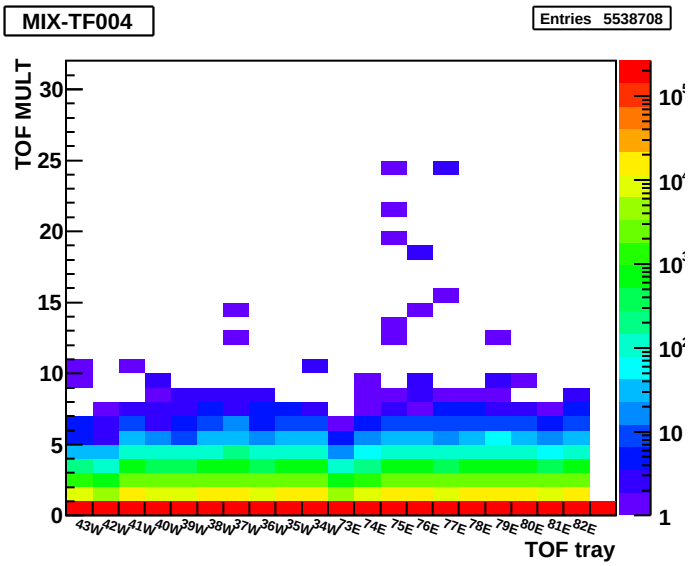

ST201 0-15 (ch6)
Entries 2109984
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

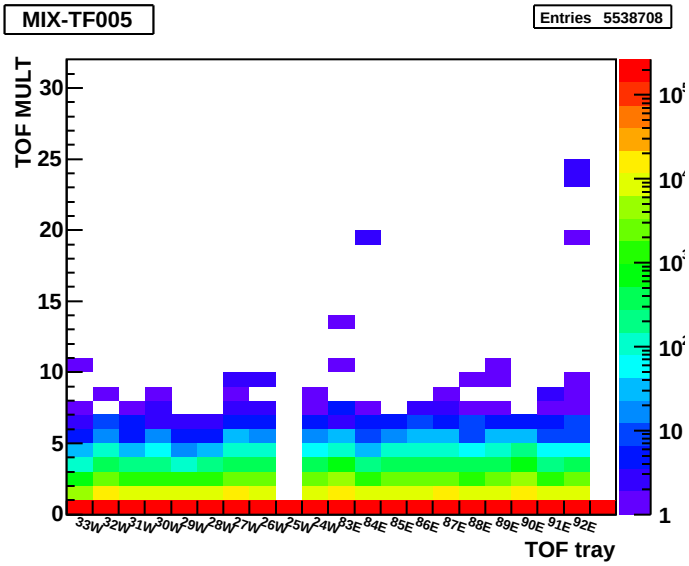

Unused (ch7)
Entries 2109984

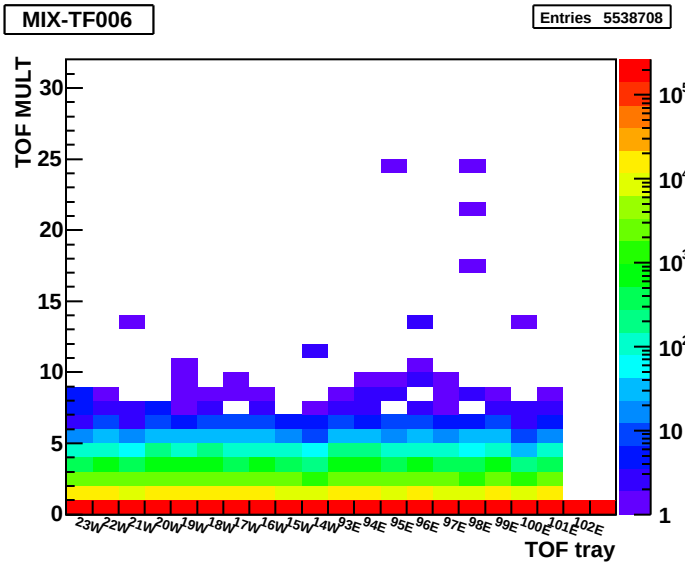

MIX-TF001
Entries 5538708
TOF MULT
TOF tray

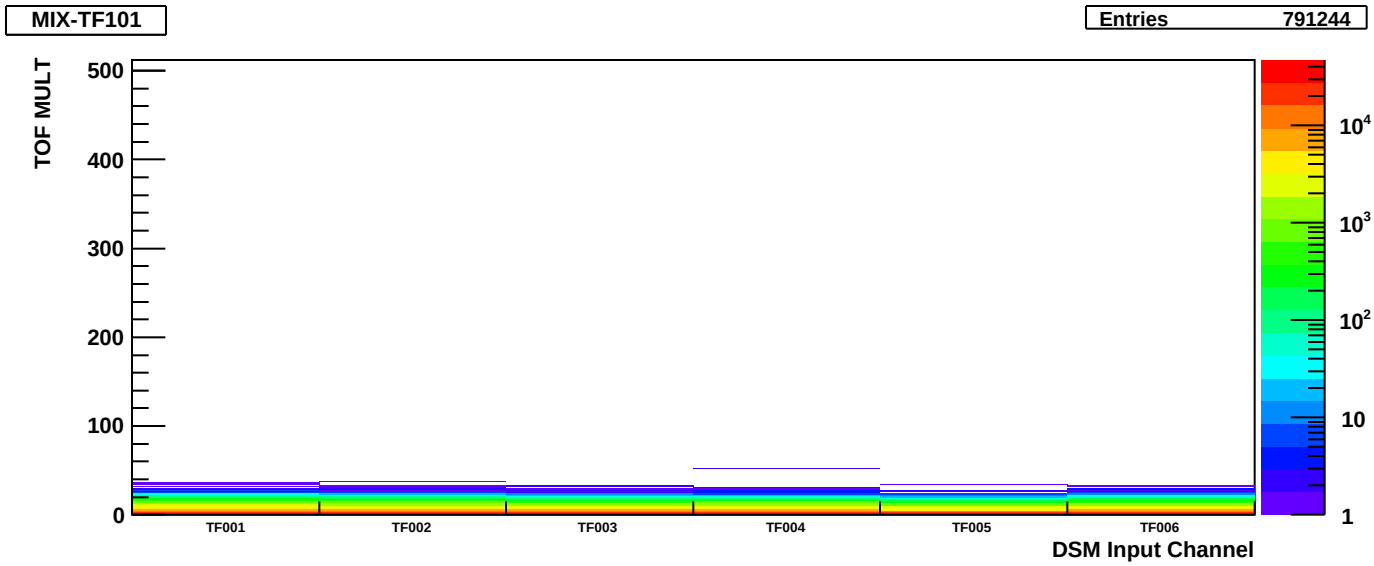

MIX-TF002
Entries 5538708
TOF MULT
TOF tray

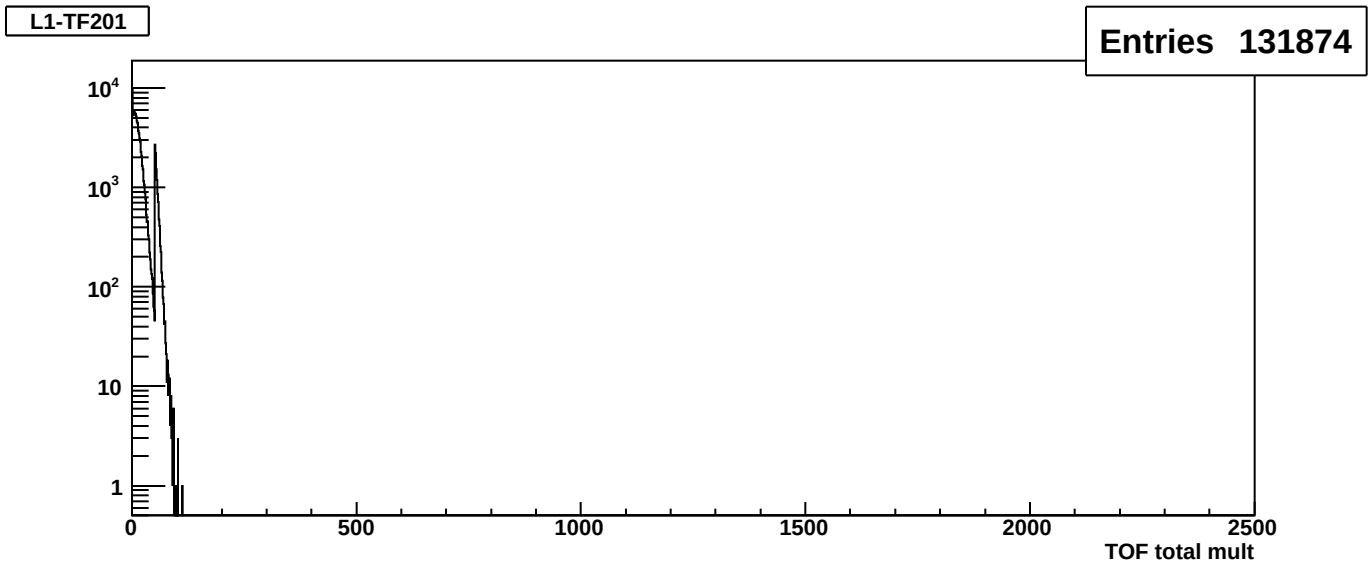

MIX-TF003
Entries 5538708
TOF MULT
TOF tray

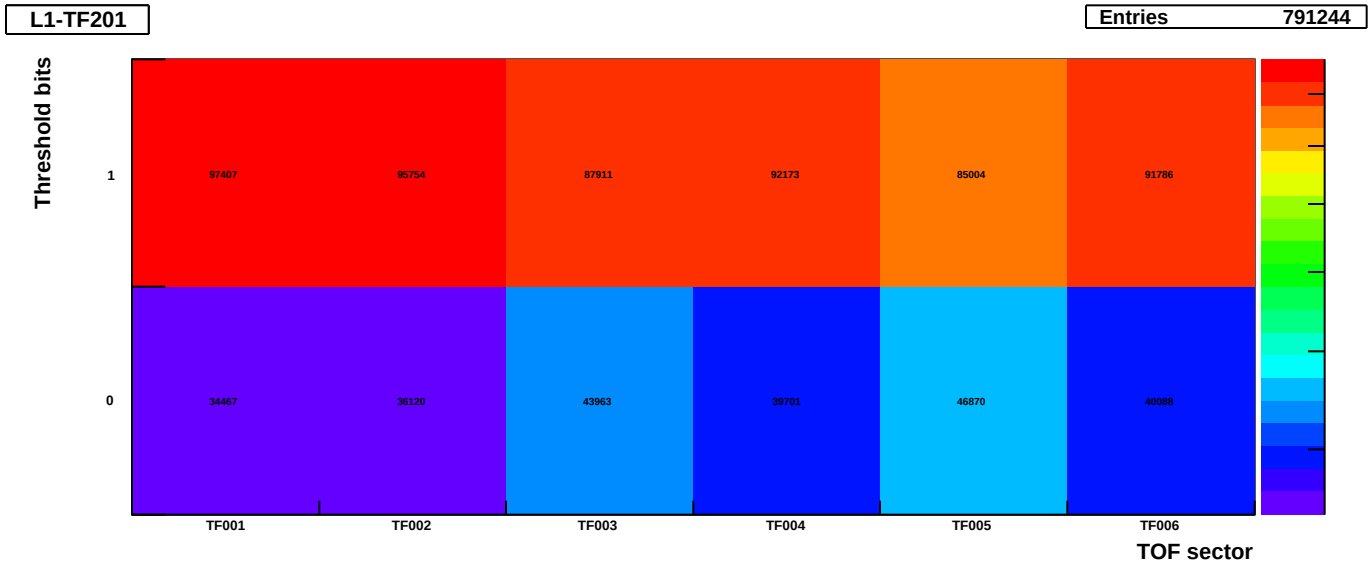

MIX-TF004
Entries 5538708
TOF MULT
TOF tray

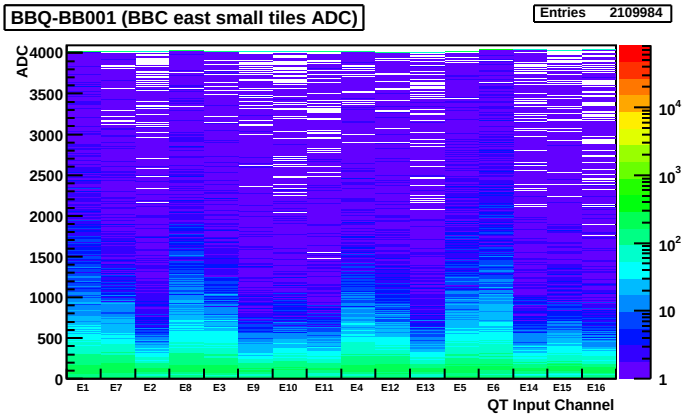

MIX-TF005
Entries 5538708
TOF MULT
TOF tray

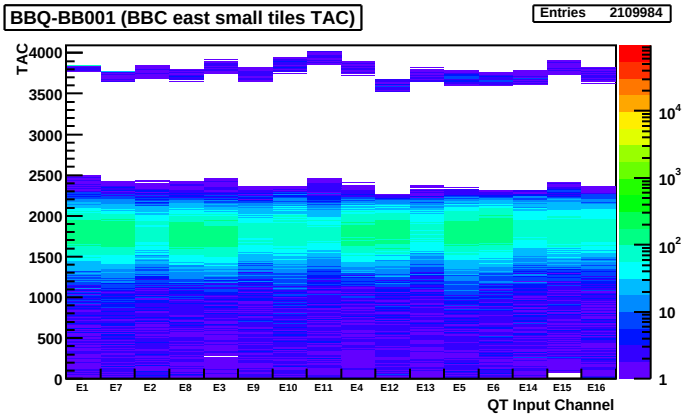

MIX-TF006
Entries 5538708
TOF MULT
TOF tray

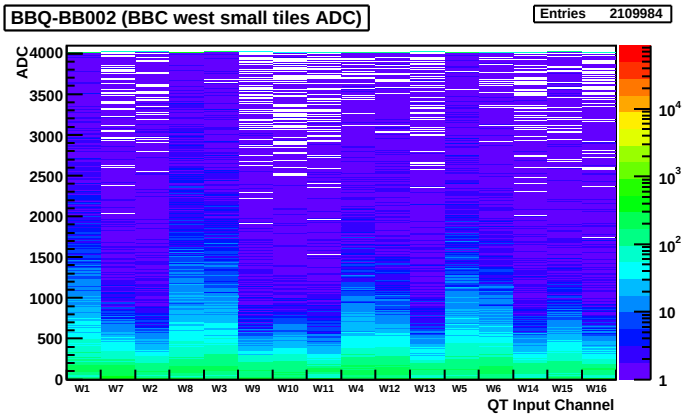

MIX-TF101
Entries 791244
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

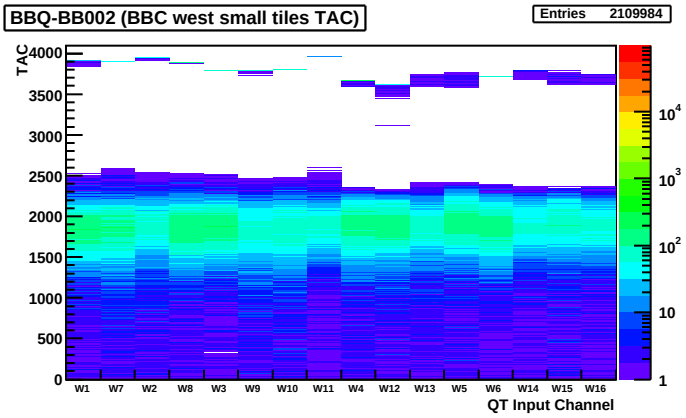

L1-TF201
Entries 131874
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

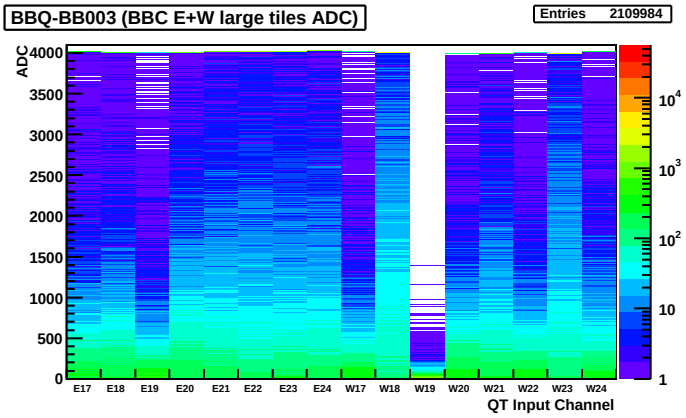

L1-TF201
Entries 791244
Threshold bits
1
0
97407
95754
87911
92173
85004
91786
34467
36120
43963
39701
46870
40088
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

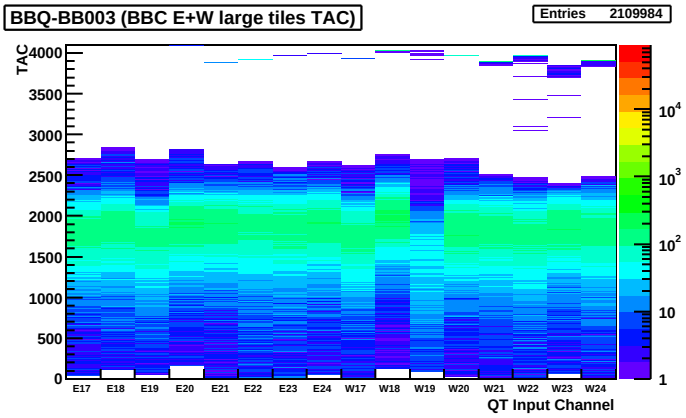

BBQ-BB001 (BBC east small tiles ADC)
Entries 2109984
ADC
QT Input Channel

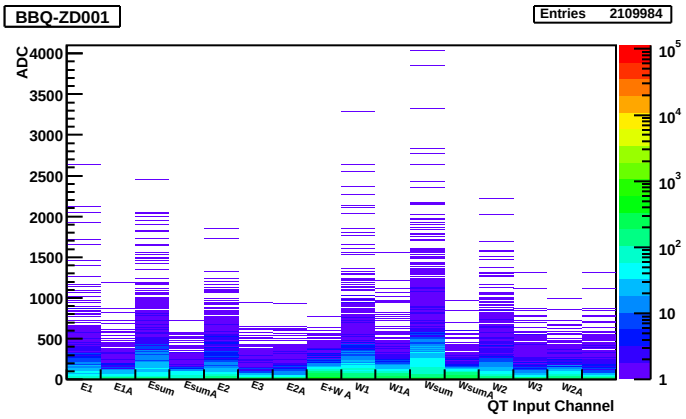

BBQ-BB001 (BBC east small tiles TAC)
Entries 2109984
TAC
QT Input Channel

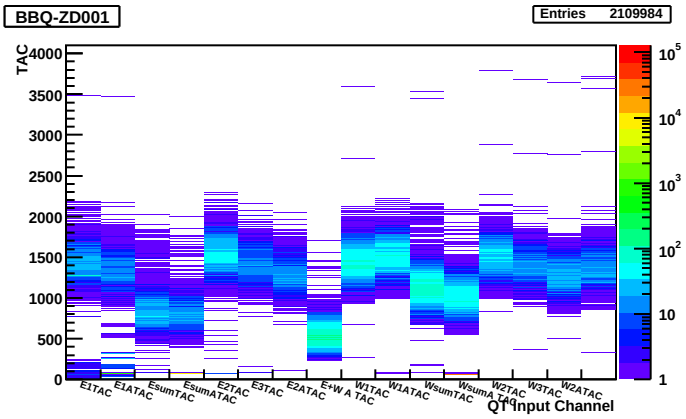

BBQ-BB002 (BBC west small tiles ADC)
Entries 2109984
ADC
QT Input Channel

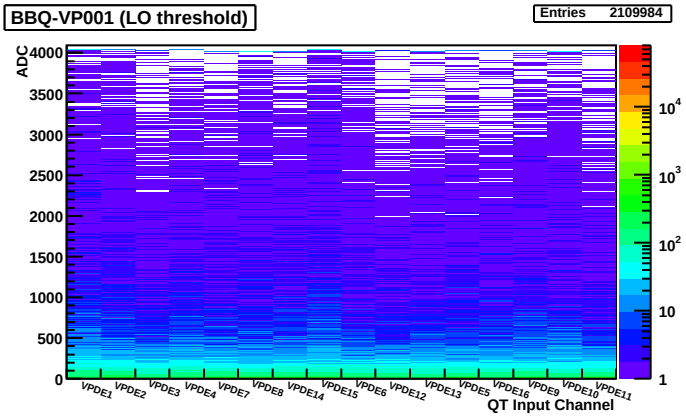

BBQ-BB002 (BBC west small tiles TAC)
Entries 2109984
TAC
QT Input Channel

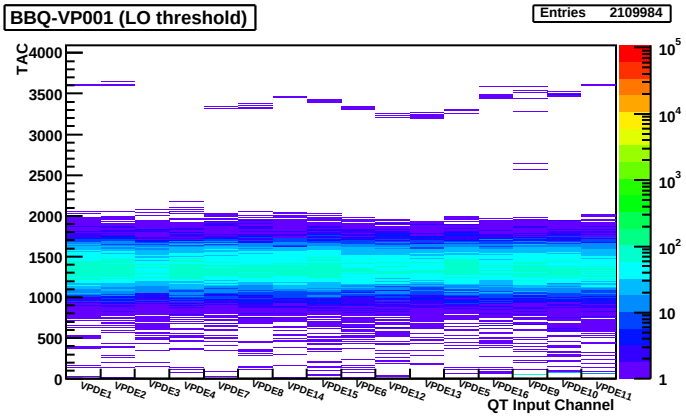

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2109984
ADC
QT Input Channel

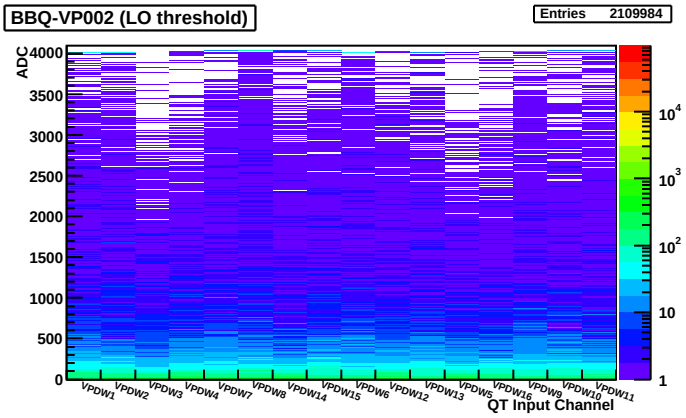

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2109984
TAC
QT Input Channel

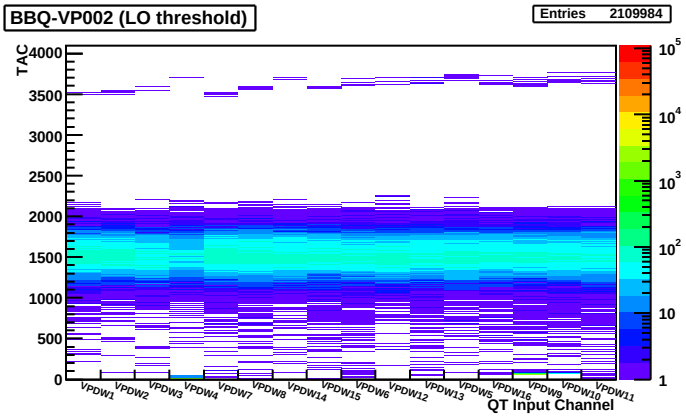

BBQ-ZD001
Entries 2109984
ADC
QT Input Channel

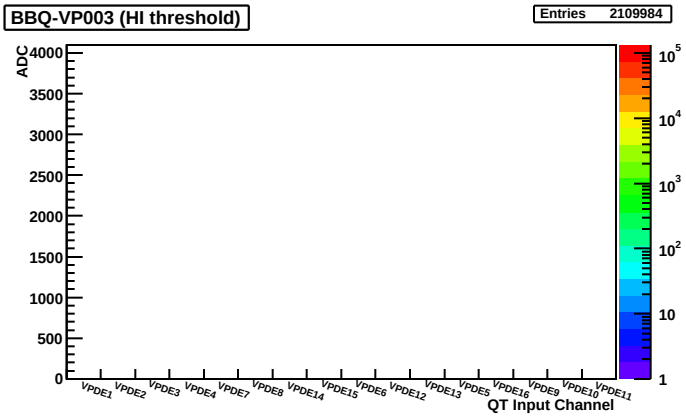

BBQ-ZD001
Entries 2109984
TAC
QT Input Channel

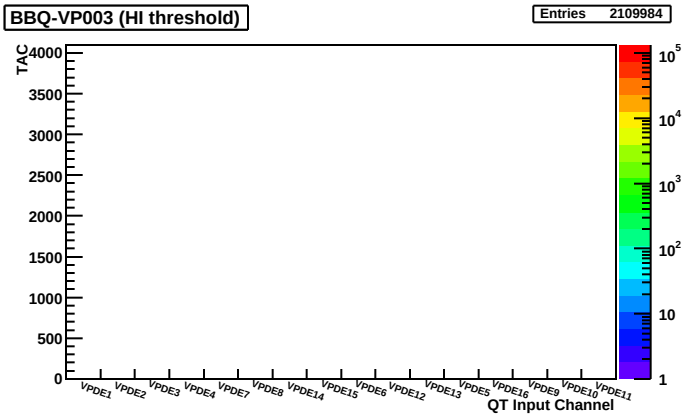

BBQ-VP001 (LO threshold)
Entries 2109984
ADC
QT Input Channel

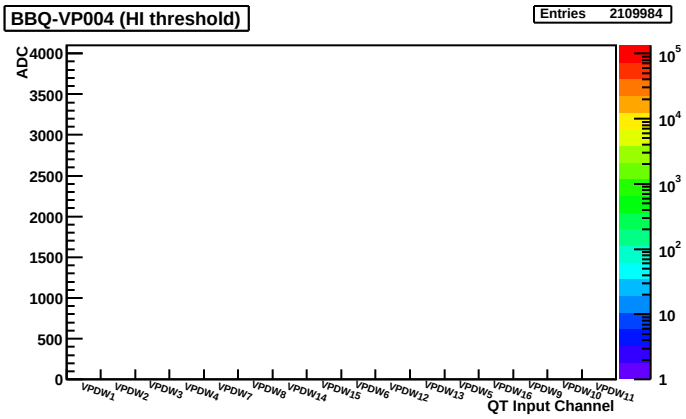

BBQ-VP001 (LO threshold)
Entries 2109984
TAC
QT Input Channel

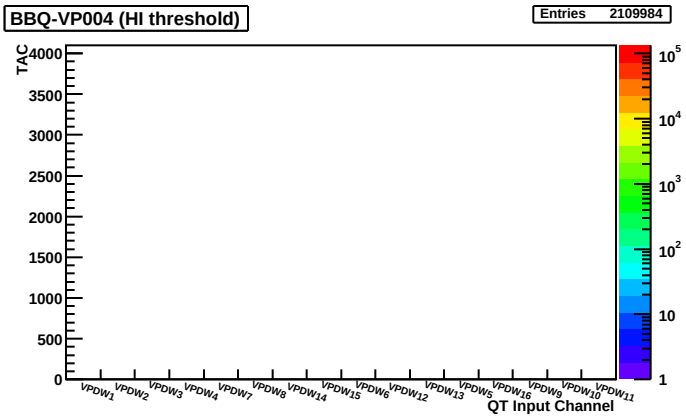

BBQ-VP002 (LO threshold)
Entries 2109984
ADC
QT Input Channel

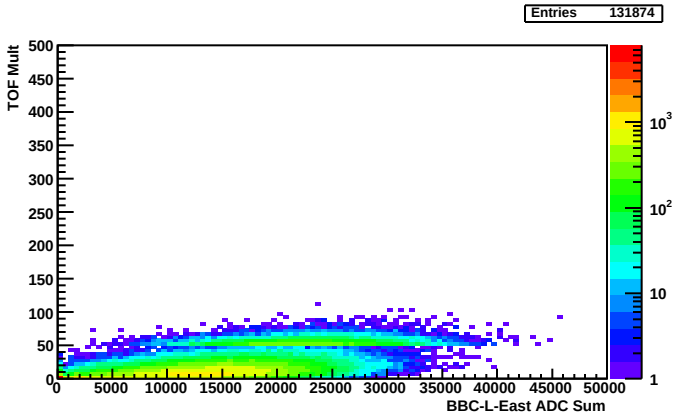

BBQ-VP002 (LO threshold)
Entries 2109984
TAC
QT Input Channel

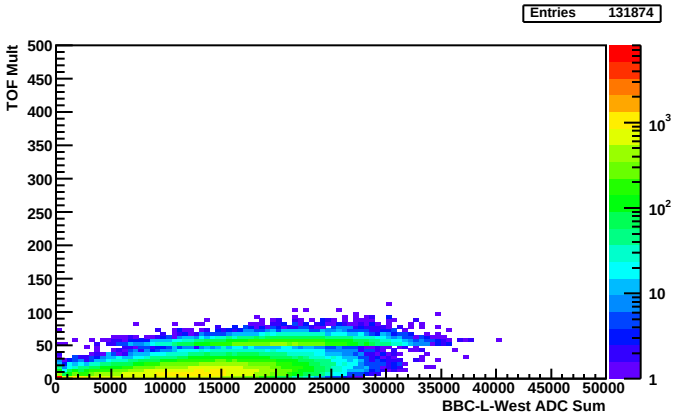

BBQ-VP003 (HI threshold)
Entries 2109984
ADC
QT Input Channel

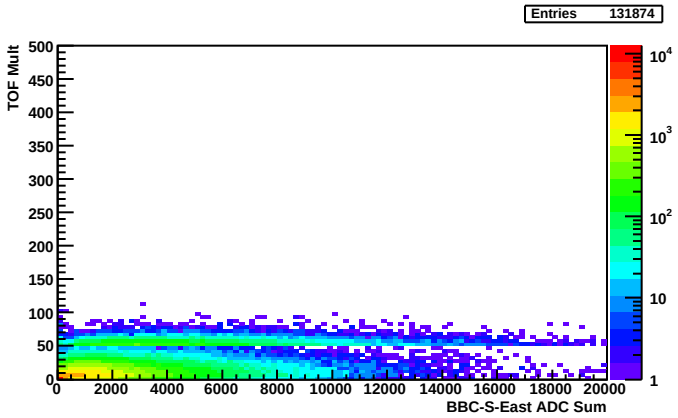

BBQ-VP003 (HI threshold)
Entries 2109984
TAC
QT Input Channel

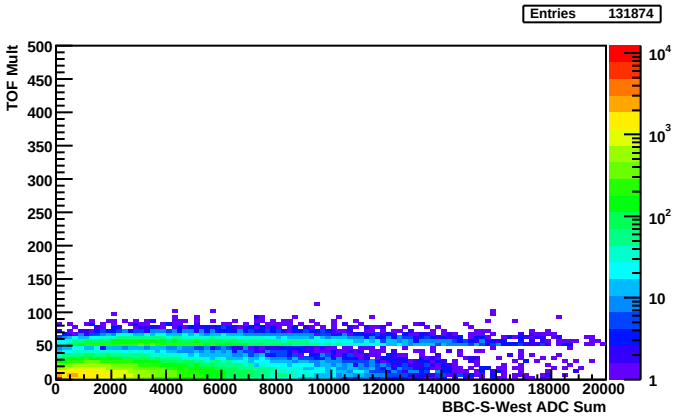

BBQ-VP004 (HI threshold)
Entries 2109984
ADC
QT Input Channel

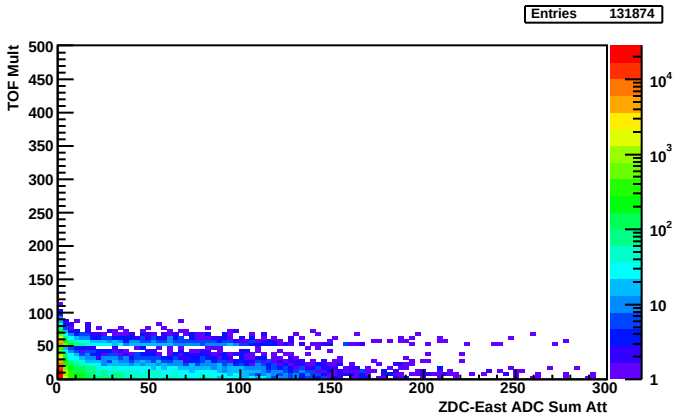

BBQ-VP004 (HI threshold)
Entries 2109984
TAC
QT Input Channel

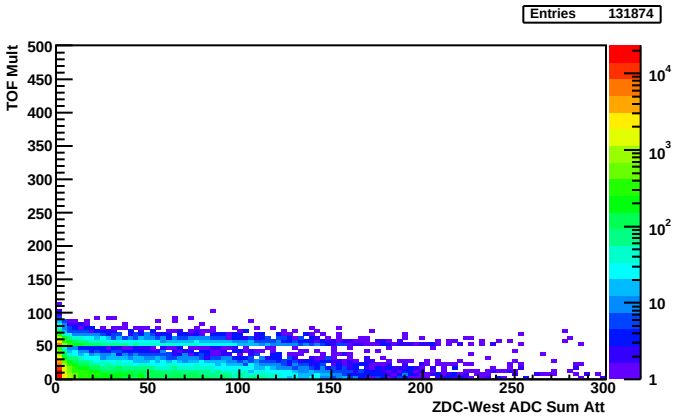

Entries 131874
TOF Mult
BBC-L-East ADC Sum

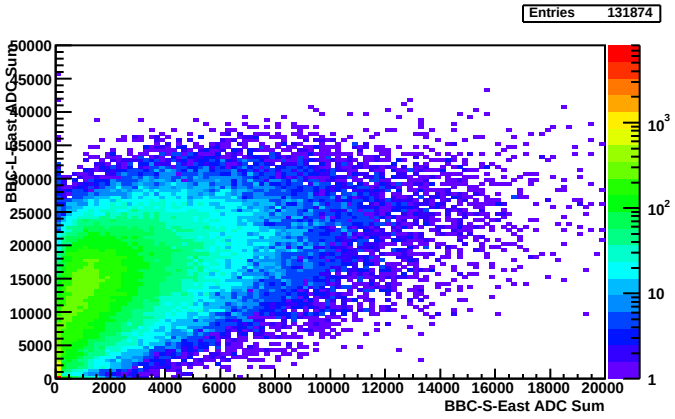

Entries 131874
TOF Mult
BBC-L-West ADC Sum

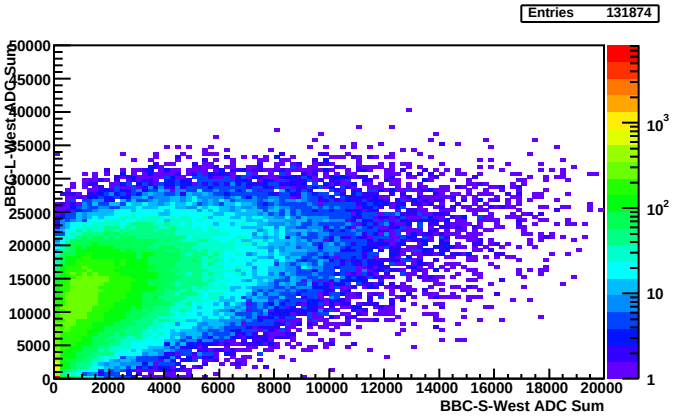

Entries 131874
TOF Mult
BBC-S-East ADC Sum

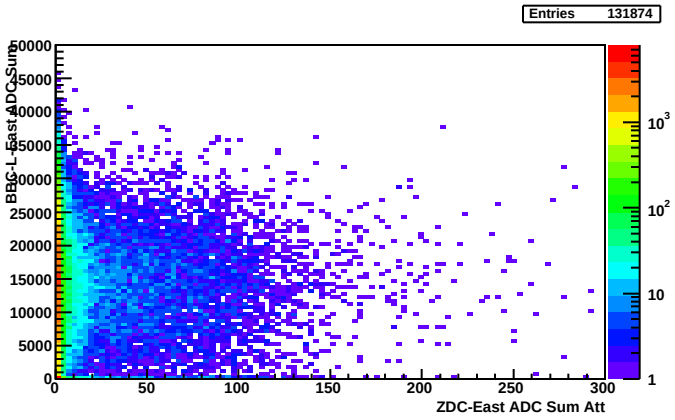

Entries 131874
TOF Mult
BBC-S-West ADC Sum

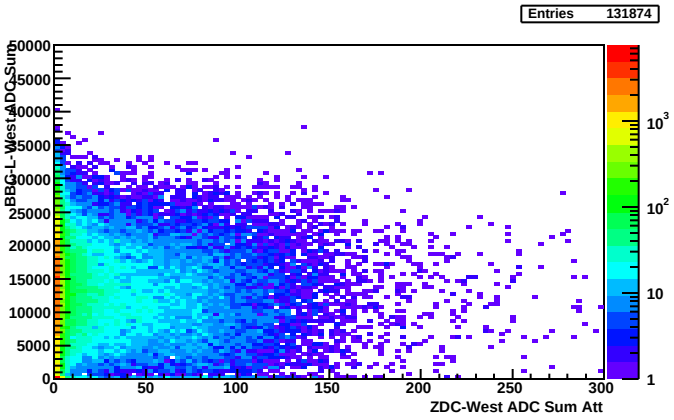

Entries 131874
TOF Mult
ZDC-East ADC Sum Att

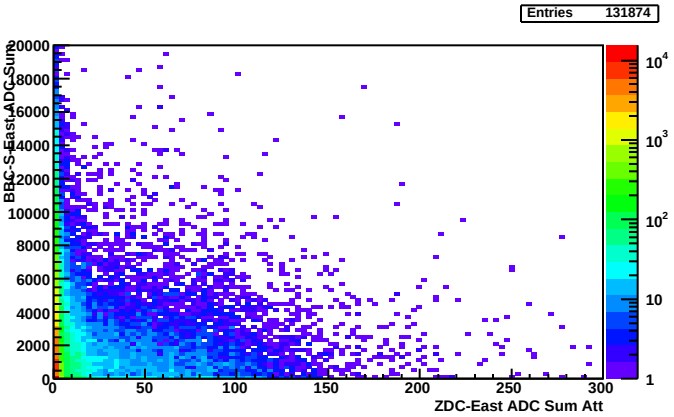

Entries 131874
TOF Mult
ZDC-West ADC Sum Att

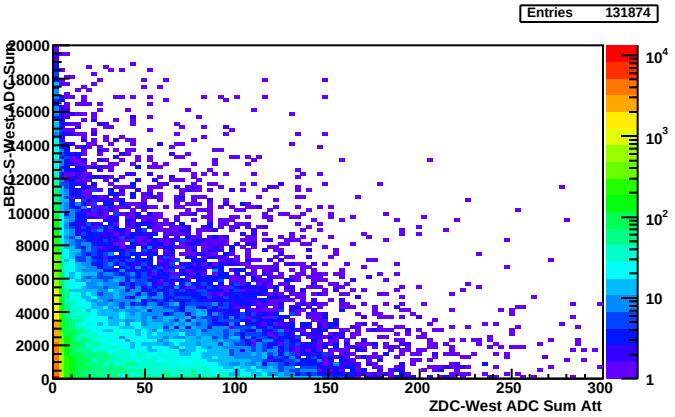

Entries 131874
BBC-L-East ADC Sum
BBC-S-East ADC Sum

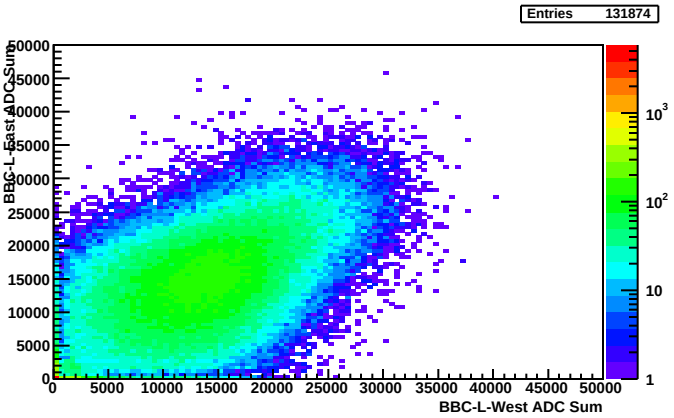

Entries 131874
BBC-L-West ADC Sum
BBC-S-West ADC Sum

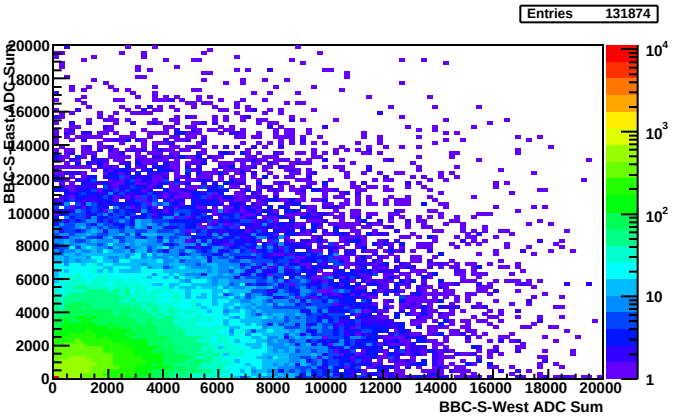

Entries 131874
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

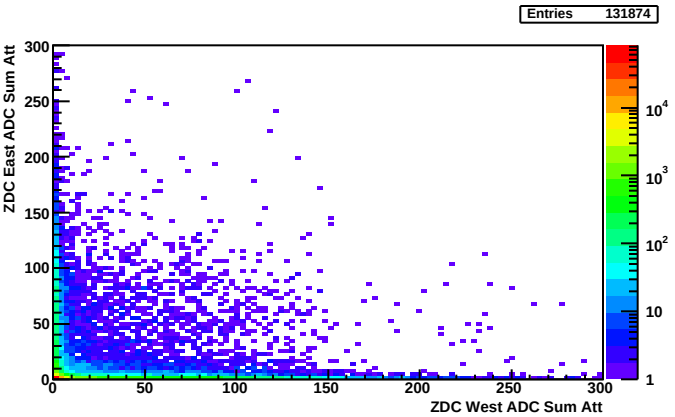

Entries 131874
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

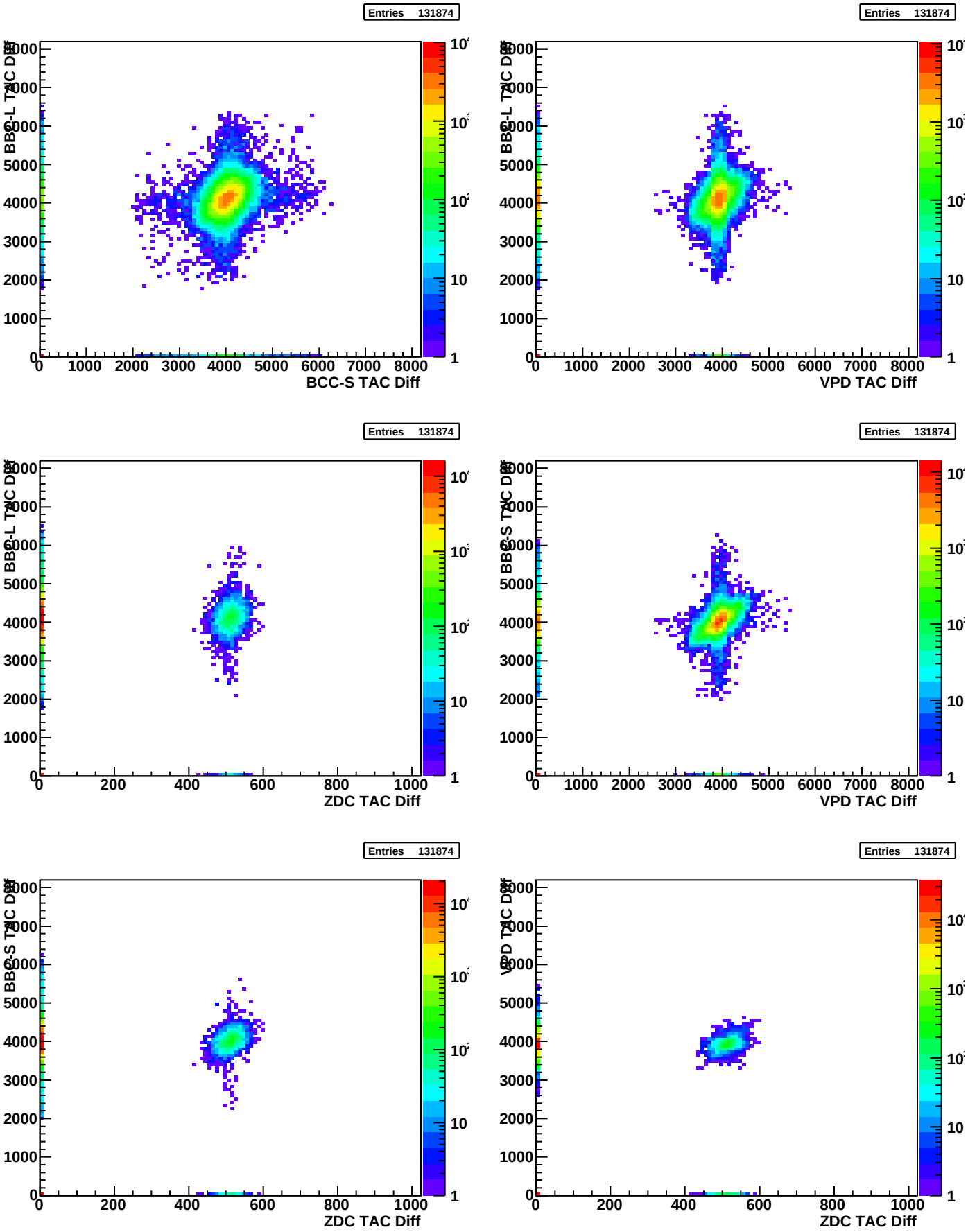

Entries 131874
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

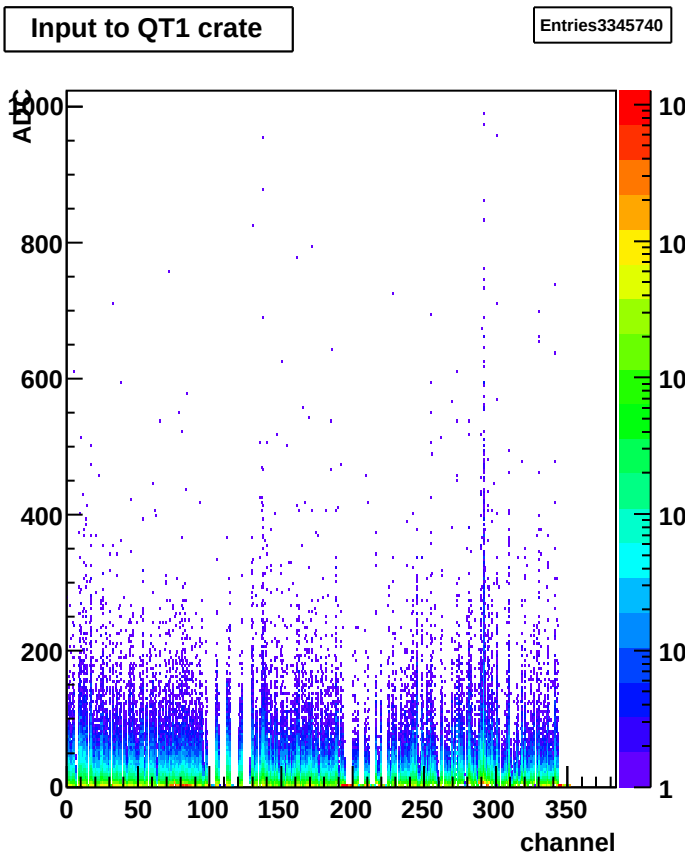

Entries 131874
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

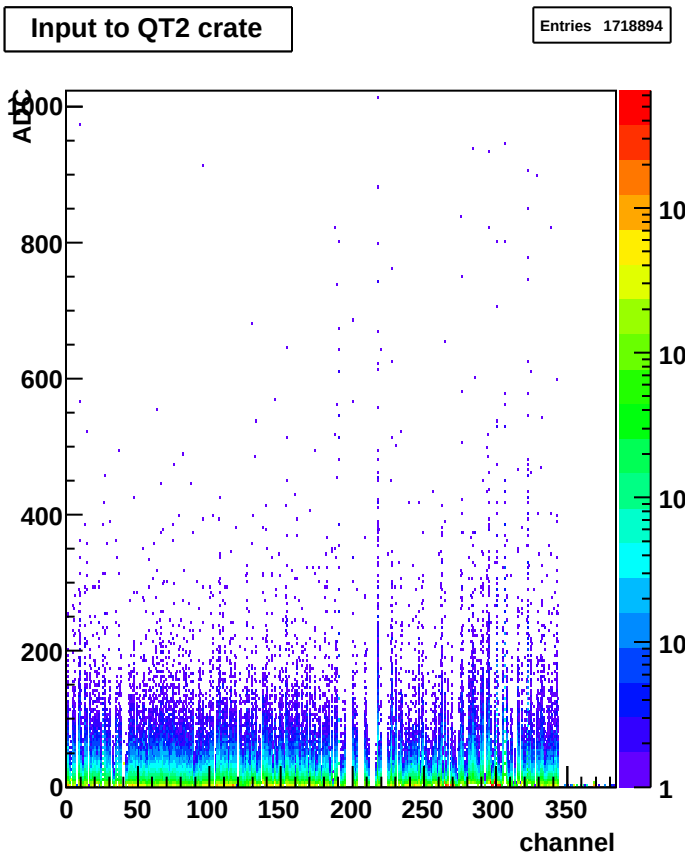

Entries 131874
BBC-L-East ADC Sum
BBC-L-West ADC Sum

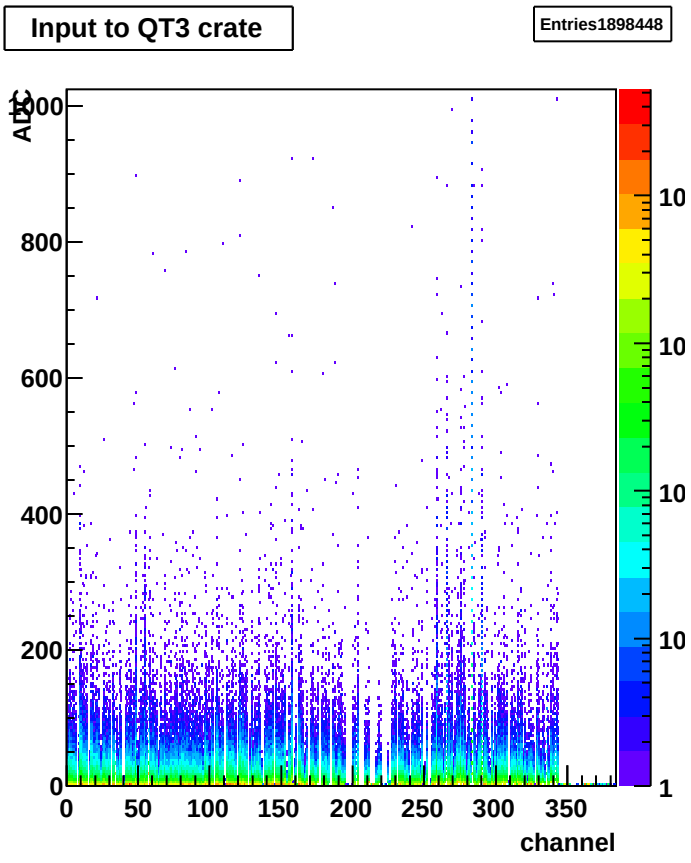

Entries 131874
BBC-S-East ADC Sum
BBC-S-West ADC Sum

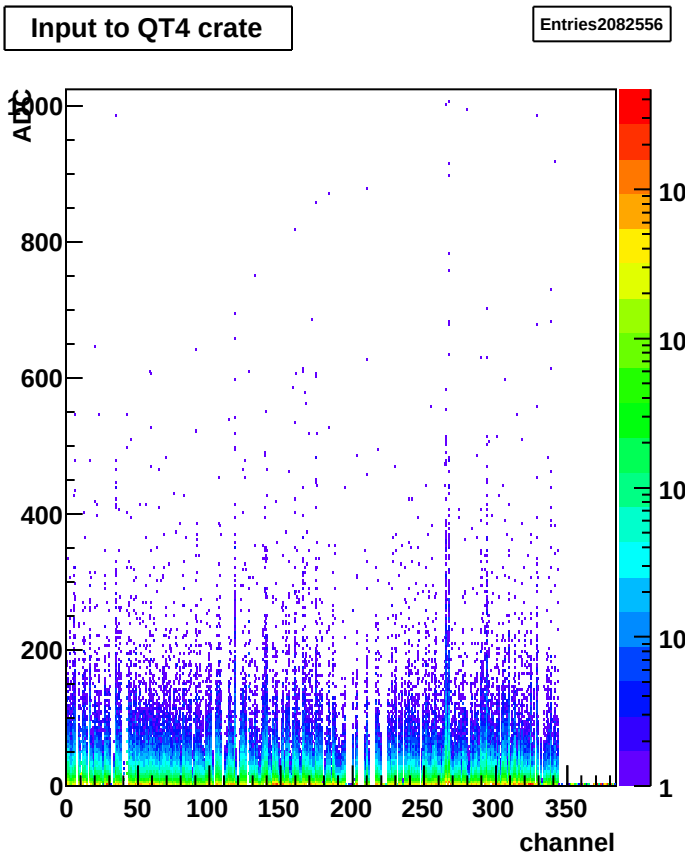

Entries 131874
ZDC East ADC Sum Att
ZDC West ADC Sum Att

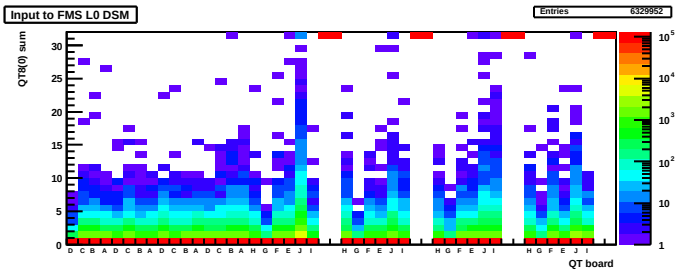

Entries 131874
BBC-L TAC Diff
BCC-S TAC Diff
Entries 131874
BBC-L TAC Diff
VPD TAC Diff
Entries 131874
BBC-L TAC Diff
ZDC TAC Diff
Entries 131874
BBC-S TAC Diff
VPD TAC Diff
Entries 131874
BBC-S TAC Diff
ZDC TAC Diff
Entries 131874
VPD TAC Diff
ZDC TAC Diff

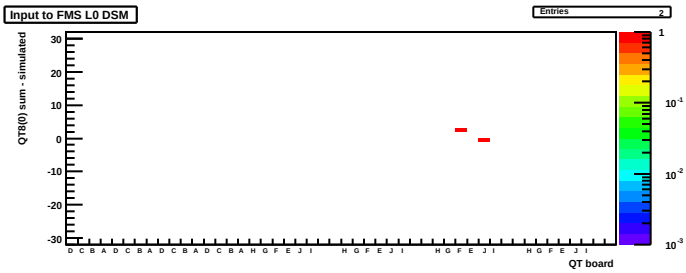

Input to QT1 crate
Entries3345740
ADC
channel

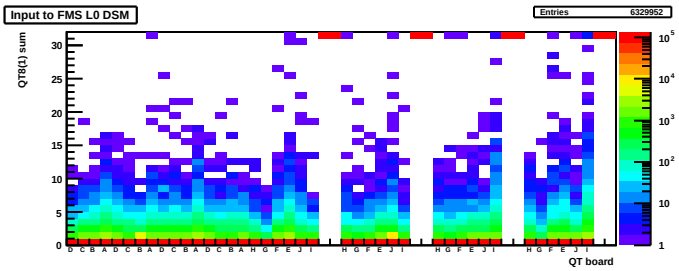

Input to QT2 crate
Entries 1718894
ADC
channel

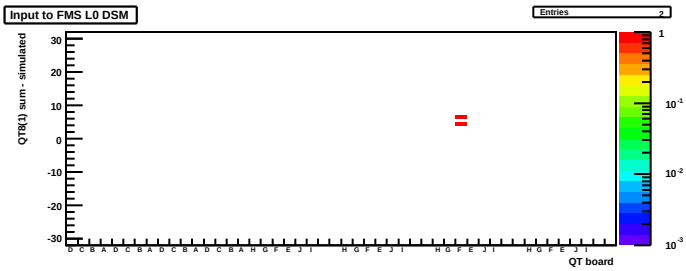

Input to QT3 crate
Entries1898448
ADC
channel

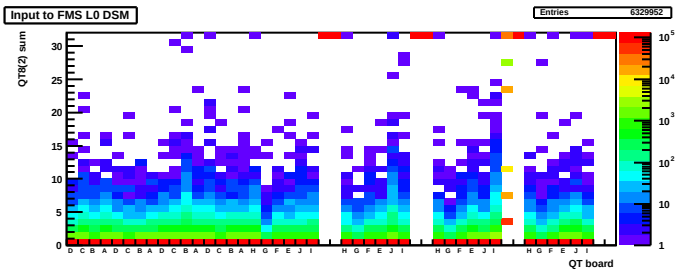

Input to QT4 crate
Entries2082556
ADC
channel

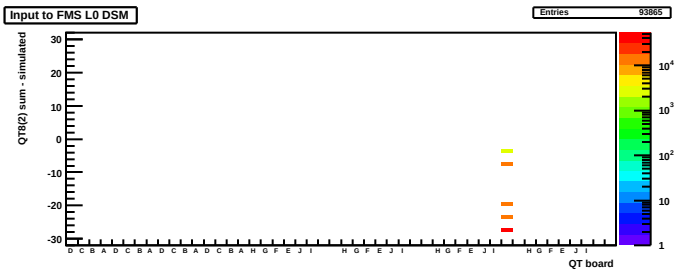

Input to FMS L0 DSM
Entries 6329952
QT8(0) sum
QT board

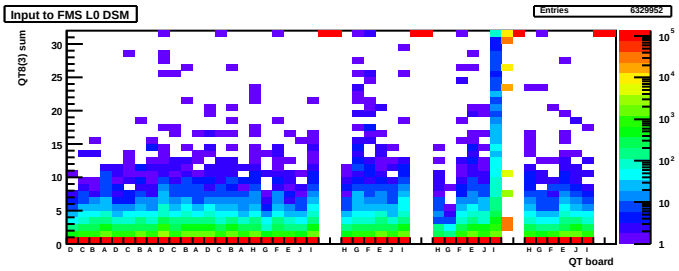

Input to FMS L0 DSM
Entries 2
QT8(0) sum - simulated
QT board

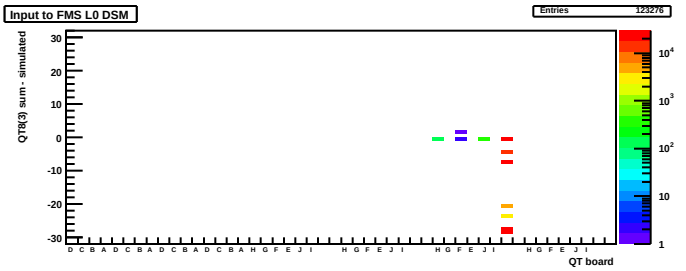

Input to FMS L0 DSM
Entries 6329952
QT8(1) sum
QT board

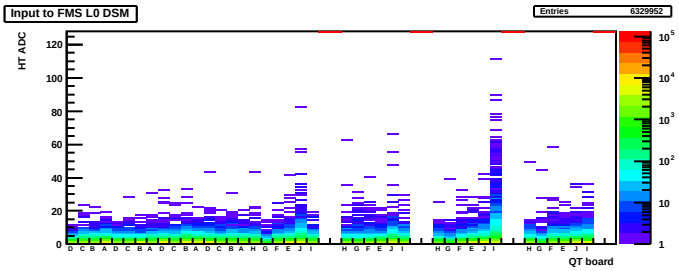

Input to FMS L0 DSM
Entries 2
QT8(1) sum - simulated
QT board

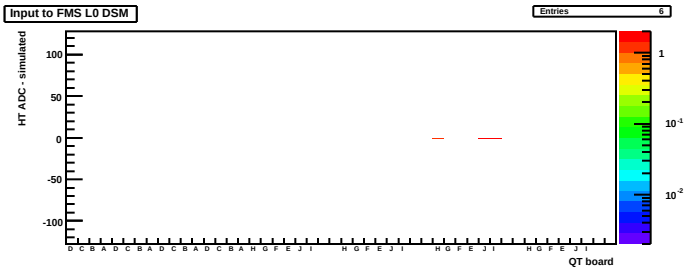

Input to FMS L0 DSM
Entries 6329952
QT8(2) sum
QT board

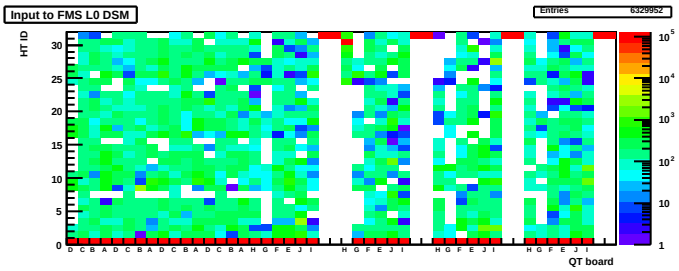

Input to FMS L0 DSM
Entries 93865
QT8(2) sum - simulated
QT board

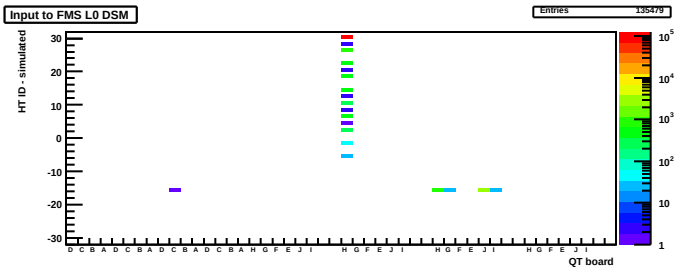

Input to FMS L0 DSM
Entries 6329952
QT8(3) sum
QT board

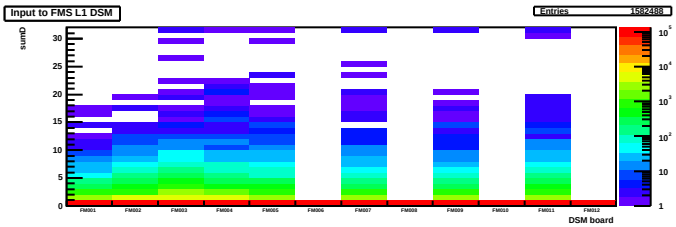

Input to FMS L0 DSM
Entries 123276
QT8(3) sum - simulated
QT board

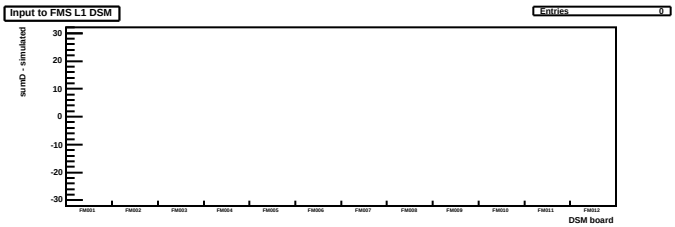

Input to FMS L0 DSM
Entries 6329952
HT ADC
QT board

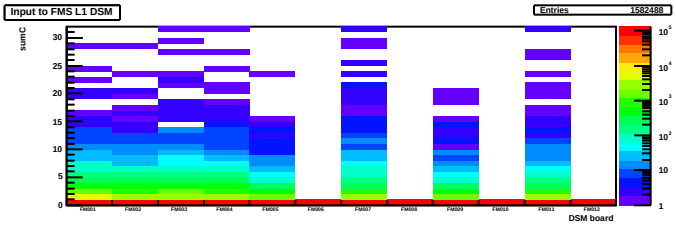

Input to FMS L0 DSM
Entries 6
HT ADC - simulated
QT board

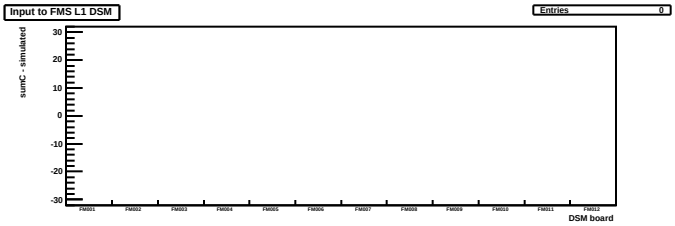

Input to FMS L0 DSM
Entries 6329952
HT ID
QT board

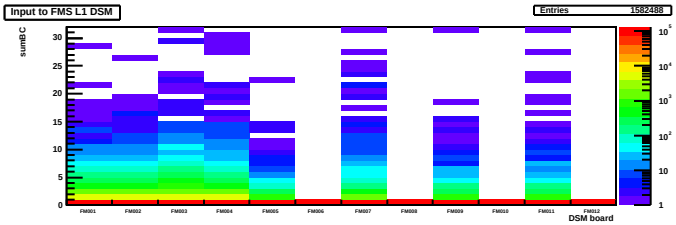

Input to FMS L0 DSM
Entries 135479
HT ID - simulated
QT board

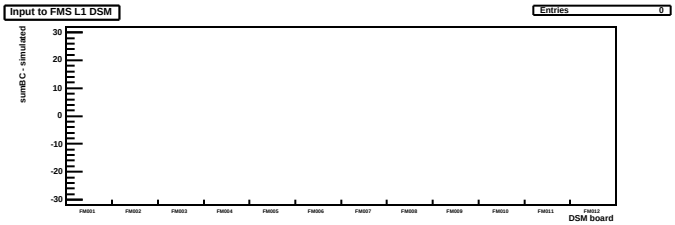

Input to FMS L1 DSM
Entries 1582488
sumD
DSM board

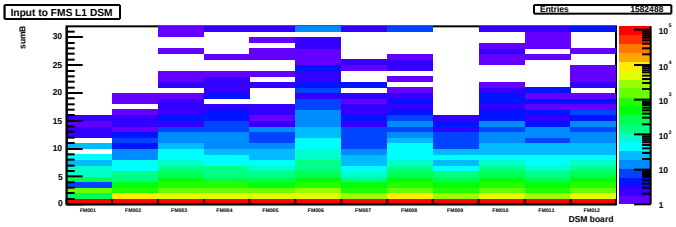

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

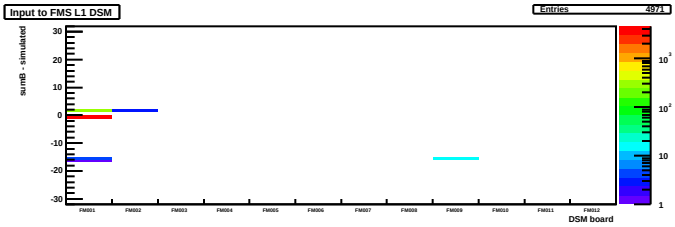

Input to FMS L1 DSM
Entries 1582488
sumC
DSM board

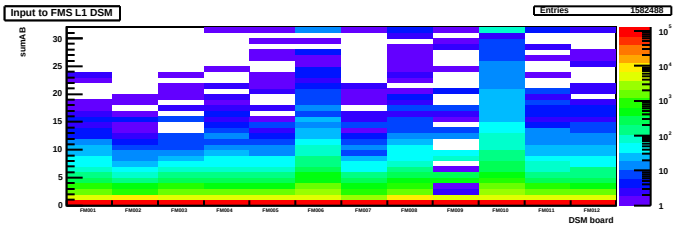

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

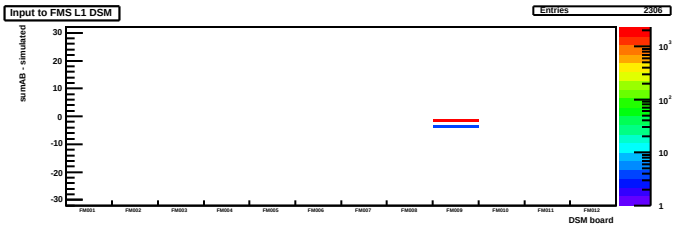

Input to FMS L1 DSM
Entries 1582488
sumBC
DSM board

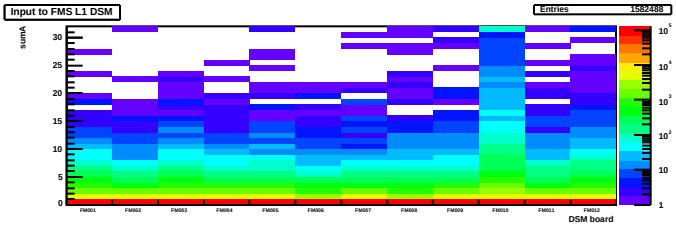

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

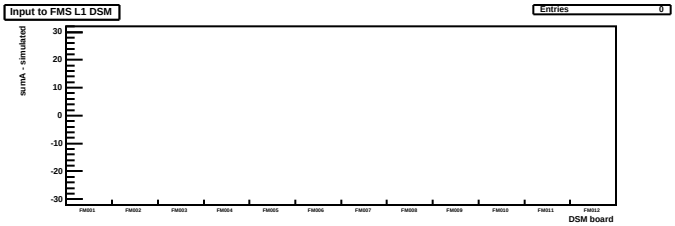

Input to FMS L1 DSM
Entries 1582488
sumB
DSM board

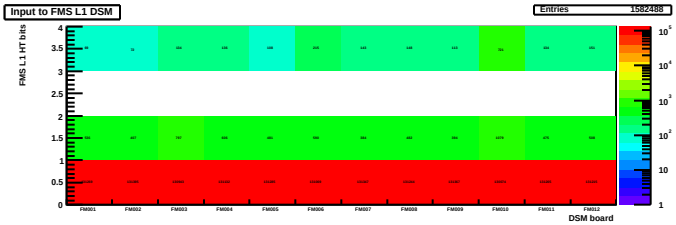

Input to FMS L1 DSM
Entries 4971
sumB - simulated
DSM board

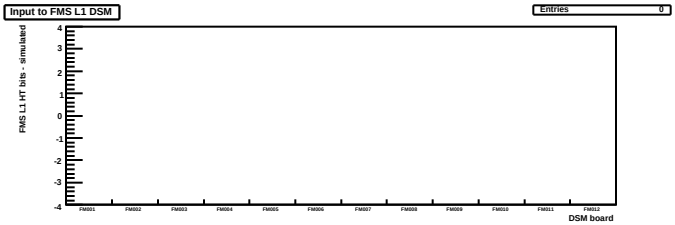

Input to FMS L1 DSM
Entries 1582488
sumAB
DSM board

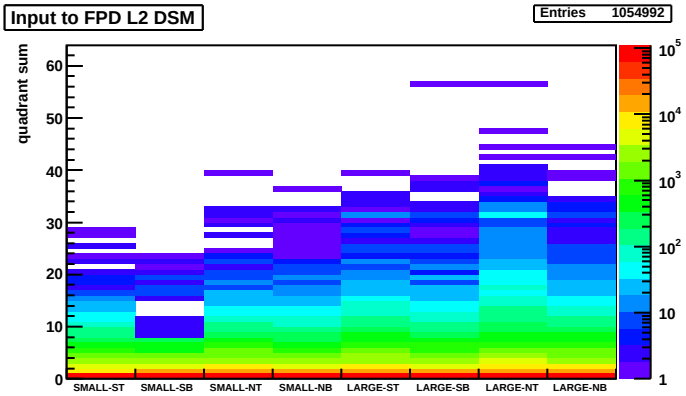

Input to FMS L1 DSM
Entries 2306
sumAB - simulated
DSM board

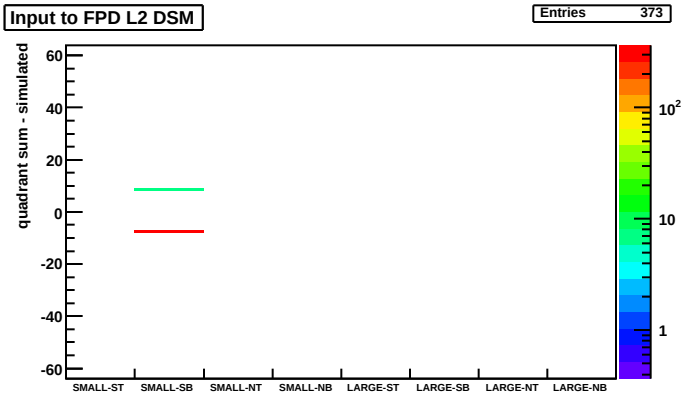

Input to FMS L1 DSM
Entries 1582488
sumA
DSM board

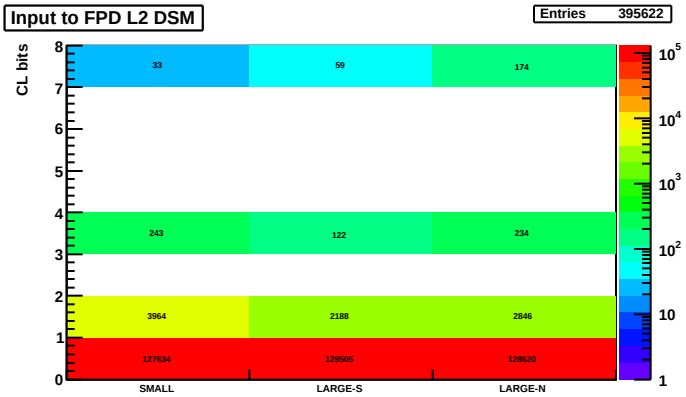

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

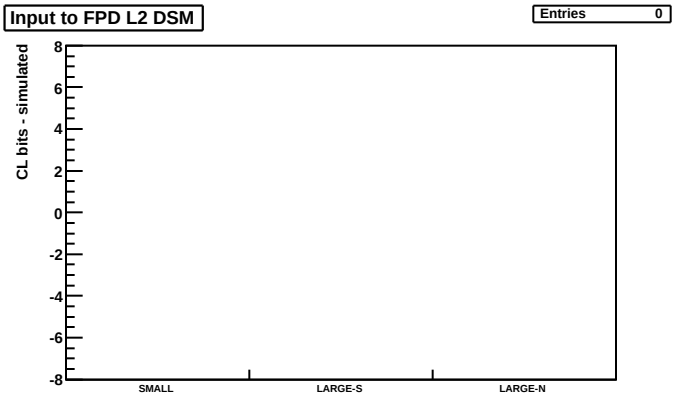

Input to FMS L1 DSM
Entries 1582488
FMS L1 HT bits
DSM board

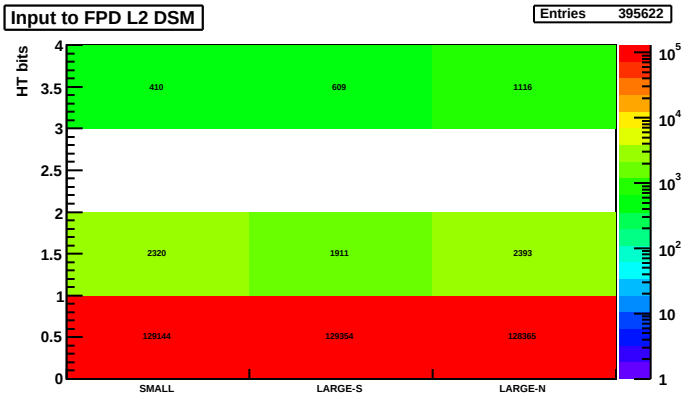

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

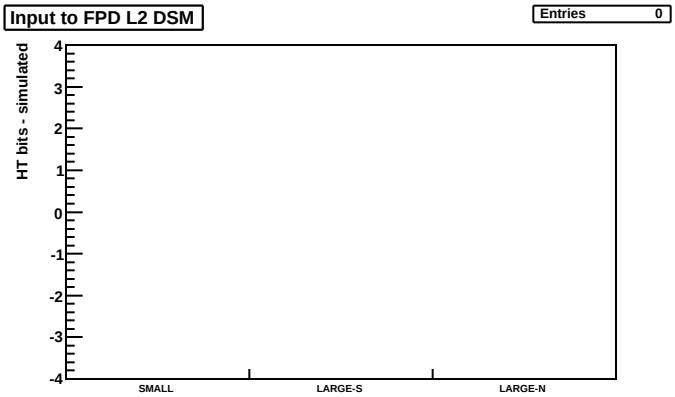

Input to FPD L2 DSM
Entries 1054992
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

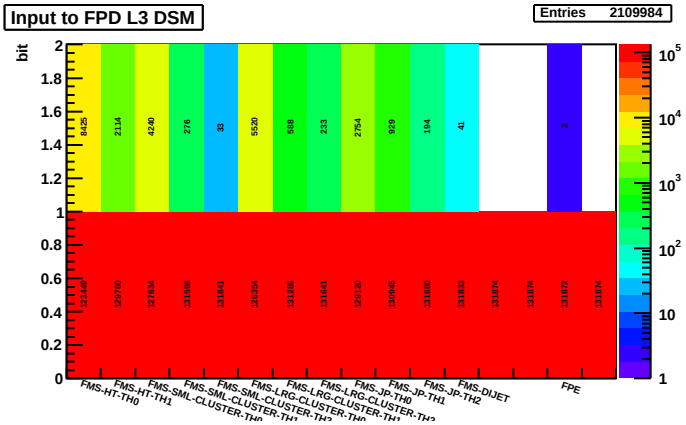

Input to FPD L2 DSM
Entries 373
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

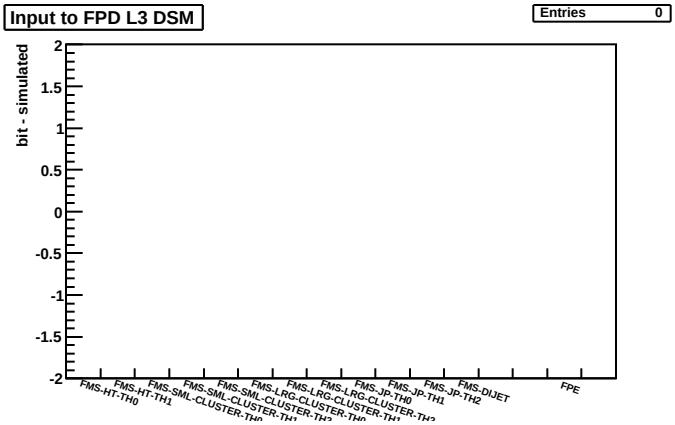

Input to FPD L2 DSM
Entries 395622
CL bits
33
59
174
243
122
234
3964
2188
2846
127634
129505
128620
SMALL
LARGE-S
LARGE-N

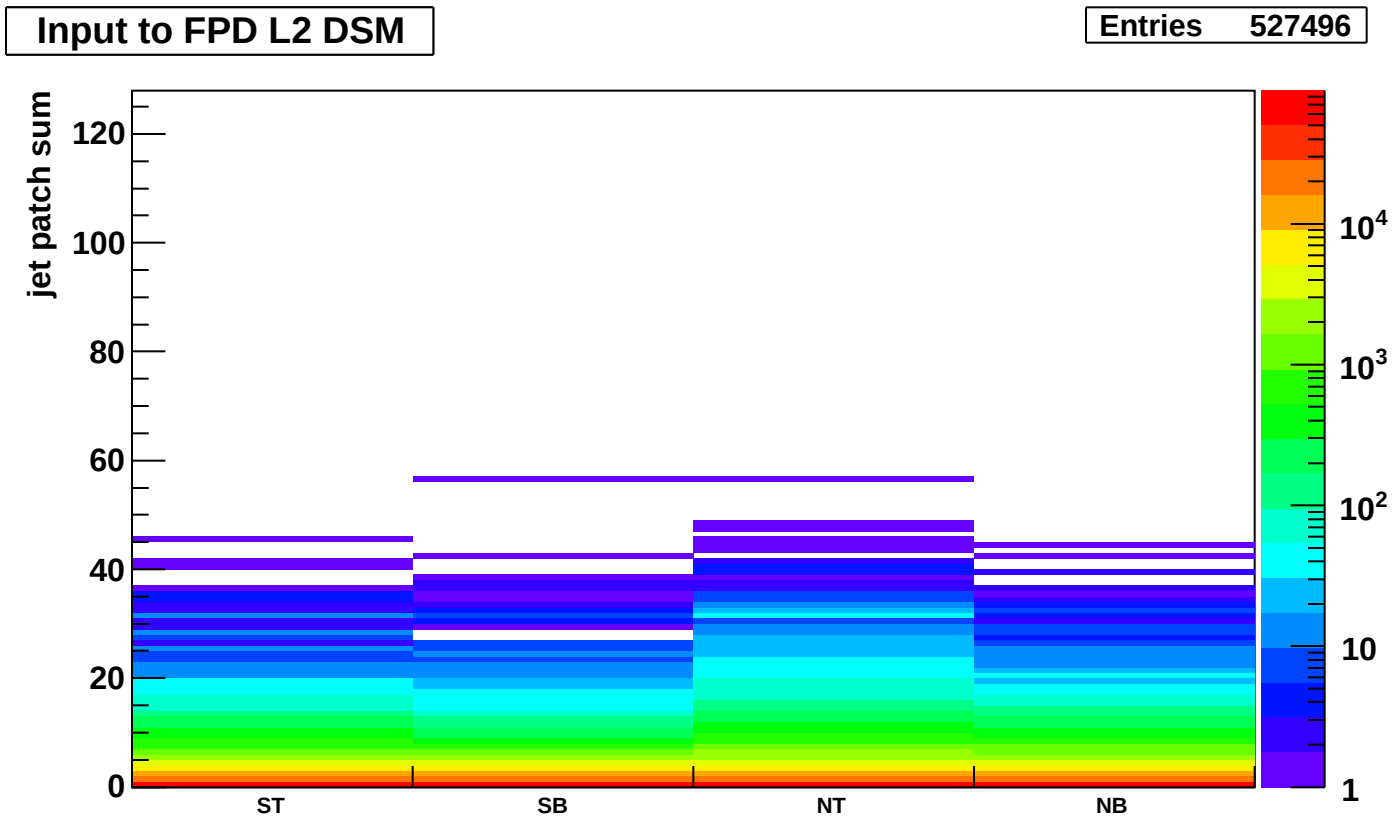

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

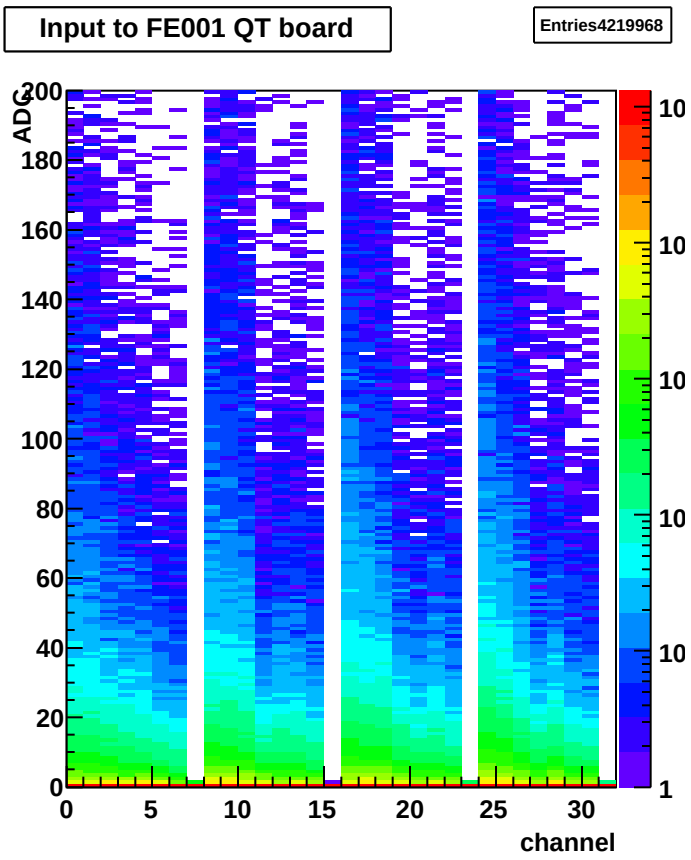

Input to FPD L2 DSM
Entries 395622
HT bits
410
609
1116
2320
1911
2393
129144
129354
128365
SMALL
LARGE-S
LARGE-N

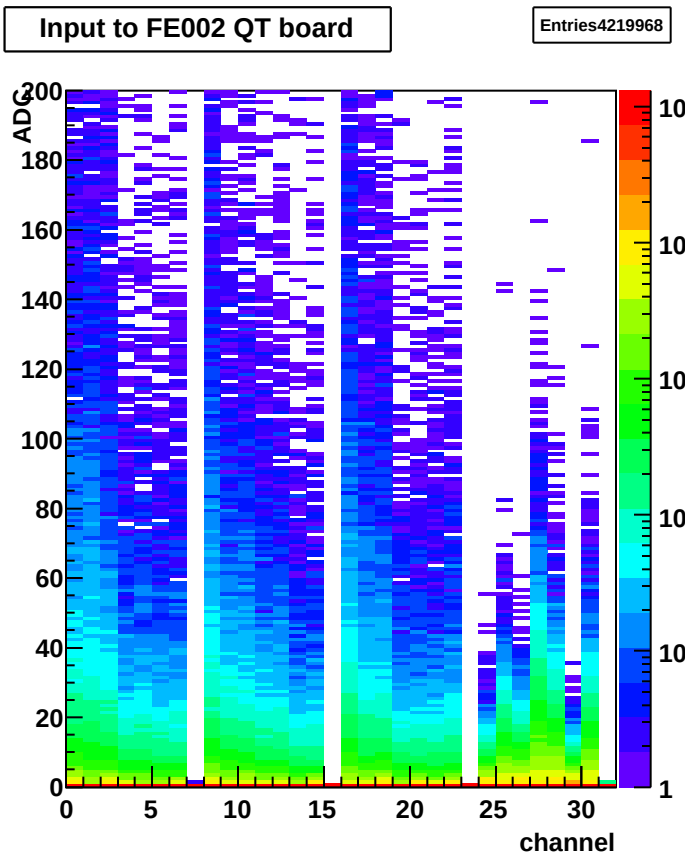

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

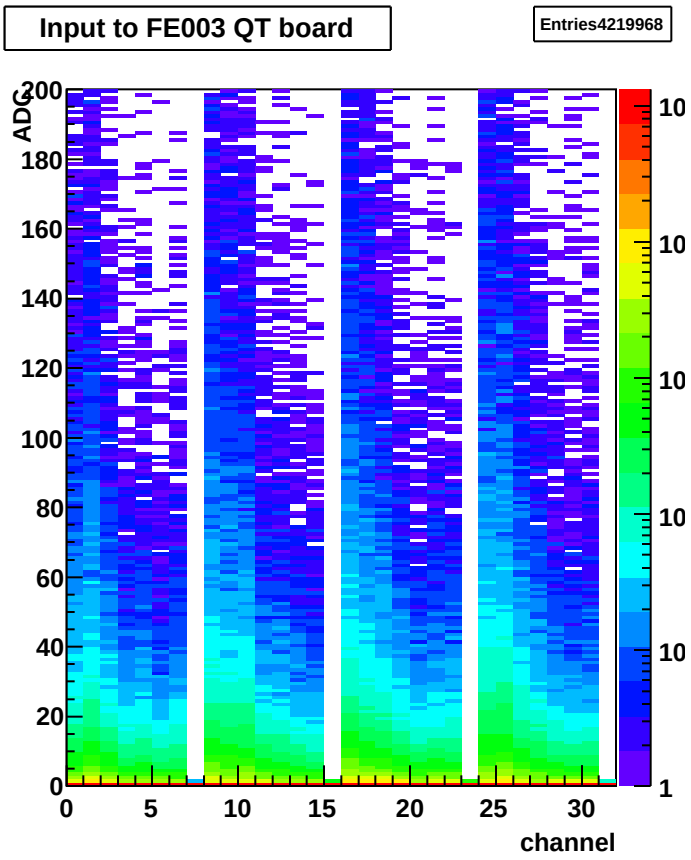

Input to FPD L3 DSM
Entries 2109984
bit
8425
2114
4240
276
33
5520
588
233
2754
929
194
41
2
123487
129760
127634
131598
131641
126354
131236
131641
129120
130945
131680
131823
131874
131874
131872
131874
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

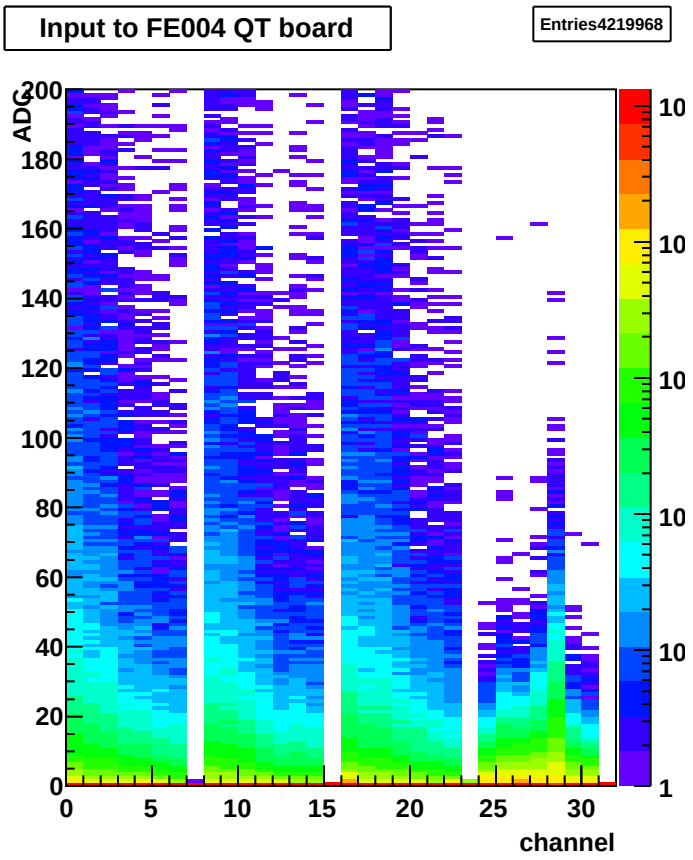

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

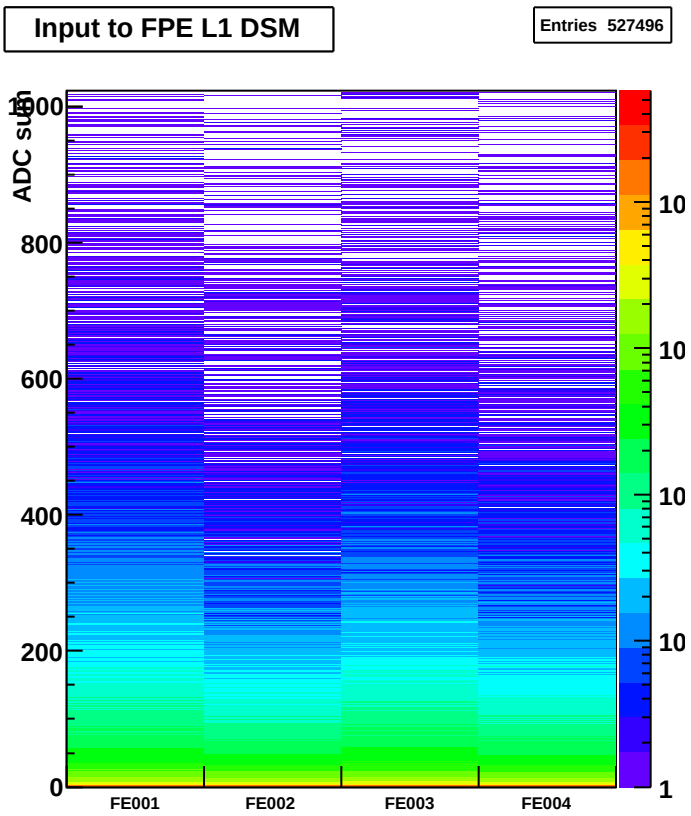

Input to FPD L2 DSM
Entries 527496
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

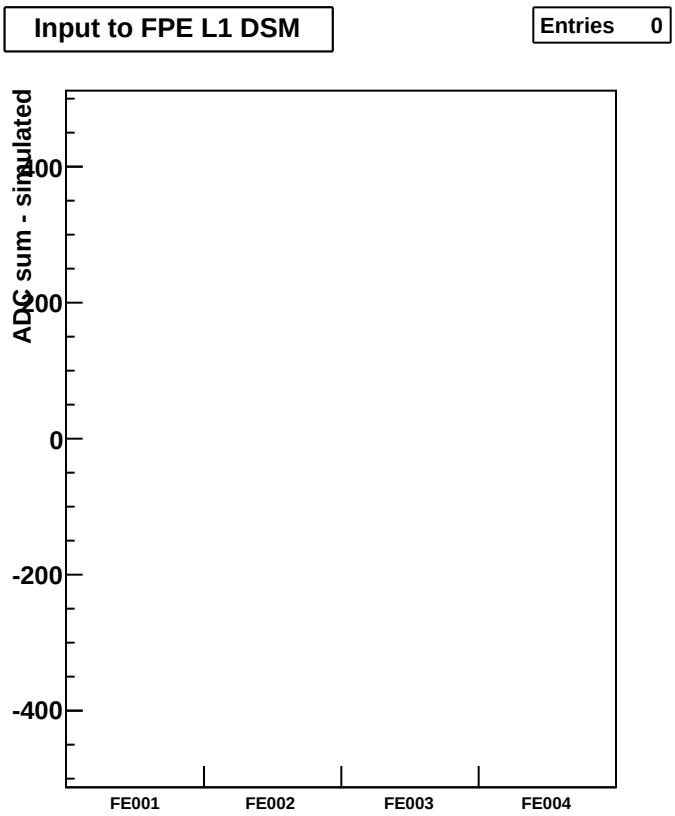

Input to FE001 QT board
Entries4219968
ADC
channel

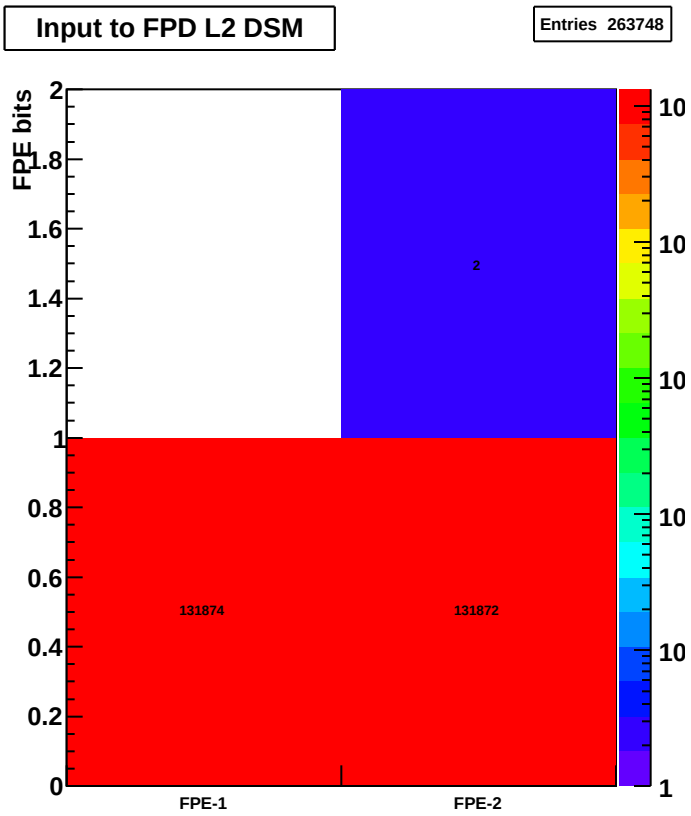

Input to FE002 QT board
Entries4219968
ADC
channel

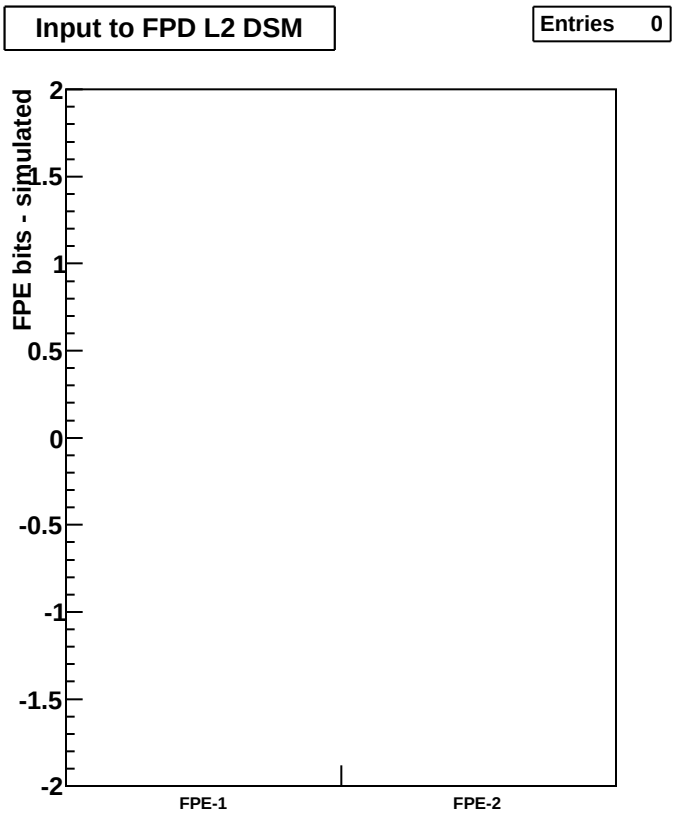

Input to FE003 QT board
Entries4219968
ADC
channel


Input to FE004 QT board
Entries4219968
ADC
channel


Input to FPE L1 DSM
Entries 527496
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 263748
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
2
131874
131872
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2