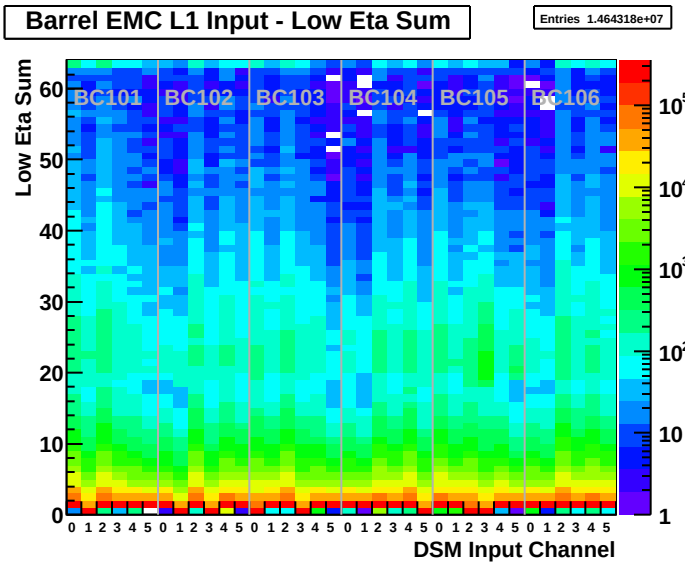
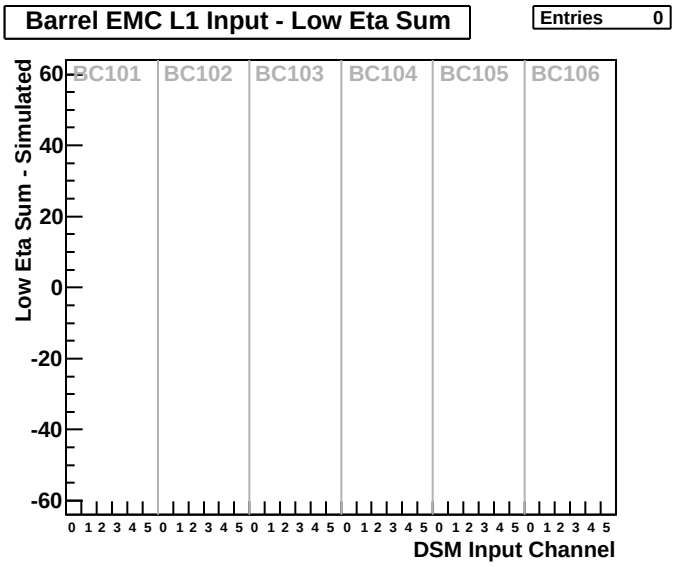
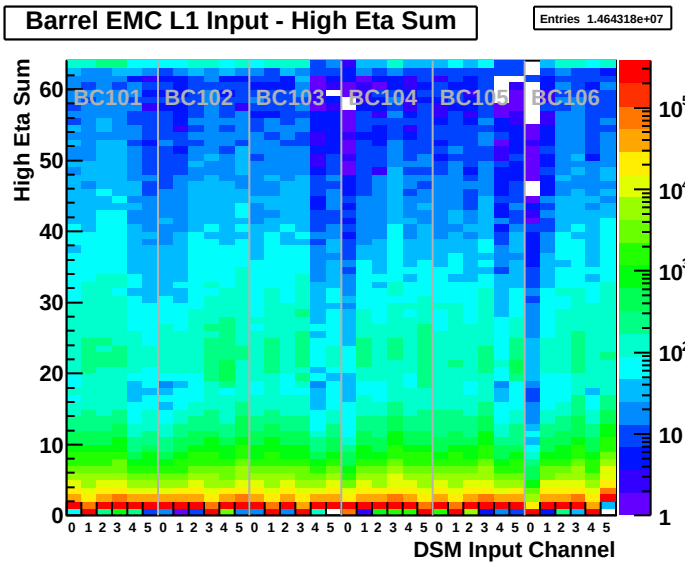
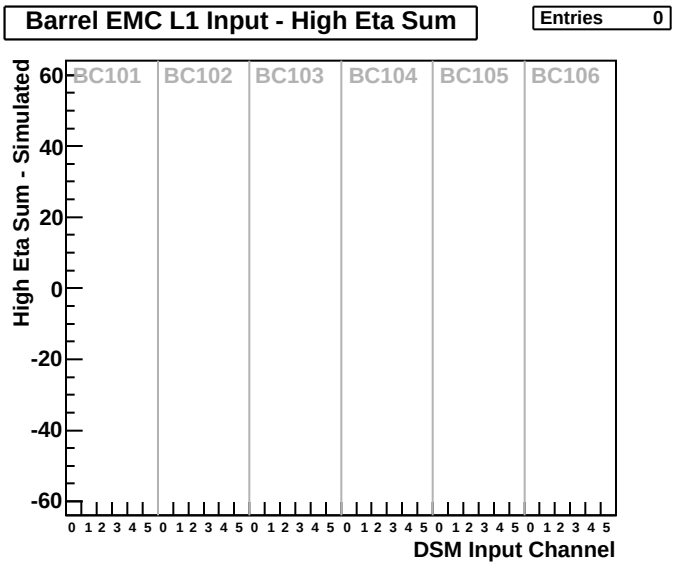
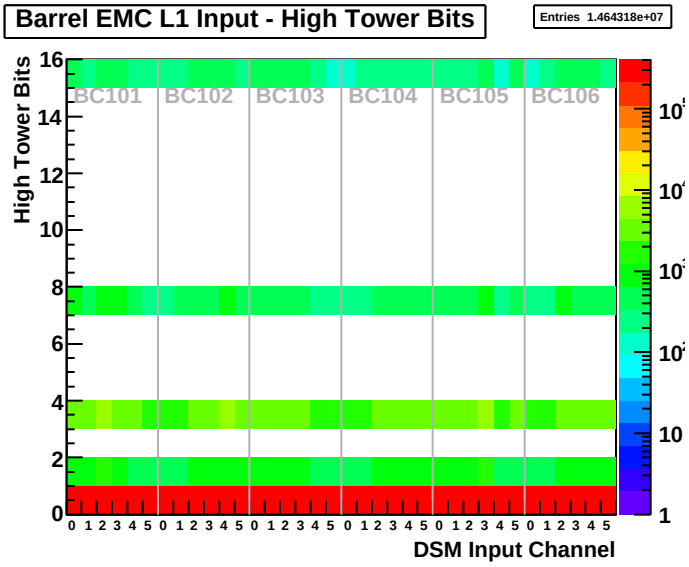
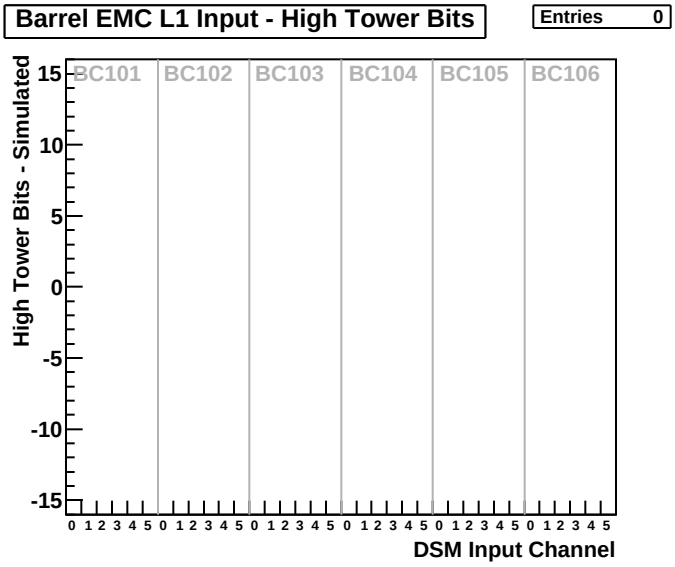

Barrel EMC L1 Input - Low Eta Sum
Entries 1.464318e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

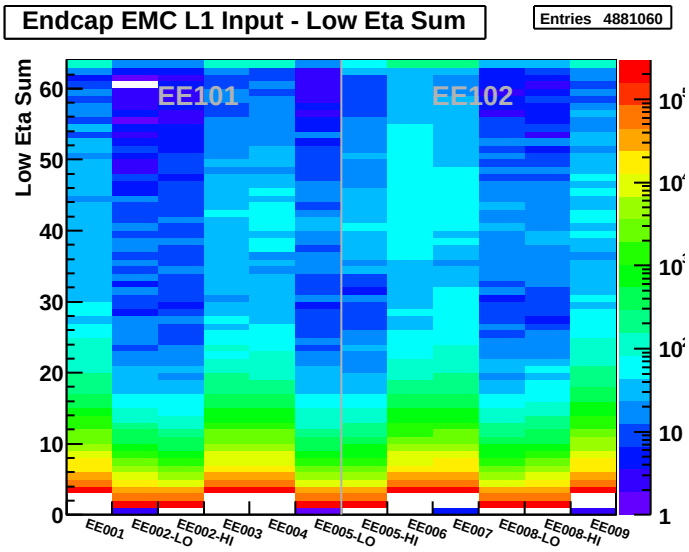

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

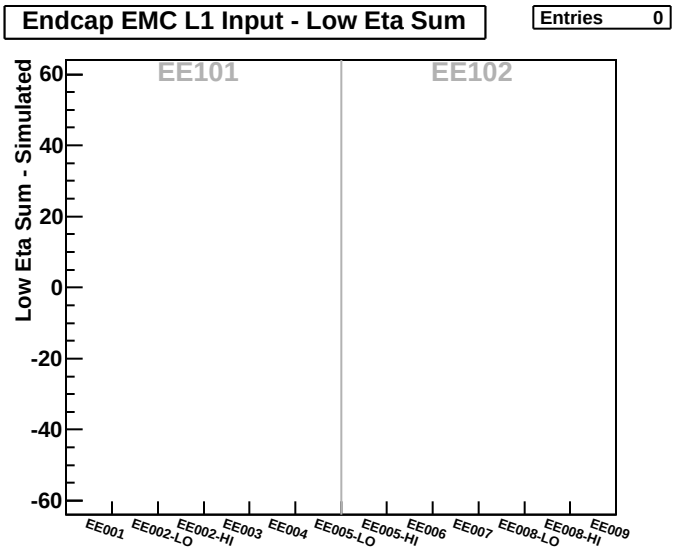

Barrel EMC L1 Input - High Eta Sum
Entries 1.464318e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

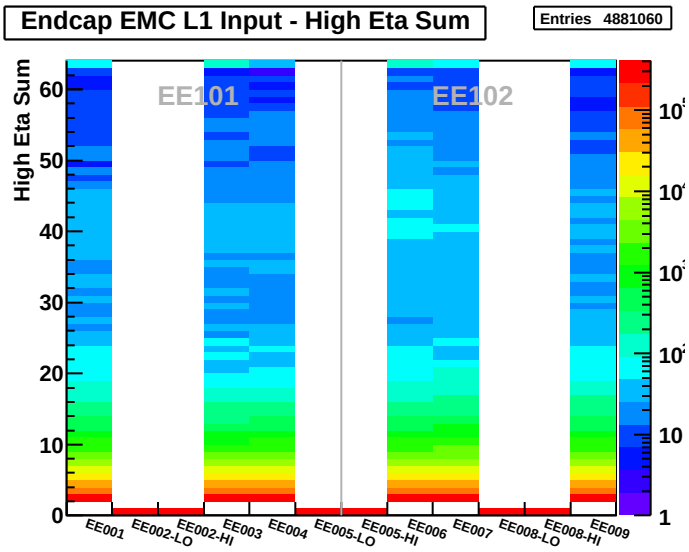

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

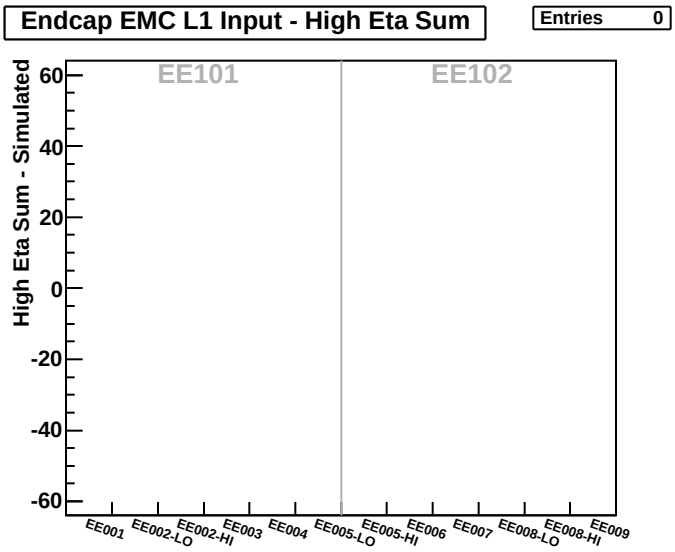

Barrel EMC L1 Input - High Tower Bits
Entries 1.464318e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

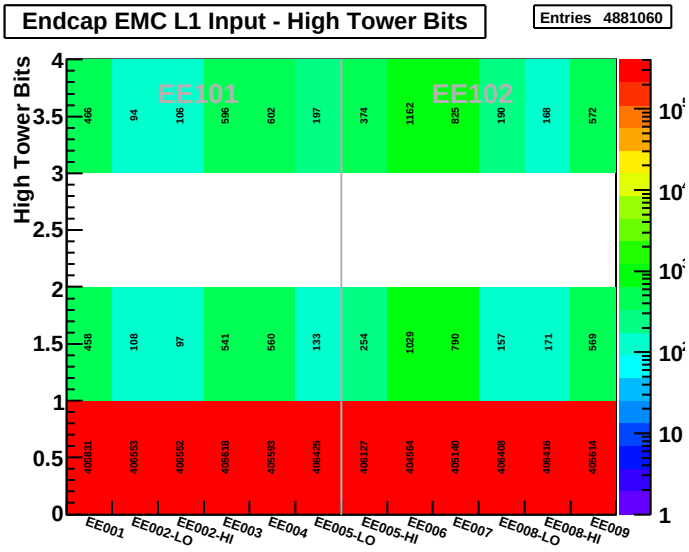

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

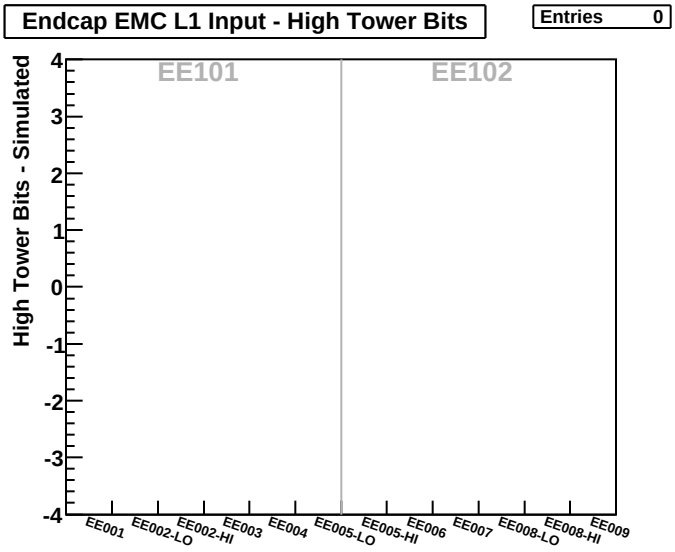

Endcap EMC L1 Input - Low Eta Sum
Entries 4881060
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

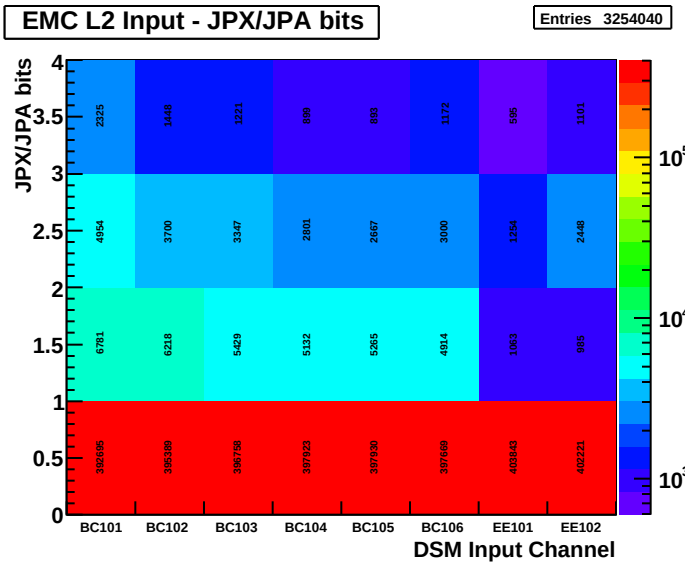

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

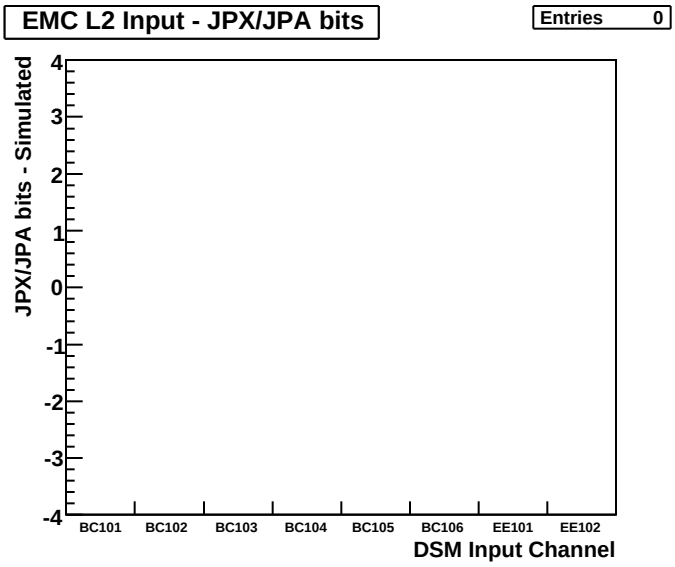

Endcap EMC L1 Input - High Eta Sum
Entries 4881060
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

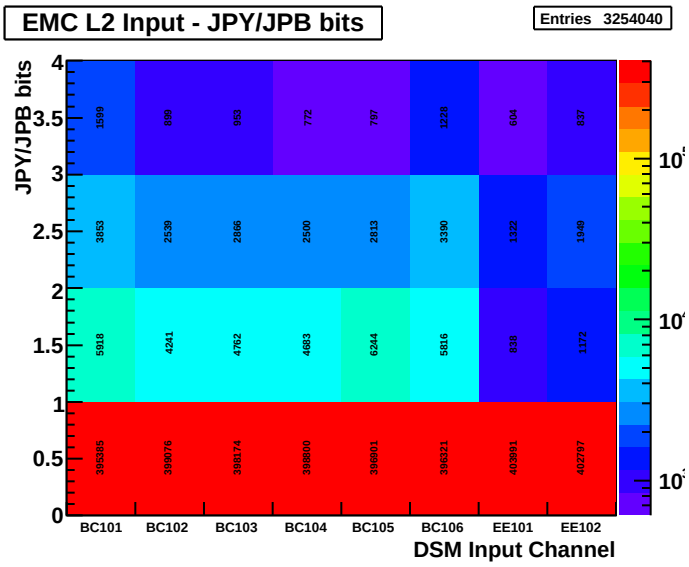

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

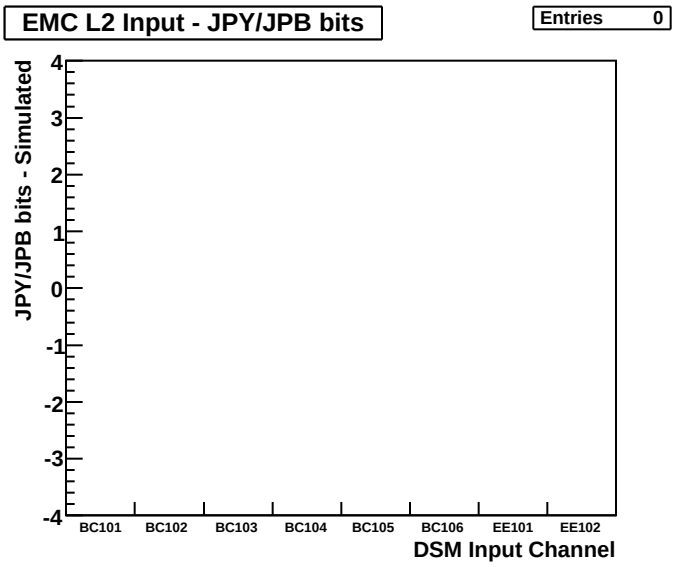

Endcap EMC L1 Input - High Tower Bits
Entries 4881060
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

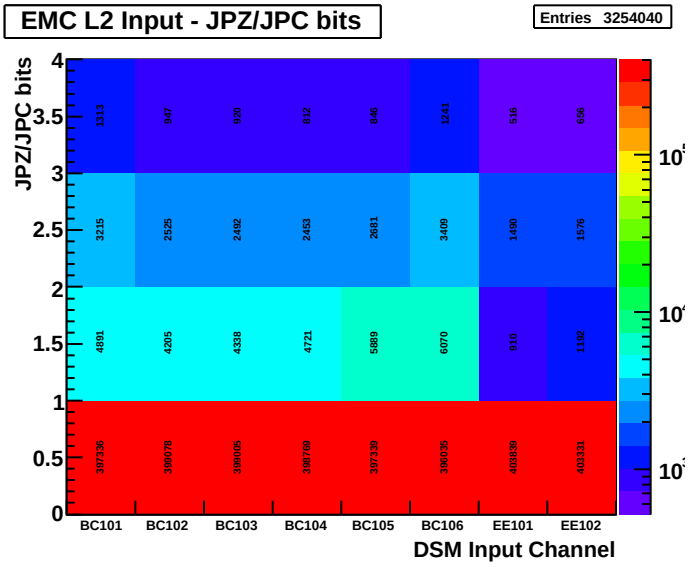

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

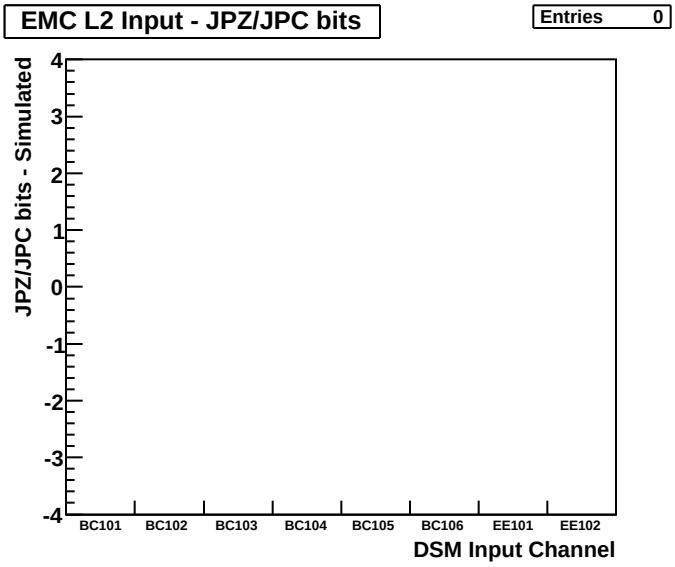

EMC L2 Input - JPX/JPA bits
Entries 3254040
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2325 1448 1221 899 893 1172 595 1101
4954 3700 3347 2801 2667 3000 1254 2448
6781 6218 5429 5132 5265 4914 1063 985
392695 395389 398758 397923 397930 397669 403843 402221

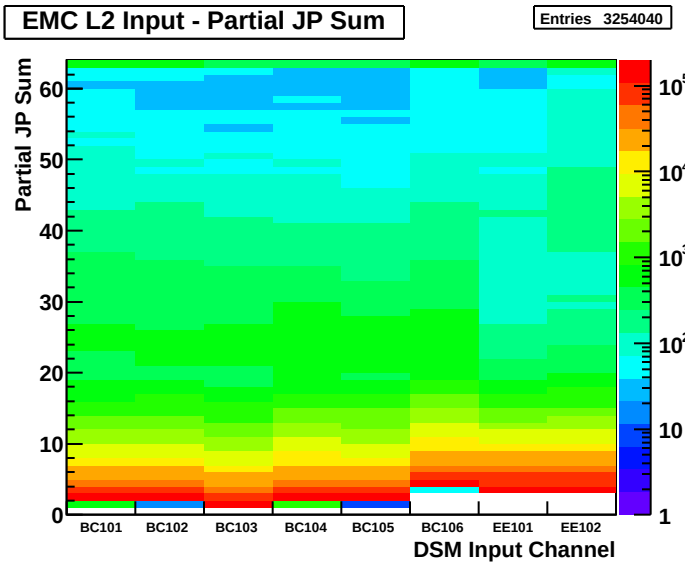

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

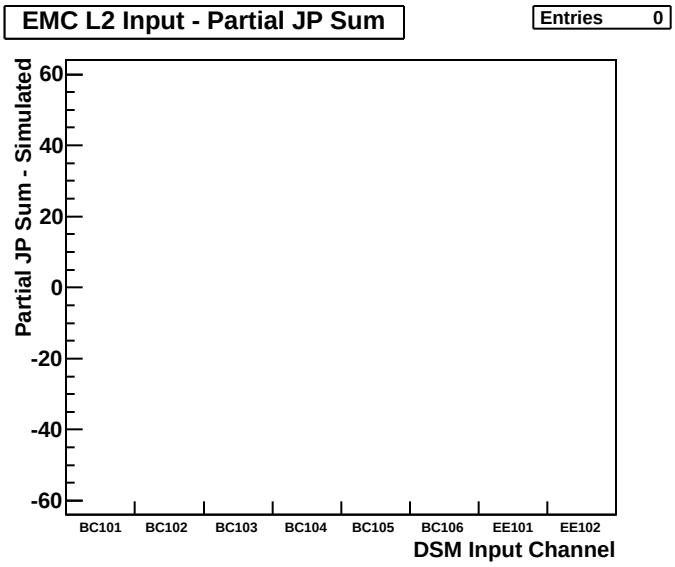

EMC L2 Input - JPY/JPB bits
Entries 3254040
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1599 899 953 772 797 1228 604 837
3853 2539 2866 2500 2813 3390 1322 1949
5918 4241 4762 4683 6244 5816 838 1172
395385 399076 398174 398800 398001 396321 403991 402797

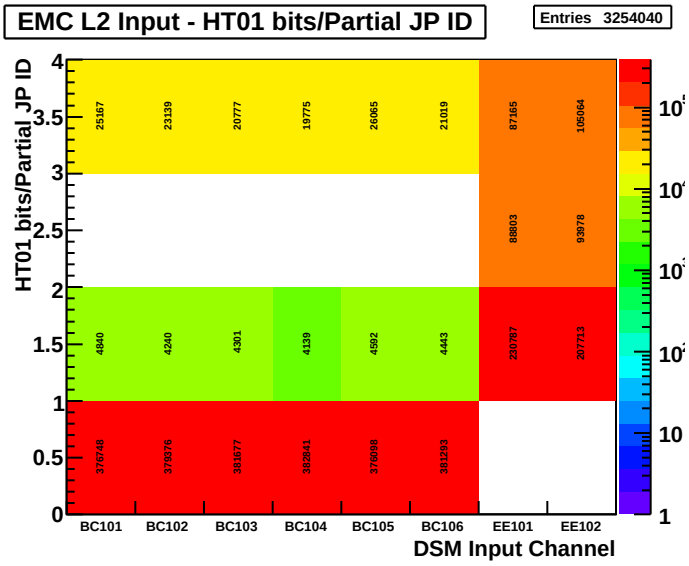

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

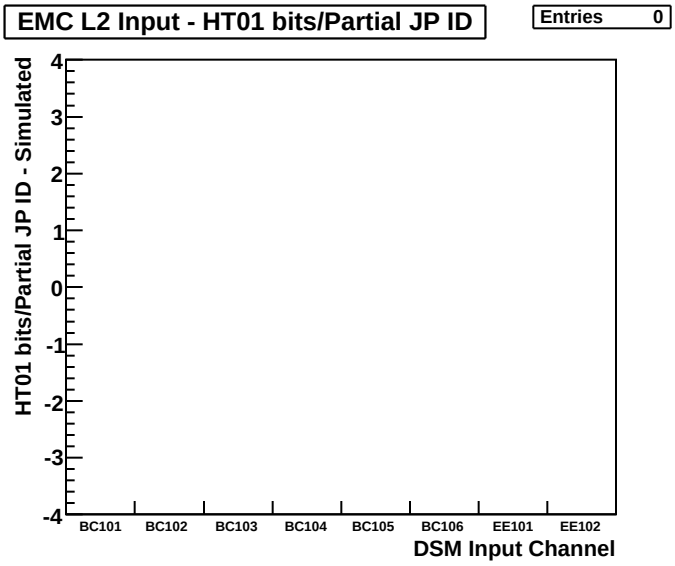

EMC L2 Input - JPZ/JPC bits
Entries 3254040
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1313 947 920 812 846 1241 516 656
3215 2525 2492 2453 2681 3409 1490 1576
4891 4205 4338 4721 5889 6070 910 1192
397336 399078 399005 398769 397339 396035 403839 403331

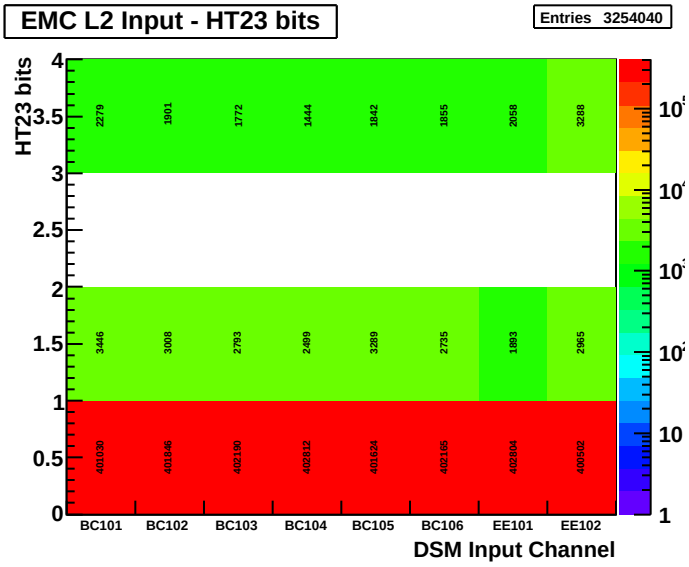

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

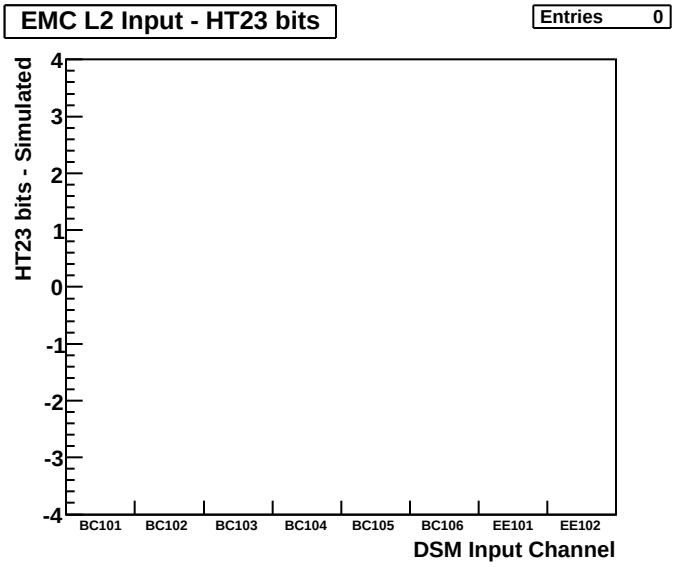

EMC L2 Input - Partial JP Sum
Entries 3254040
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

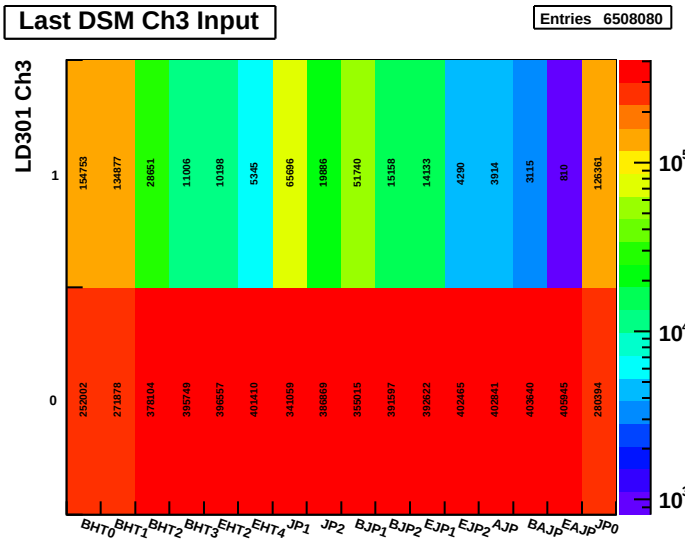

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

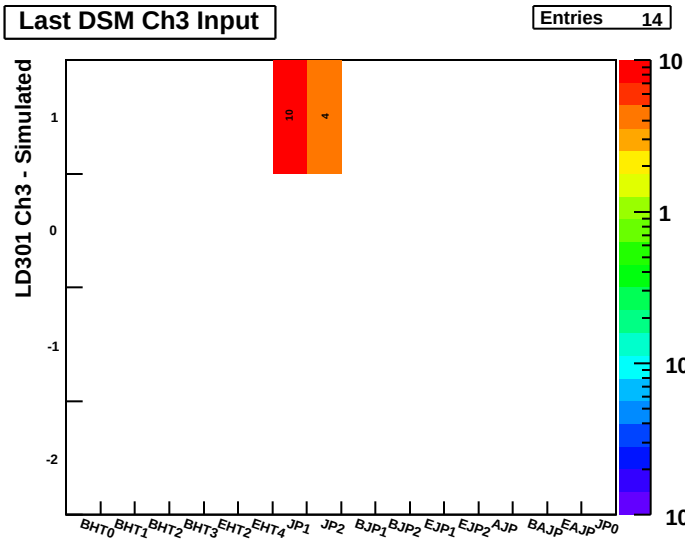

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3254040
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

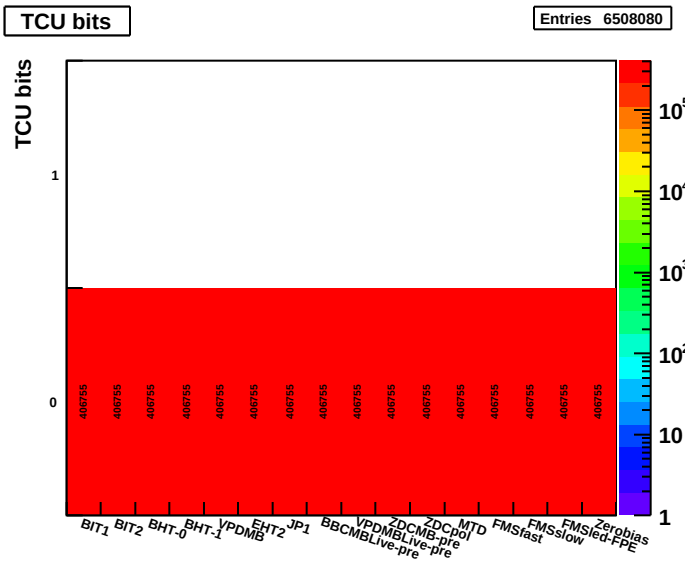

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

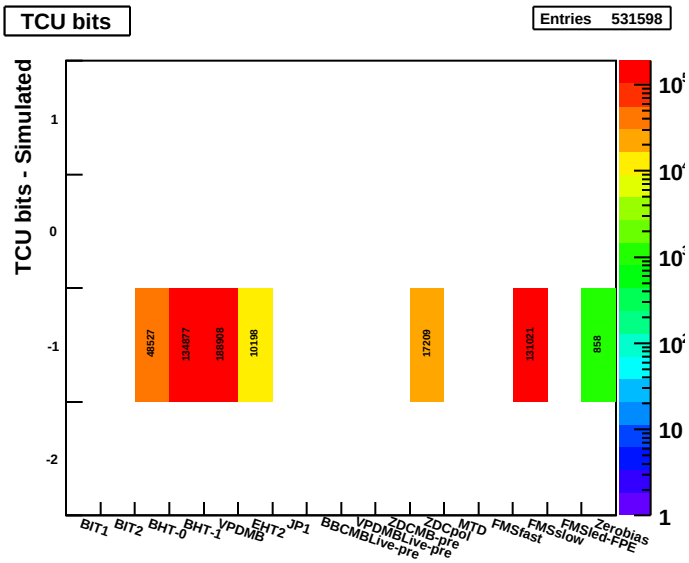

EMC L2 Input - HT23 bits
Entries 3254040
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

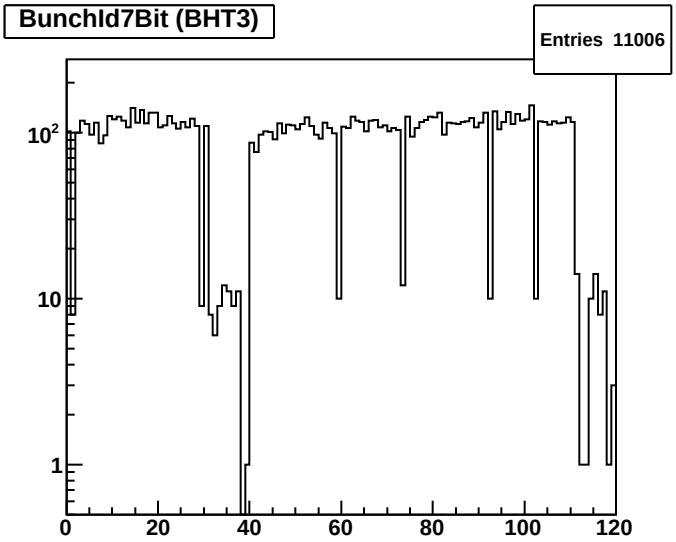

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

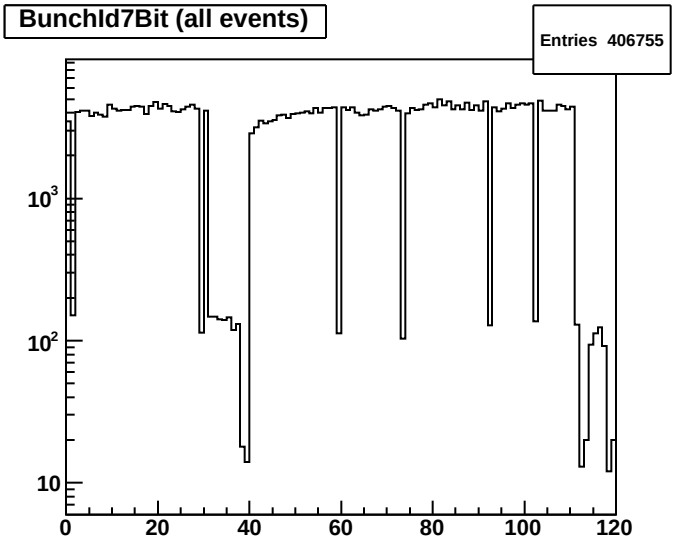

Last DSM Ch3 Input
Entries 6508080
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

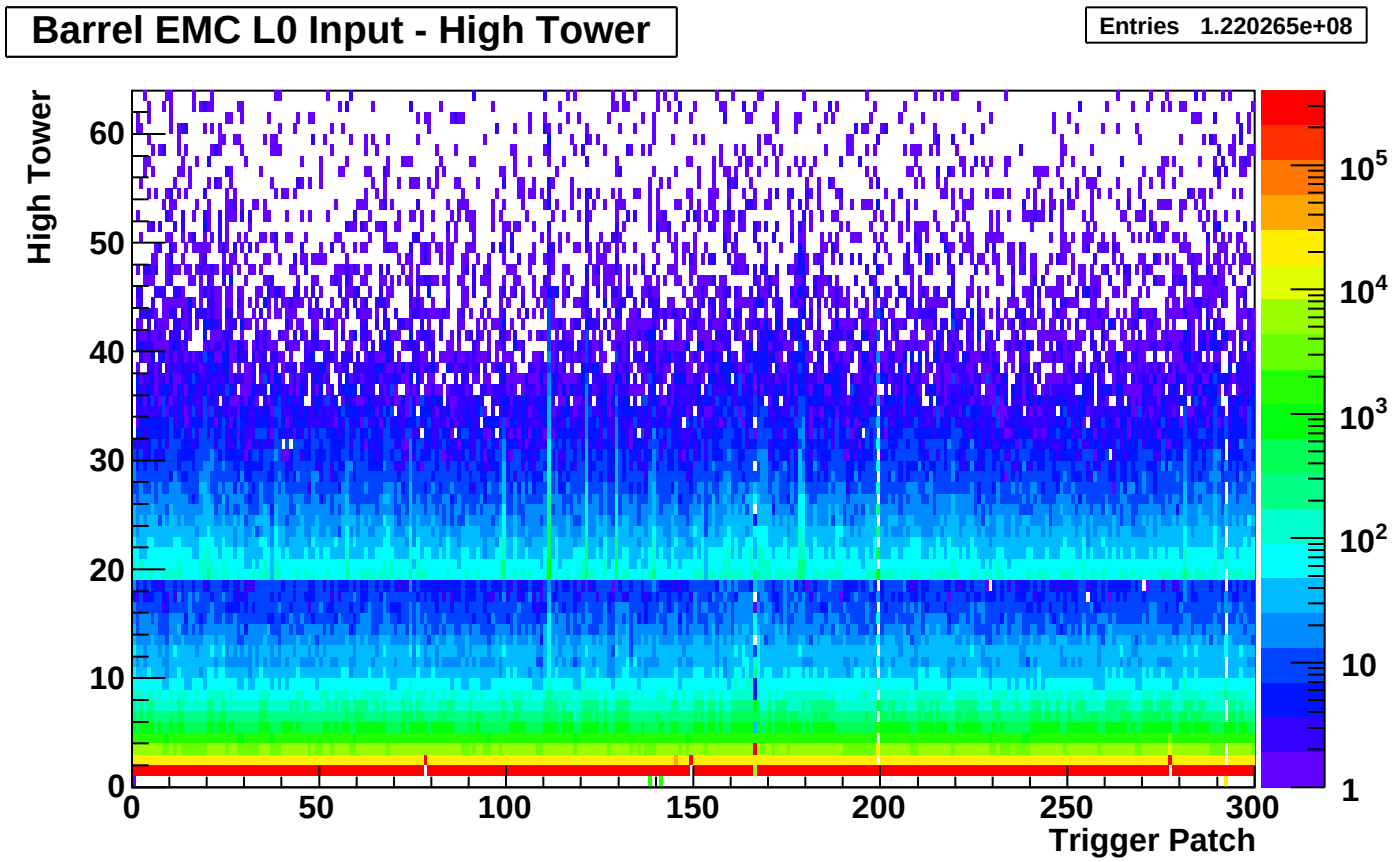

Last DSM Ch3 Input
Entries 14
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

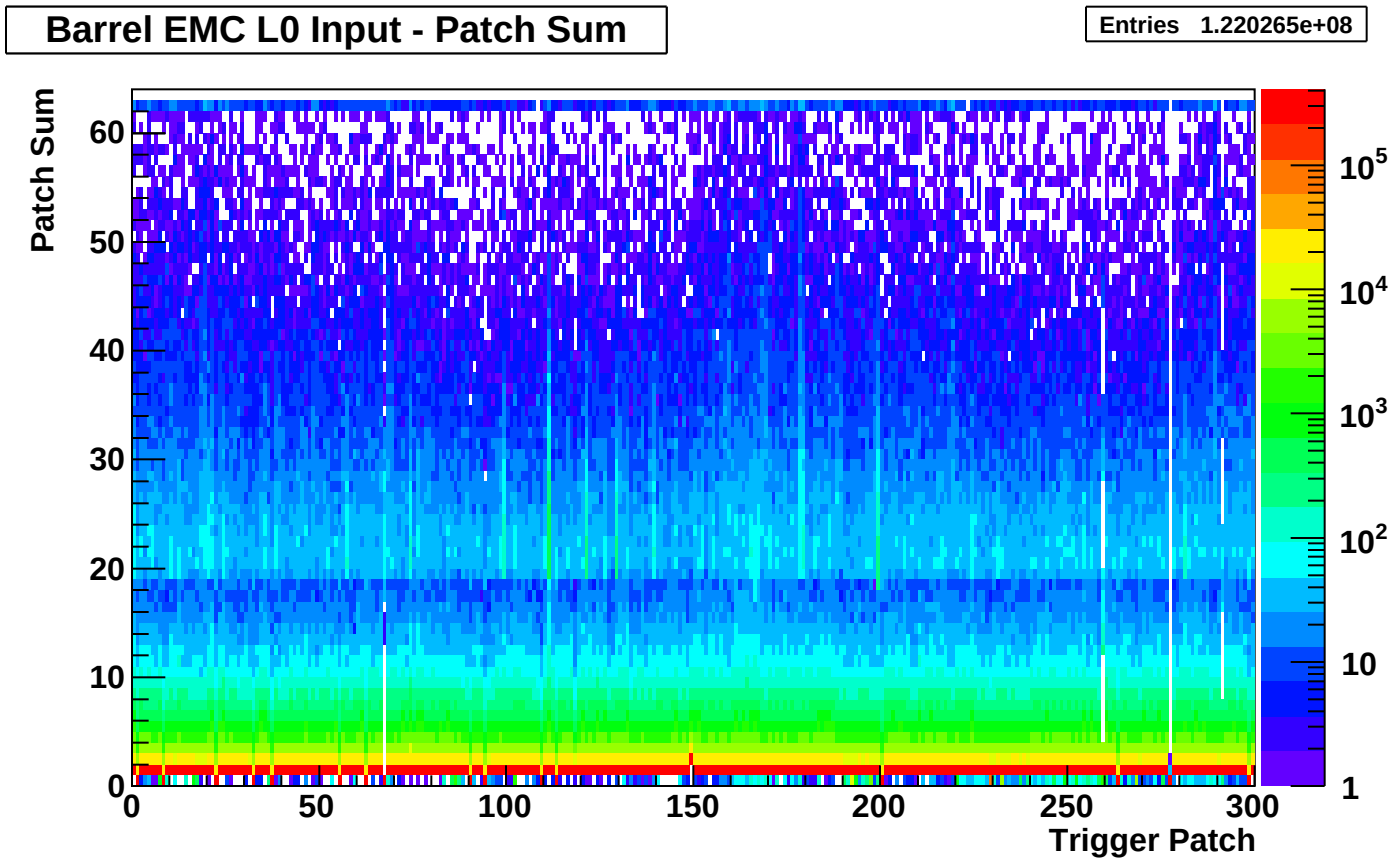

TCU bits
Entries 6508080
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

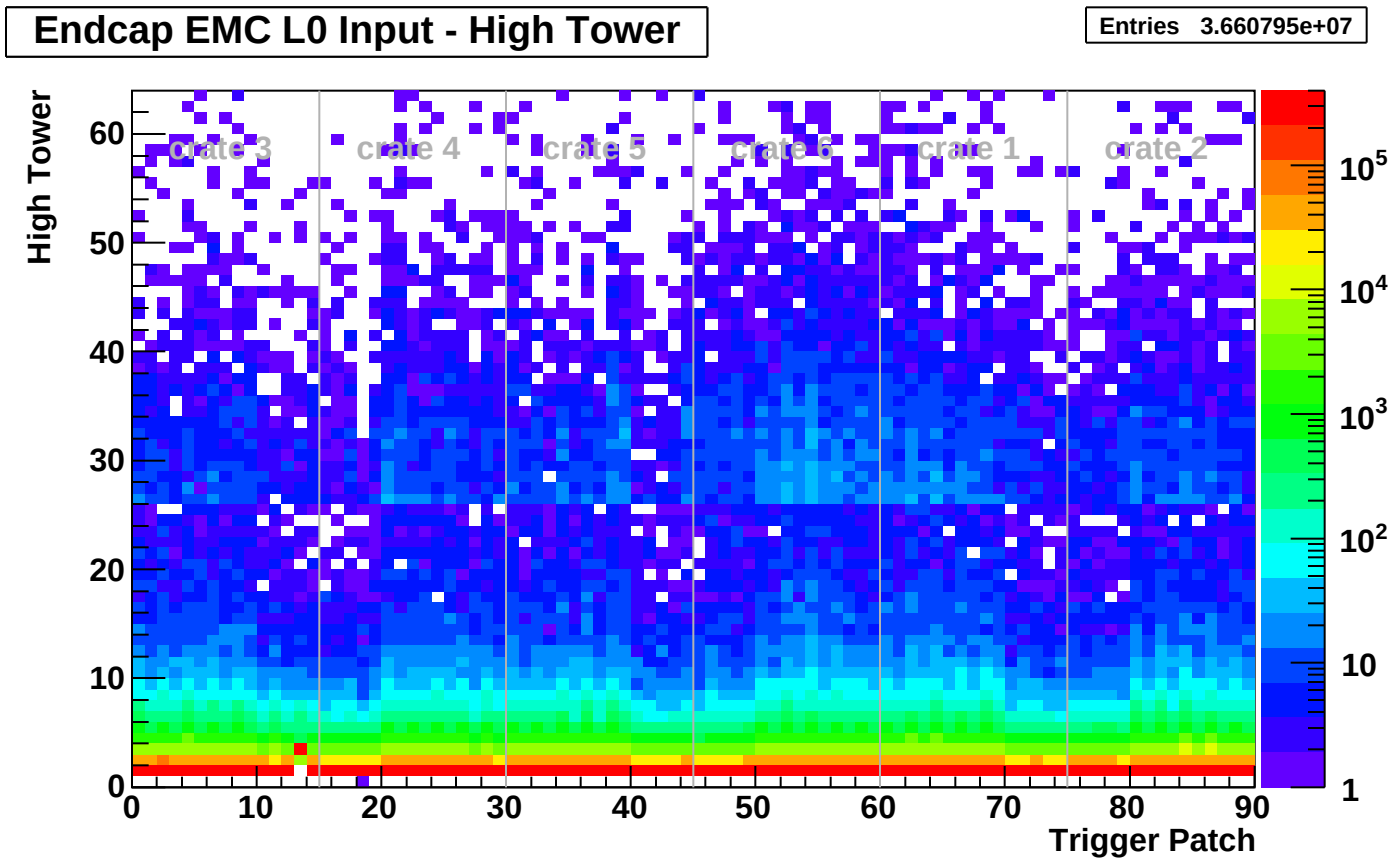

TCU bits
Entries 531598
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

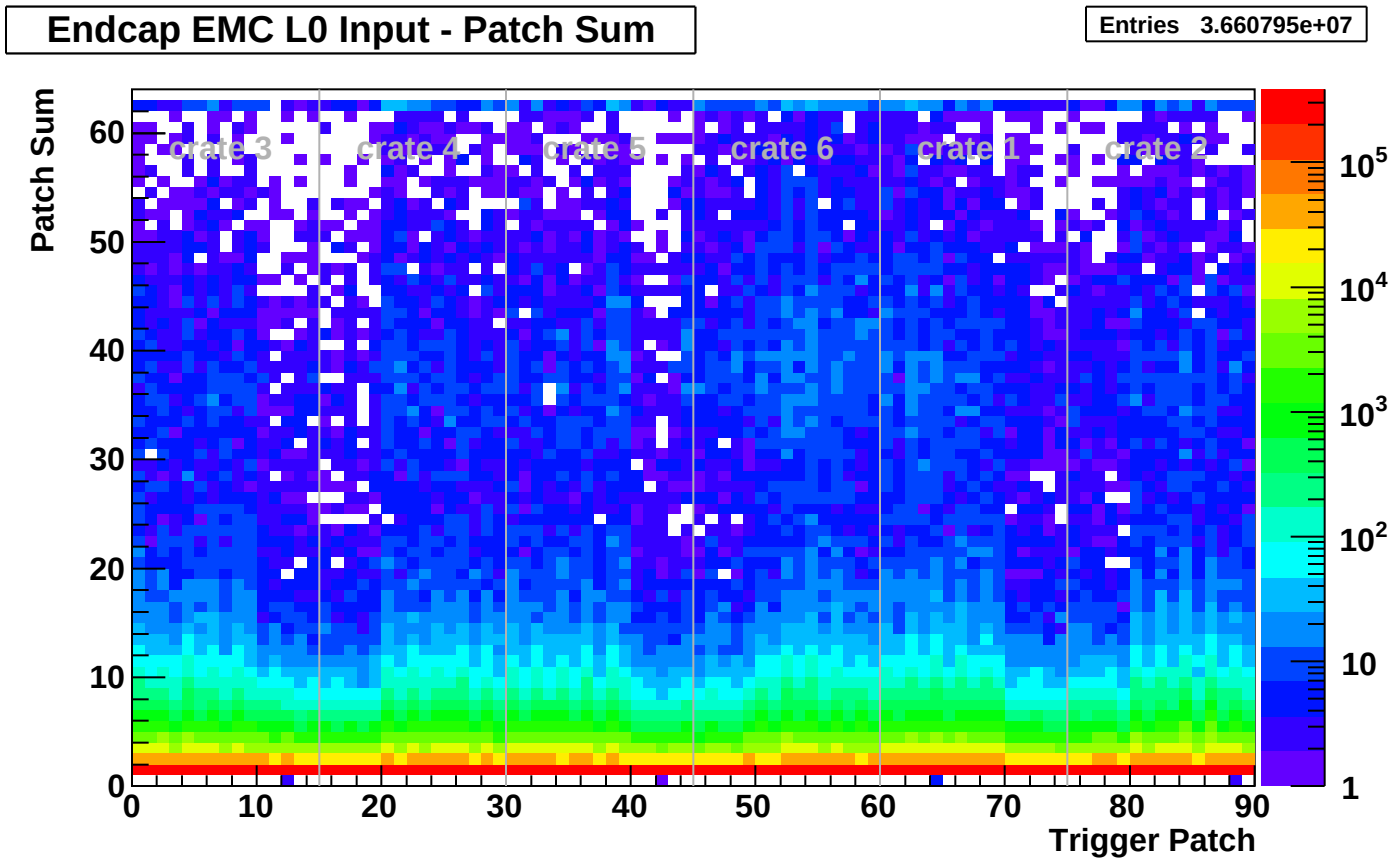

BunchId7Bit (BHT3)
Entries 11006

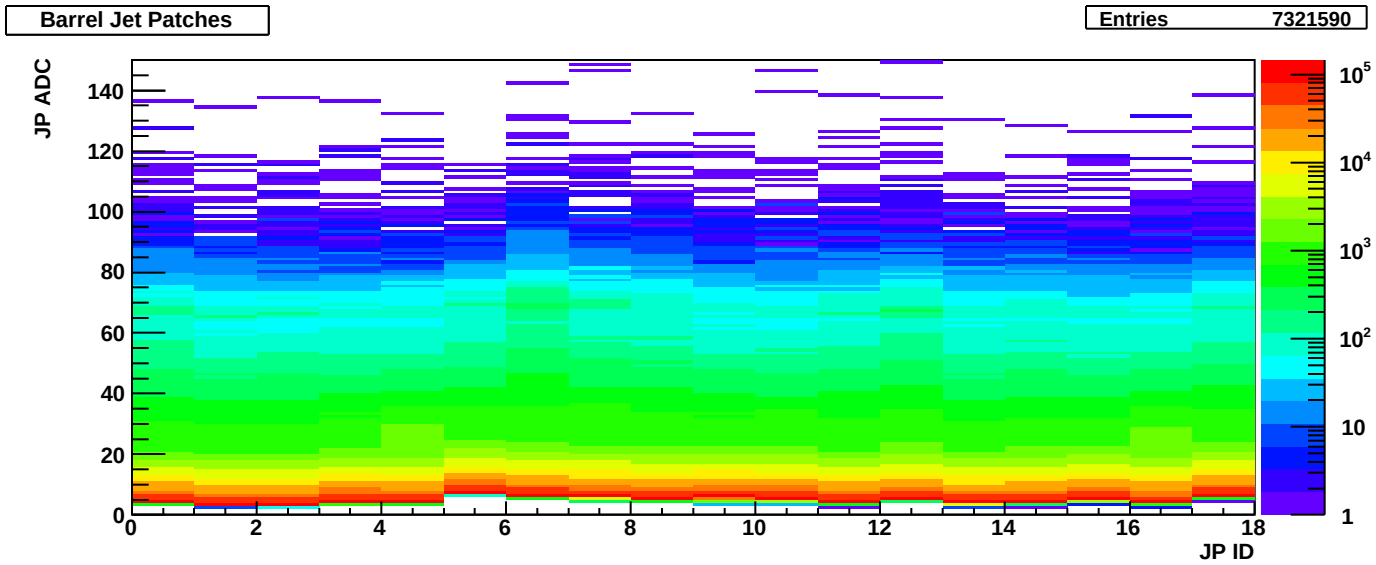

BunchId7Bit (all events)
Entries 406755

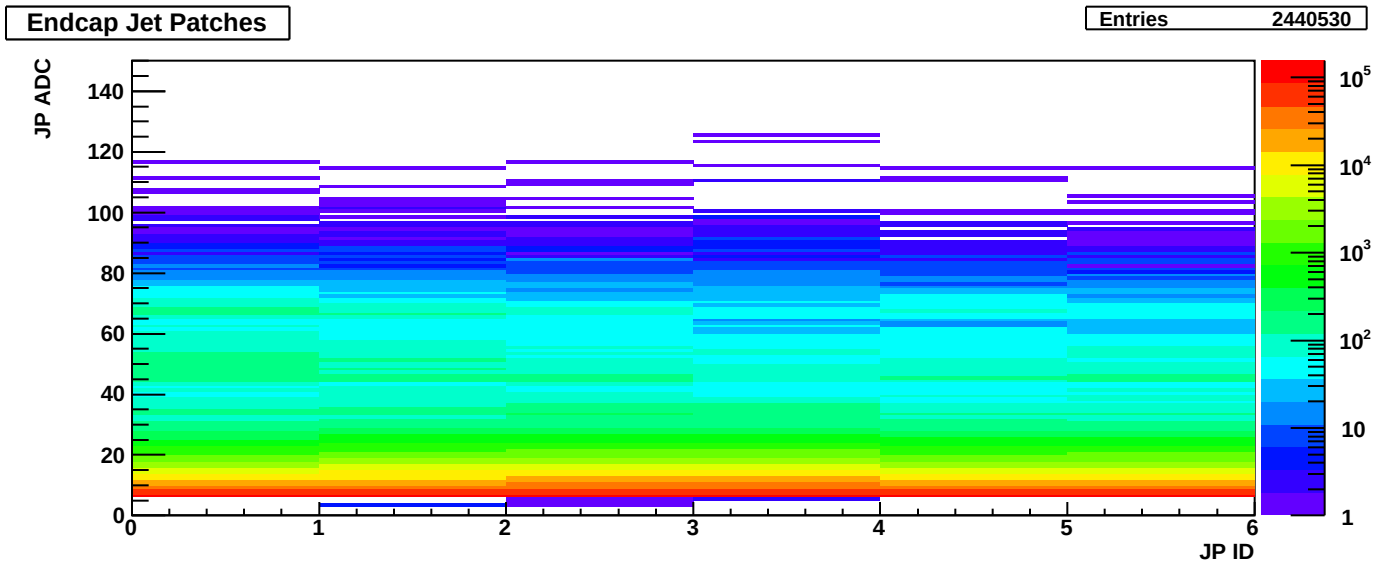

Barrel EMC L0 Input - High Tower
Entries 1.220265e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

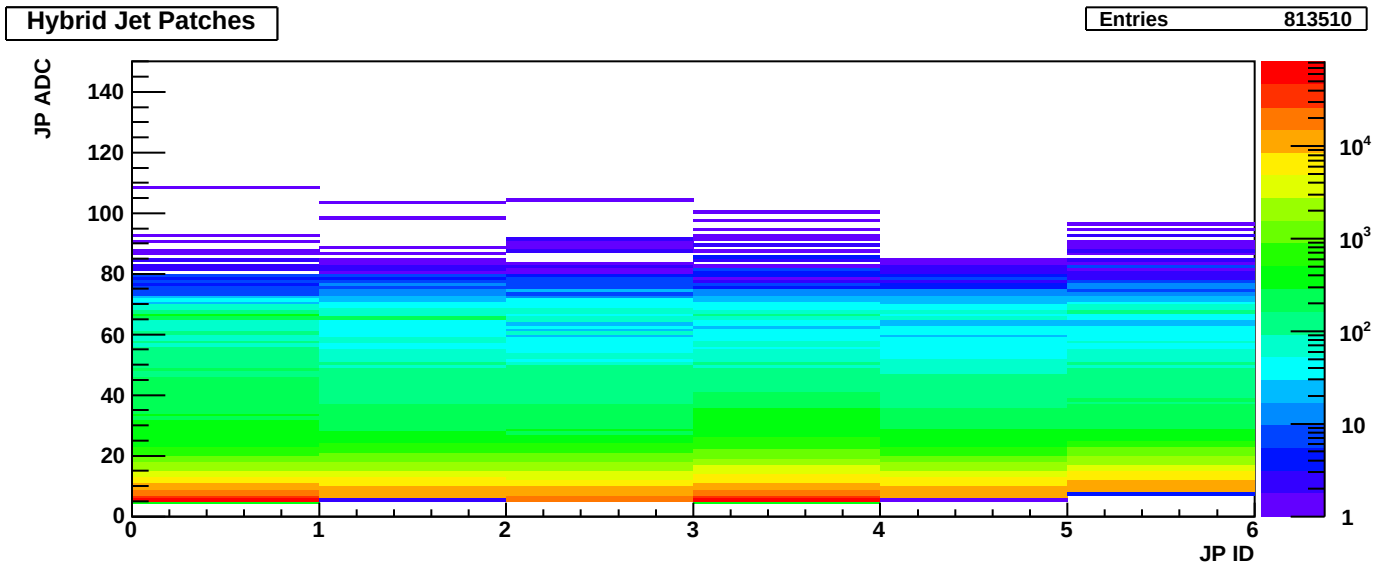

Barrel EMC L0 Input - Patch Sum
Entries 1.220265e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

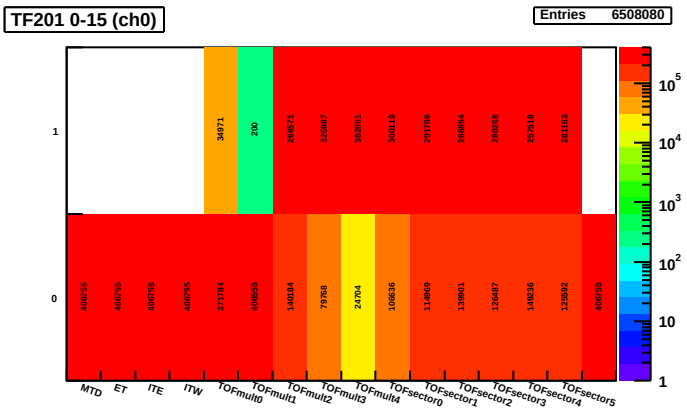

Endcap EMC L0 Input - High Tower
Entries 3.660795e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

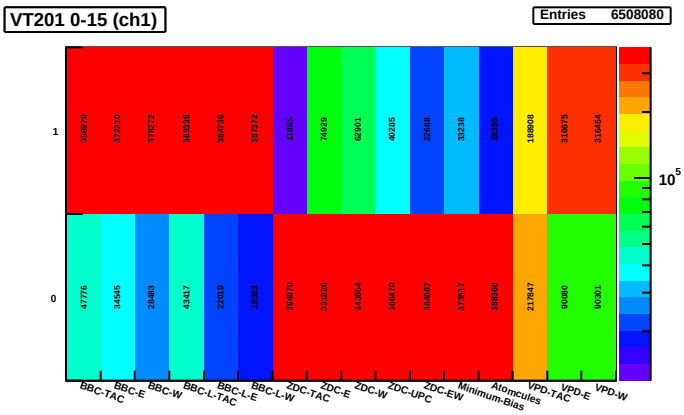

Endcap EMC L0 Input - Patch Sum
Entries 3.660795e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

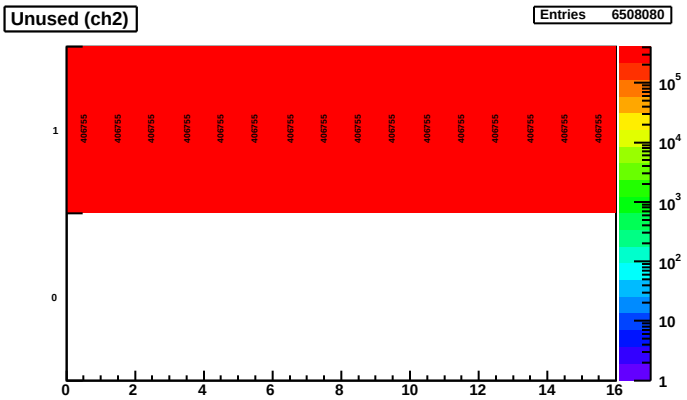

Barrel Jet Patches
Entries 7321590
JP ADC
JP ID

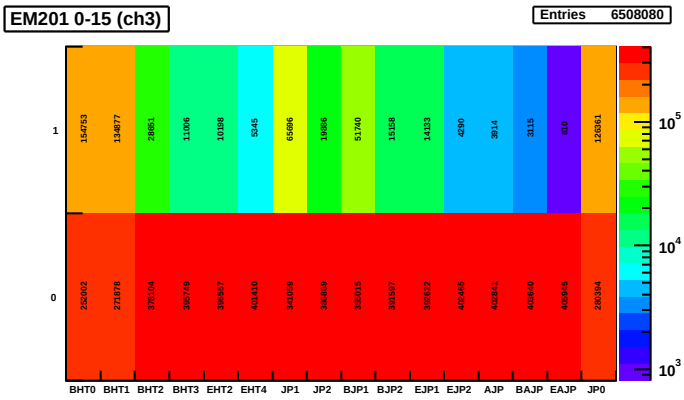

Endcap Jet Patches
Entries 2440530
JP ADC
JP ID

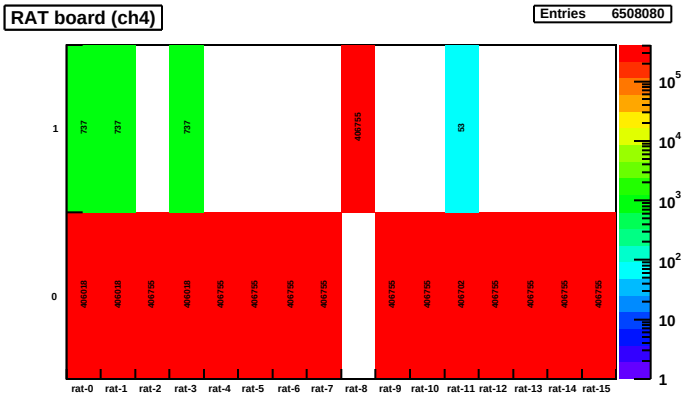

Hybrid Jet Patches
Entries 813510
JP ADC
JP ID

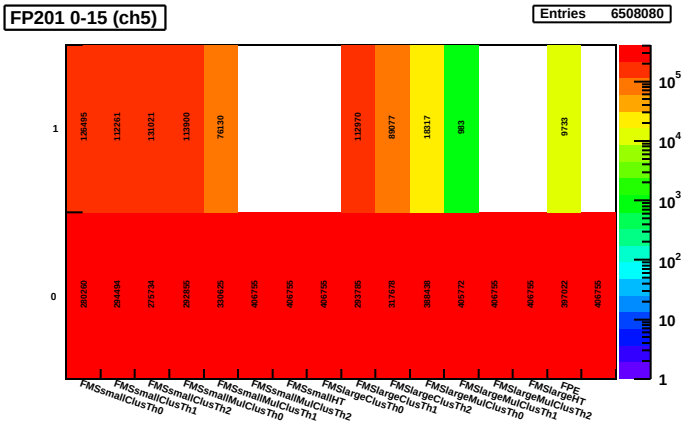

TF201 0-15 (ch0)
Entries 6508080
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

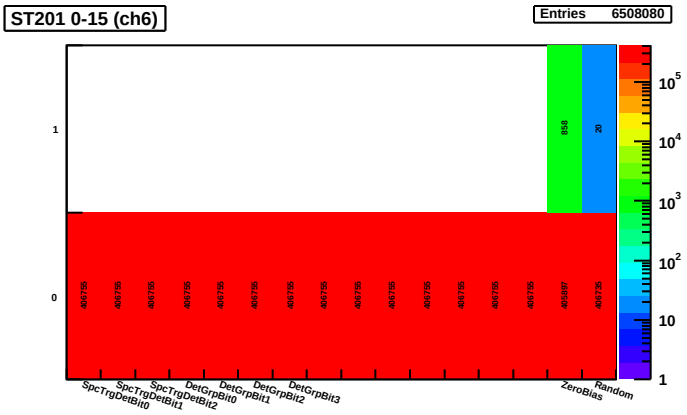

VT201 0-15 (ch1)
Entries 6508080
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

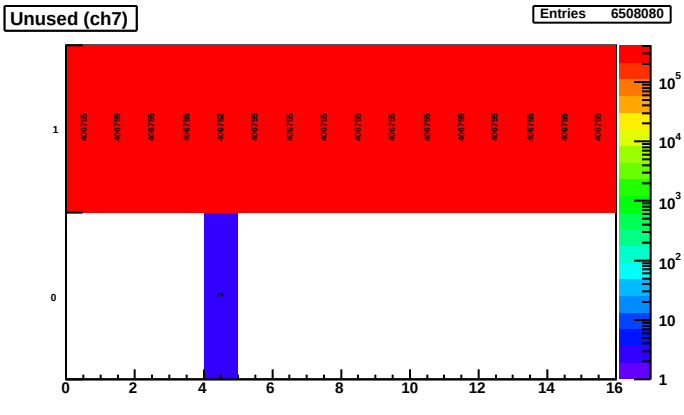

Unused (ch2)
Entries 6508080

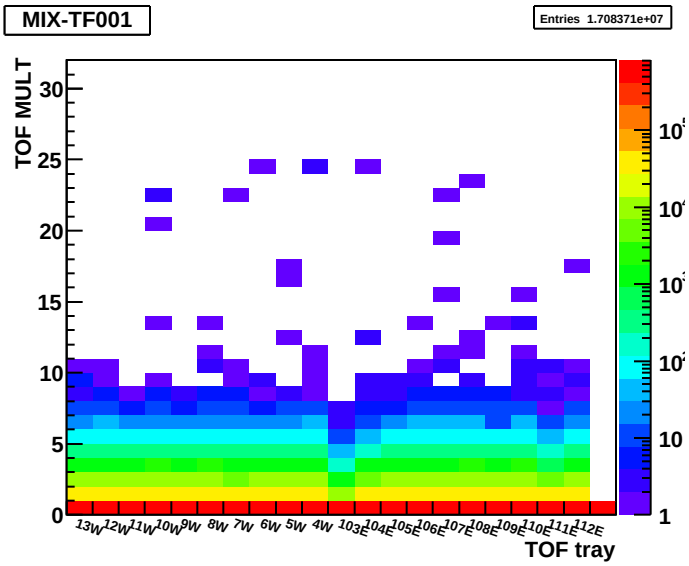

EM201 0-15 (ch3)
Entries 6508080
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

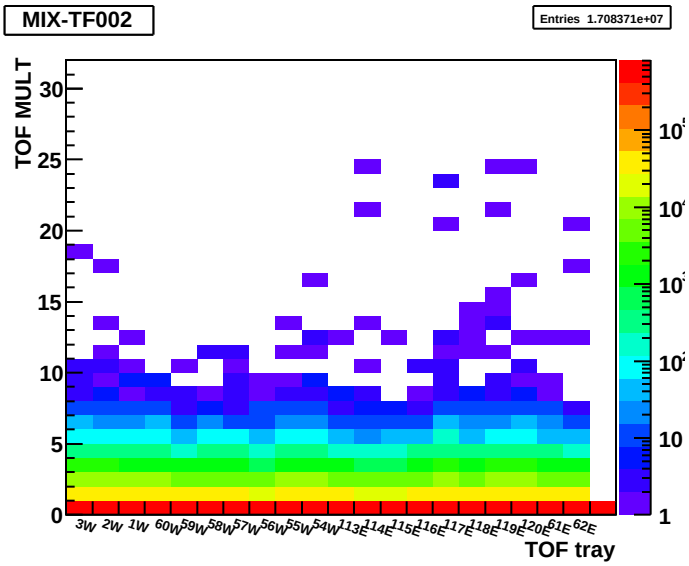

RAT board (ch4)
Entries 6508080
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

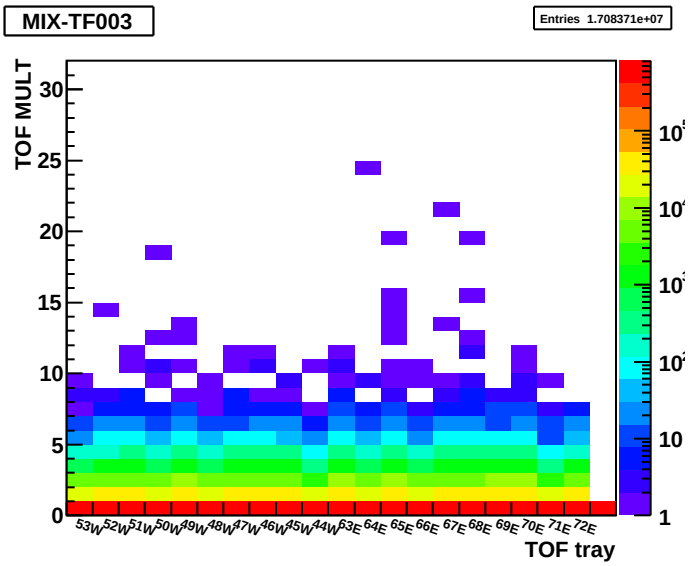

FP201 0-15 (ch5)
Entries 6508080
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

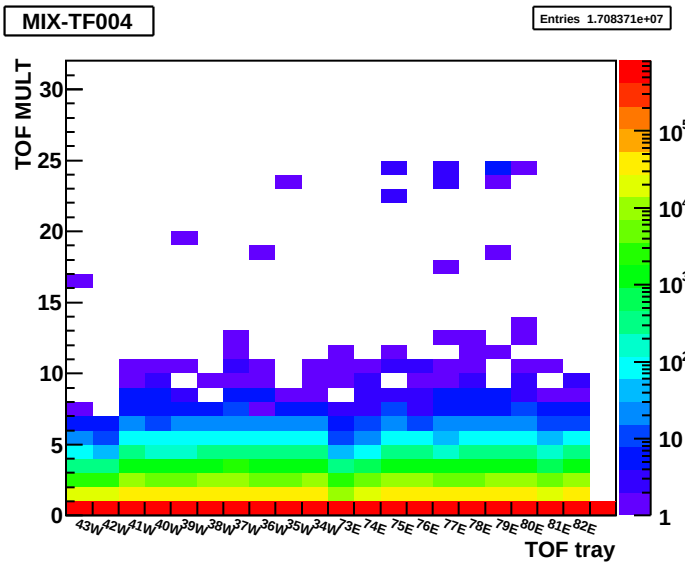

ST201 0-15 (ch6)
Entries 6508080
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

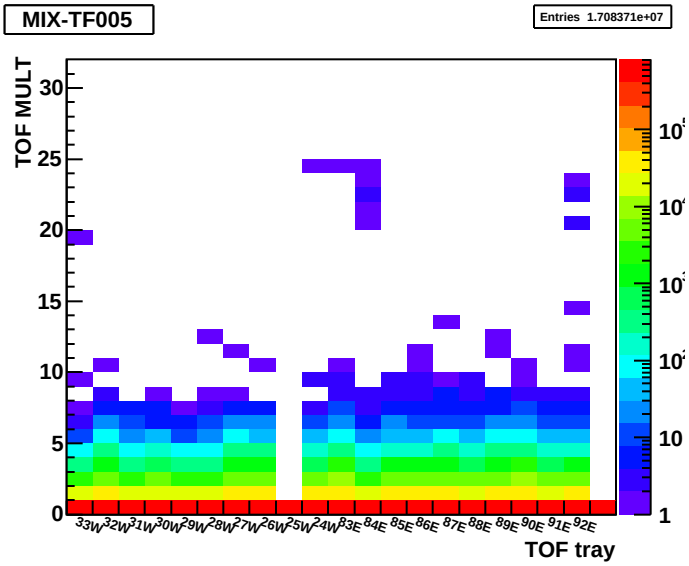

Unused (ch7)
Entries 6508080

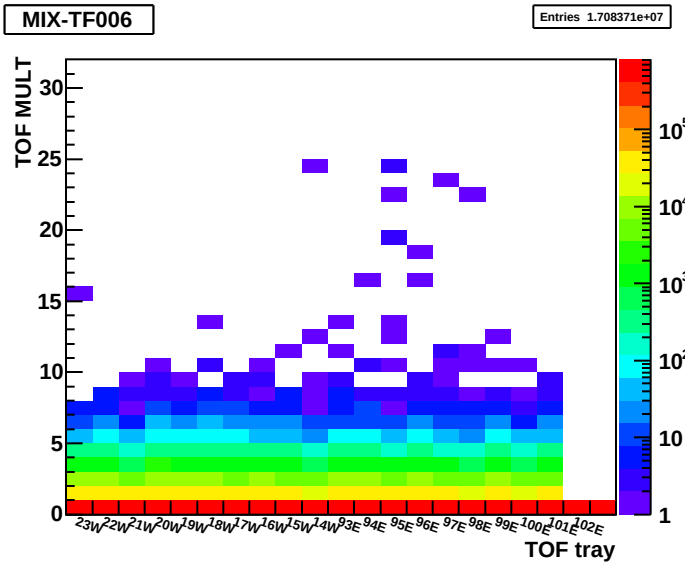

MIX-TF001
Entries 1.708371e+07
TOF MULT
TOF tray

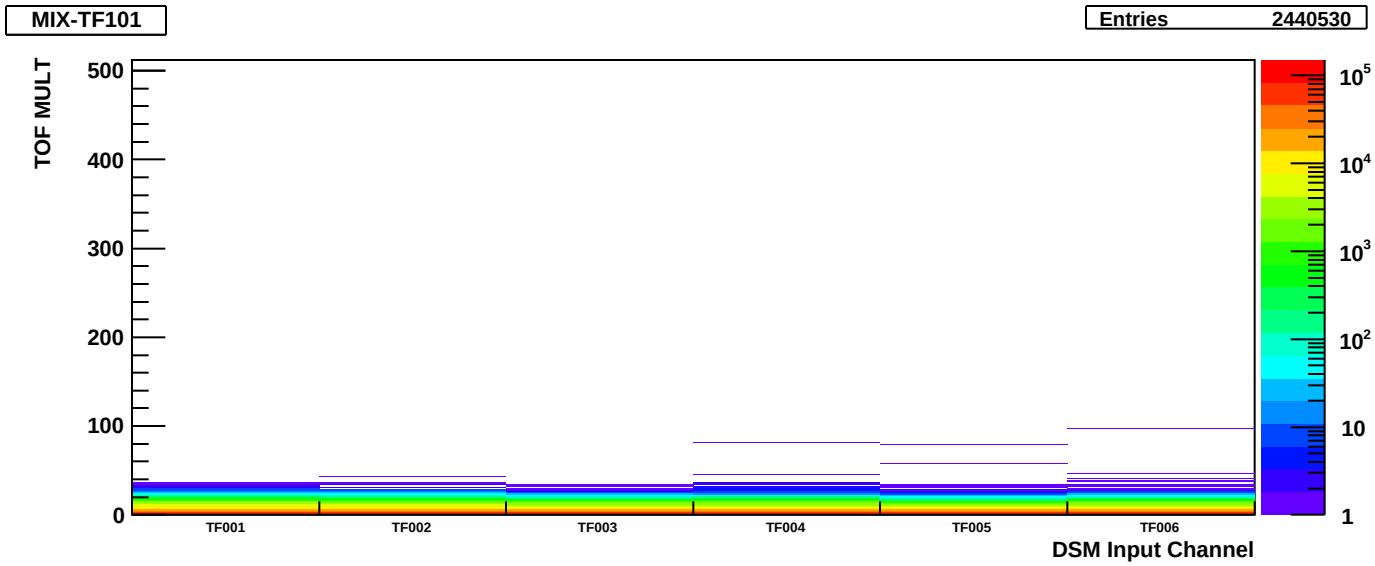

MIX-TF002
Entries 1.708371e+07
TOF MULT
TOF tray

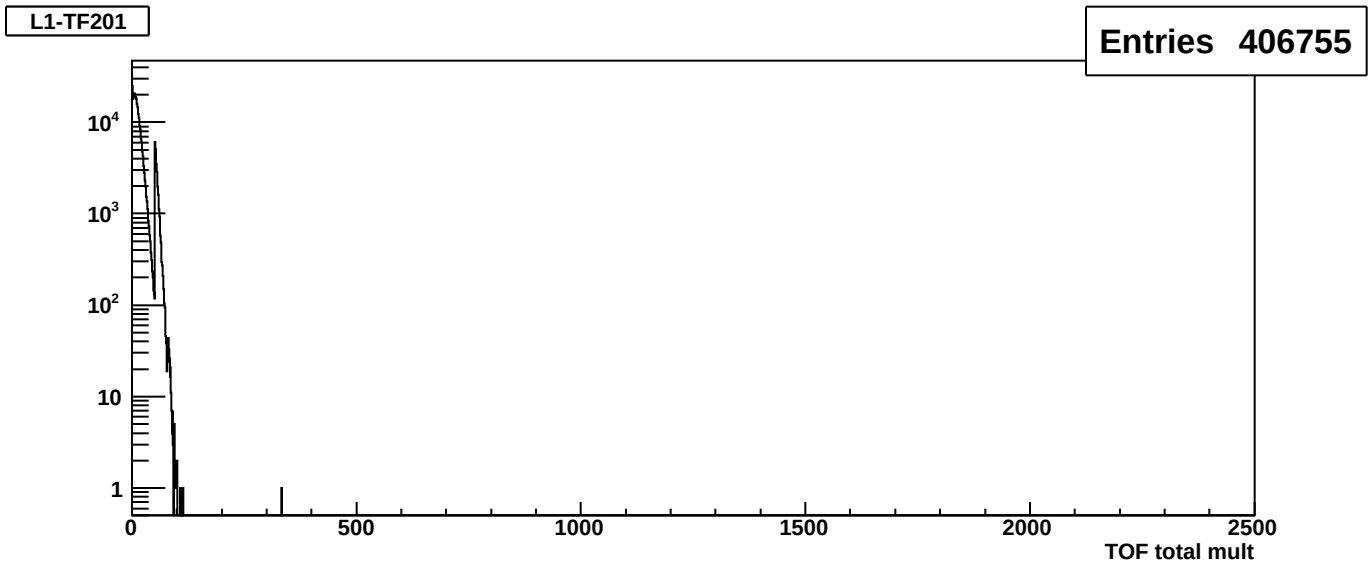

MIX-TF003
Entries 1.708371e+07
TOF MULT
TOF tray

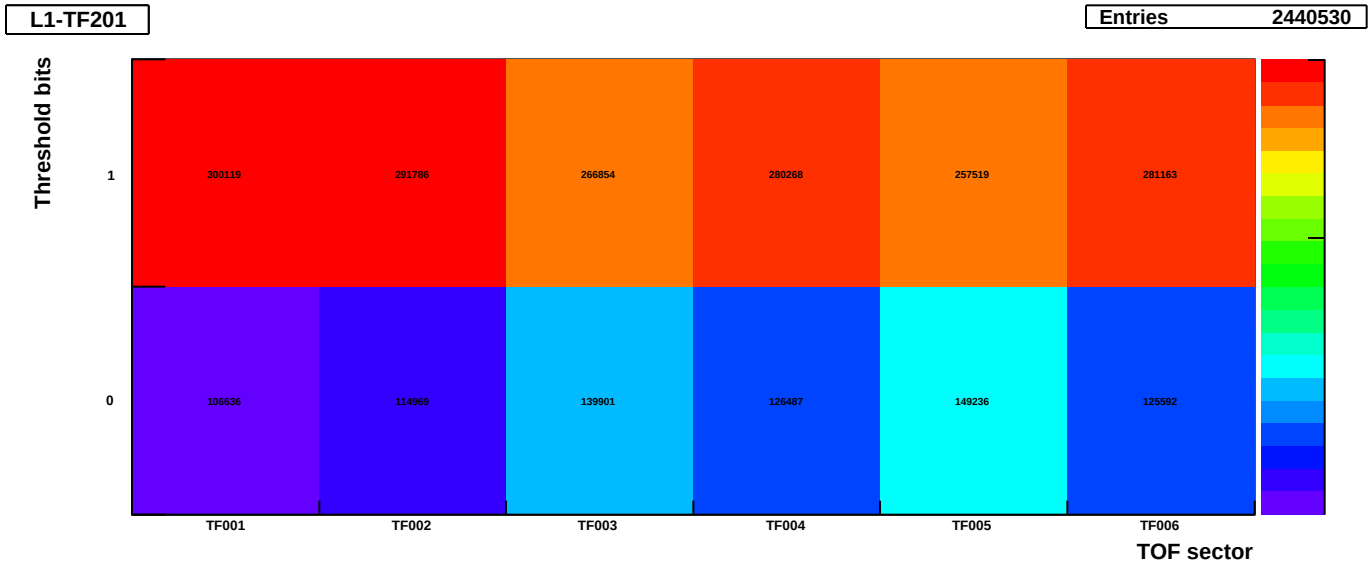

MIX-TF004
Entries 1.708371e+07
TOF MULT
TOF tray

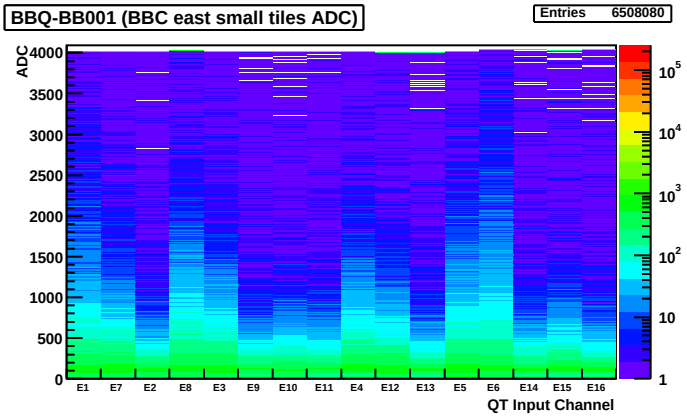

MIX-TF005
Entries 1.708371e+07
TOF MULT
TOF tray

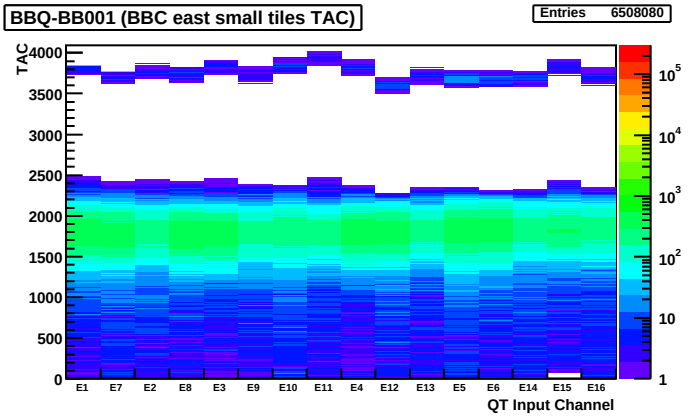

MIX-TF006
Entries 1.708371e+07
TOF MULT
TOF tray

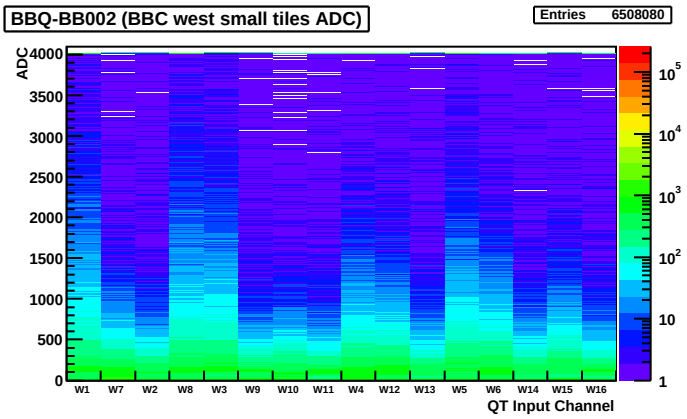

MIX-TF101
Entries 2440530
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

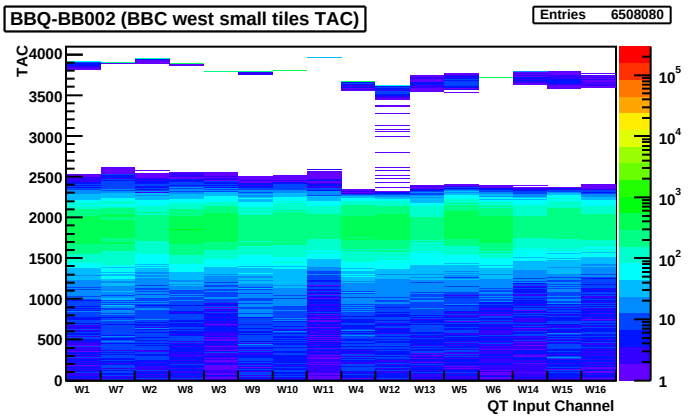

L1-TF201
Entries 406755
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

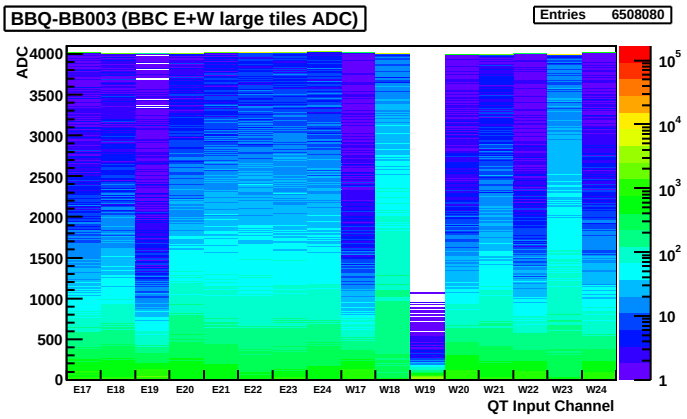

L1-TF201
Entries 2440530
Threshold bits
1
0
300119
291786
266854
280268
257519
281163
106636
114969
139901
126487
149236
125592
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

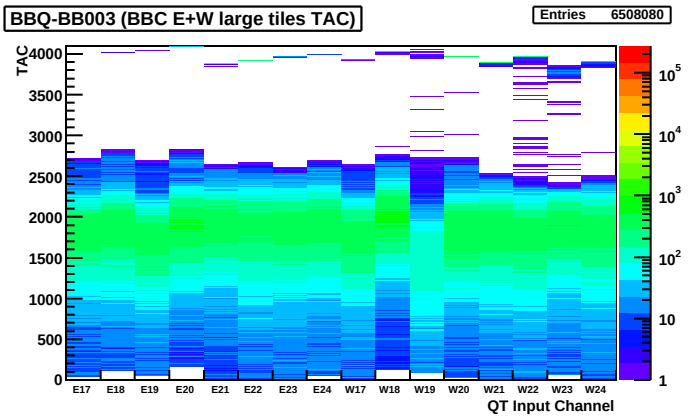

BBQ-BB001 (BBC east small tiles ADC)
Entries 6508080
ADC
QT Input Channel

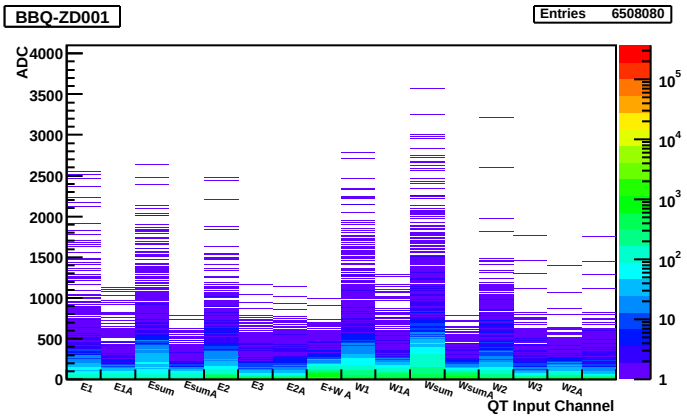

BBQ-BB001 (BBC east small tiles TAC)
Entries 6508080
TAC
QT Input Channel

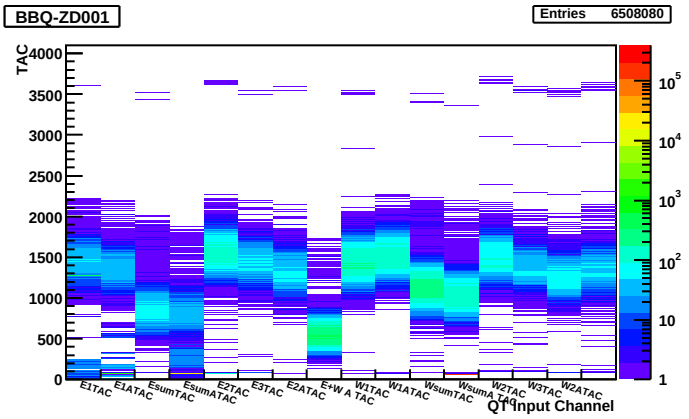

BBQ-BB002 (BBC west small tiles ADC)
Entries 6508080
ADC
QT Input Channel

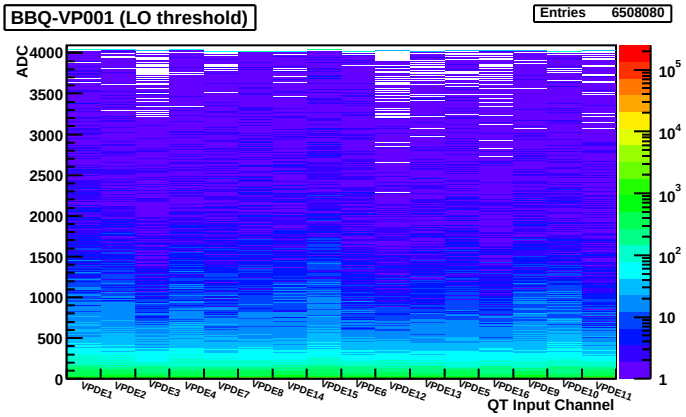

BBQ-BB002 (BBC west small tiles TAC)
Entries 6508080
TAC
QT Input Channel

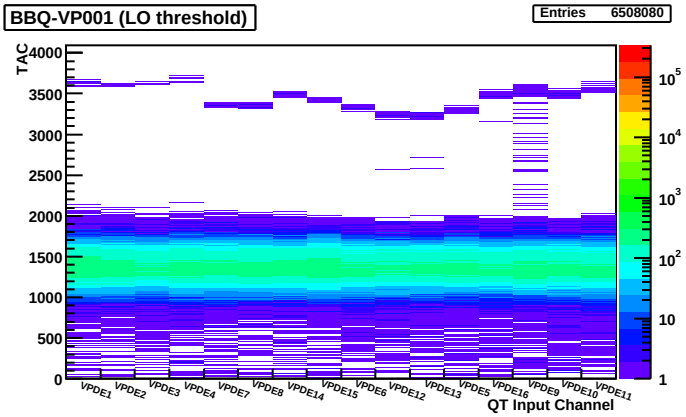

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 6508080
ADC
QT Input Channel

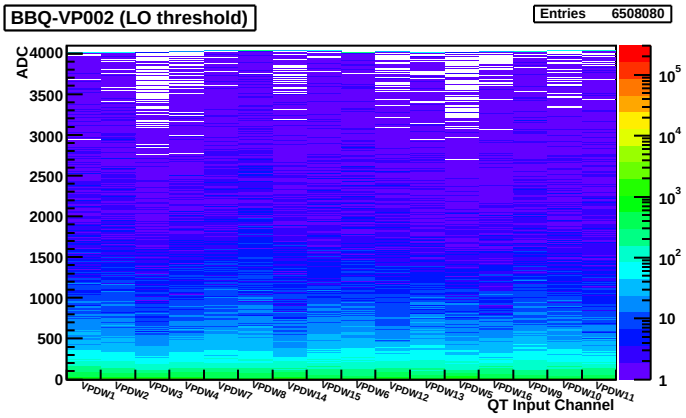

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 6508080
TAC
QT Input Channel

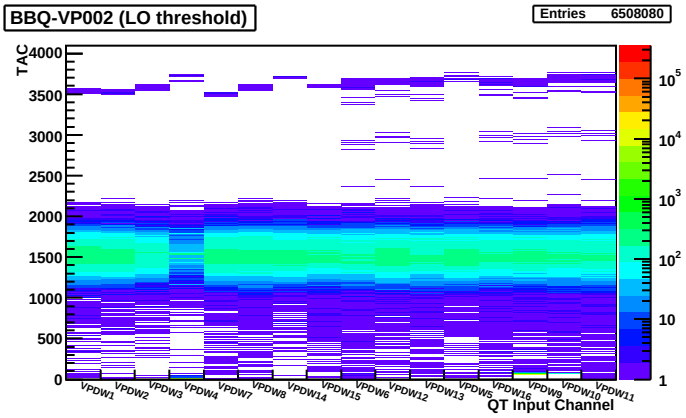

BBQ-ZD001
Entries 6508080
ADC
QT Input Channel

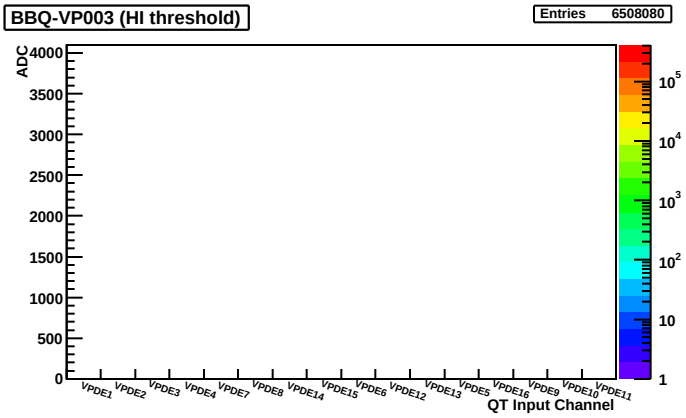

BBQ-ZD001
Entries 6508080
TAC
QT Input Channel

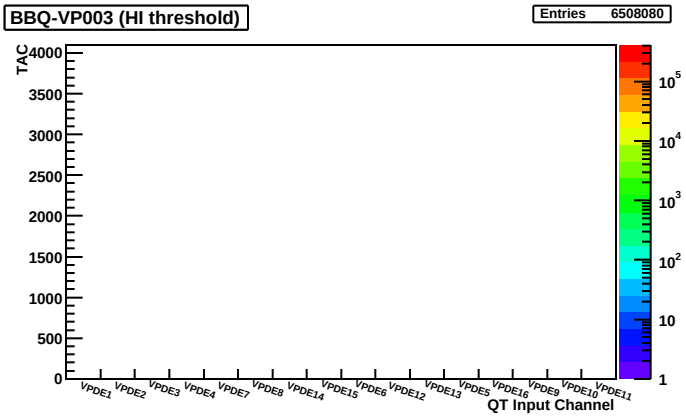

BBQ-VP001 (LO threshold)
Entries 6508080
ADC
QT Input Channel

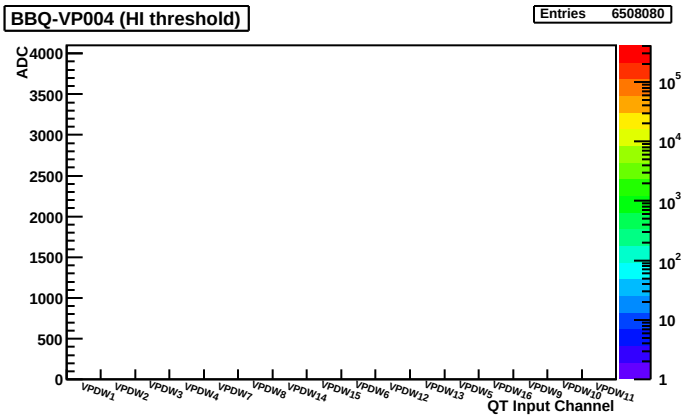

BBQ-VP001 (LO threshold)
Entries 6508080
TAC
QT Input Channel

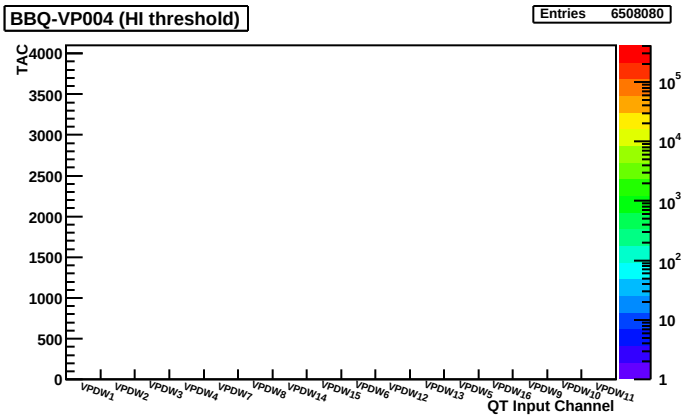

BBQ-VP002 (LO threshold)
Entries 6508080
ADC
QT Input Channel

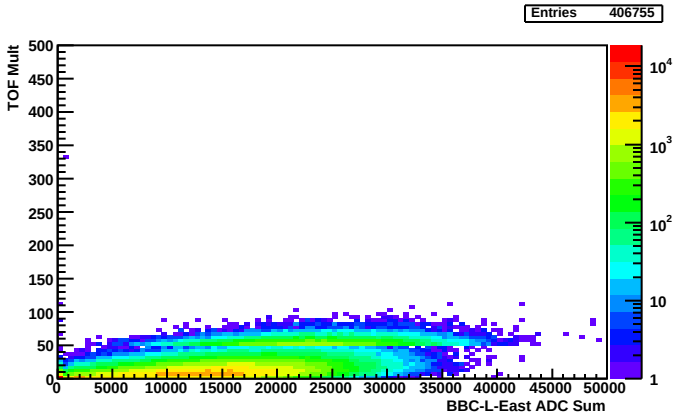

BBQ-VP002 (LO threshold)
Entries 6508080
TAC
QT Input Channel

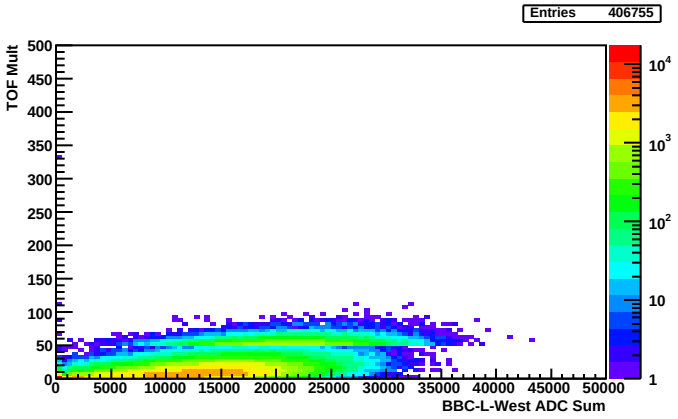

BBQ-VP003 (HI threshold)
Entries 6508080
ADC
QT Input Channel

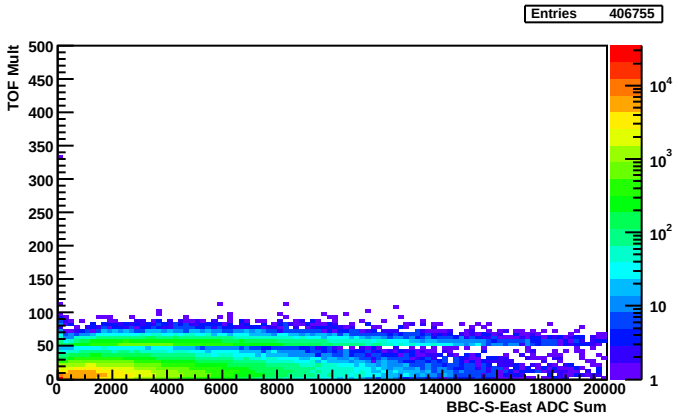

BBQ-VP003 (HI threshold)
Entries 6508080
TAC
QT Input Channel

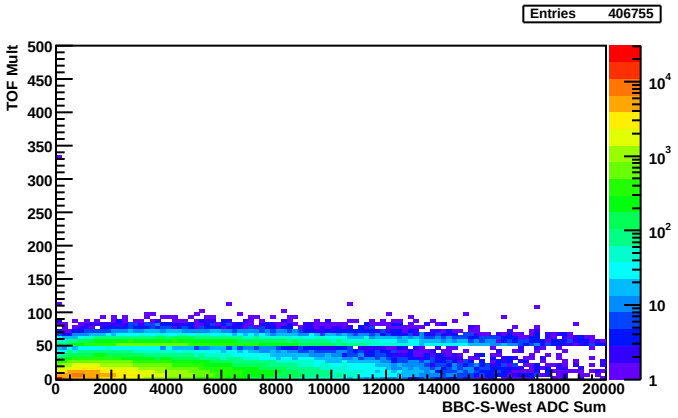

BBQ-VP004 (HI threshold)
Entries 6508080
ADC
QT Input Channel

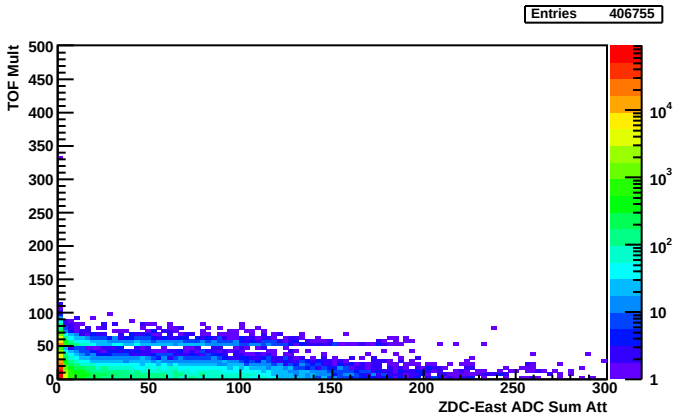

BBQ-VP004 (HI threshold)
Entries 6508080
TAC
QT Input Channel

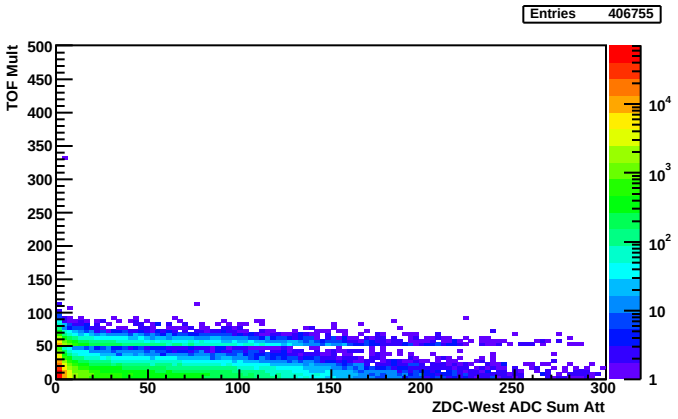

Entries 406755
TOF Mult
BBC-L-East ADC Sum

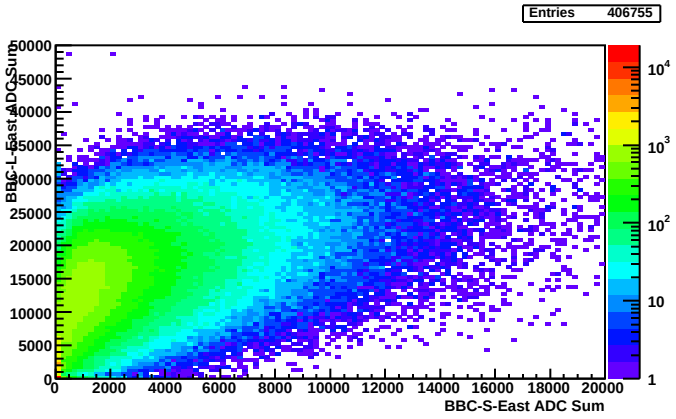

Entries 406755
TOF Mult
BBC-L-West ADC Sum

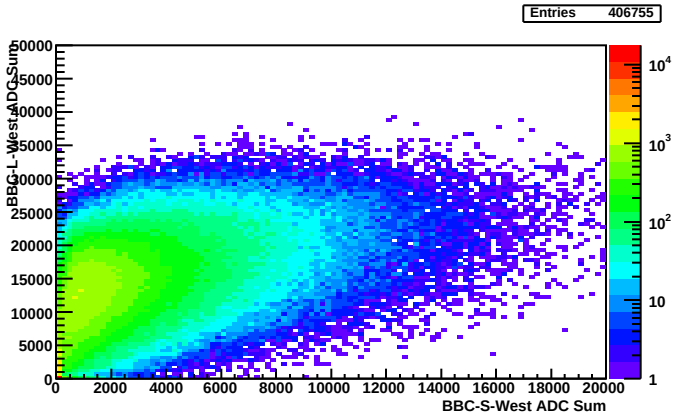

Entries 406755
TOF Mult
BBC-S-East ADC Sum

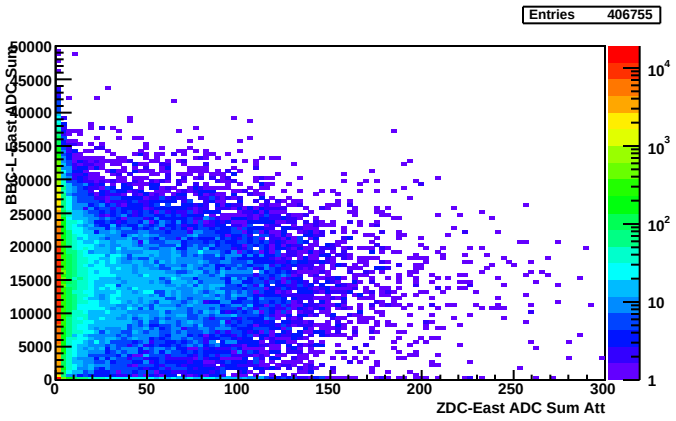

Entries 406755
TOF Mult
BBC-S-West ADC Sum

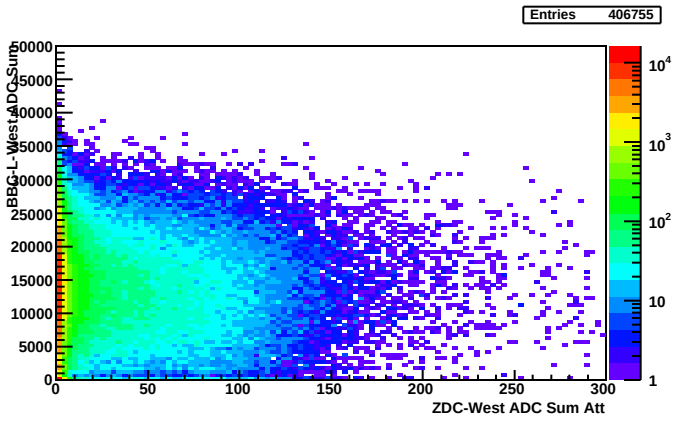

Entries 406755
TOF Mult
ZDC-East ADC Sum Att

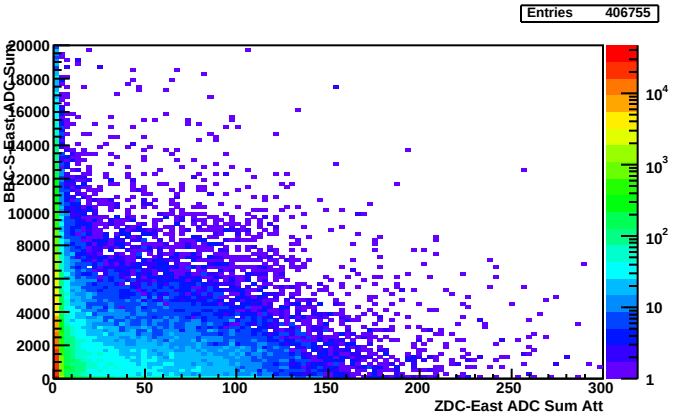

Entries 406755
TOF Mult
ZDC-West ADC Sum Att

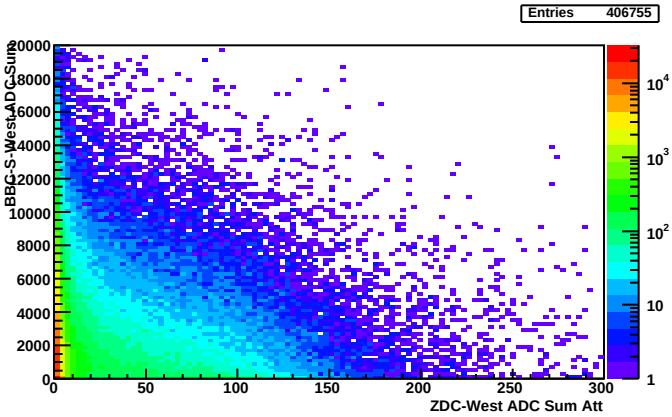

Entries 406755
BBC-L-East ADC Sum
BBC-S-East ADC Sum

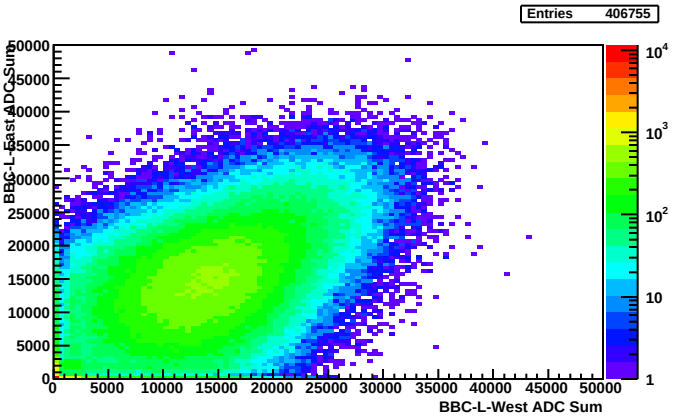

Entries 406755
BBC-L-West ADC Sum
BBC-S-West ADC Sum

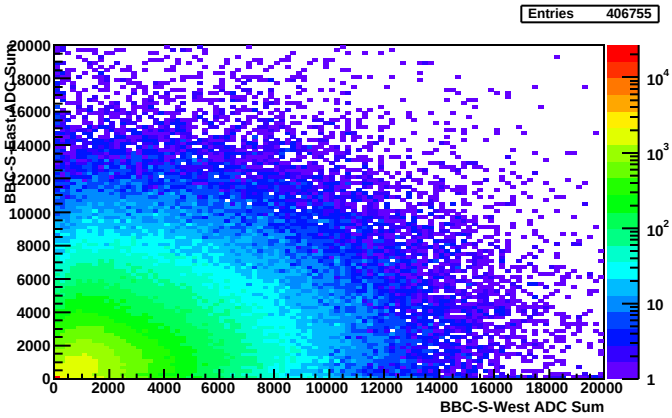

Entries 406755
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

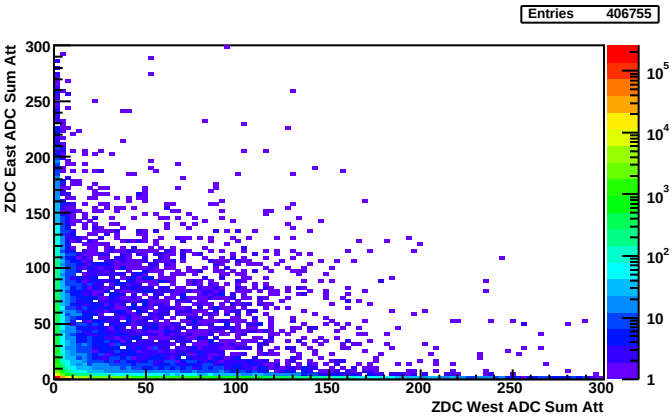

Entries 406755
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

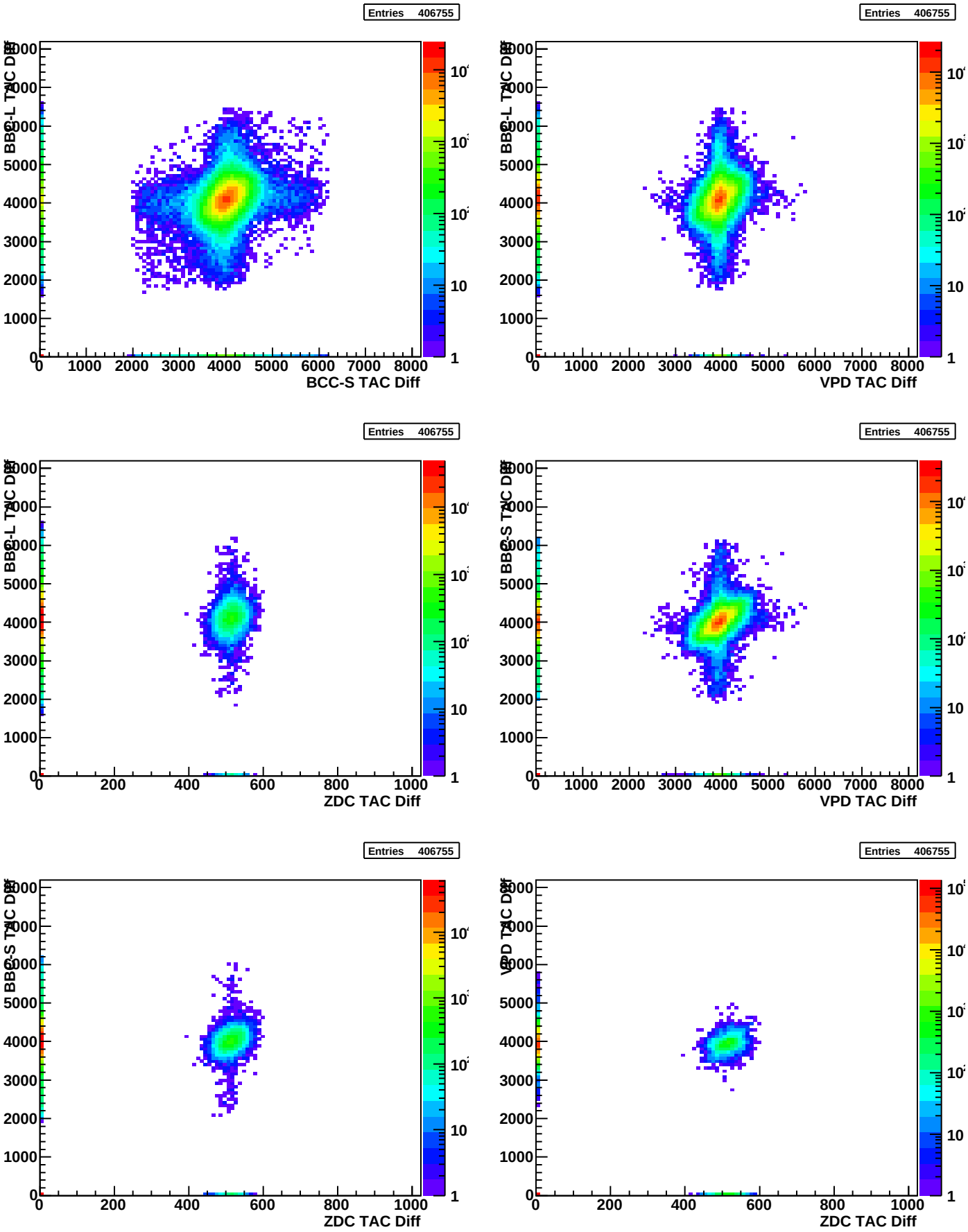

Entries 406755
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

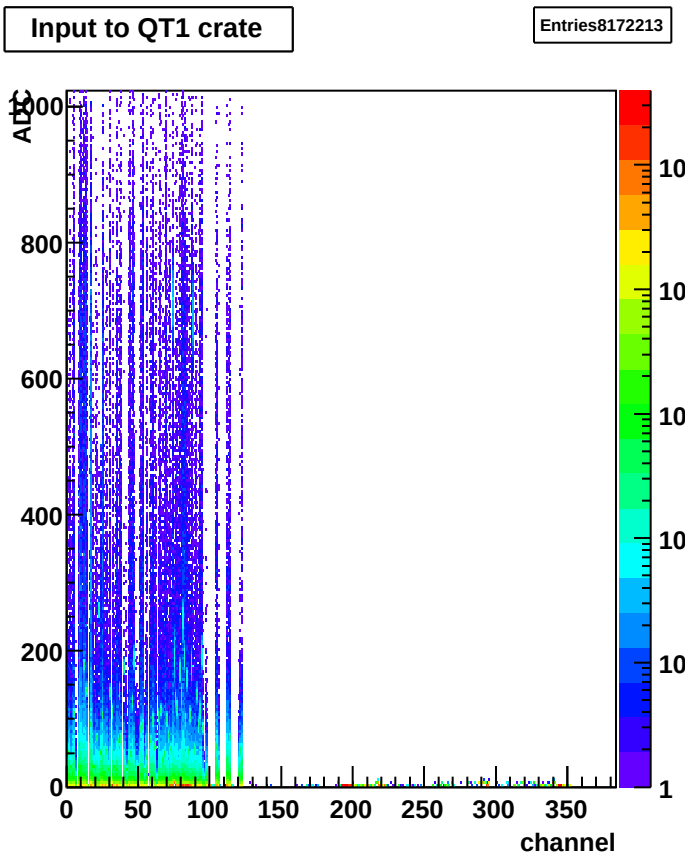

Entries 406755
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

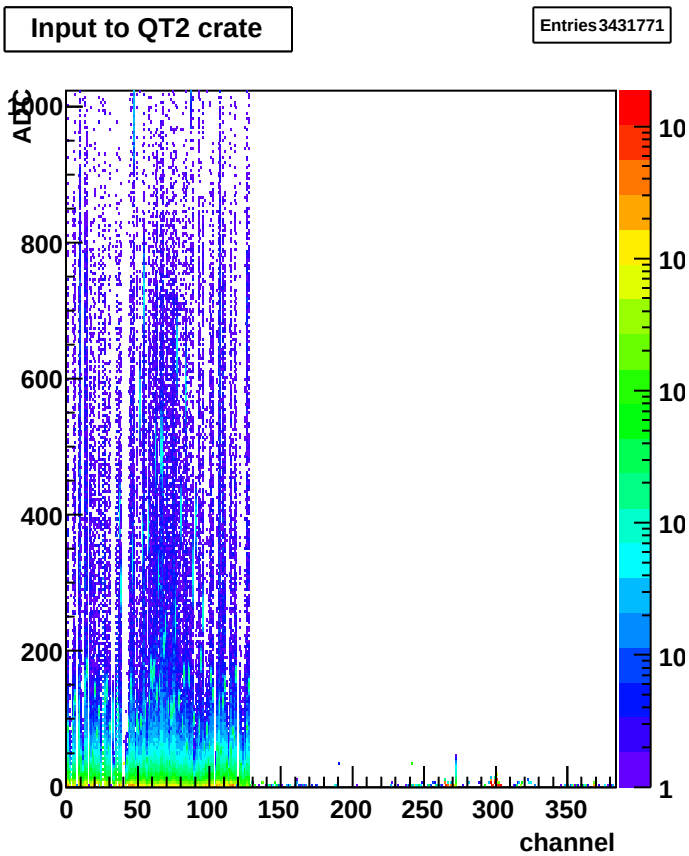

Entries 406755
BBC-L-East ADC Sum
BBC-L-West ADC Sum

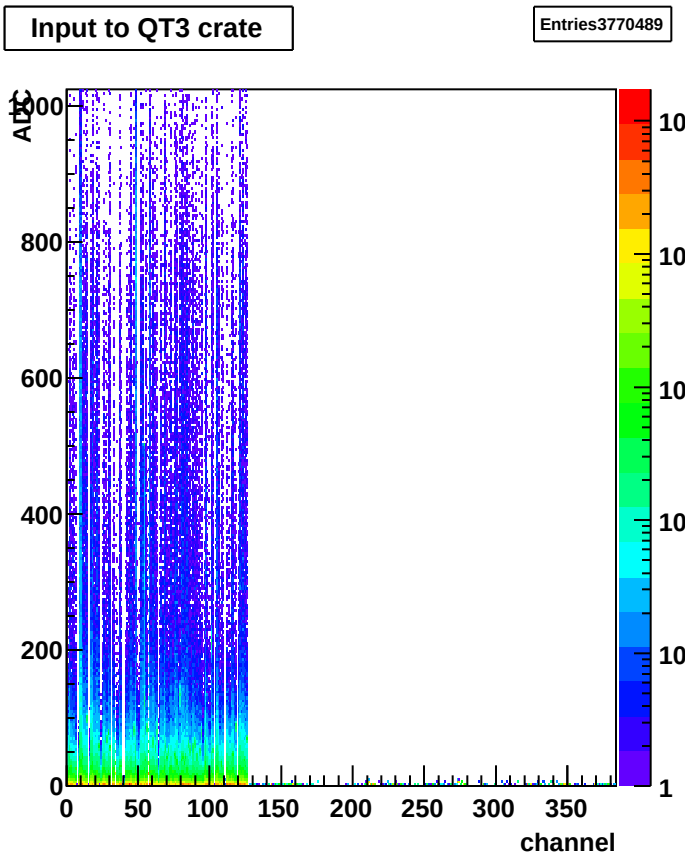

Entries 406755
BBC-S-East ADC Sum
BBC-S-West ADC Sum

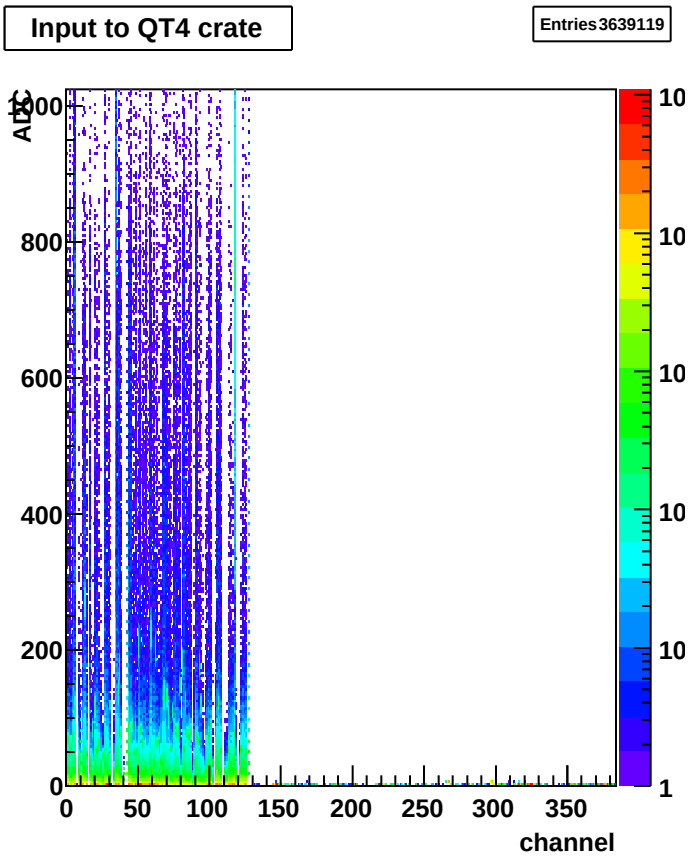

Entries 406755
ZDC East ADC Sum Att
ZDC West ADC Sum Att

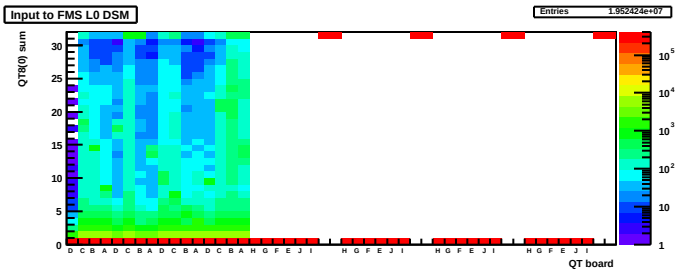

Entries 406755
BBC-L TAC Diff
BCC-S TAC Diff
Entries 406755
BBC-L TAC Diff
VPD TAC Diff
Entries 406755
BBC-L TAC Diff
ZDC TAC Diff
Entries 406755
BBC-S TAC Diff
VPD TAC Diff
Entries 406755
BBC-S TAC Diff
ZDC TAC Diff
Entries 406755
VPD TAC Diff
ZDC TAC Diff

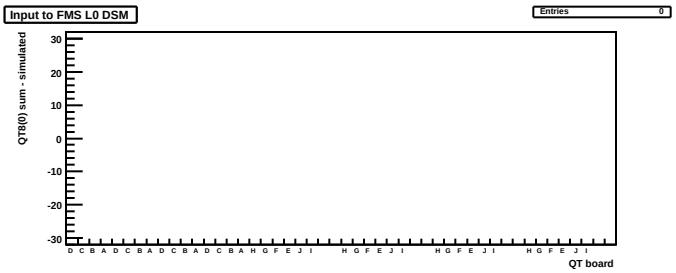

Input to QT1 crate
Entries8172213
ADC
channel

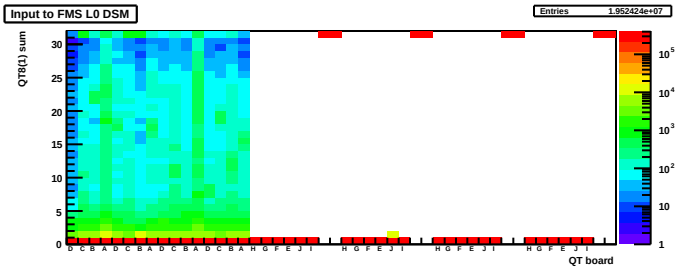

Input to QT2 crate
Entries3431771
ADC
channel

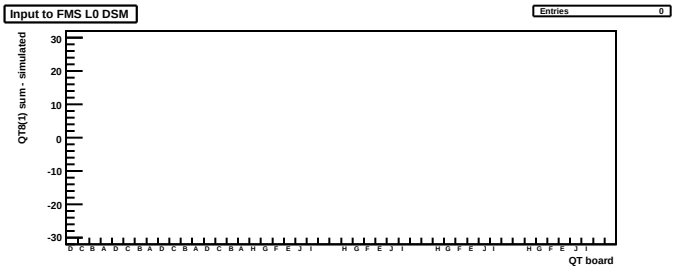

Input to QT3 crate
Entries3770489
ADC
channel

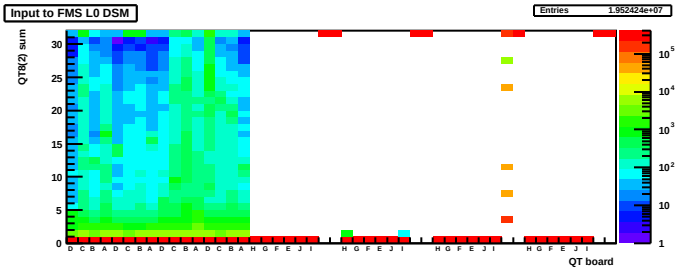

Input to QT4 crate
Entries3639119
ADC
channel

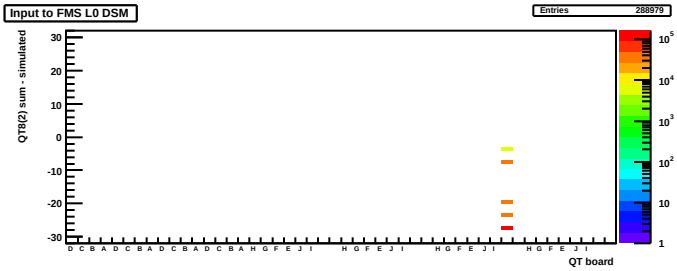

Input to FMS L0 DSM
Entries 1.952424e+07
QT8(0) sum
QT board

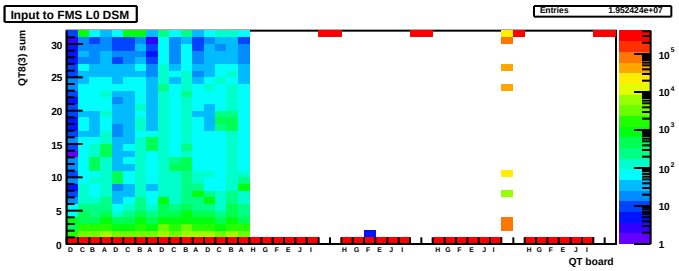

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

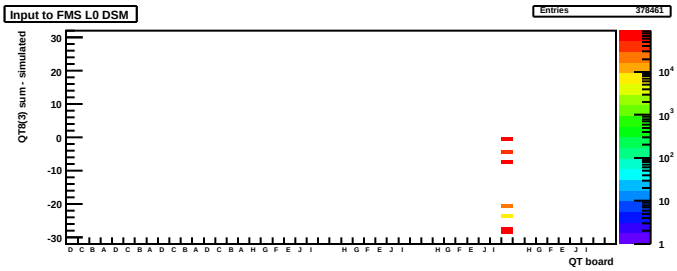

Input to FMS L0 DSM
Entries 1.952424e+07
QT8(1) sum
QT board

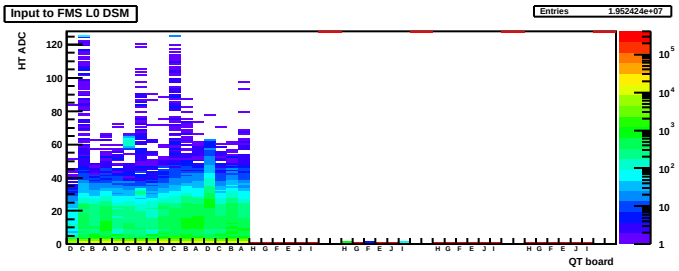

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

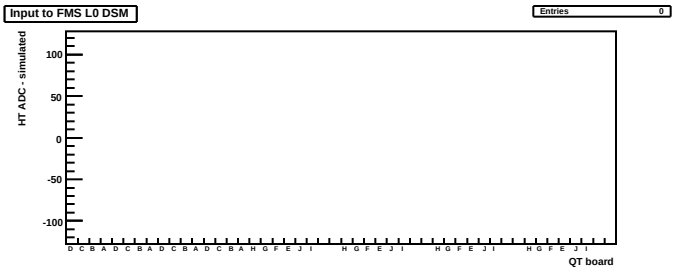

Input to FMS L0 DSM
Entries 1.952424e+07
QT8(2) sum
QT board

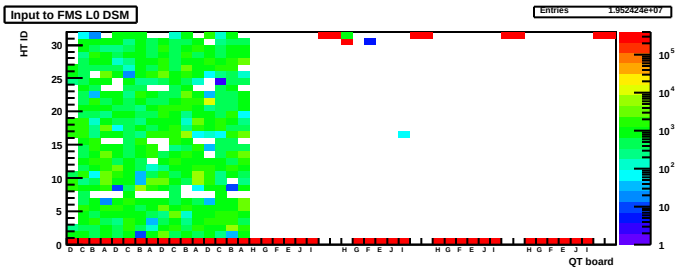

Input to FMS L0 DSM
Entries 288979
QT8(2) sum - simulated
QT board

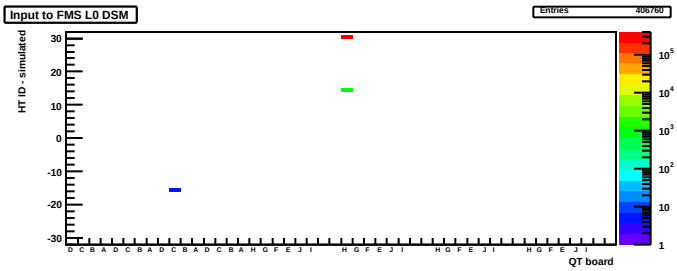

Input to FMS L0 DSM
Entries 1.952424e+07
QT8(3) sum
QT board

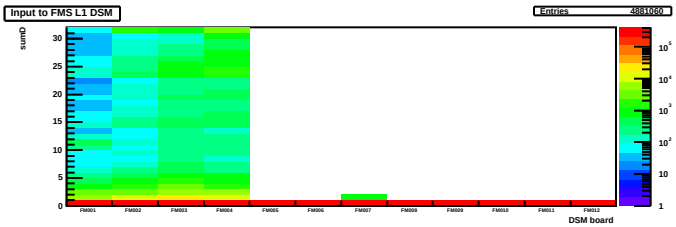

Input to FMS L0 DSM
Entries 378461
QT8(3) sum - simulated
QT board

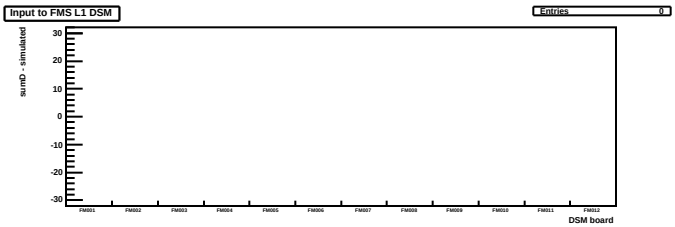

Input to FMS L0 DSM
Entries 1.952424e+07
HT ADC
QT board

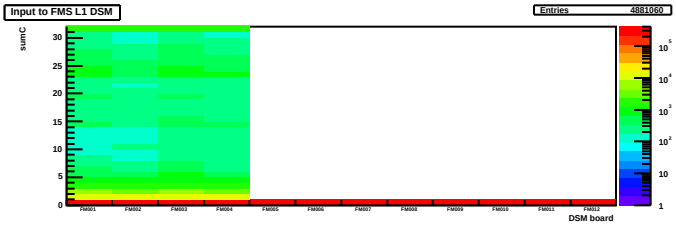

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

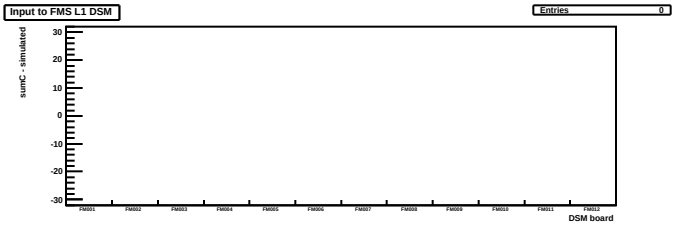

Input to FMS L0 DSM
Entries 1.952424e+07
HT ID
QT board

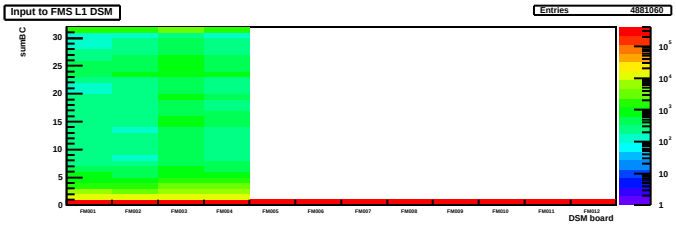

Input to FMS L0 DSM
Entries 406760
HT ID - simulated
QT board

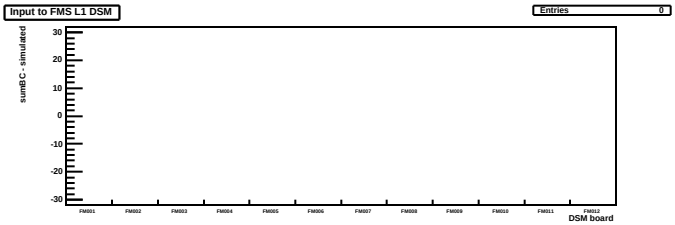

Input to FMS L1 DSM
Entries 4881060
sumD
DSM board

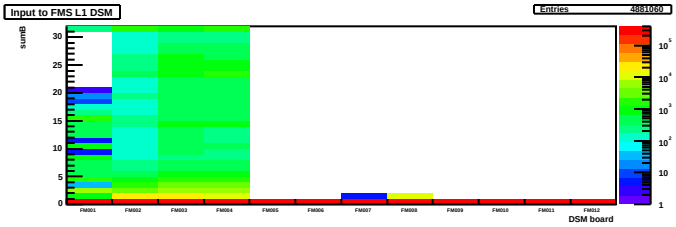

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

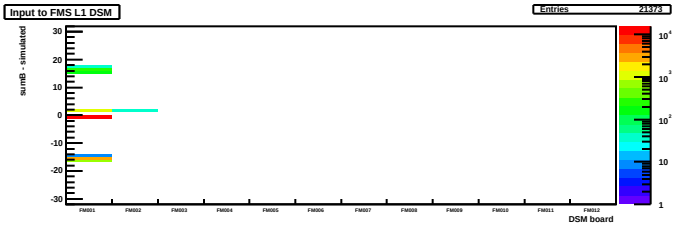

Input to FMS L1 DSM
Entries 4881060
sumC
DSM board

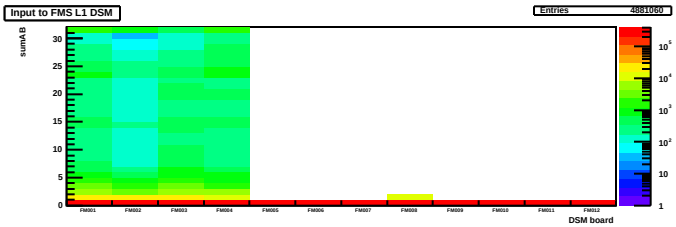

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

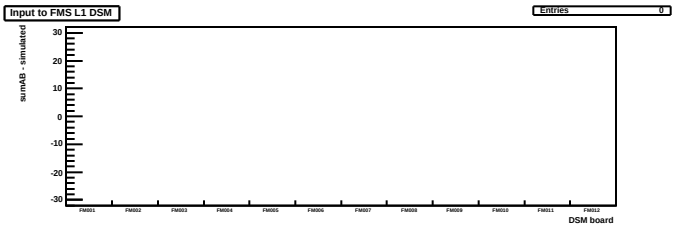

Input to FMS L1 DSM
Entries 4881060
sumBC
DSM board

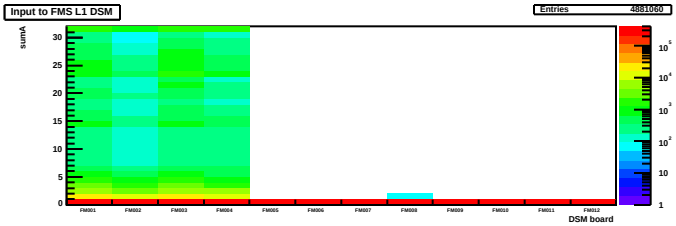

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

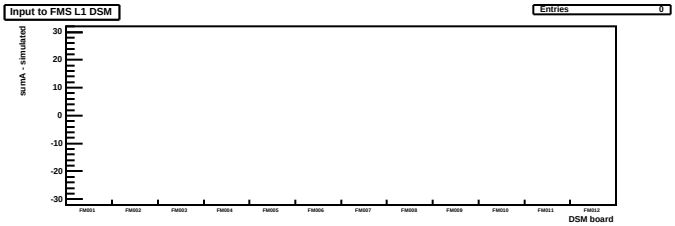

Input to FMS L1 DSM
Entries 4881060
sumB
DSM board

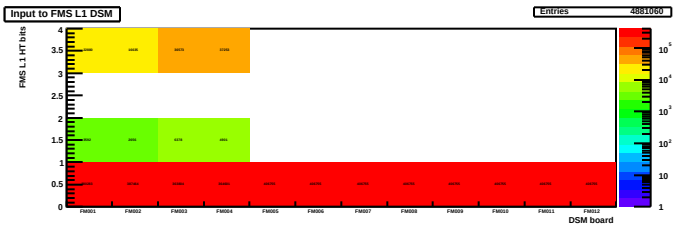

Input to FMS L1 DSM
Entries 21373
sumB - simulated
DSM board

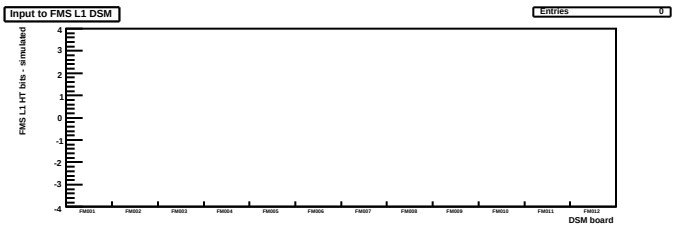

Input to FMS L1 DSM
Entries 4881060
sumAB
DSM board

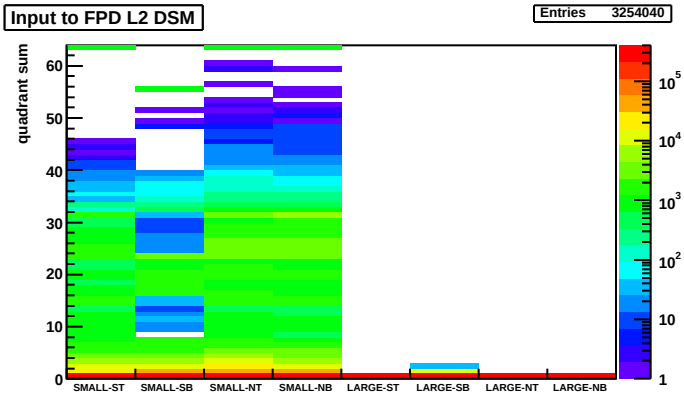

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

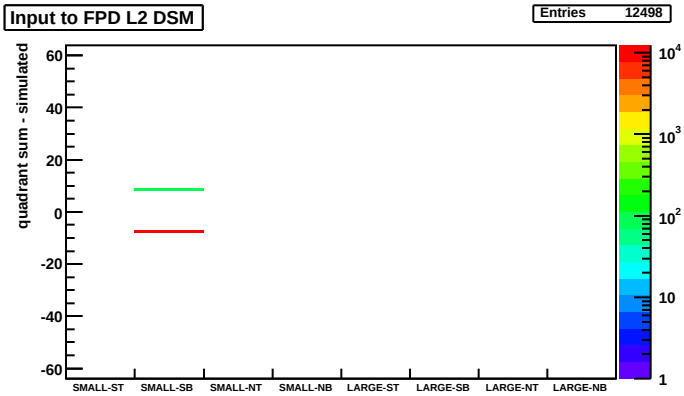

Input to FMS L1 DSM
Entries 4881060
sumA
DSM board

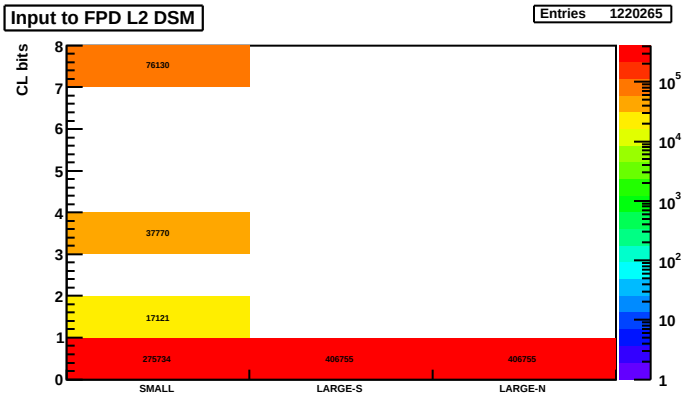

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

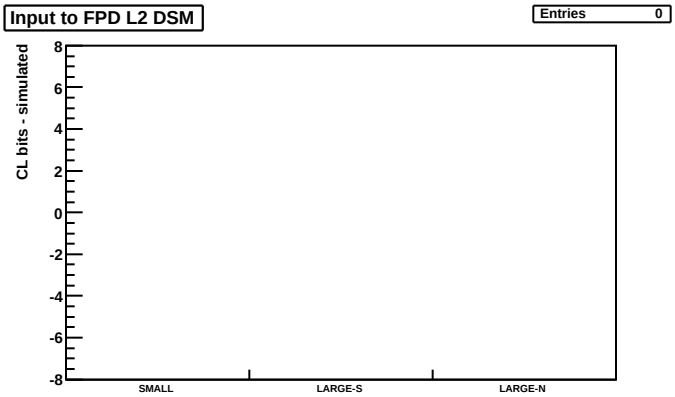

Input to FMS L1 DSM
Entries 4881060
FMS L1 HT bits
DSM board

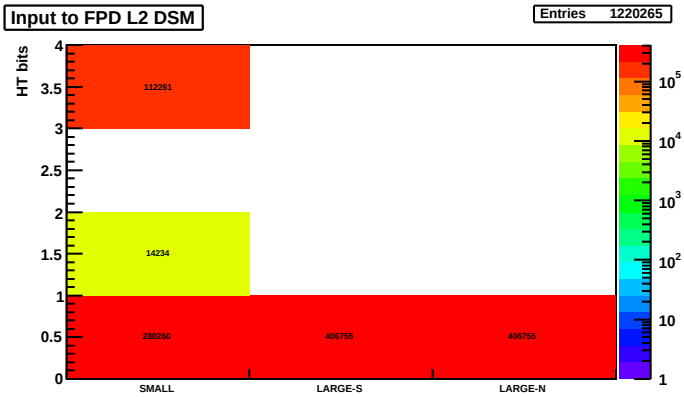

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

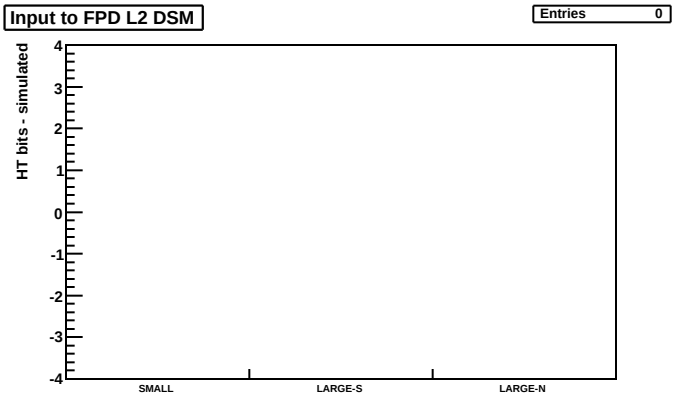

Input to FPD L2 DSM
Entries 3254040
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

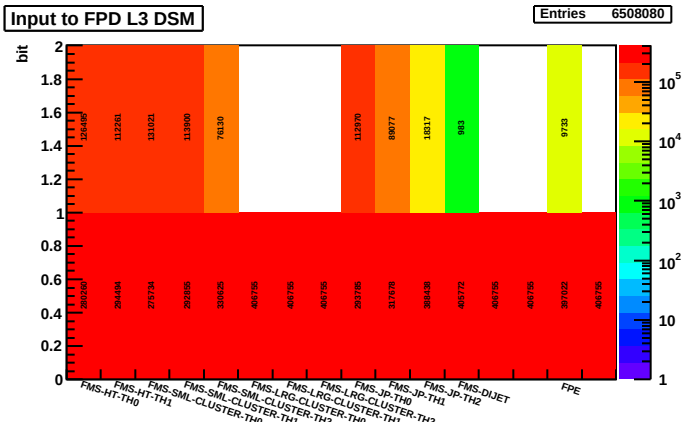

Input to FPD L2 DSM
Entries 12498
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

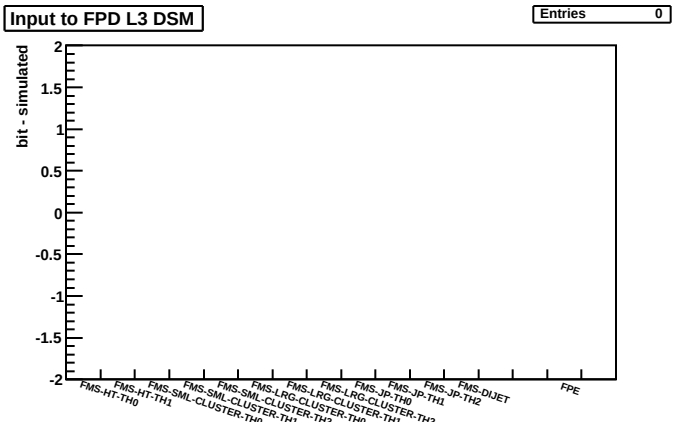

Input to FPD L2 DSM
Entries 1220265
CL bits
76130
37770
17121
275734
406755
406755
SMALL
LARGE-S
LARGE-N

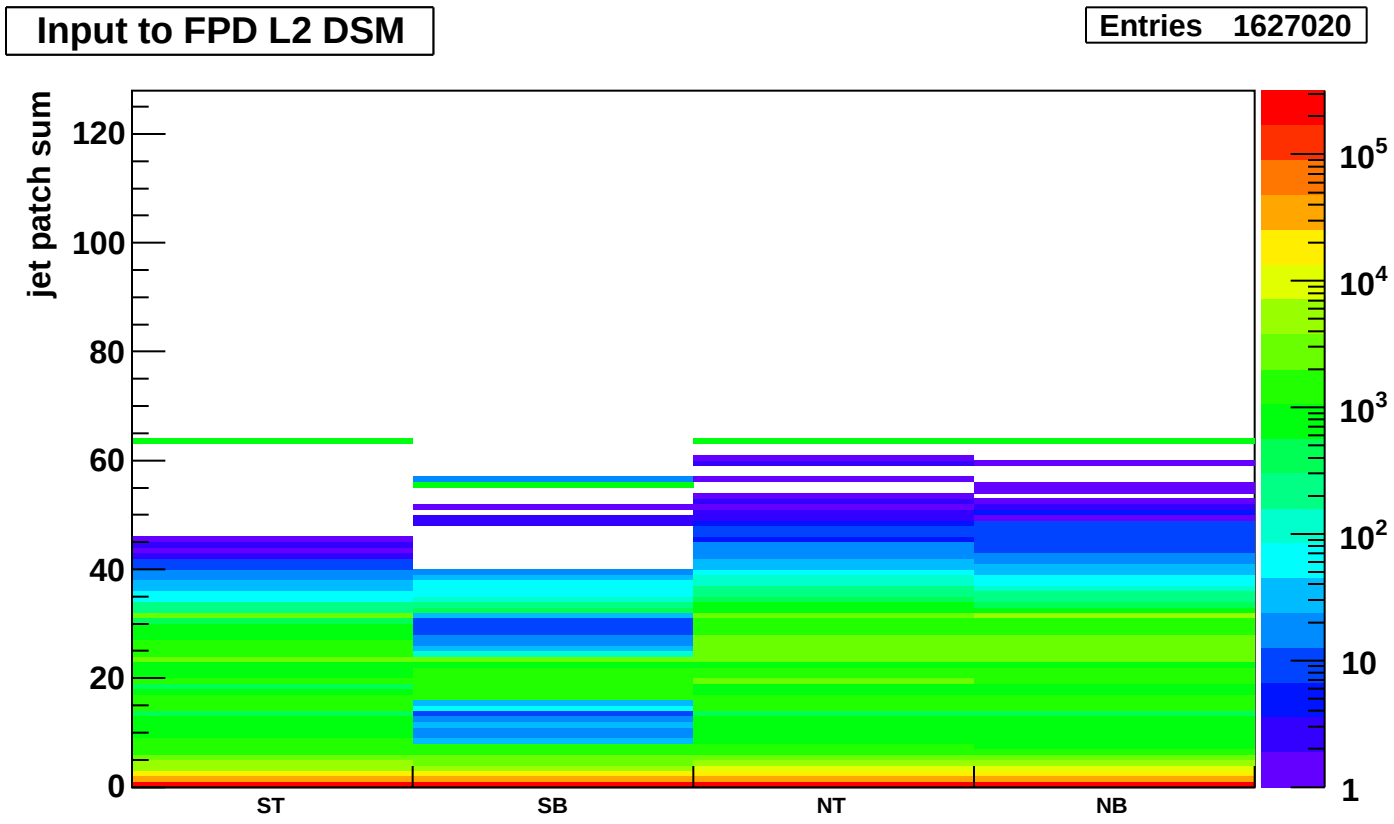

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

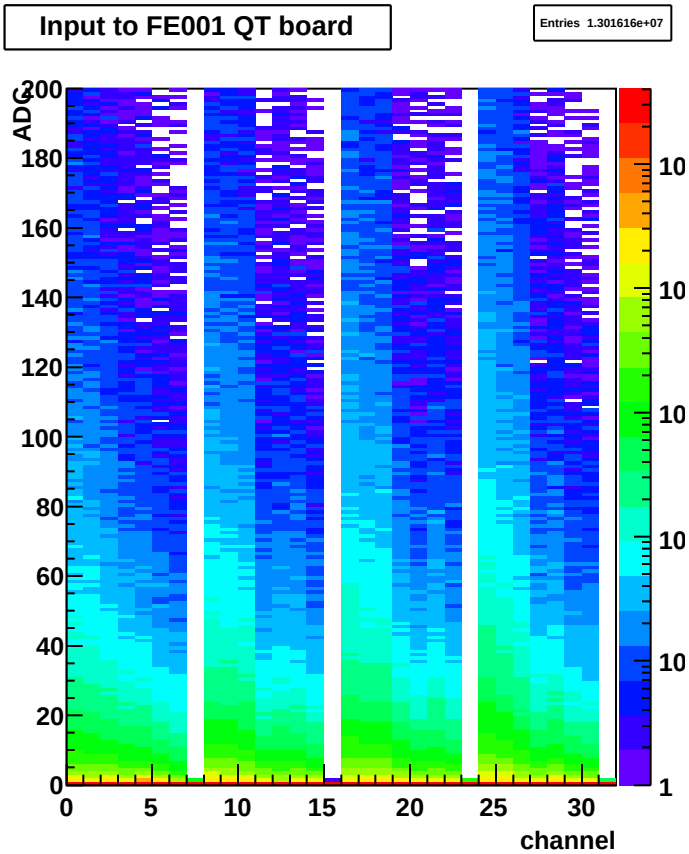

Input to FPD L2 DSM
Entries 1220265
HT bits
112261
14234
280260
406755
406755
SMALL
LARGE-S
LARGE-N

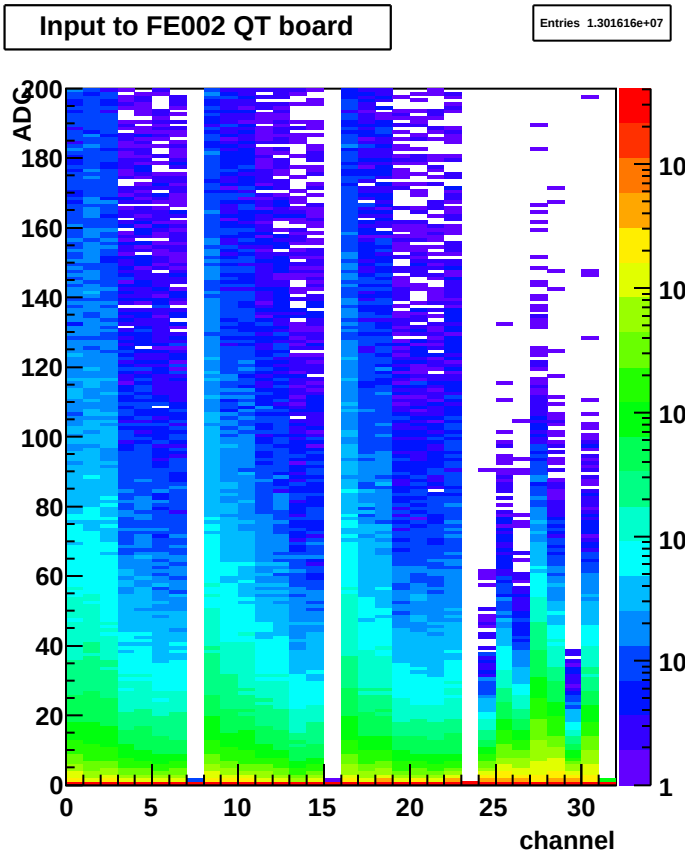

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

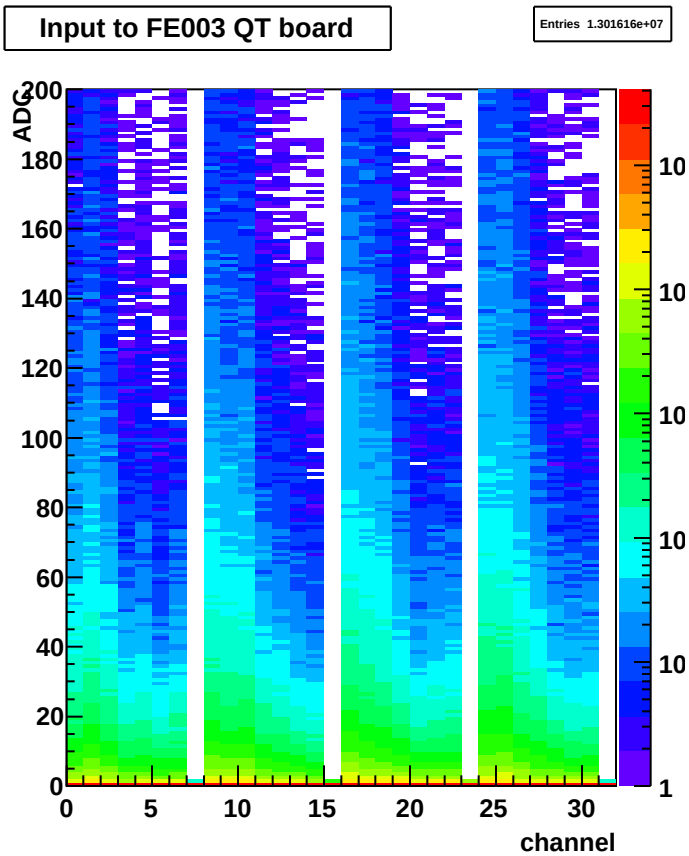

Input to FPD L3 DSM
Entries 6508080
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

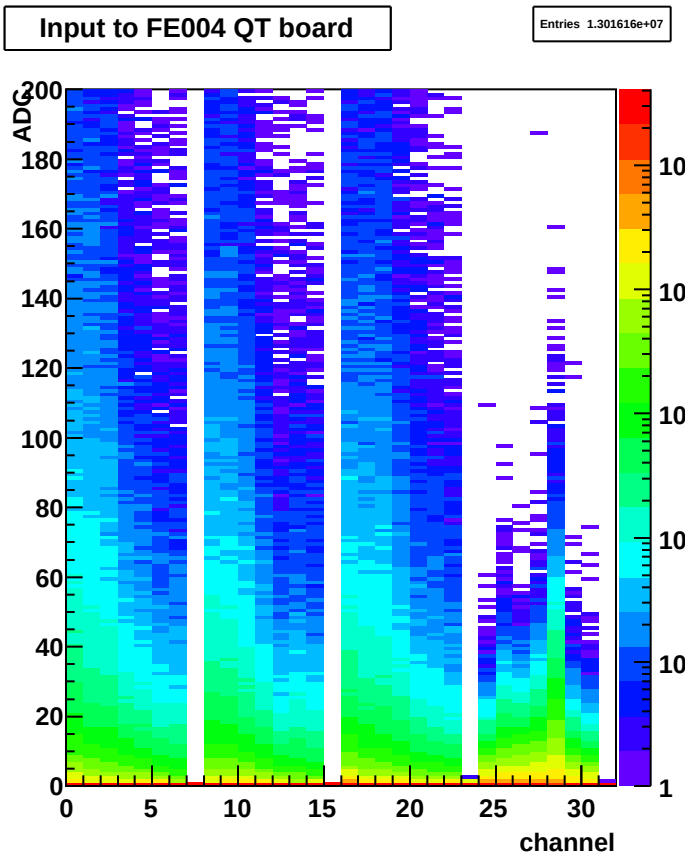

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

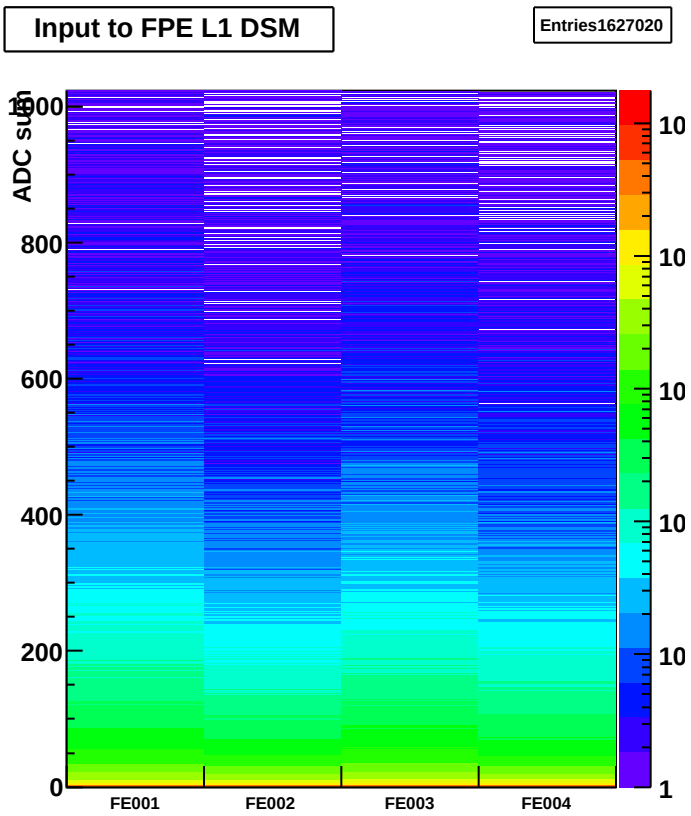

Input to FPD L2 DSM
Entries 1627020
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

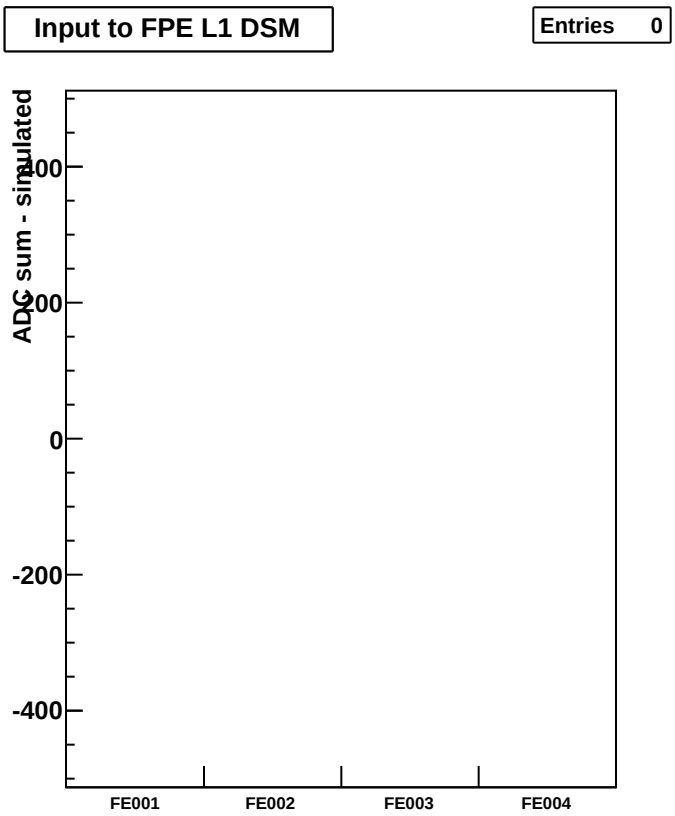

Input to FE001 QT board
Entries 1.301616e+07
ADC
channel

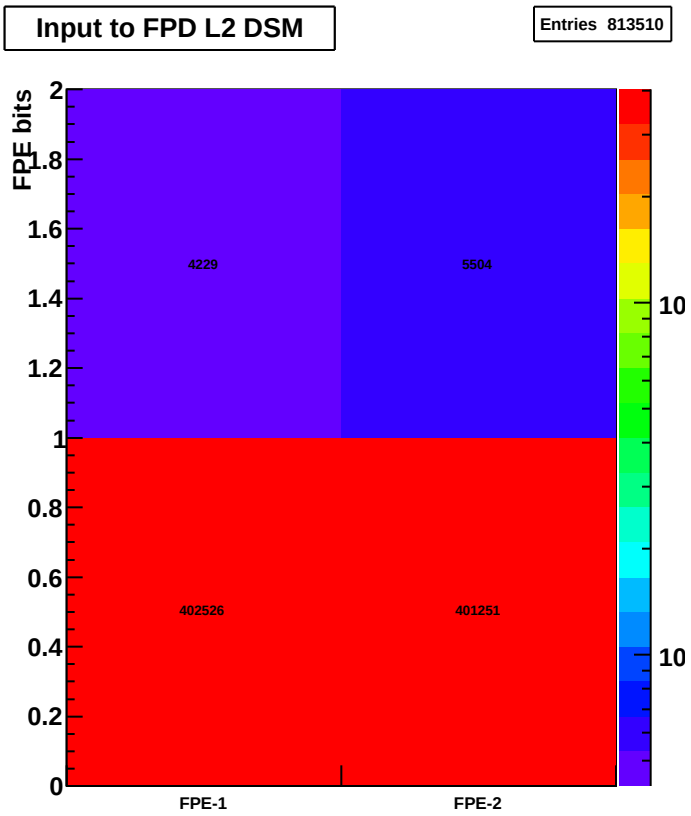

Input to FE002 QT board
Entries 1.301616e+07
ADC
channel

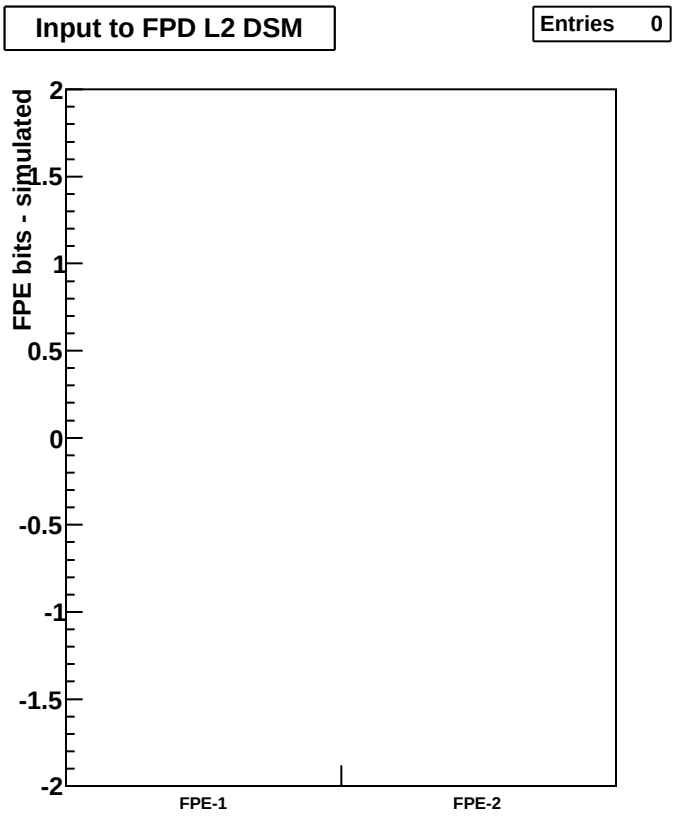

Input to FE003 QT board
Entries 1.301616e+07
ADC
channel


Input to FE004 QT board
Entries 1.301616e+07
ADC
channel


Input to FPE L1 DSM
Entries1627020
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 813510
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
4229
5504
402526
401251
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2