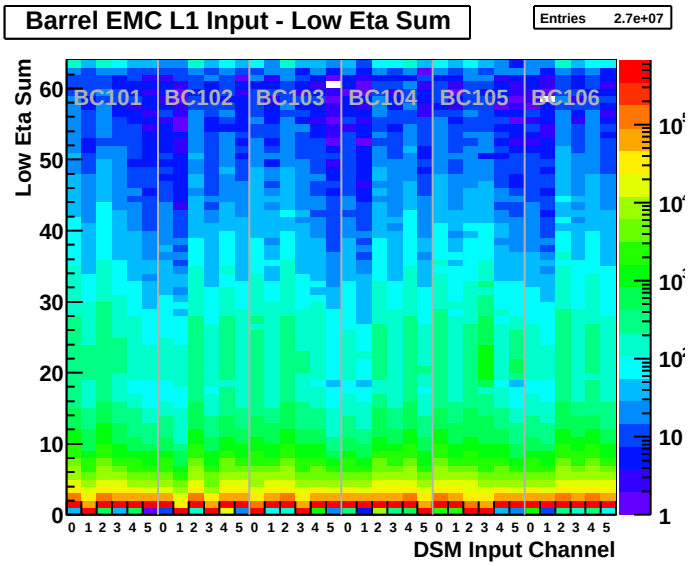
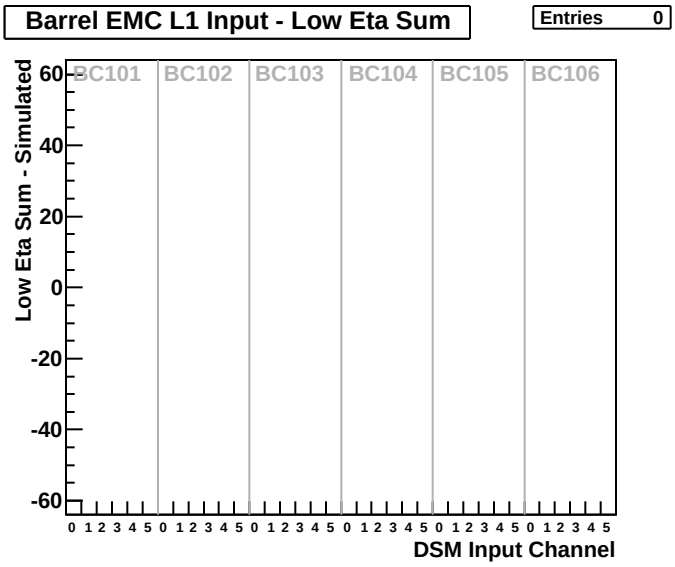
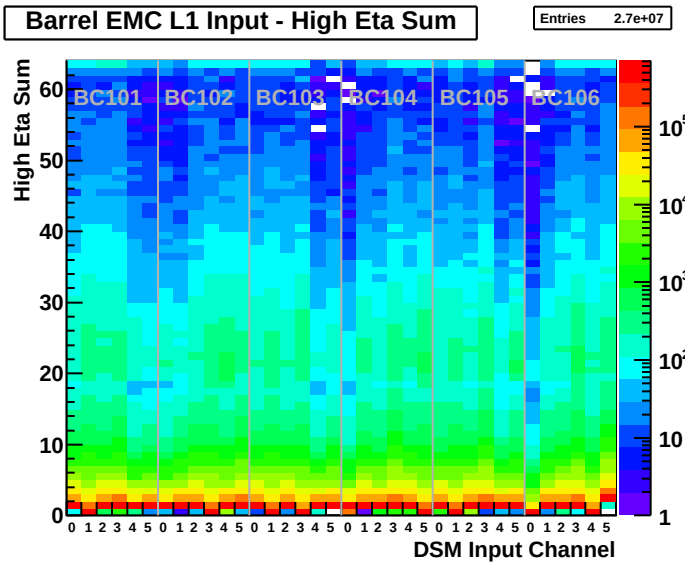
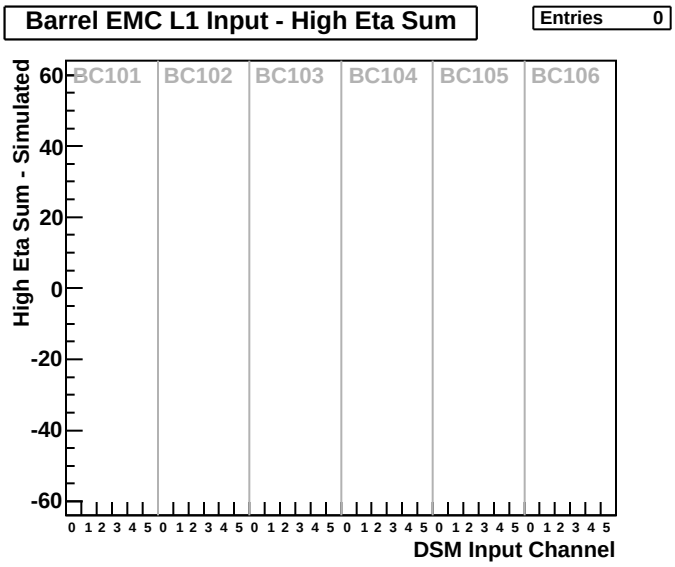
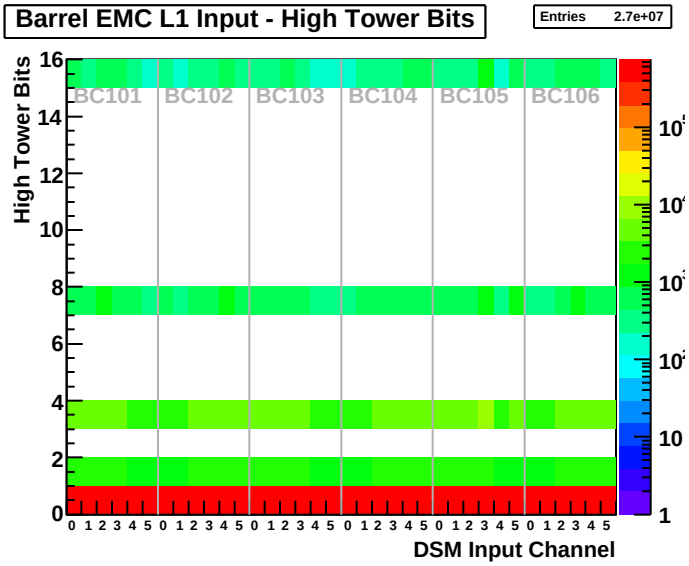
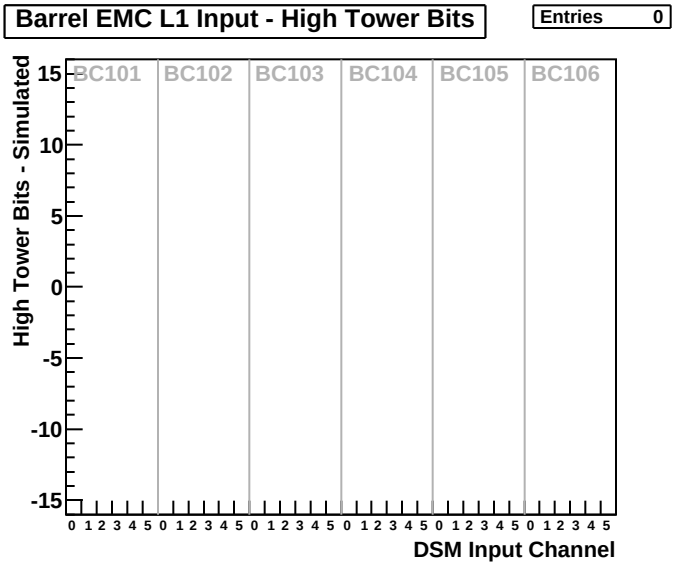

Barrel EMC L1 Input - Low Eta Sum
Entries 2.7e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

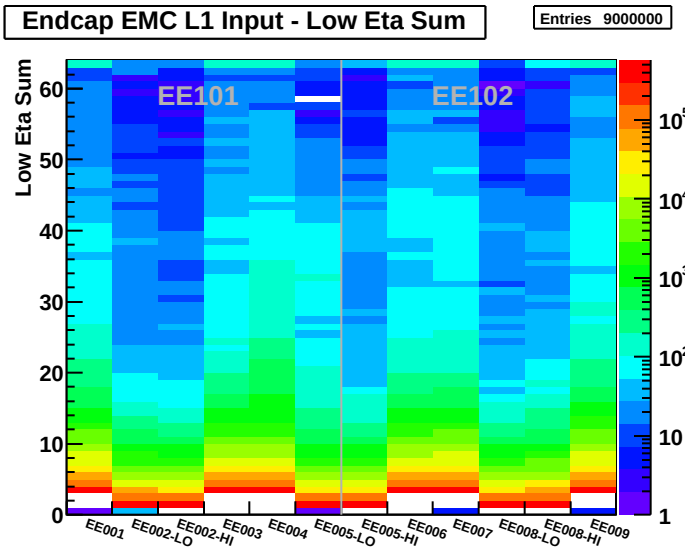

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

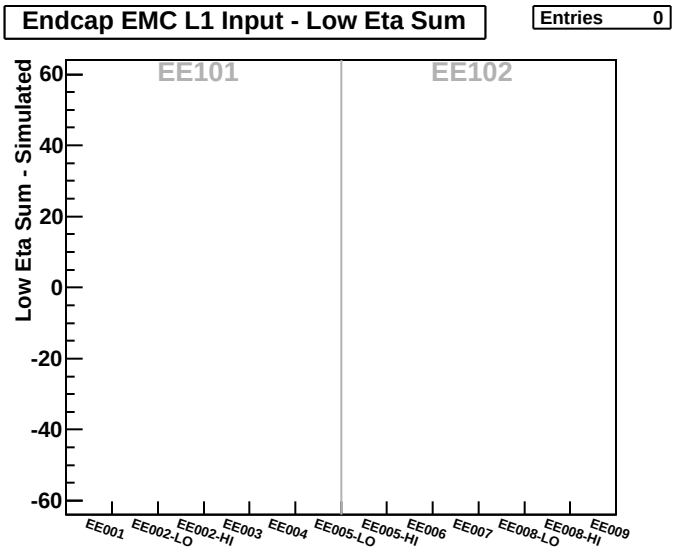

Barrel EMC L1 Input - High Eta Sum
Entries 2.7e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

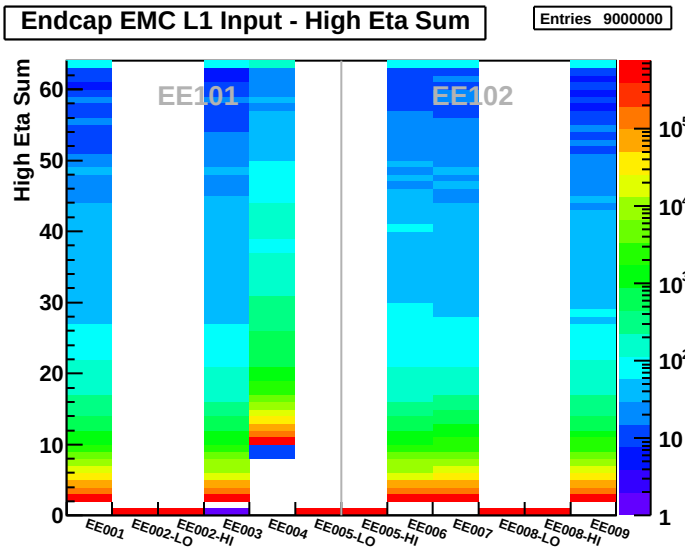

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

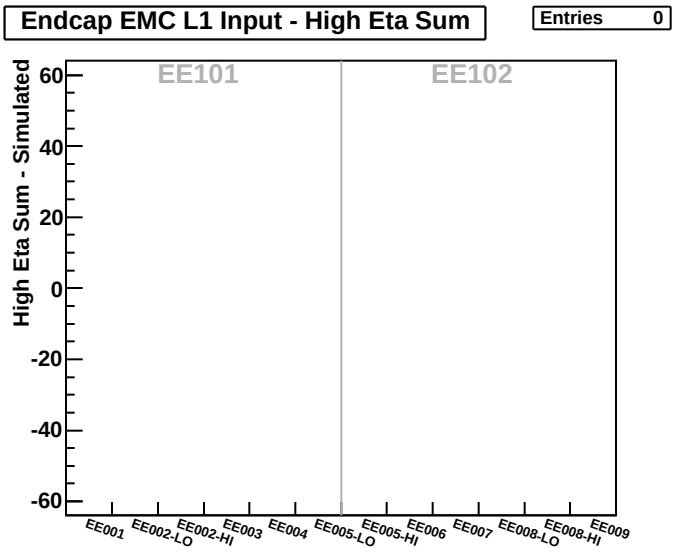

Barrel EMC L1 Input - High Tower Bits
Entries 2.7e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

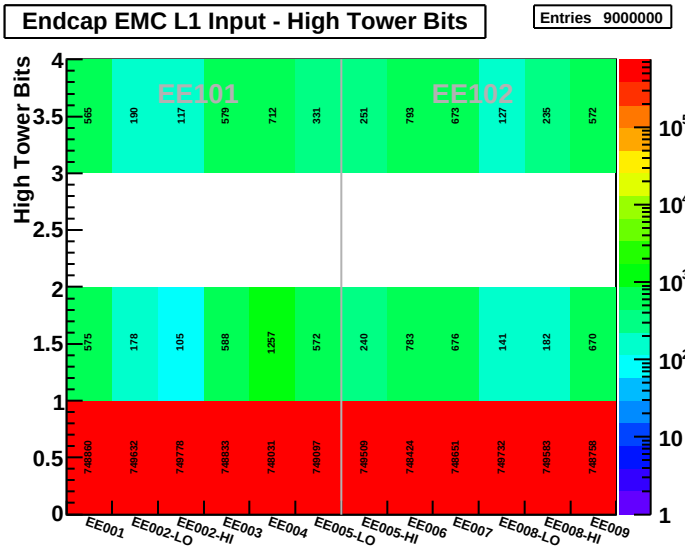

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

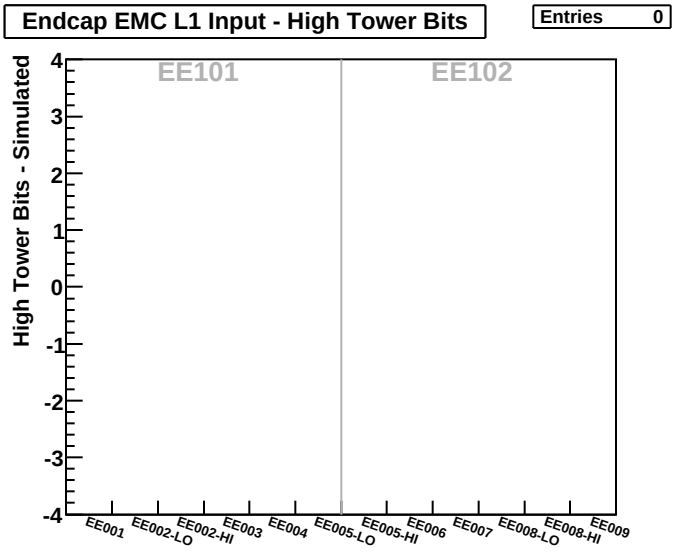

Endcap EMC L1 Input - Low Eta Sum
Entries 9000000
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

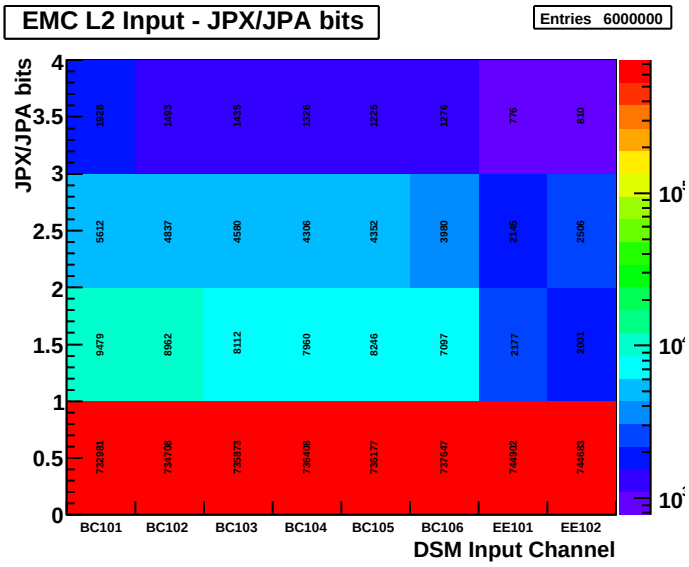

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

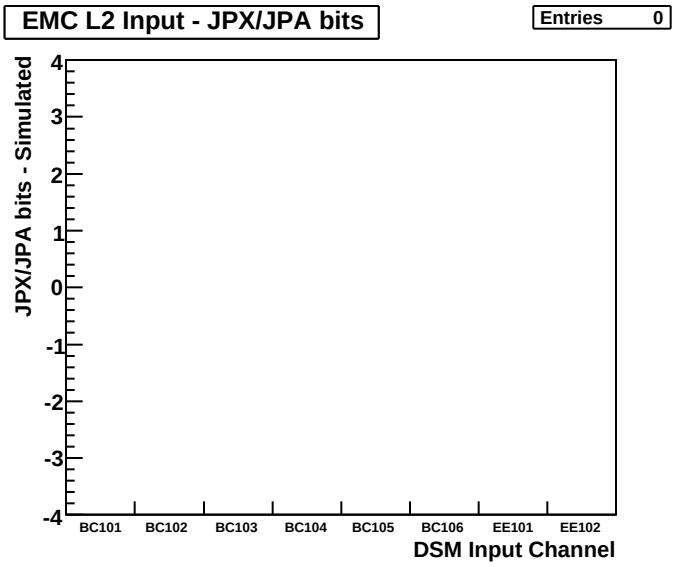

Endcap EMC L1 Input - High Eta Sum
Entries 9000000
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

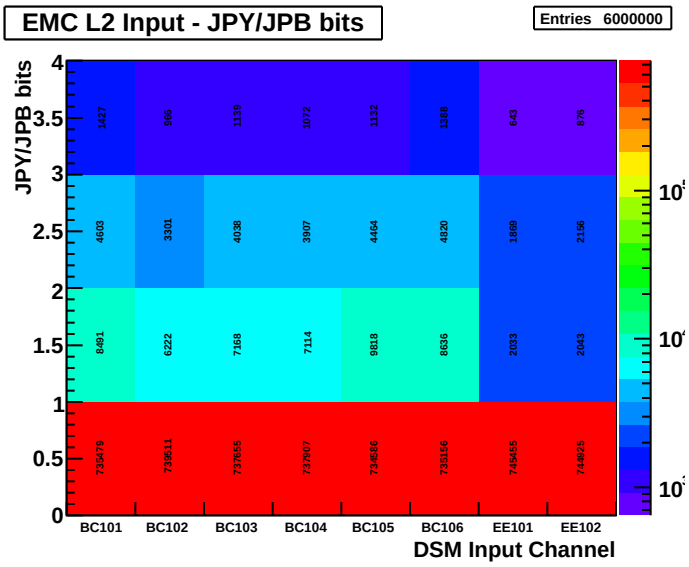

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

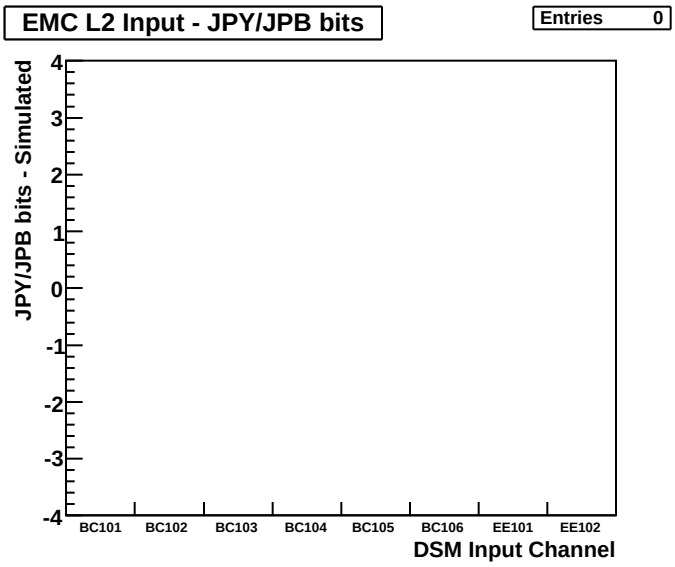

Endcap EMC L1 Input - High Tower Bits
Entries 9000000
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

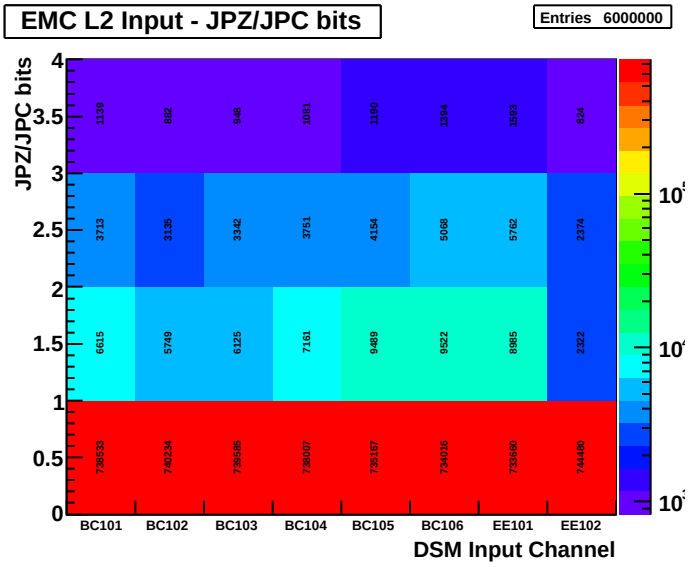

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

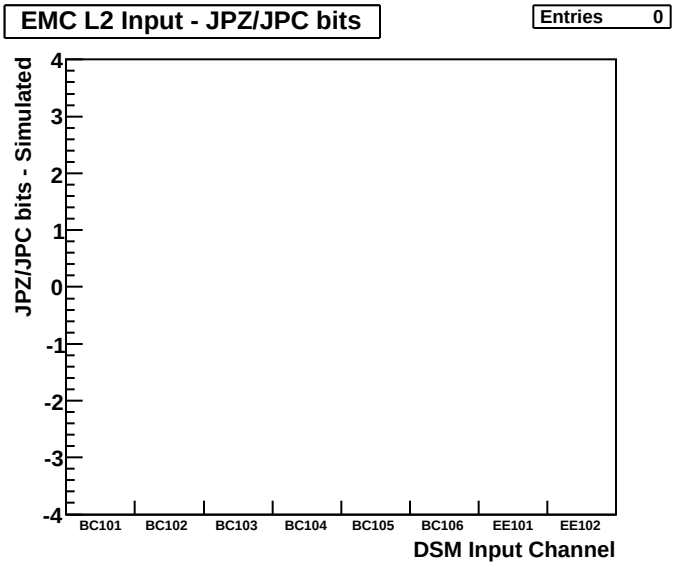

EMC L2 Input - JPX/JPA bits
Entries 6000000
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1928 1493 1435 1326 1225 1276 776 810
5612 4837 4580 4306 4352 3980 2145 2506
9479 8962 8112 7960 8246 7097 2177 2001
732961 734708 735873 736408 736177 737647 744902 744683

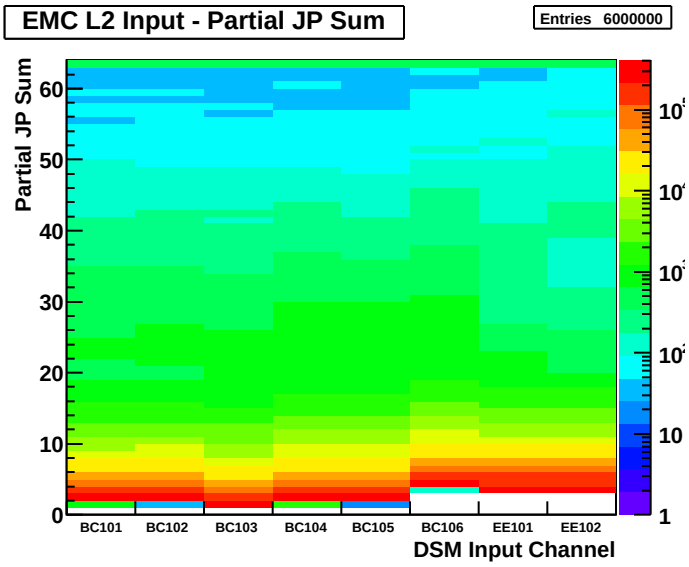

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

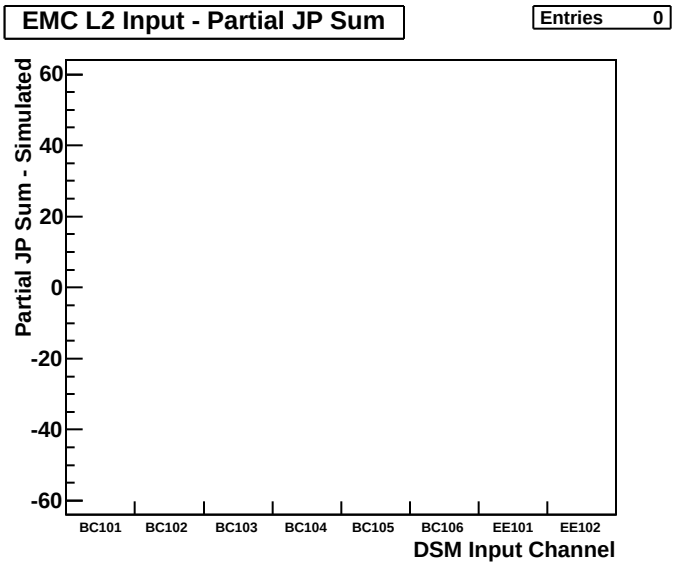

EMC L2 Input - JPY/JPB bits
Entries 6000000
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1427 966 1139 1072 1132 1398 643 876
4603 3301 4038 3907 4464 4820 1669 2156
8491 6222 7168 7114 9818 8636 2033 2043
735479 738511 737655 737907 734586 735156 745455 744925

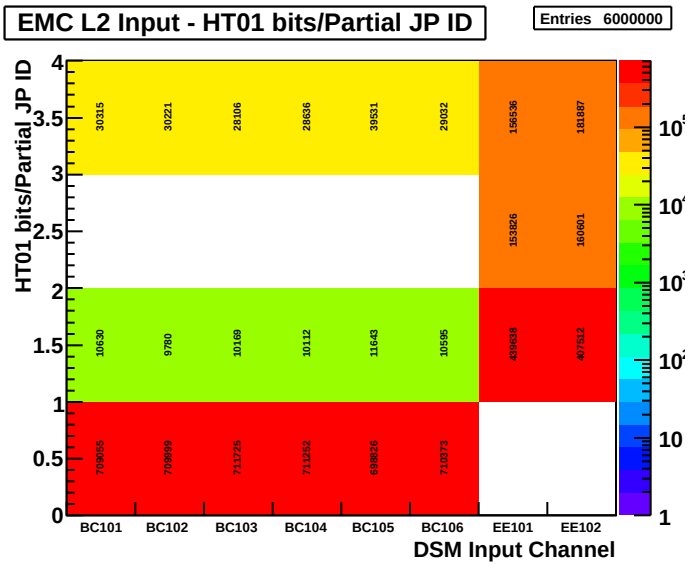

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

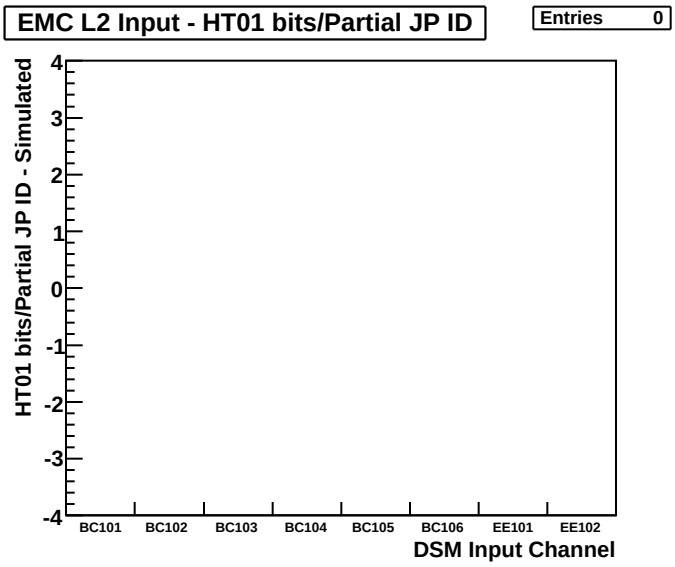

EMC L2 Input - JPZ/JPC bits
Entries 6000000
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1139 882 948 1081 1190 1394 1593 824
3713 3135 3342 3751 4154 5068 5762 2374
6615 5749 6125 7161 9489 9522 8985 2322
738533 740234 739585 738007 735167 734016 733660 744480

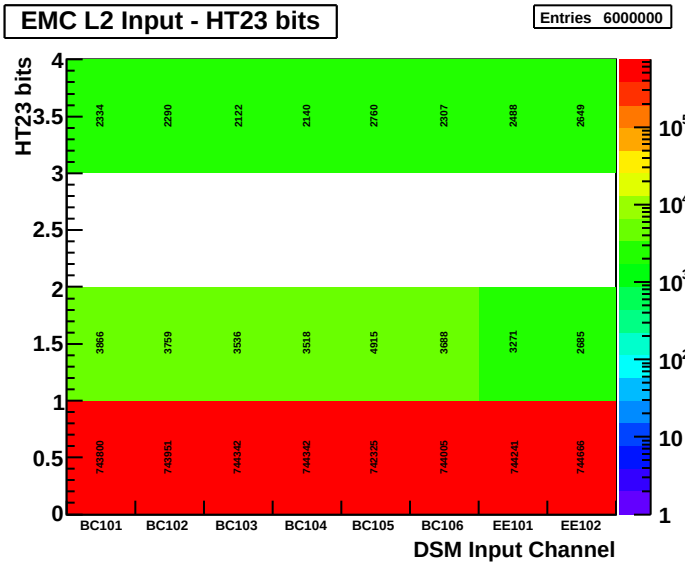

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

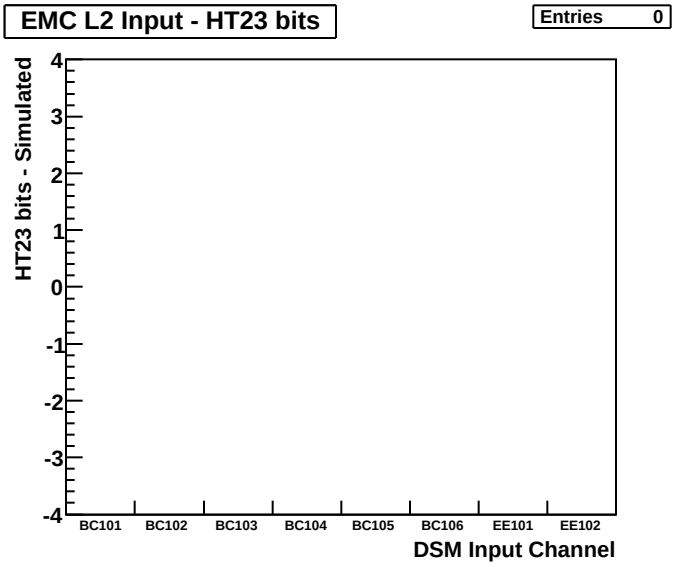

EMC L2 Input - Partial JP Sum
Entries 6000000
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

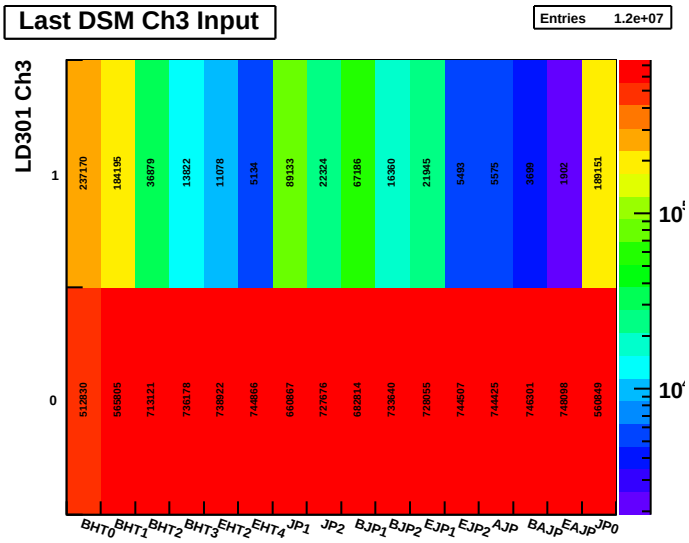

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

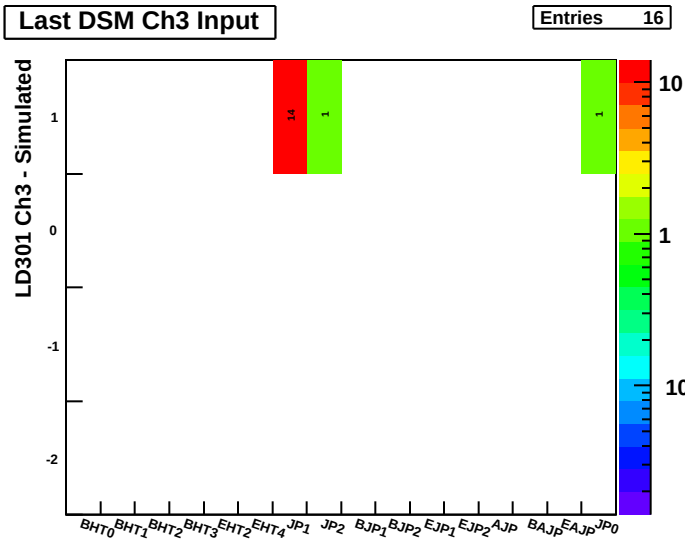

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6000000
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

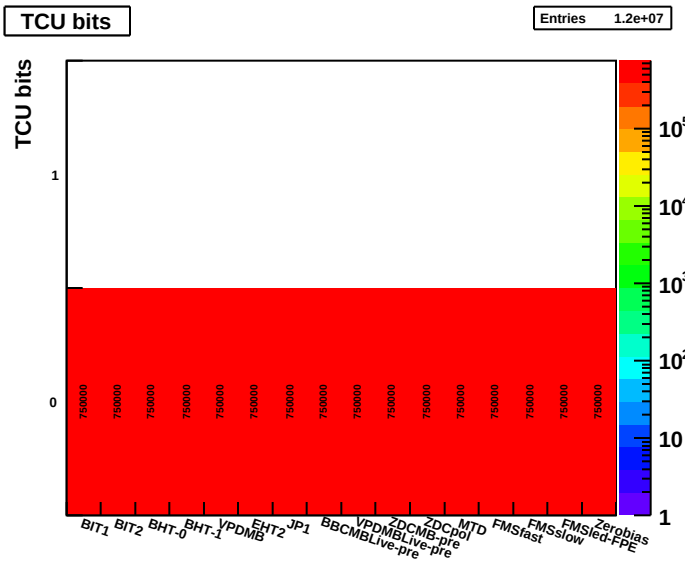

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

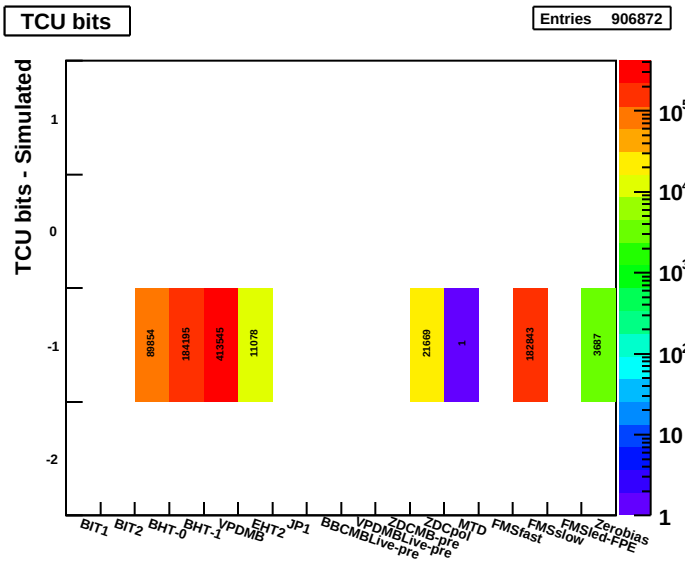

EMC L2 Input - HT23 bits
Entries 6000000
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

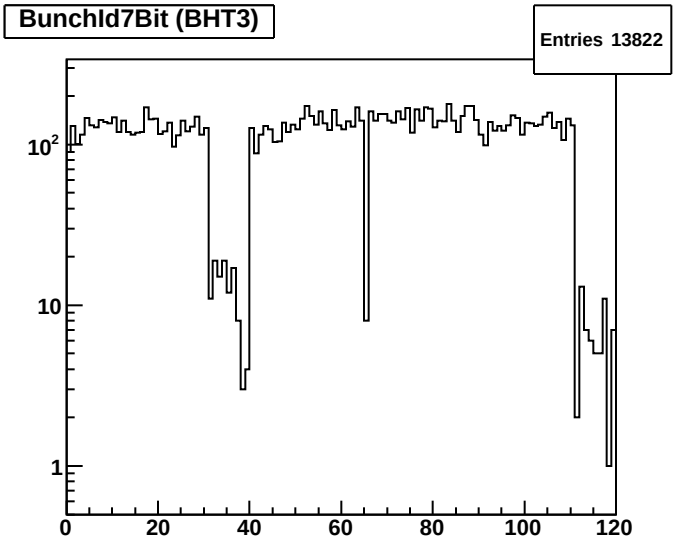

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

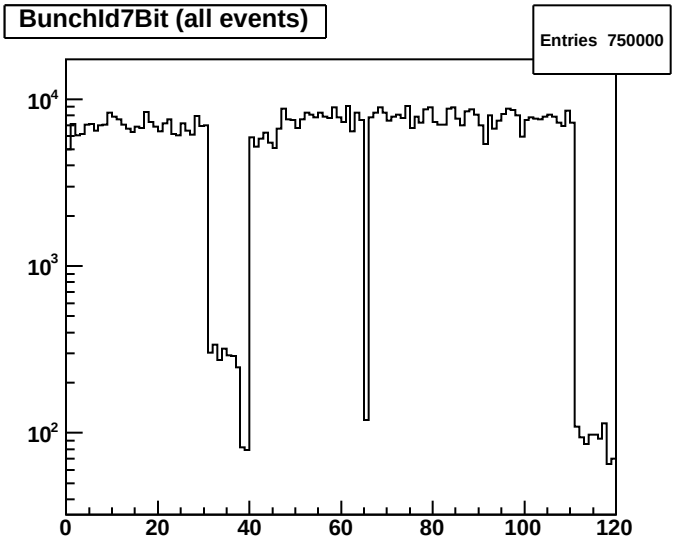

Last DSM Ch3 Input
Entries 1.2e+07
LD301 Ch3
237170 184195 36879 13822 11078 5134 89133 22324 67186 16360 21945 5493 5575 3699 1902 189151
512830 565805 713121 736178 738922 744866 660867 727676 682814 733640 728055 744507 744425 746301 748098 560849
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

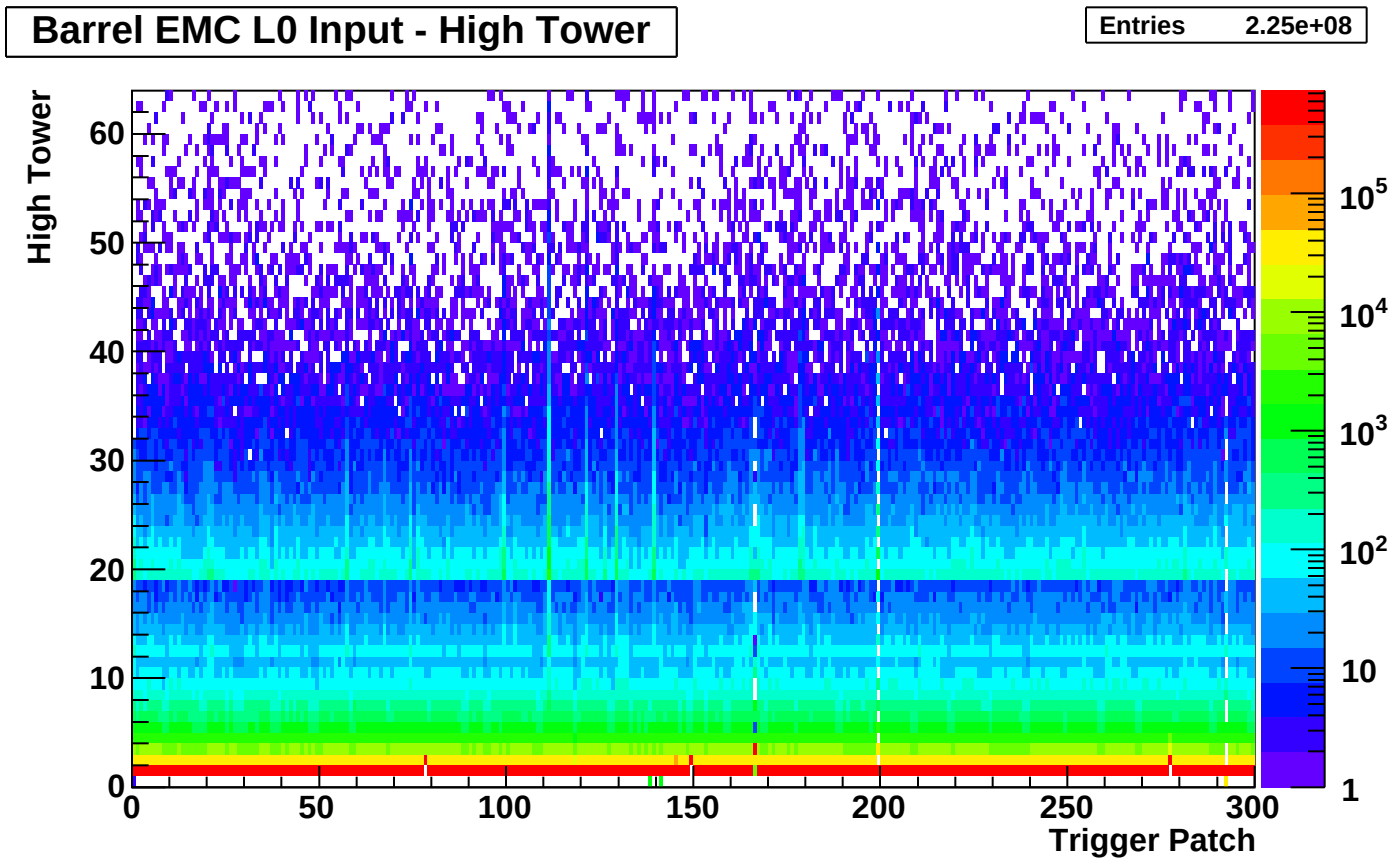

Last DSM Ch3 Input
Entries 16
LD301 Ch3 - Simulated
14 1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

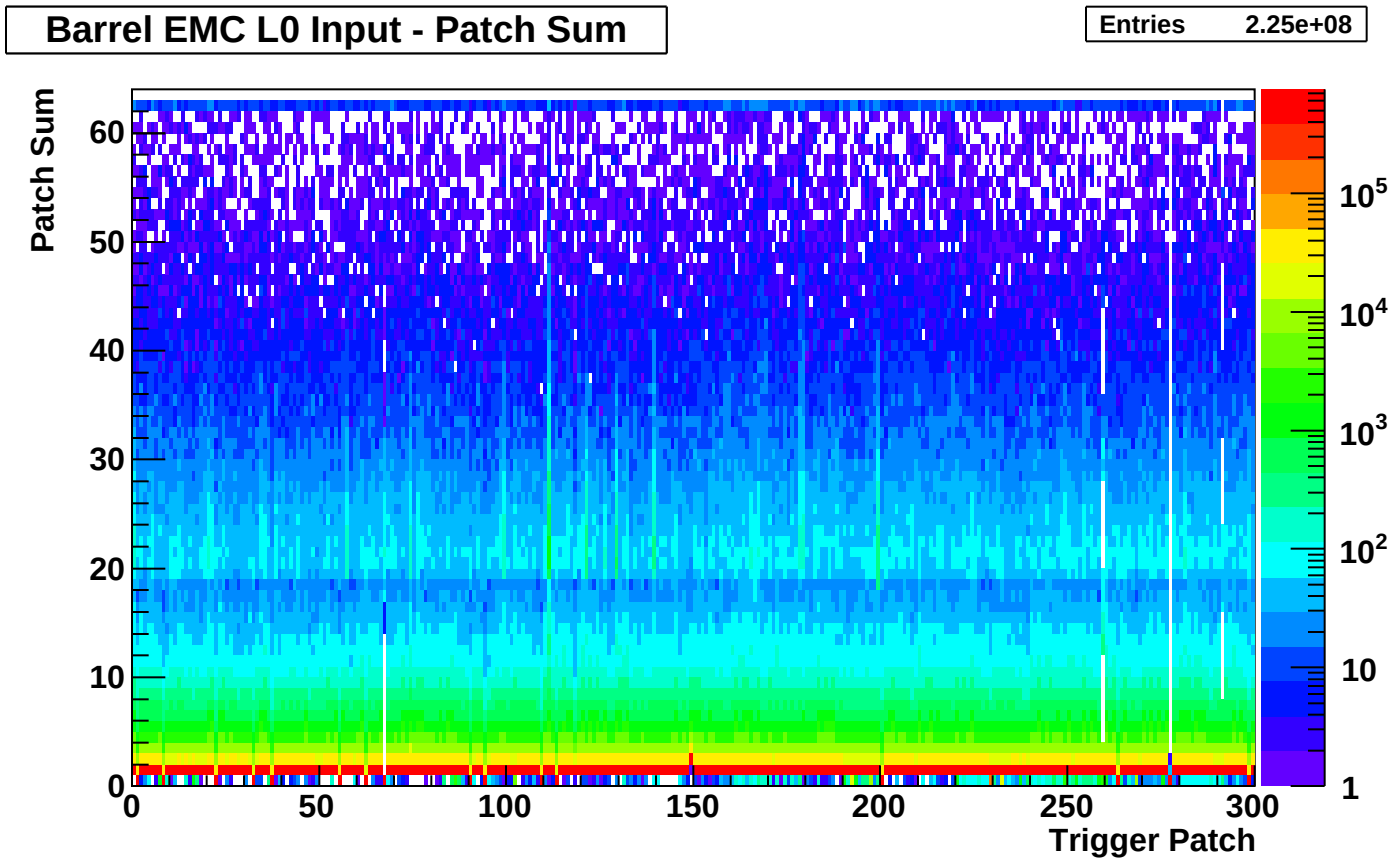

TCU bits
Entries 1.2e+07
TCU bits
750000 750000 750000 750000 750000 750000 750000 750000 750000 750000 750000 750000 750000 750000 750000 750000
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

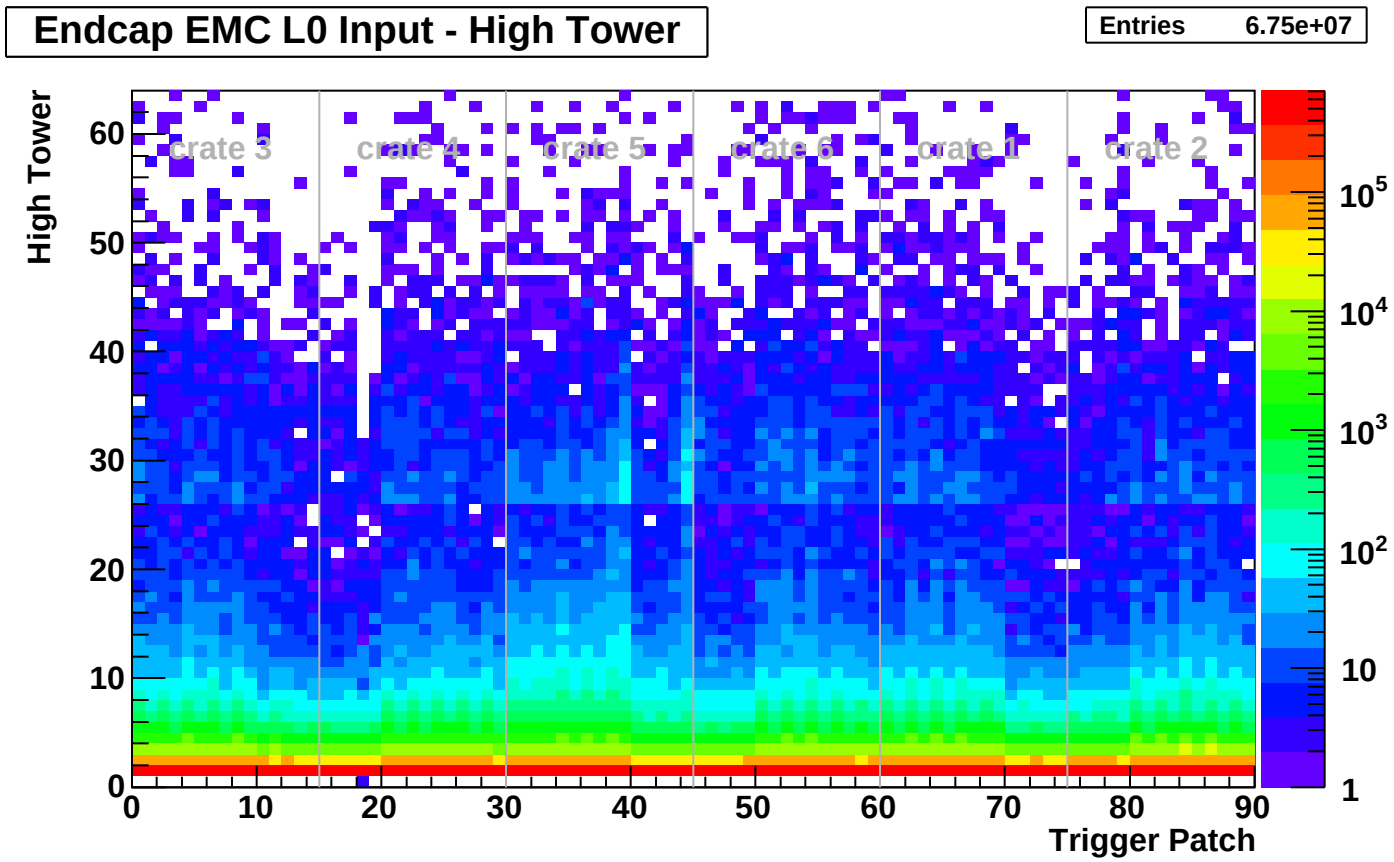

TCU bits
Entries 906872
TCU bits - Simulated
89854 184195 413545 11078 21669 1 182943 3687
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

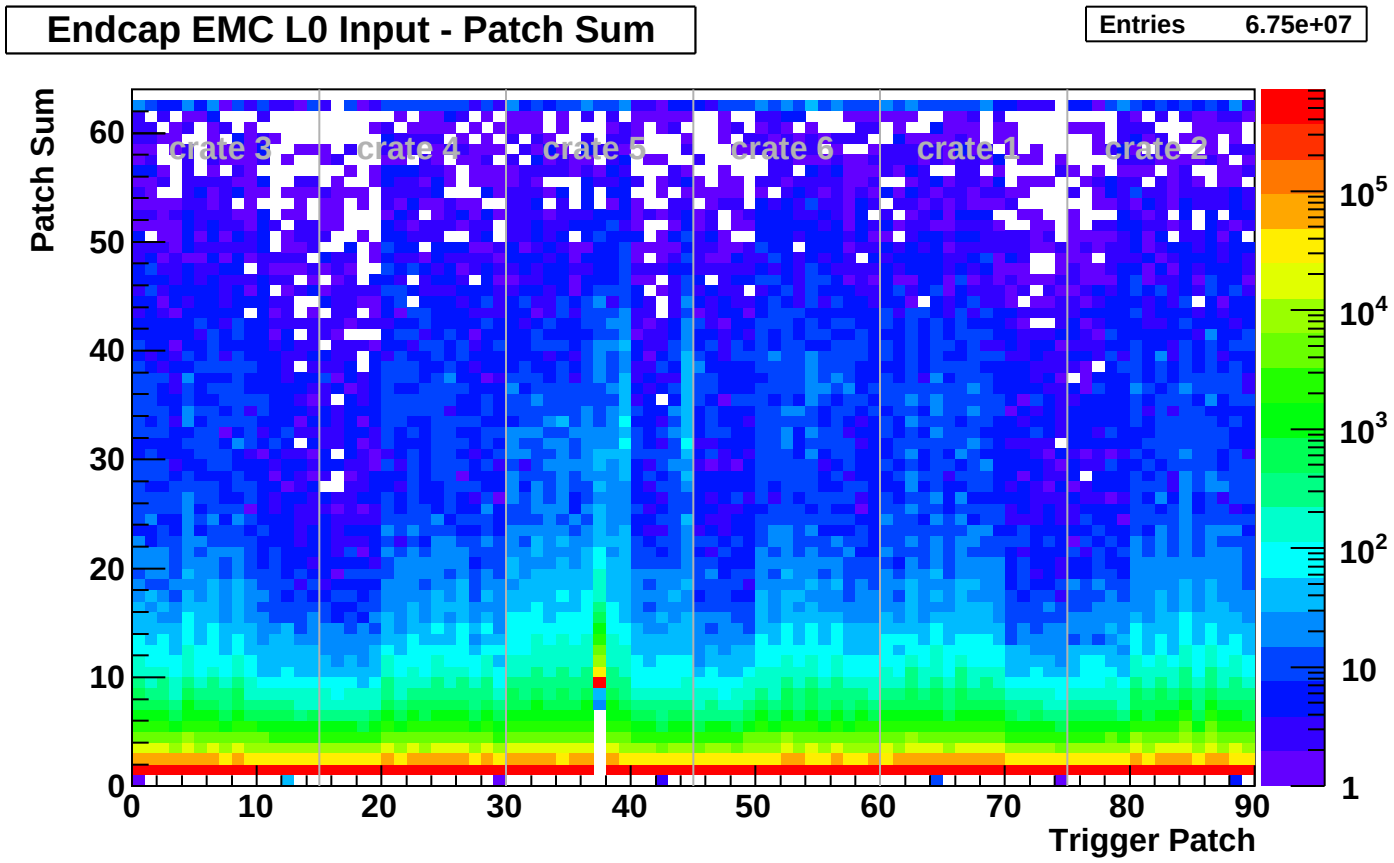

BunchId7Bit (BHT3)
Entries 13822

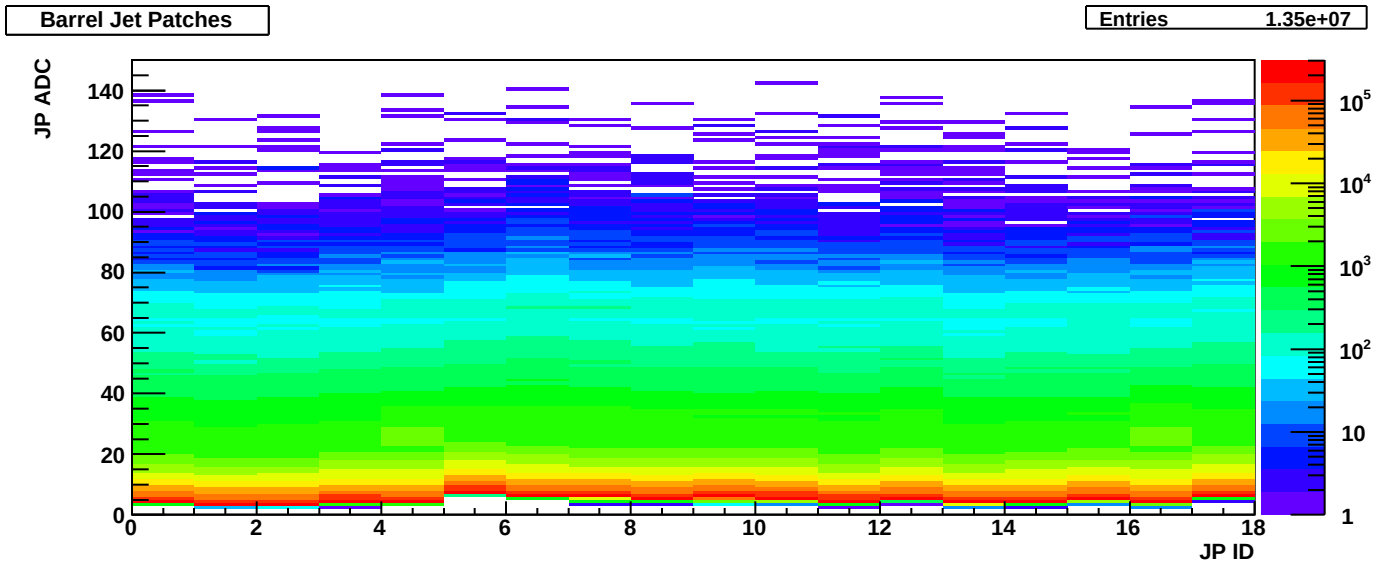

BunchId7Bit (all events)
Entries 750000

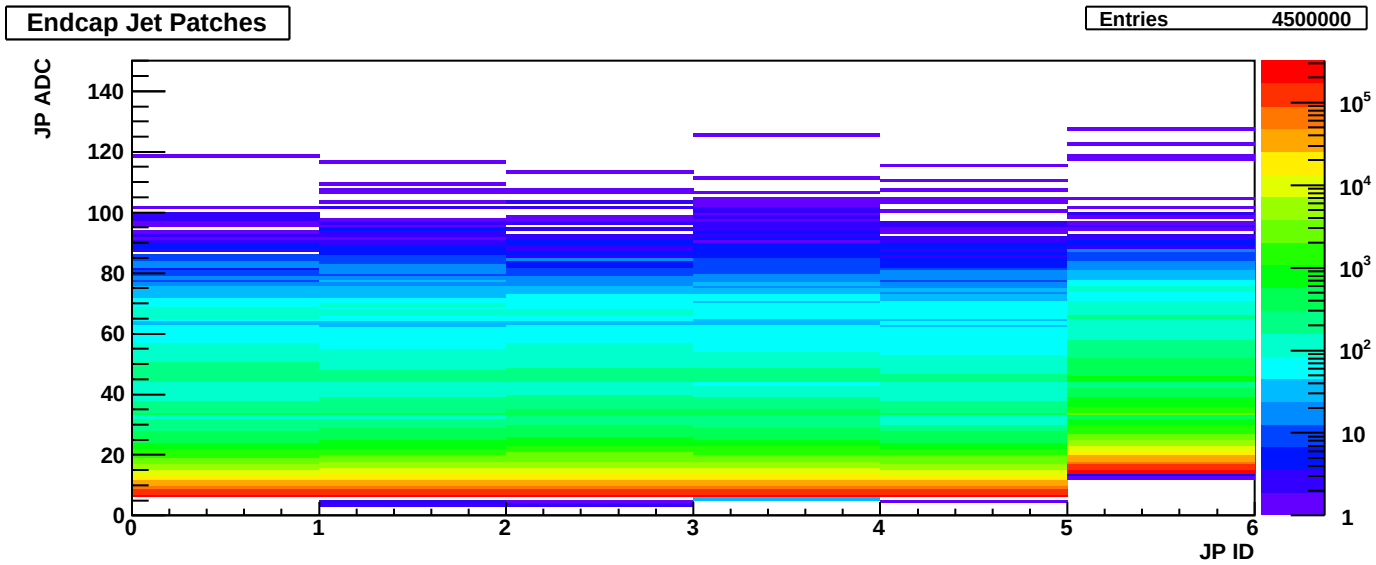

Barrel EMC L0 Input - High Tower
Entries 2.25e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

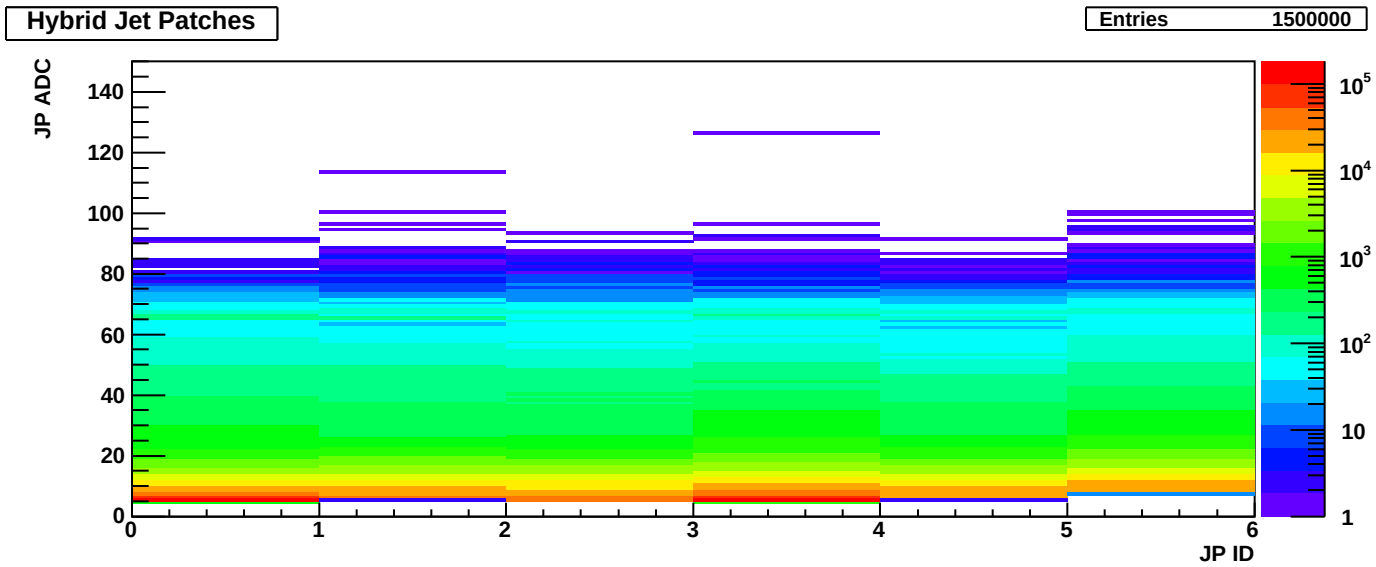

Barrel EMC L0 Input - Patch Sum
Entries 2.25e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

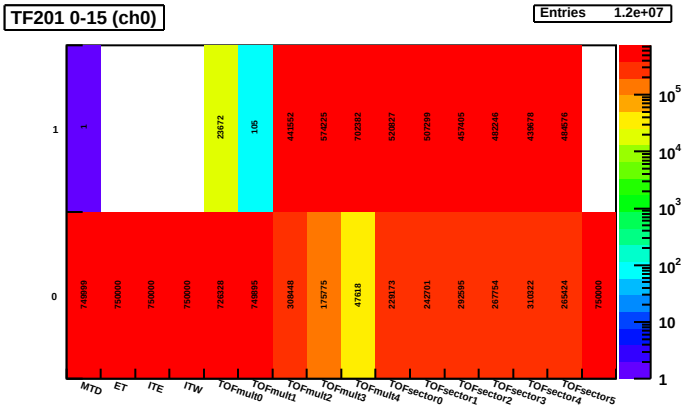

Endcap EMC L0 Input - High Tower
Entries 6.75e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

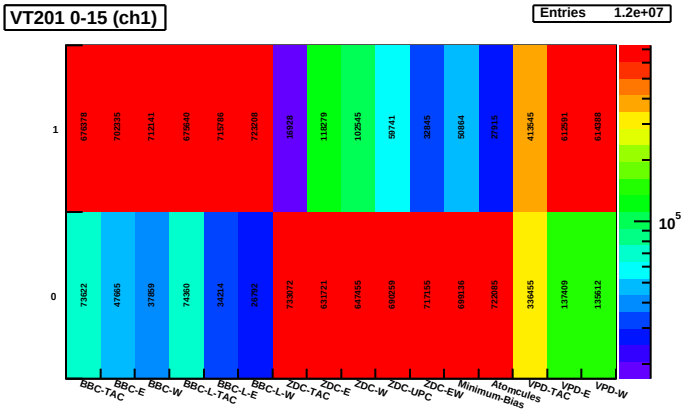

Endcap EMC L0 Input - Patch Sum
Entries 6.75e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

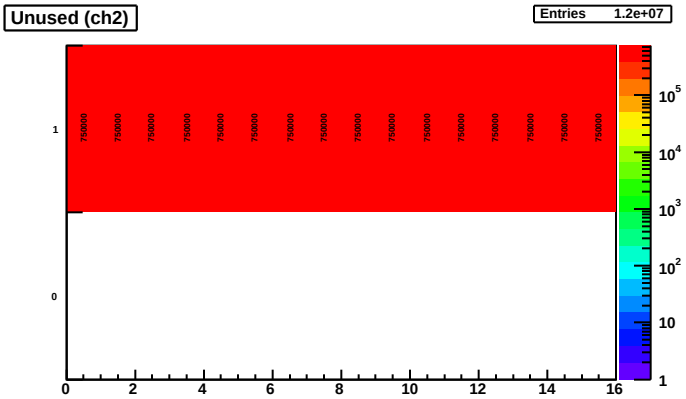

Barrel Jet Patches
Entries 1.35e+07
JP ADC
JP ID

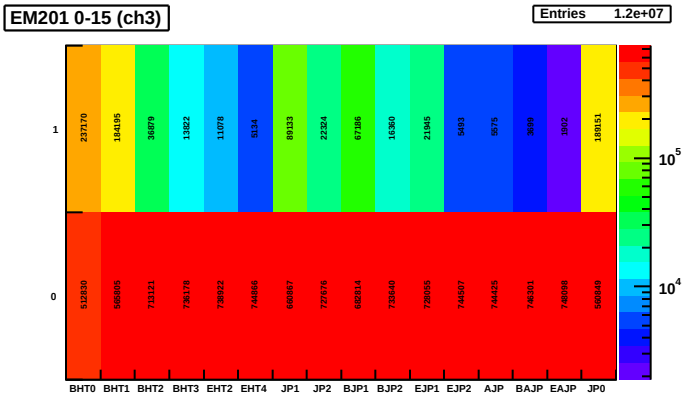

Endcap Jet Patches
Entries 4500000
JP ADC
JP ID

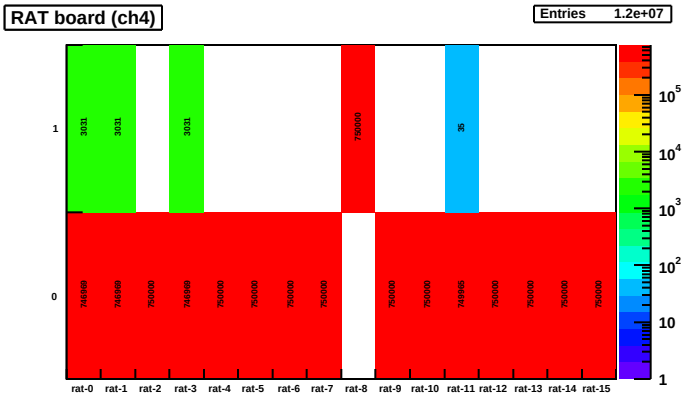

Hybrid Jet Patches
Entries 1500000
JP ADC
JP ID

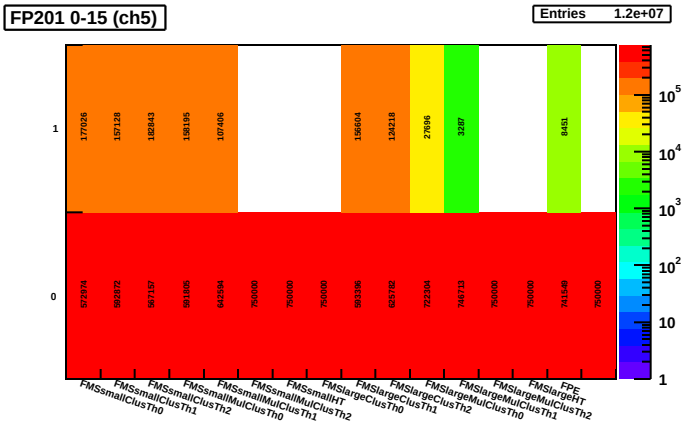

TF201 0-15 (ch0)
Entries 1.2e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

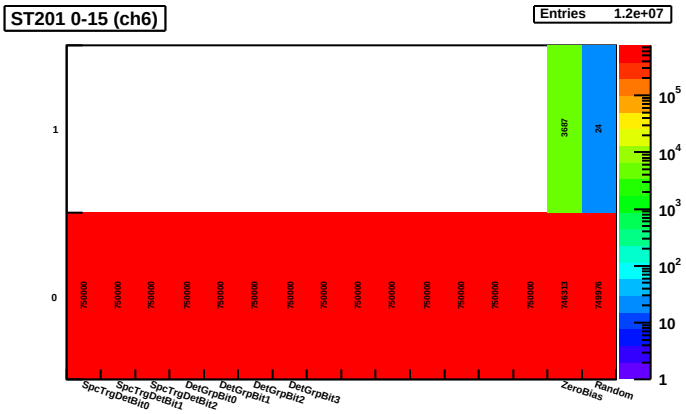

VT201 0-15 (ch1)
Entries 1.2e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

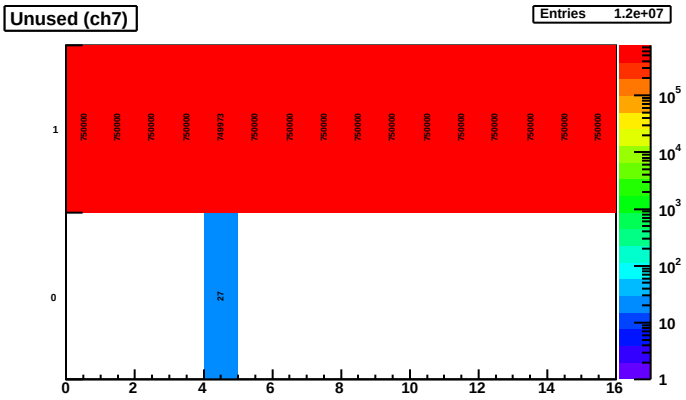

Unused (ch2)
Entries 1.2e+07

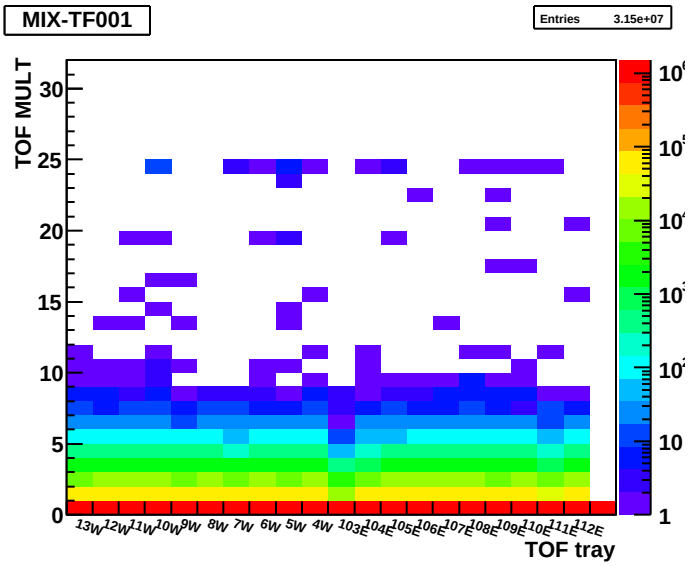

EM201 0-15 (ch3)
Entries 1.2e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

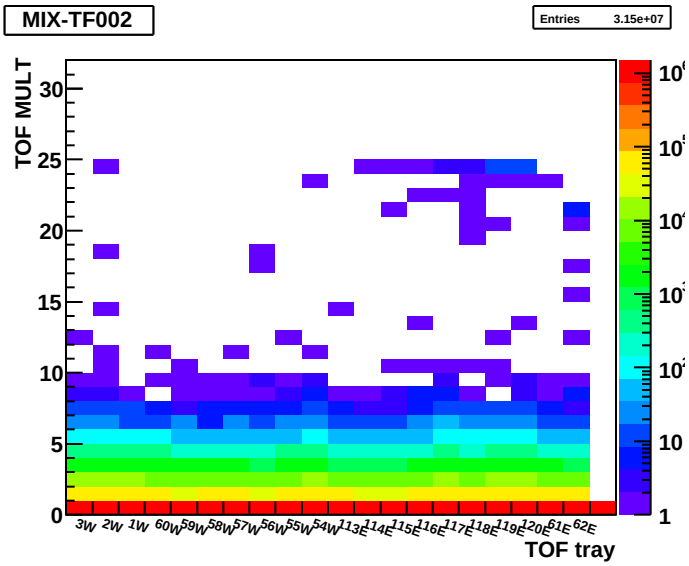

RAT board (ch4)
Entries 1.2e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

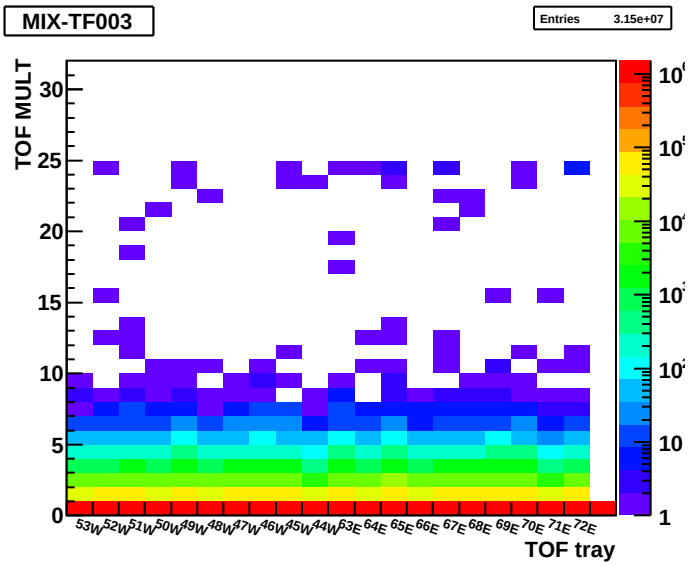

FP201 0-15 (ch5)
Entries 1.2e+07
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

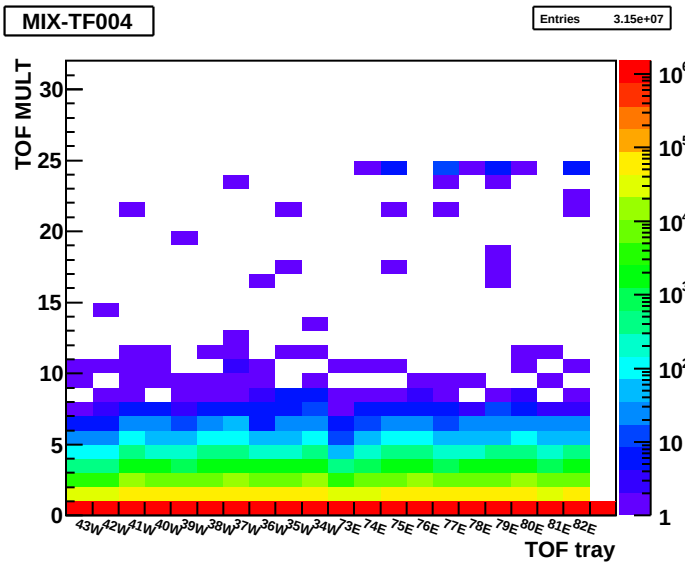

ST201 0-15 (ch6)
Entries 1.2e+07
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

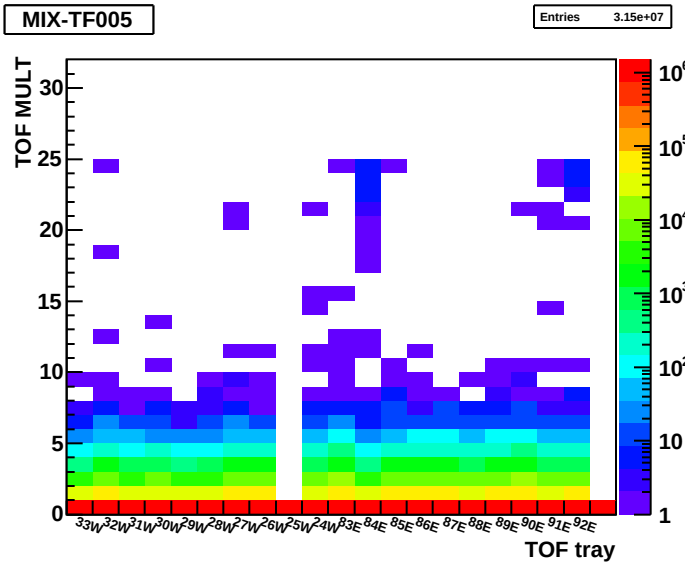

Unused (ch7)
Entries 1.2e+07

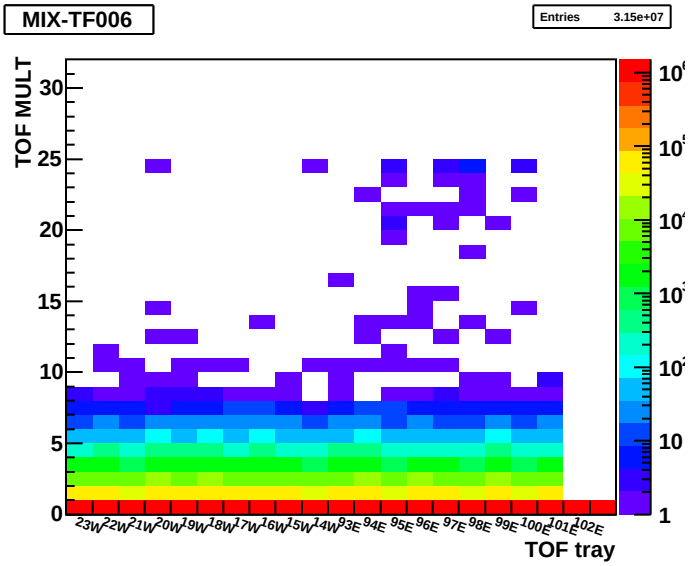

MIX-TF001
Entries 3.15e+07
TOF MULT
TOF tray

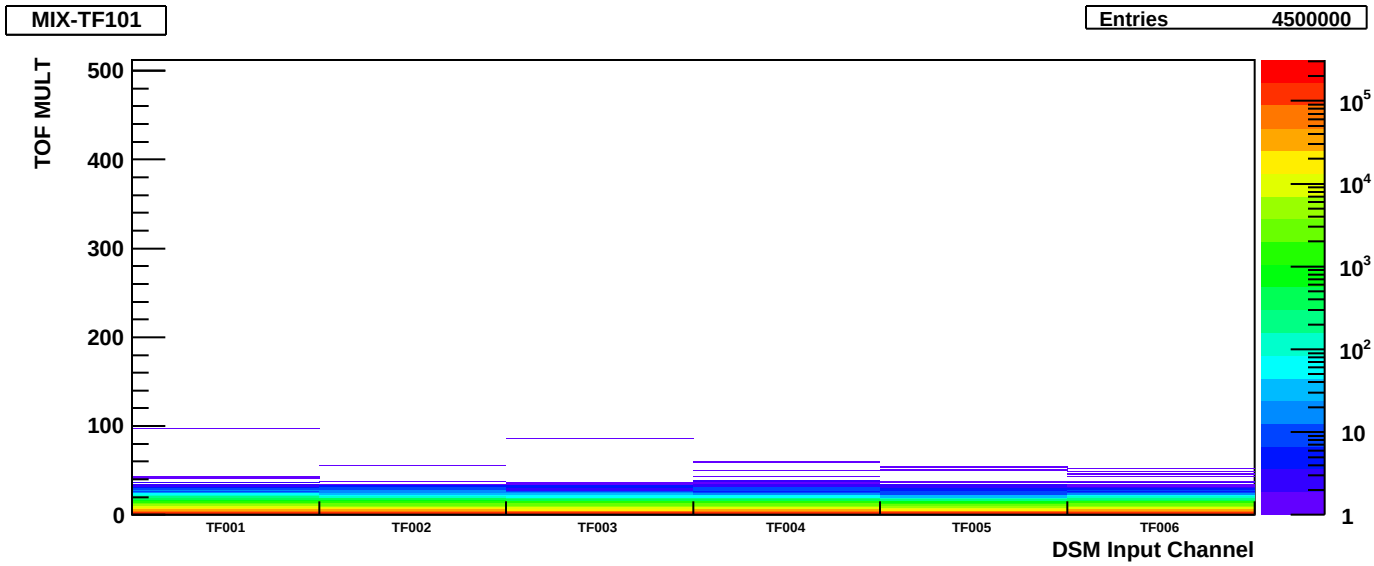

MIX-TF002
Entries 3.15e+07
TOF MULT
TOF tray

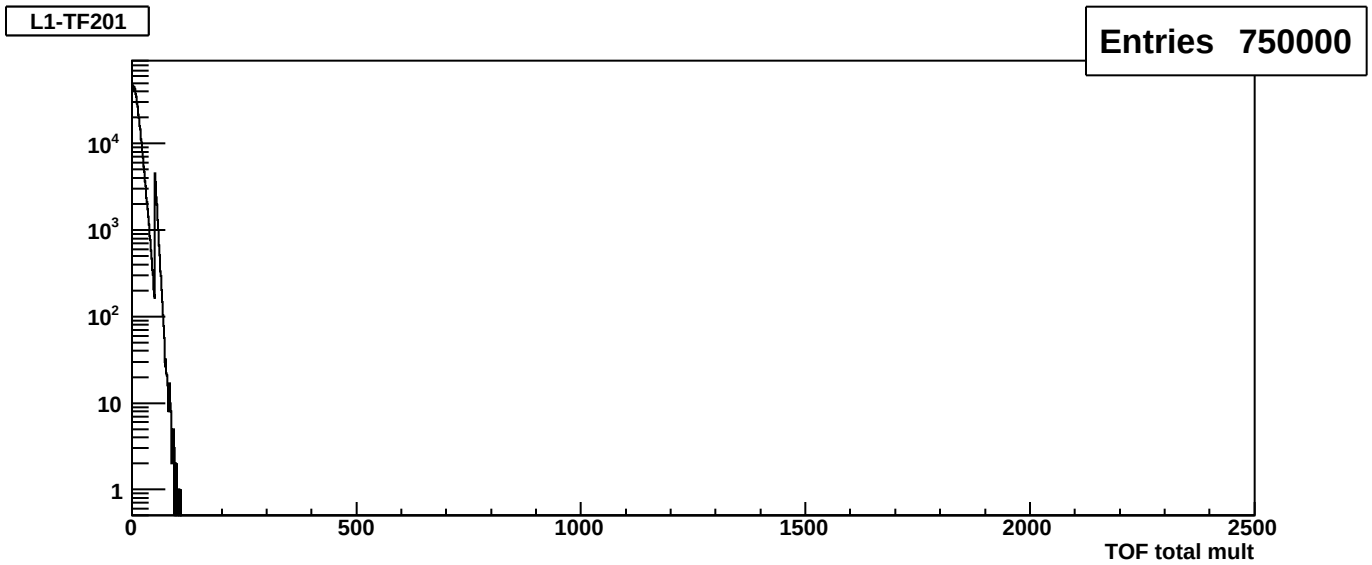

MIX-TF003
Entries 3.15e+07
TOF MULT
TOF tray

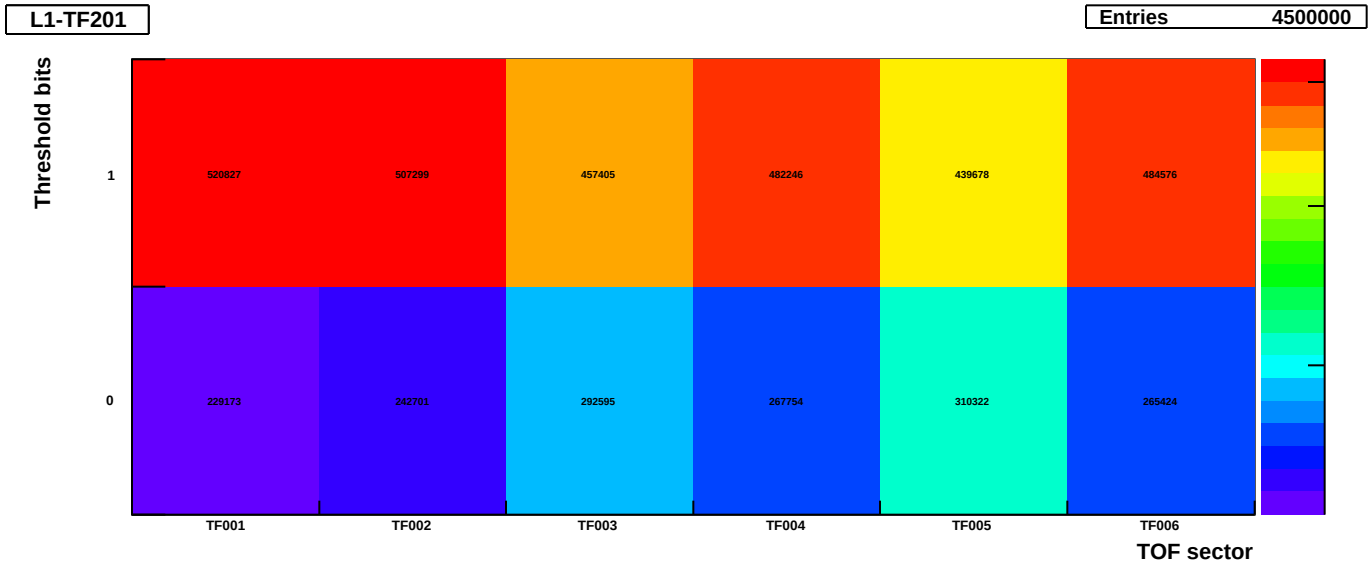

MIX-TF004
Entries 3.15e+07
TOF MULT
TOF tray

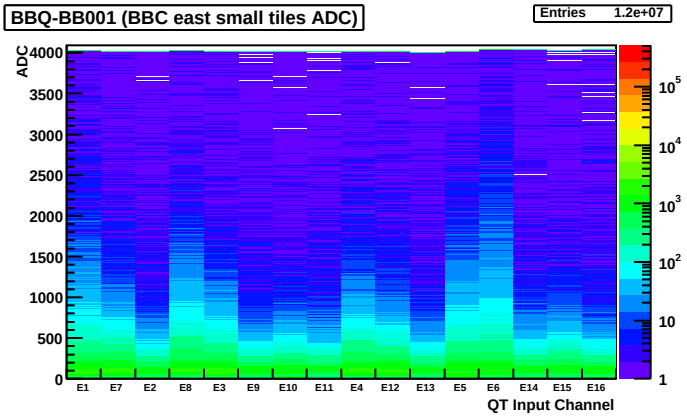

MIX-TF005
Entries 3.15e+07
TOF MULT
TOF tray

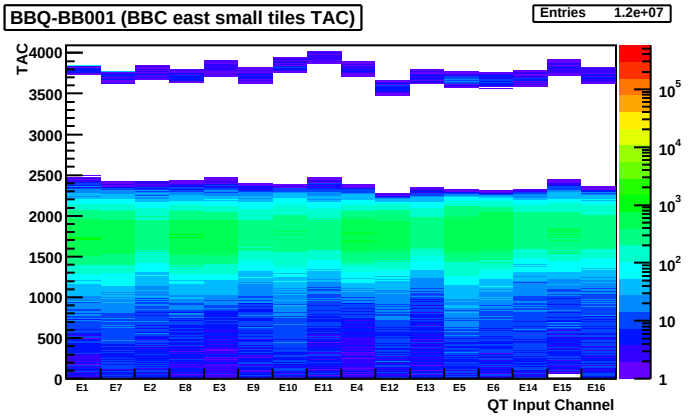

MIX-TF006
Entries 3.15e+07
TOF MULT
TOF tray

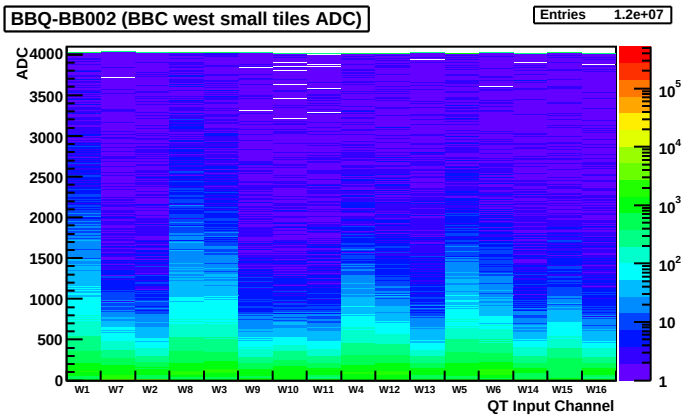

MIX-TF101
Entries 4500000
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

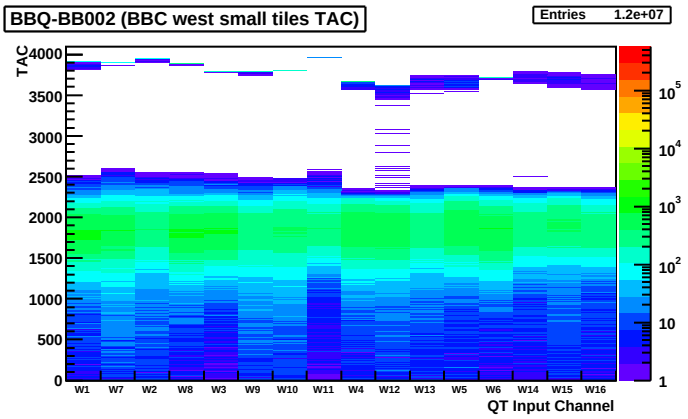

L1-TF201
Entries 750000
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

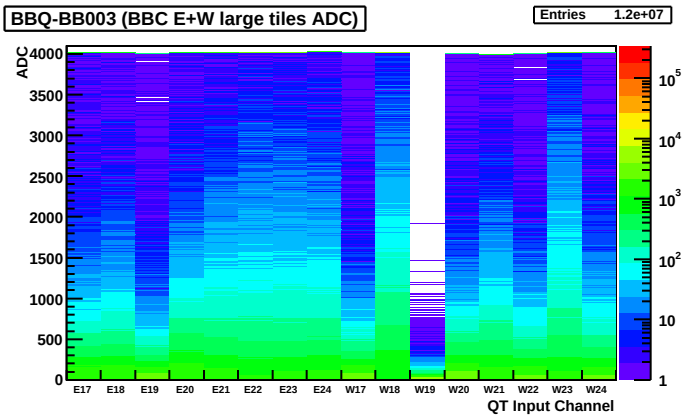

L1-TF201
Entries 4500000
Threshold bits
1
0
520827
507299
457405
482246
439678
484576
229173
242701
292595
267754
310322
265424
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

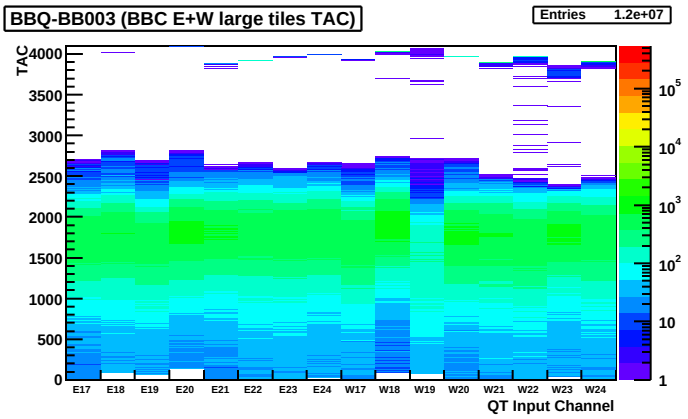

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.2e+07
ADC
QT Input Channel

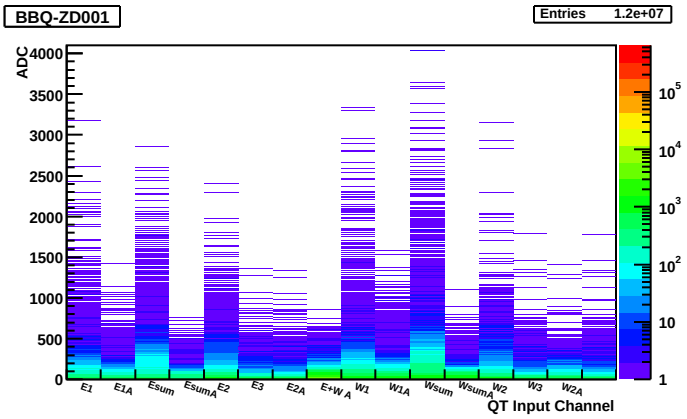

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.2e+07
TAC
QT Input Channel

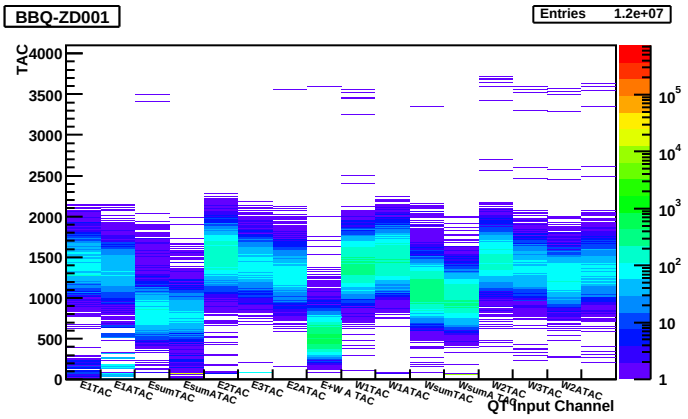

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.2e+07
ADC
QT Input Channel

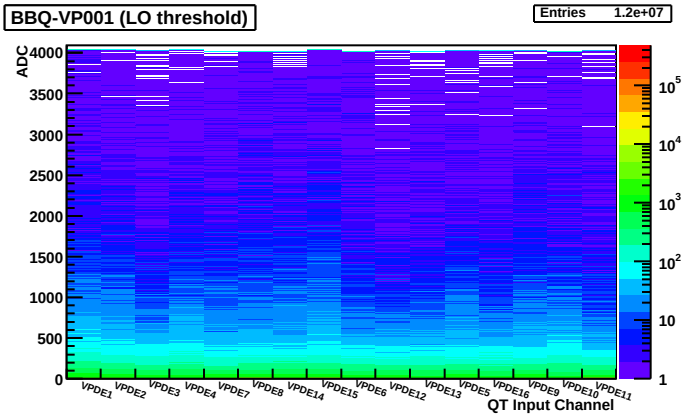

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.2e+07
TAC
QT Input Channel

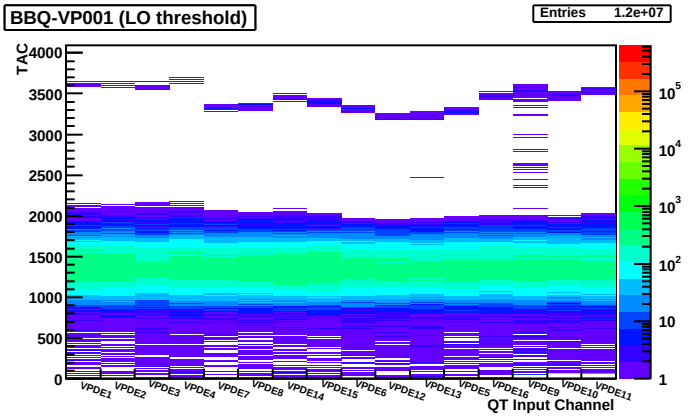

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.2e+07
ADC
QT Input Channel

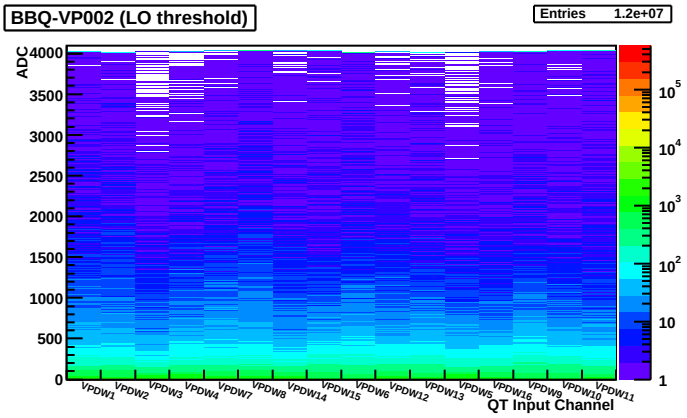

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.2e+07
TAC
QT Input Channel

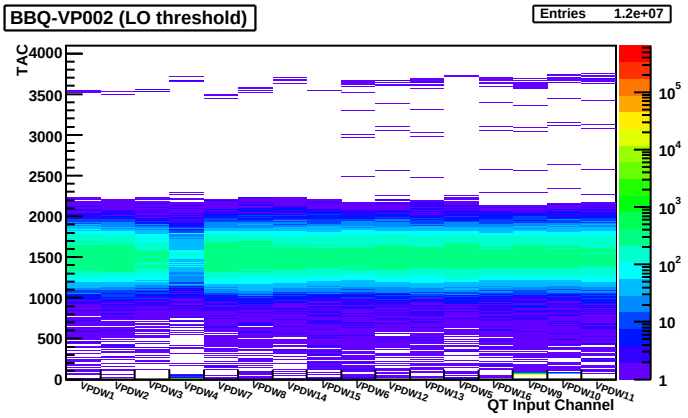

BBQ-ZD001
Entries 1.2e+07
ADC
QT Input Channel

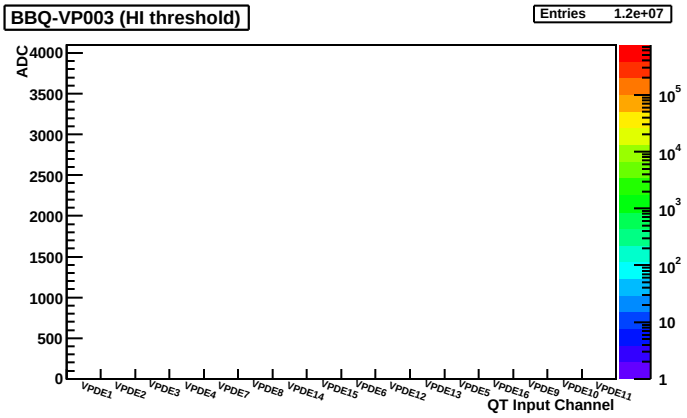

BBQ-ZD001
Entries 1.2e+07
TAC
QT Input Channel

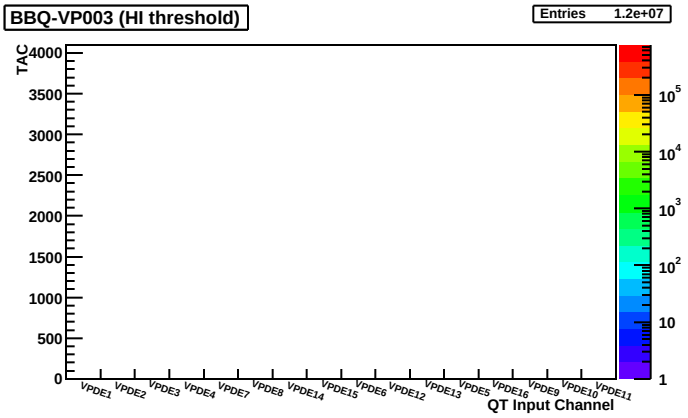

BBQ-VP001 (LO threshold)
Entries 1.2e+07
ADC
QT Input Channel

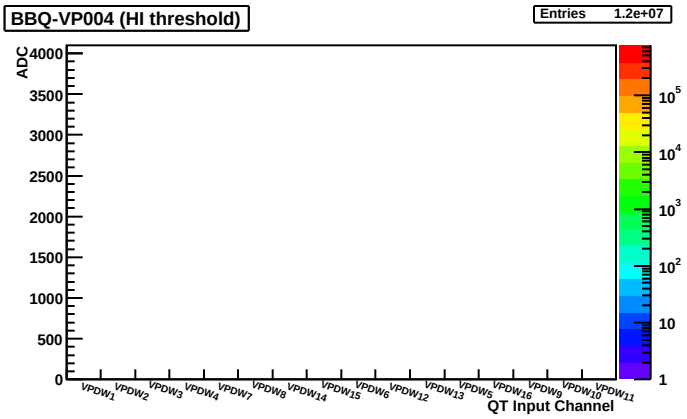

BBQ-VP001 (LO threshold)
Entries 1.2e+07
TAC
QT Input Channel

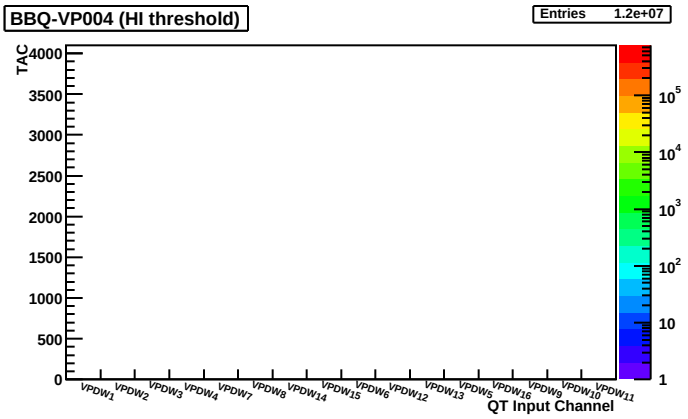

BBQ-VP002 (LO threshold)
Entries 1.2e+07
ADC
QT Input Channel

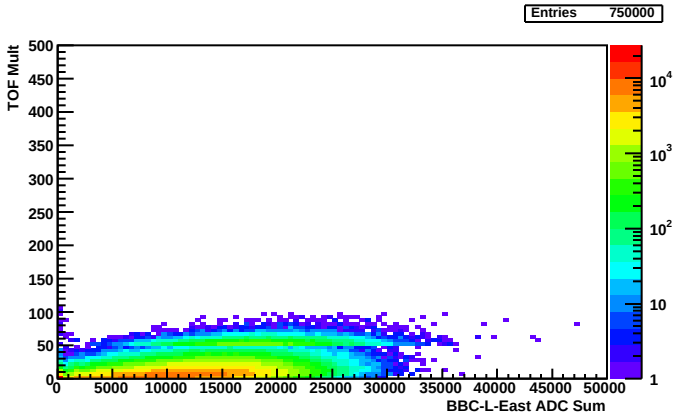

BBQ-VP002 (LO threshold)
Entries 1.2e+07
TAC
QT Input Channel

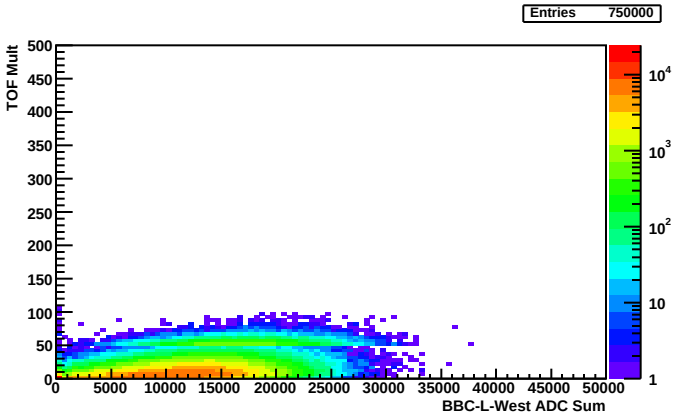

BBQ-VP003 (HI threshold)
Entries 1.2e+07
ADC
QT Input Channel

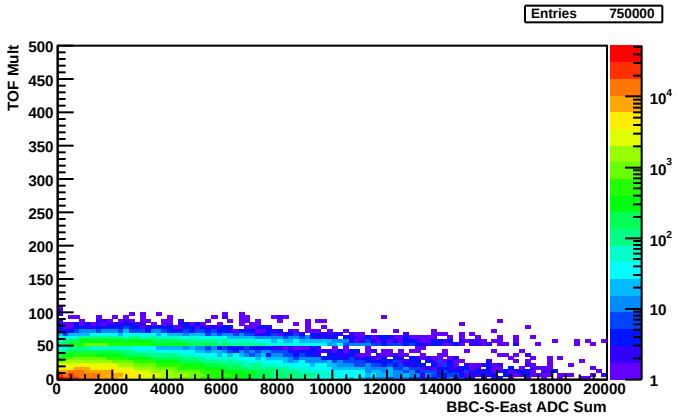

BBQ-VP003 (HI threshold)
Entries 1.2e+07
TAC
QT Input Channel

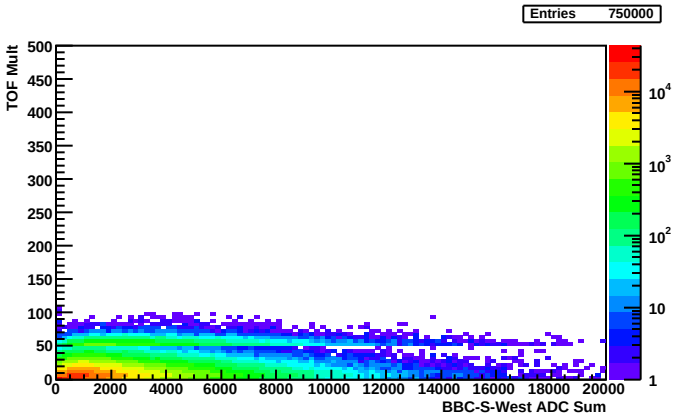

BBQ-VP004 (HI threshold)
Entries 1.2e+07
ADC
QT Input Channel

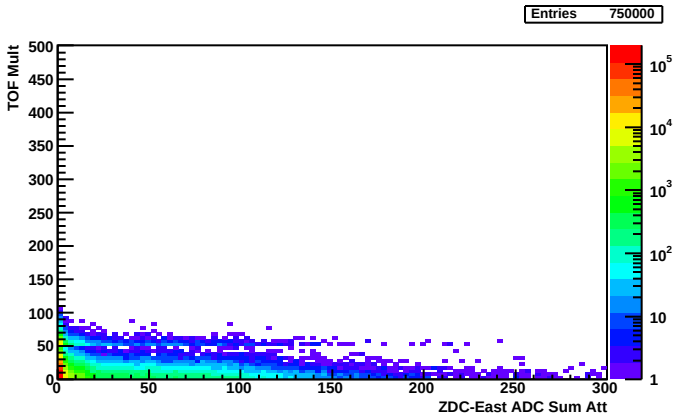

BBQ-VP004 (HI threshold)
Entries 1.2e+07
TAC
QT Input Channel

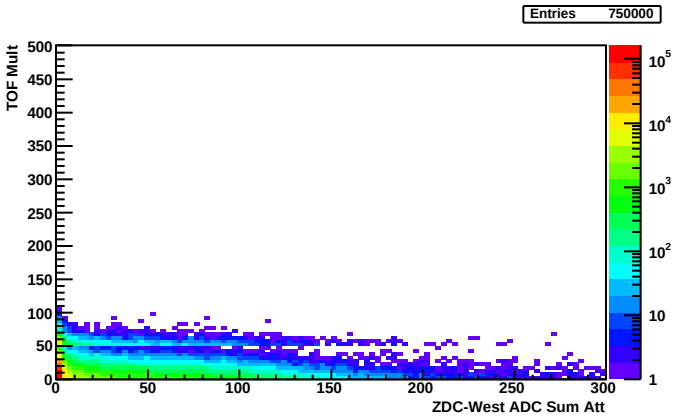

Entries 750000
TOF Mult
BBC-L-East ADC Sum

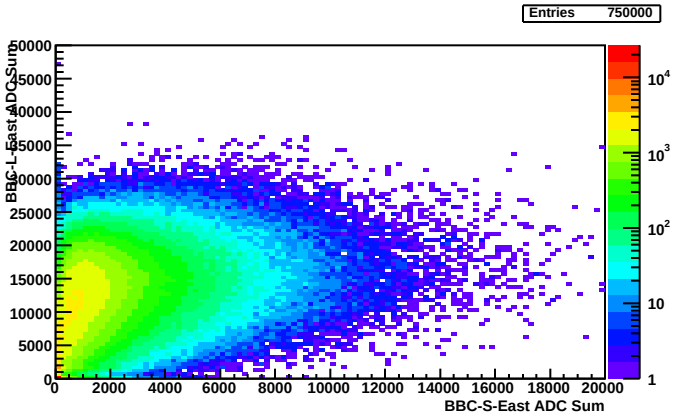

Entries 750000
TOF Mult
BBC-L-West ADC Sum

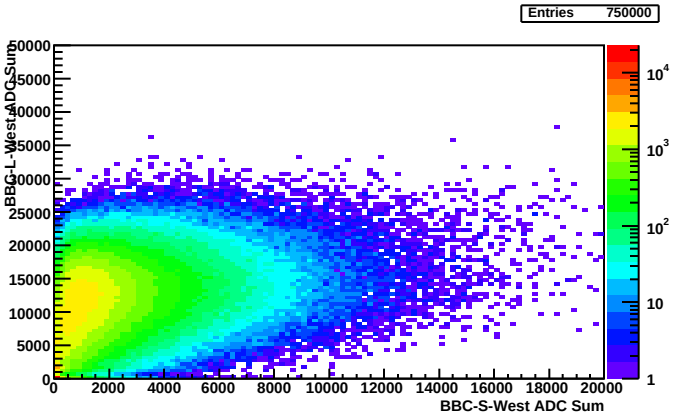

Entries 750000
TOF Mult
BBC-S-East ADC Sum

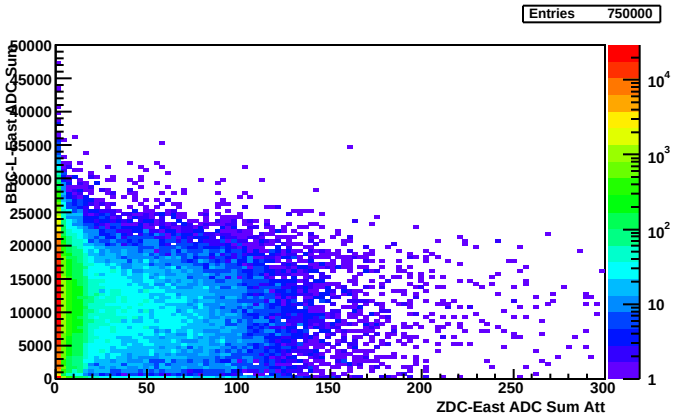

Entries 750000
TOF Mult
BBC-S-West ADC Sum

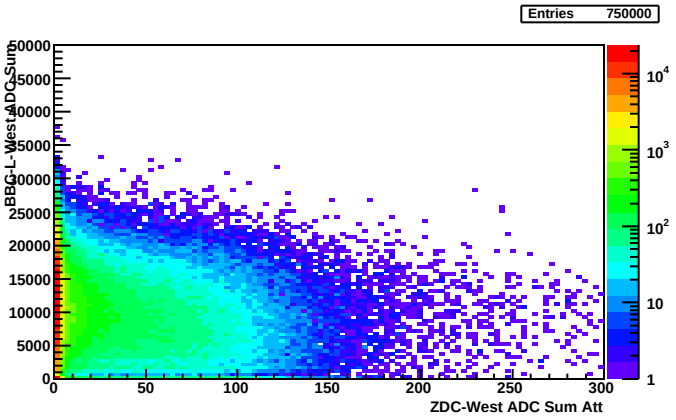

Entries 750000
TOF Mult
ZDC-East ADC Sum Att

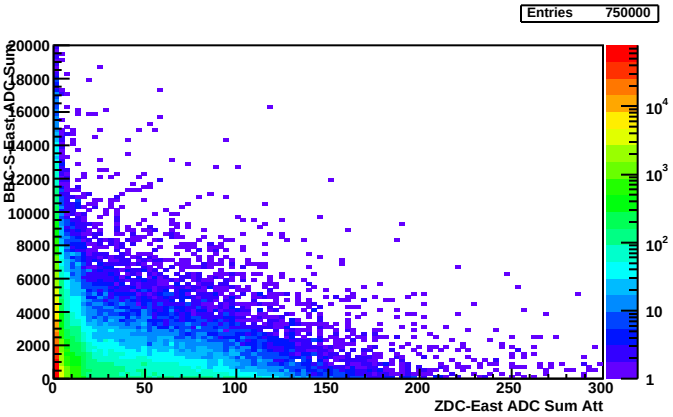

Entries 750000
TOF Mult
ZDC-West ADC Sum Att

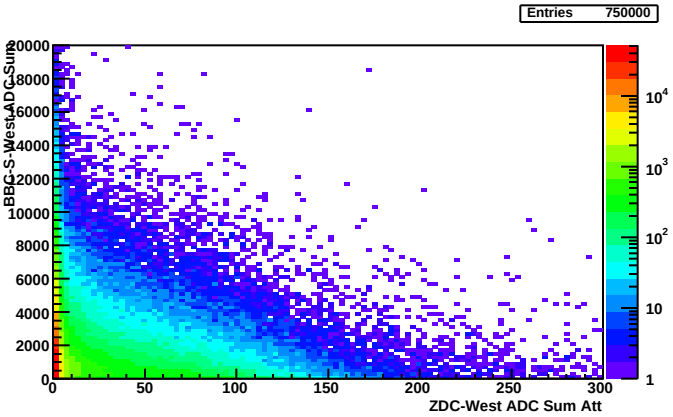

Entries 750000
BBC-L-East ADC Sum
BBC-S-East ADC Sum

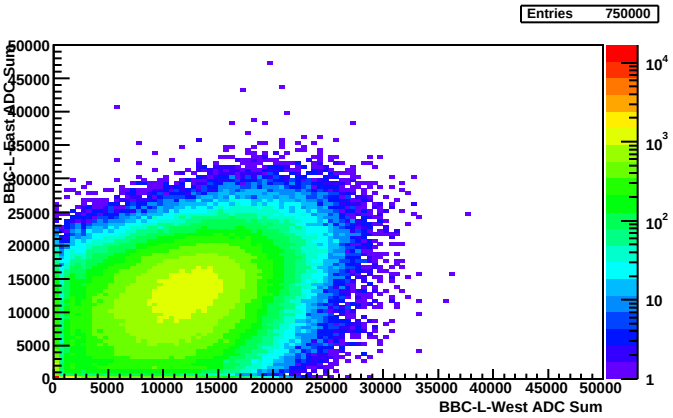

Entries 750000
BBC-L-West ADC Sum
BBC-S-West ADC Sum

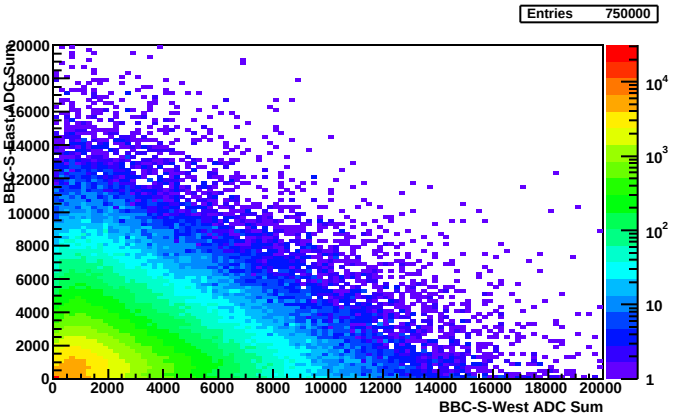

Entries 750000
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

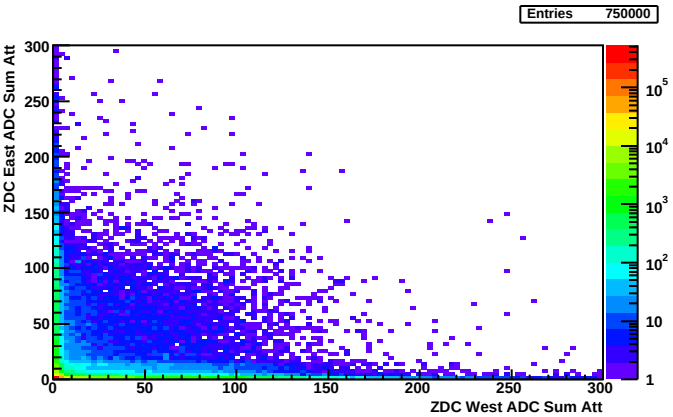

Entries 750000
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

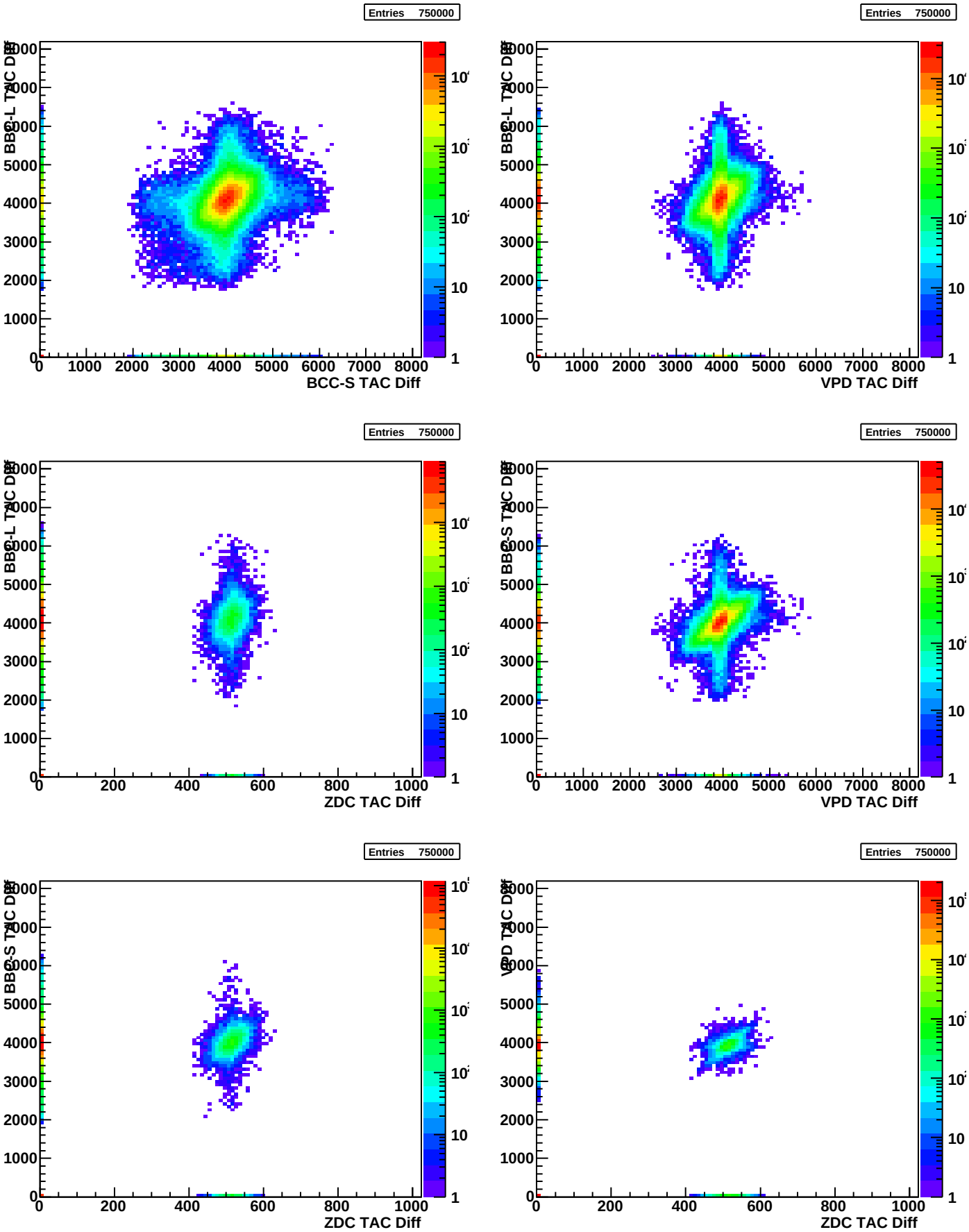

Entries 750000
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

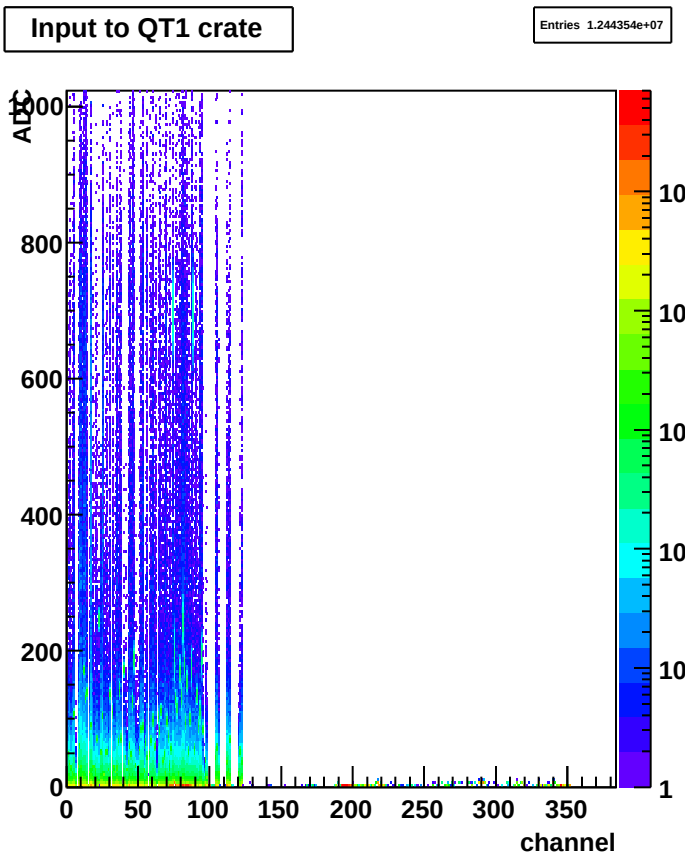

Entries 750000
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

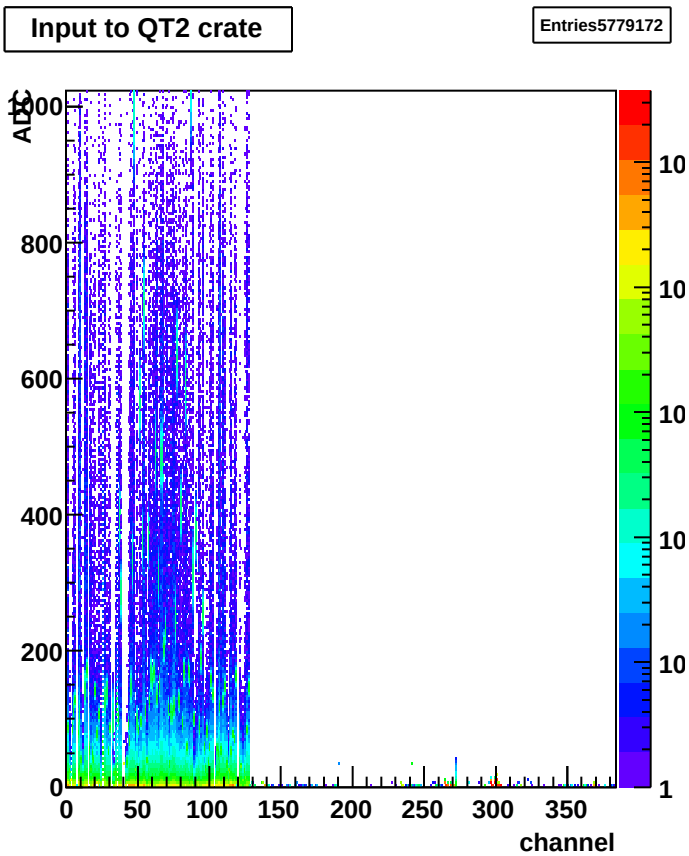

Entries 750000
BBC-L-East ADC Sum
BBC-L-West ADC Sum

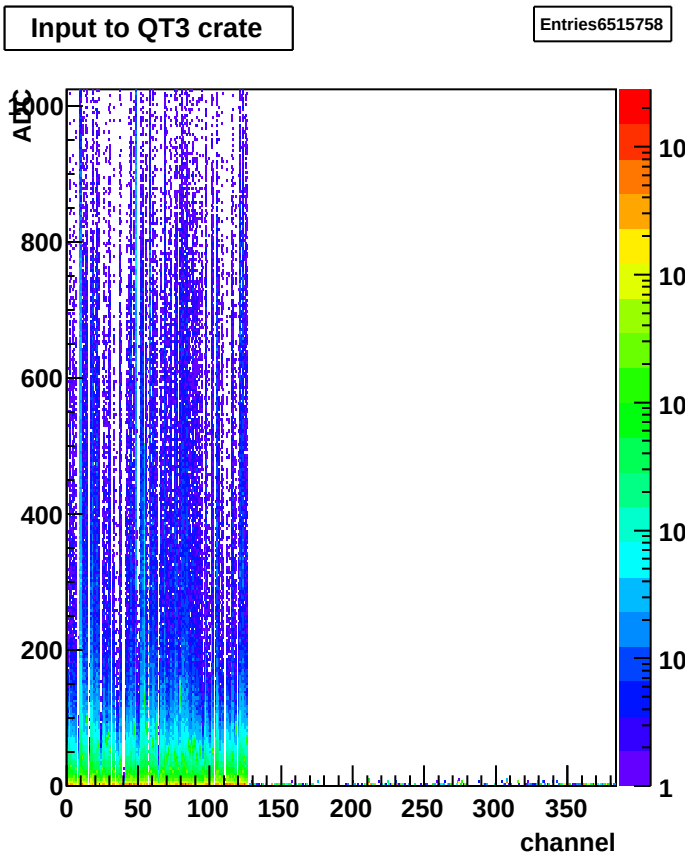

Entries 750000
BBC-S-East ADC Sum
BBC-S-West ADC Sum

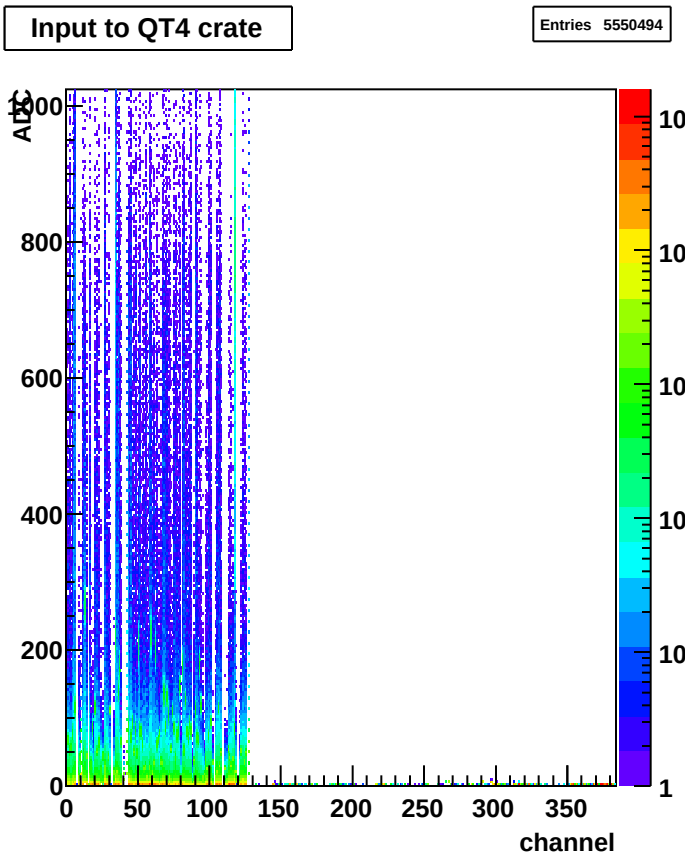

Entries 750000
ZDC East ADC Sum Att
ZDC West ADC Sum Att

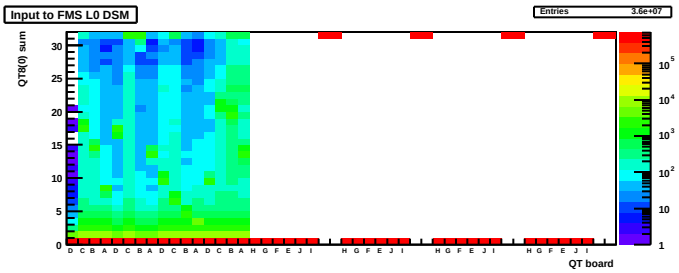

Entries 750000
BBC-L TAC Diff
BCC-S TAC Diff
Entries 750000
BBC-L TAC Diff
VPD TAC Diff
Entries 750000
BBC-L TAC Diff
ZDC TAC Diff
Entries 750000
BBC-S TAC Diff
VPD TAC Diff
Entries 750000
BBC-S TAC Diff
ZDC TAC Diff
Entries 750000
VPD TAC Diff
ZDC TAC Diff

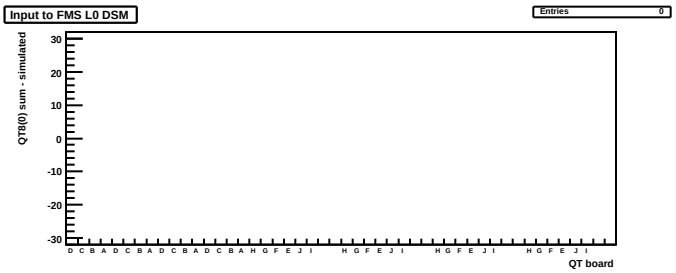

Input to QT1 crate
Entries 1.244354e+07
ADC
channel

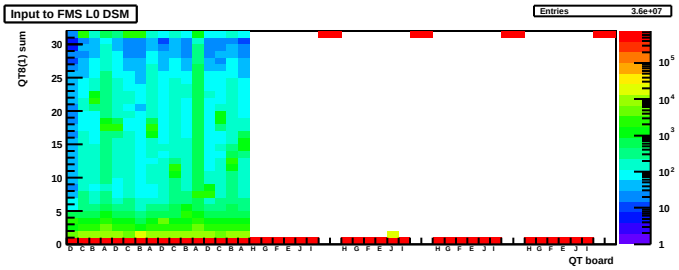

Input to QT2 crate
Entries5779172
ADC
channel

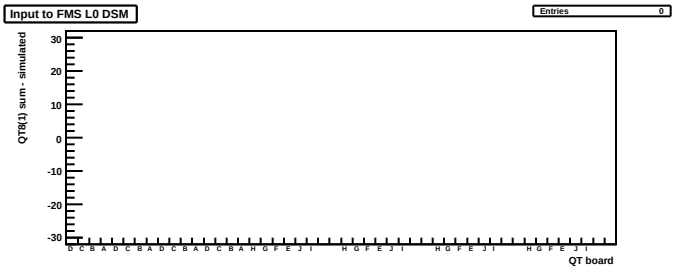

Input to QT3 crate
Entries6515758
ADC
channel

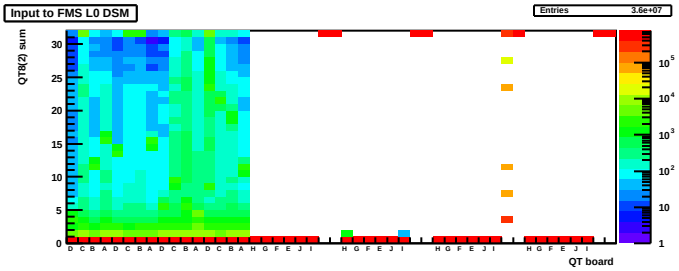

Input to QT4 crate
Entries 5550494
ADC
channel

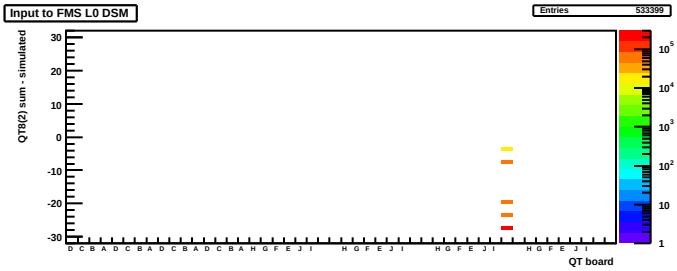

Input to FMS L0 DSM
Entries 3.6e+07
QT8(0) sum
QT board

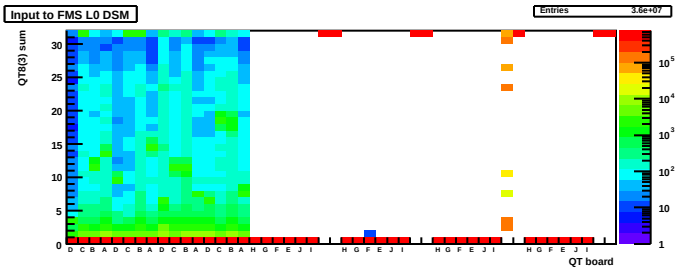

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

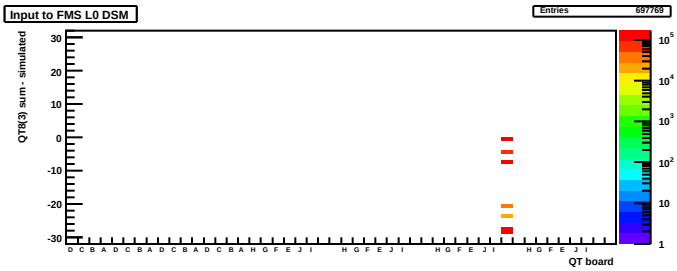

Input to FMS L0 DSM
Entries 3.6e+07
QT8(1) sum
QT board

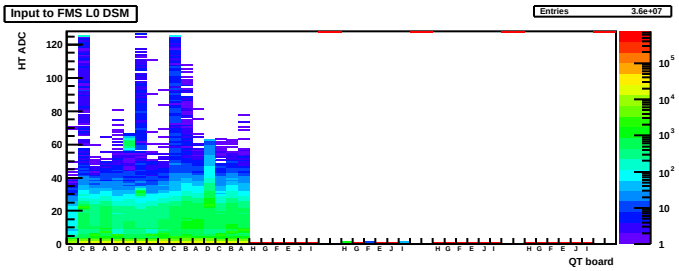

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

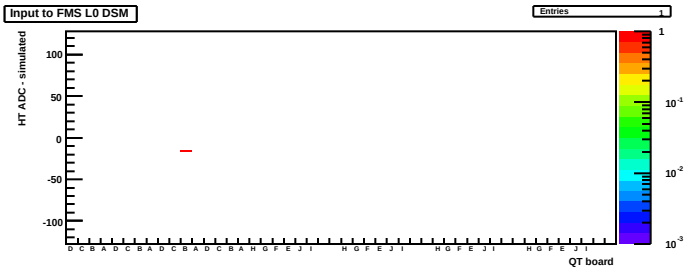

Input to FMS L0 DSM
Entries 3.6e+07
QT8(2) sum
QT board

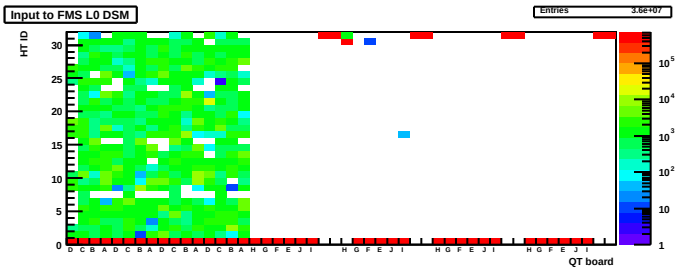

Input to FMS L0 DSM
Entries 533399
QT8(2) sum - simulated
QT board

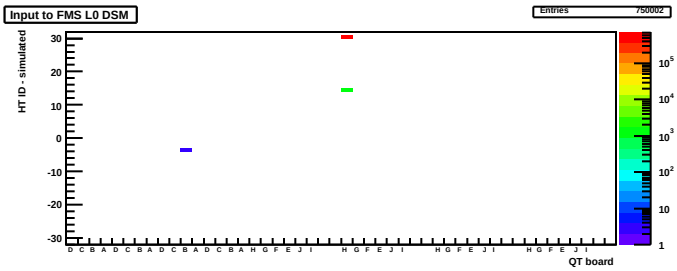

Input to FMS L0 DSM
Entries 3.6e+07
QT8(3) sum
QT board

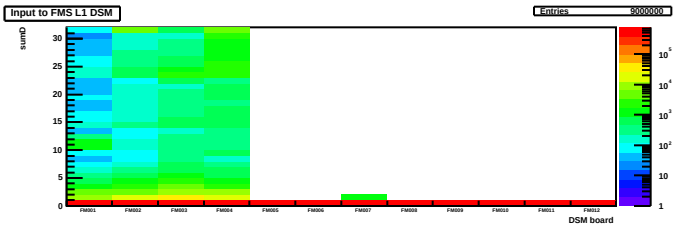

Input to FMS L0 DSM
Entries 697769
QT8(3) sum - simulated
QT board

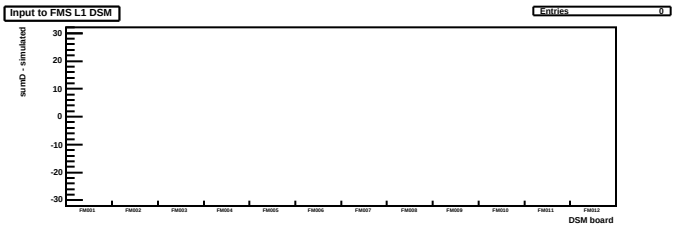

Input to FMS L0 DSM
Entries 3.6e+07
HT ADC
QT board

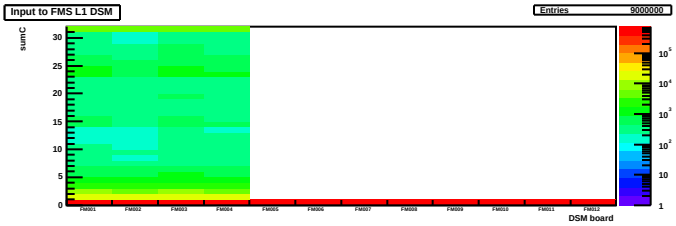

Input to FMS L0 DSM
Entries 1
HT ADC - simulated
QT board

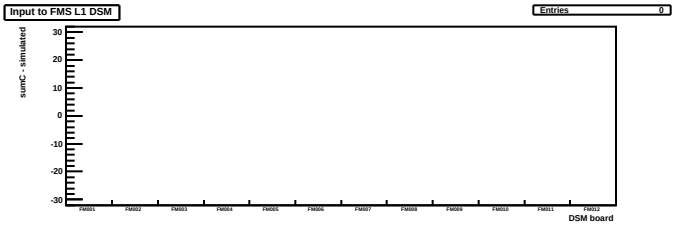

Input to FMS L0 DSM
Entries 3.6e+07
HT ID
QT board

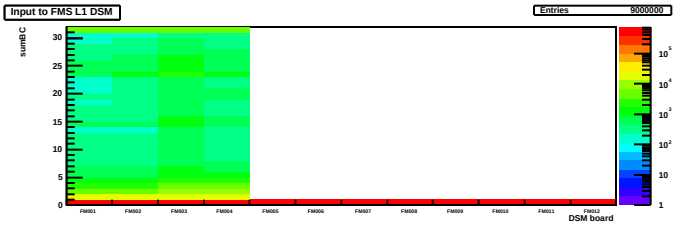

Input to FMS L0 DSM
Entries 750002
HT ID - simulated
QT board

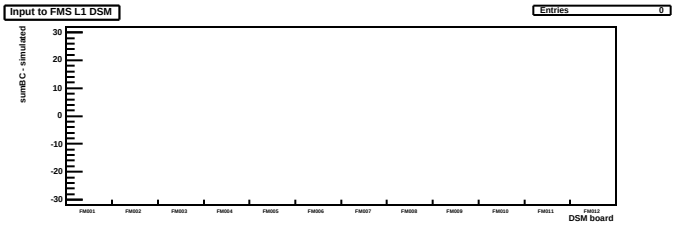

Input to FMS L1 DSM
Entries 9000000
sumD
DSM board

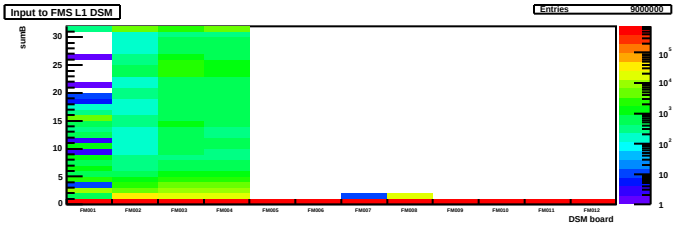

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

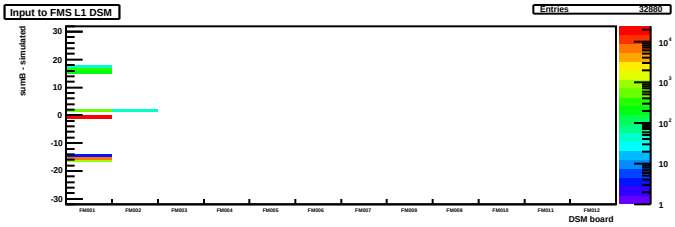

Input to FMS L1 DSM
Entries 9000000
sumC
DSM board

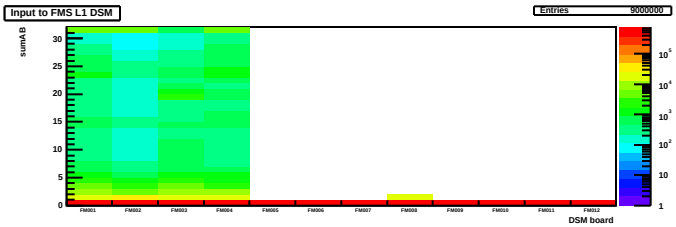

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

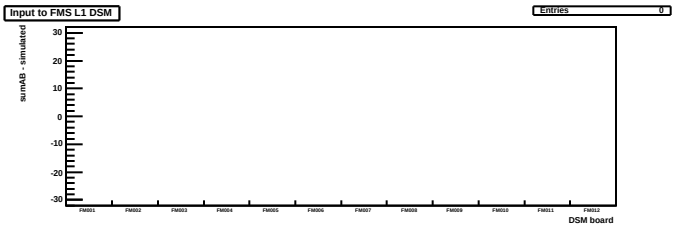

Input to FMS L1 DSM
Entries 9000000
sumBC
DSM board

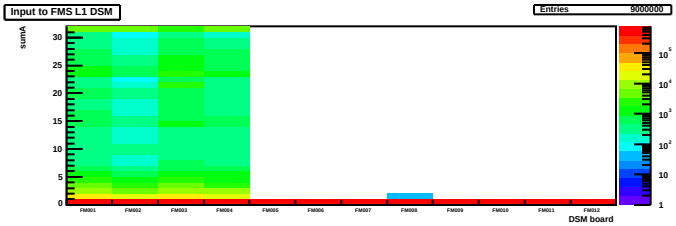

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

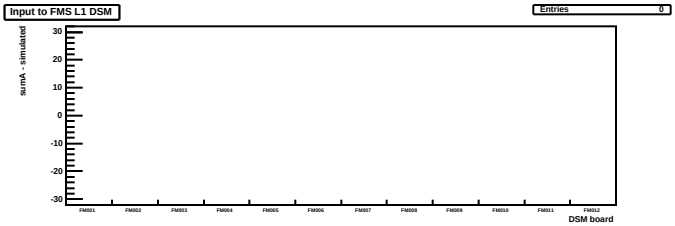

Input to FMS L1 DSM
Entries 9000000
sumB
DSM board

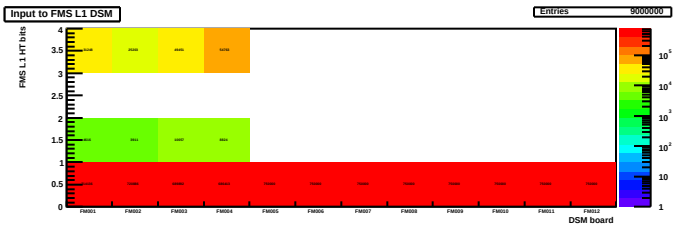

Input to FMS L1 DSM
Entries 32880
sumB - simulated
DSM board

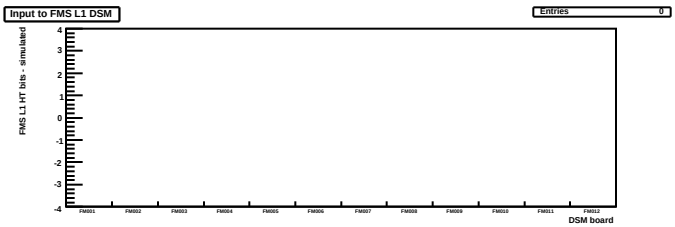

Input to FMS L1 DSM
Entries 9000000
sumAB
DSM board

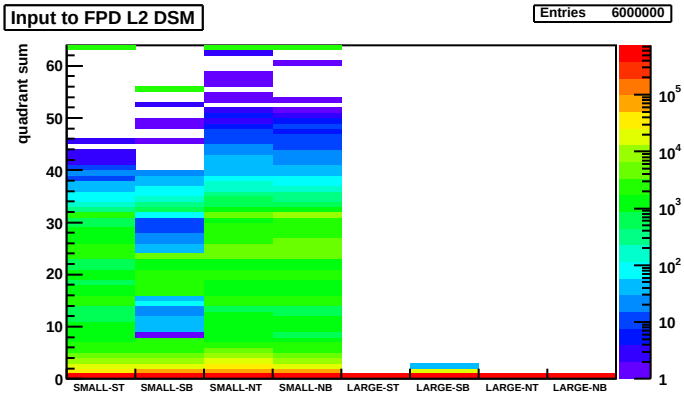

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

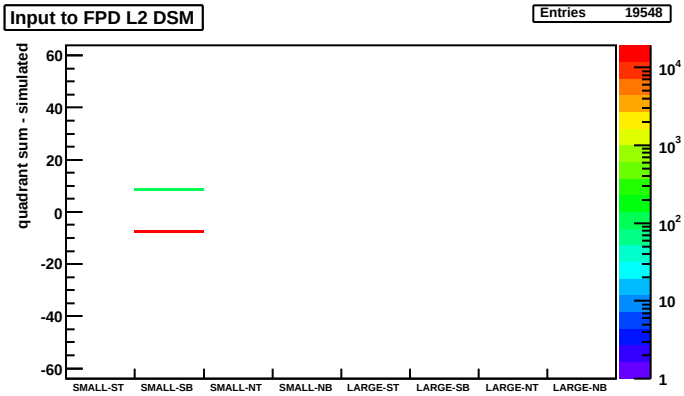

Input to FMS L1 DSM
Entries 9000000
sumA
DSM board

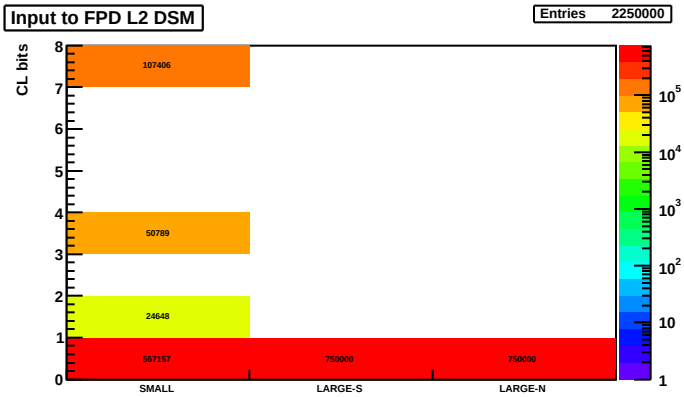

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

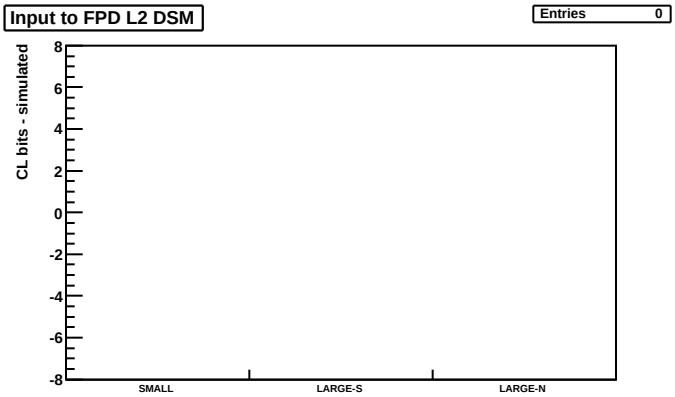

Input to FMS L1 DSM
Entries 9000000
FMS L1 HT bits
DSM board

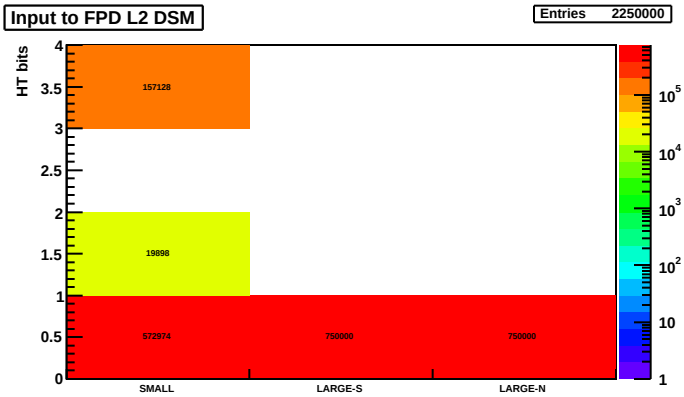

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

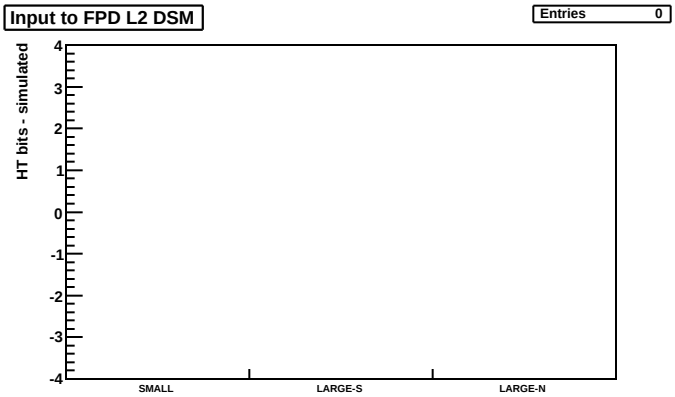

Input to FPD L2 DSM
Entries 6000000
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

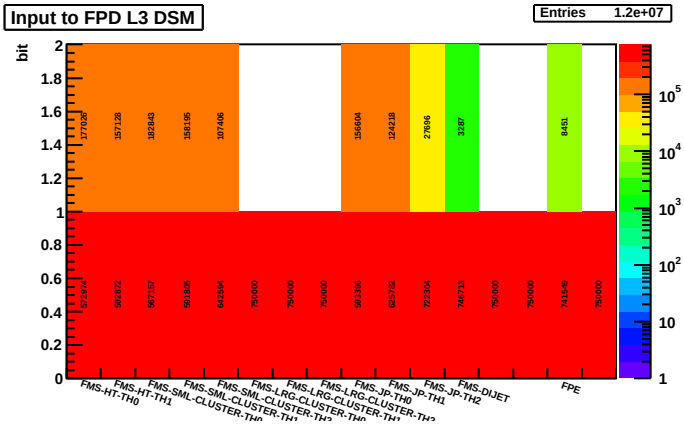

Input to FPD L2 DSM
Entries 19548
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

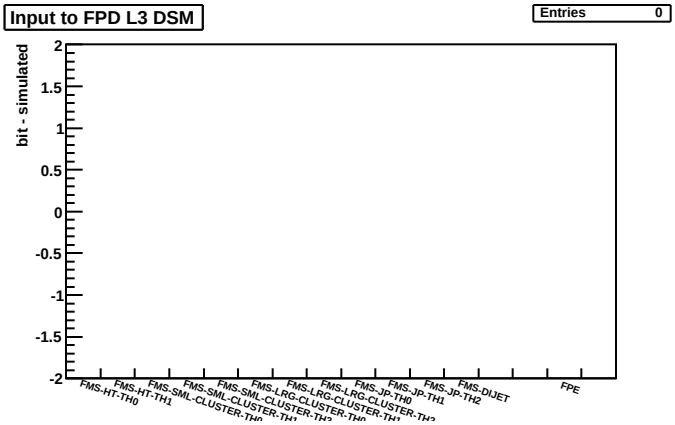

Input to FPD L2 DSM
Entries 2250000
CL bits
107406
50789
24648
567157
750000
750000
SMALL
LARGE-S
LARGE-N

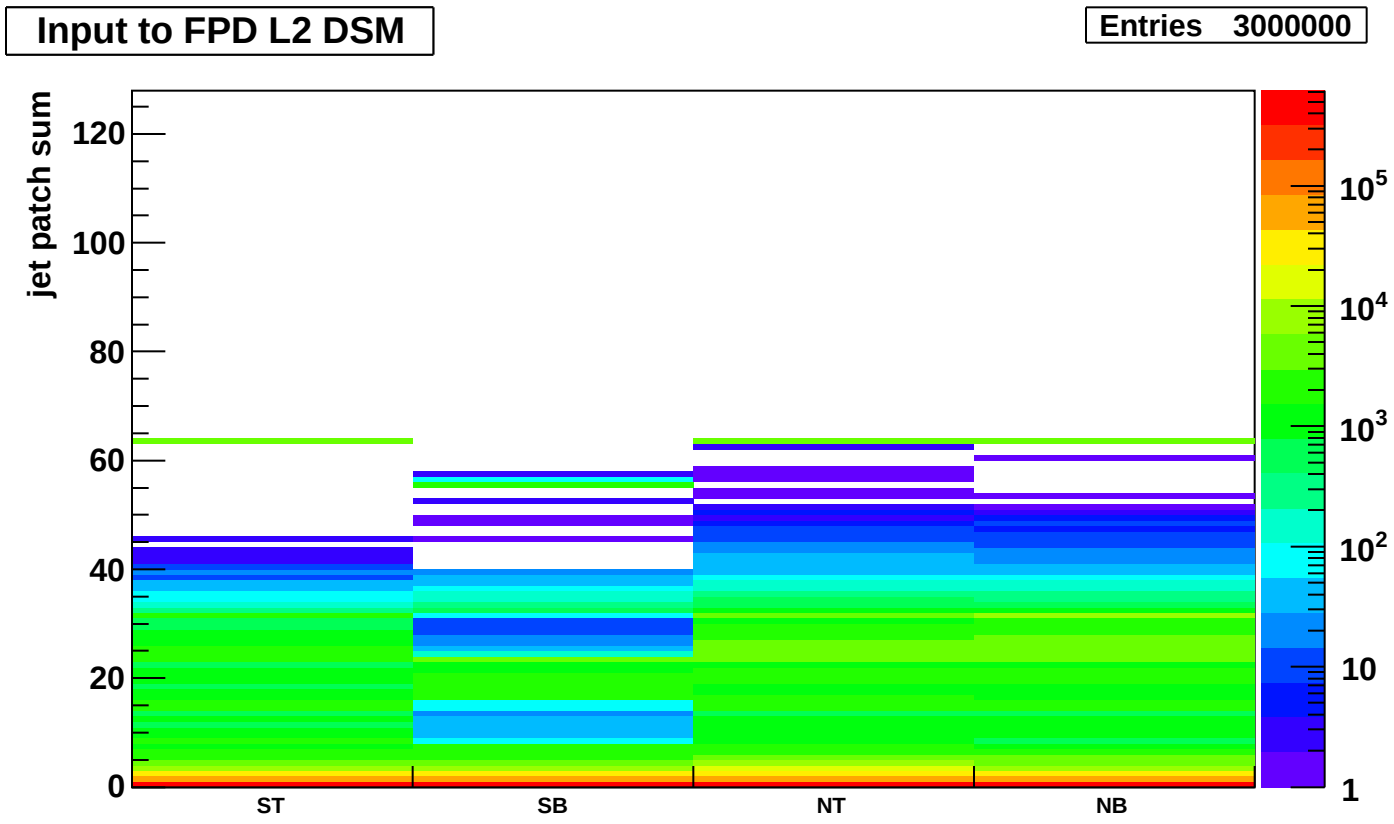

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

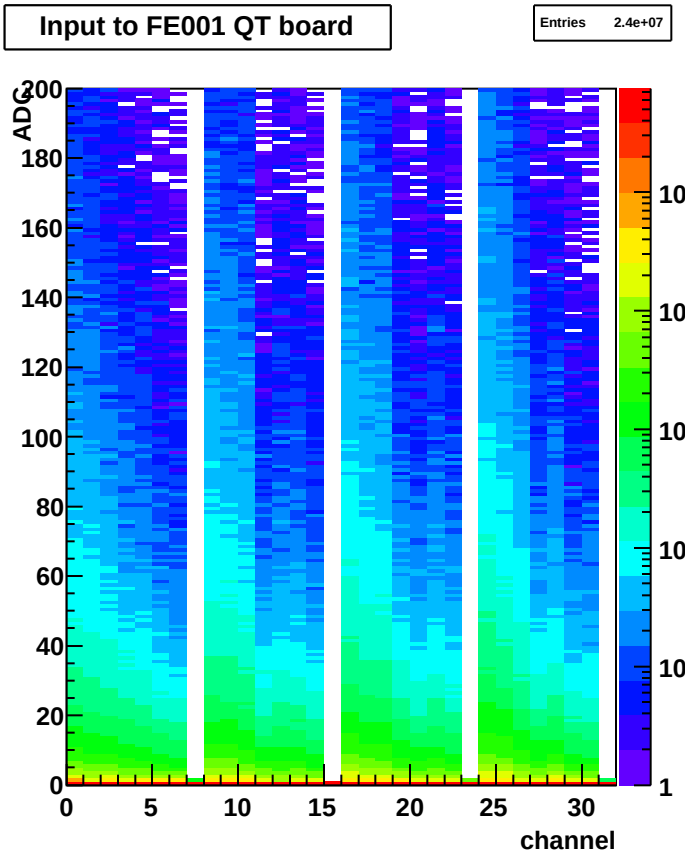

Input to FPD L2 DSM
Entries 2250000
HT bits
157128
19898
572974
750000
750000
SMALL
LARGE-S
LARGE-N

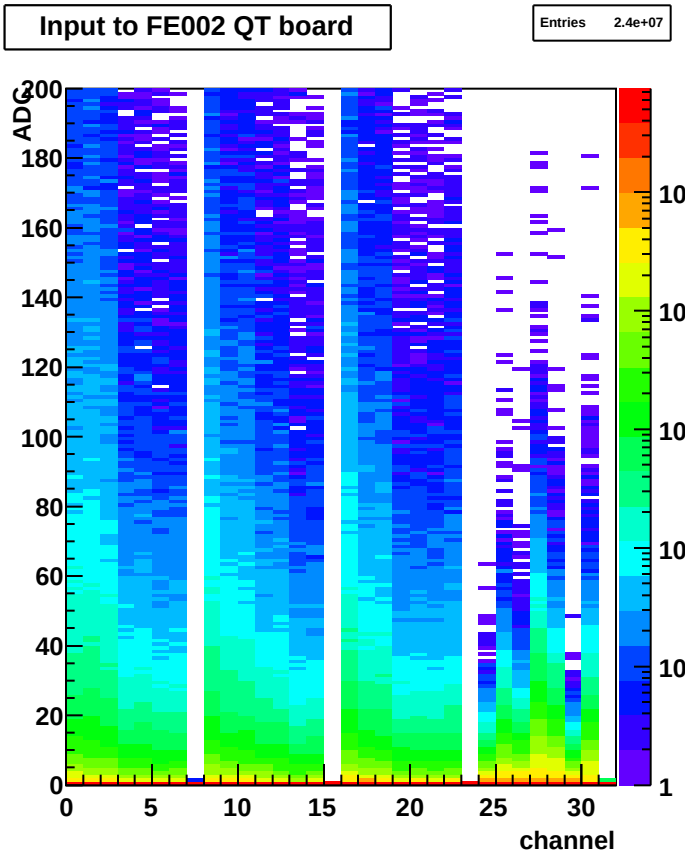

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

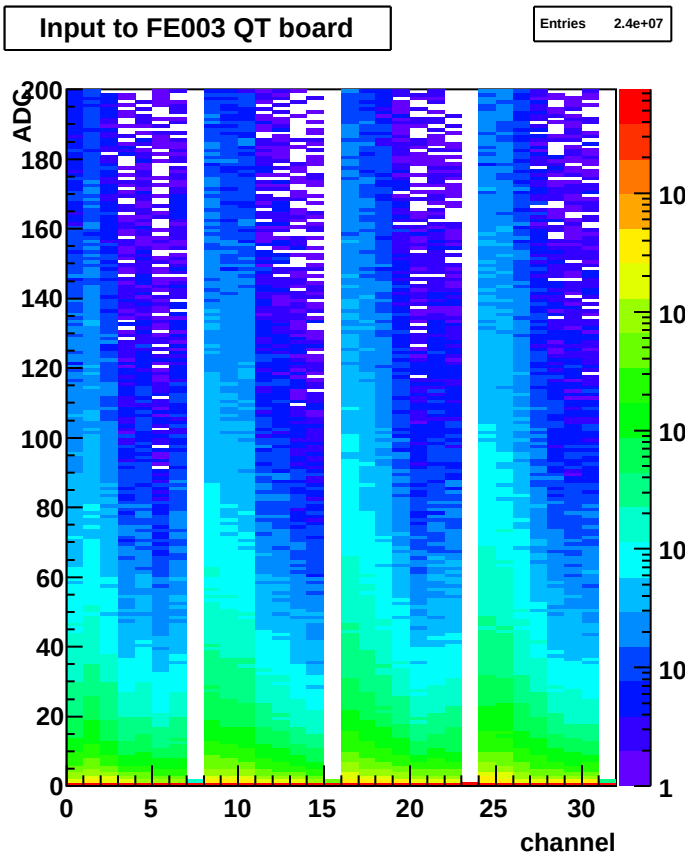

Input to FPD L3 DSM
Entries 1.2e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

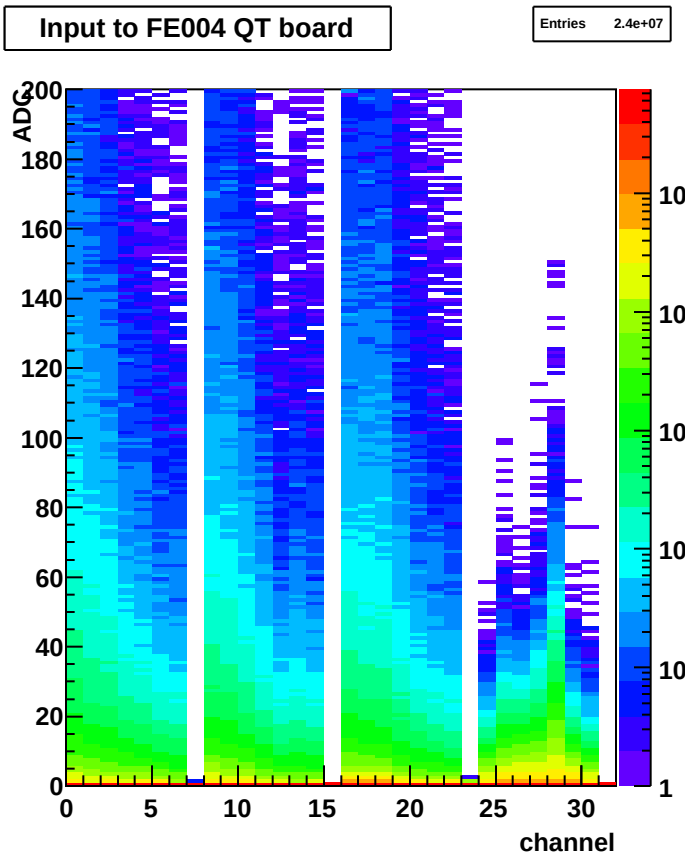

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

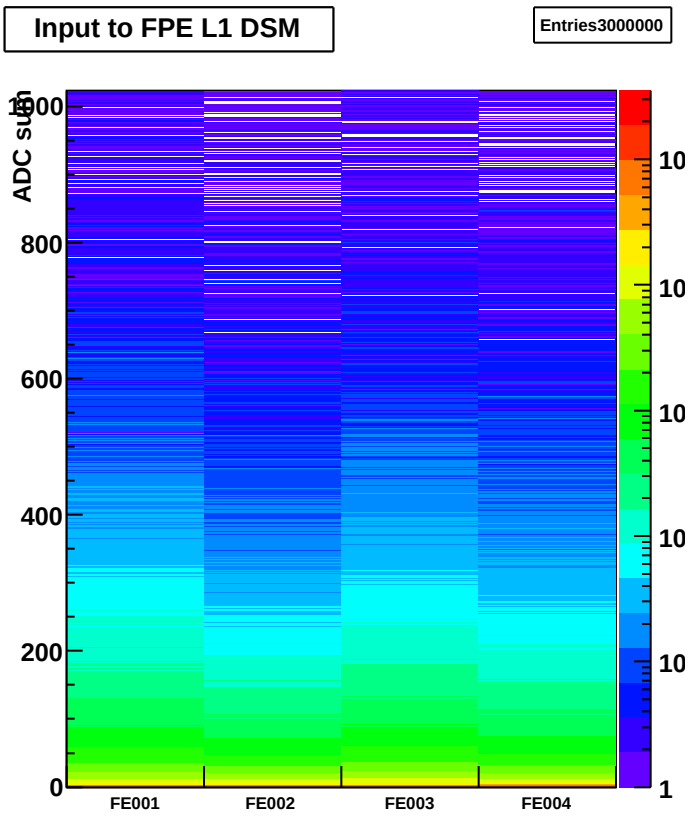

Input to FPD L2 DSM
Entries 3000000
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

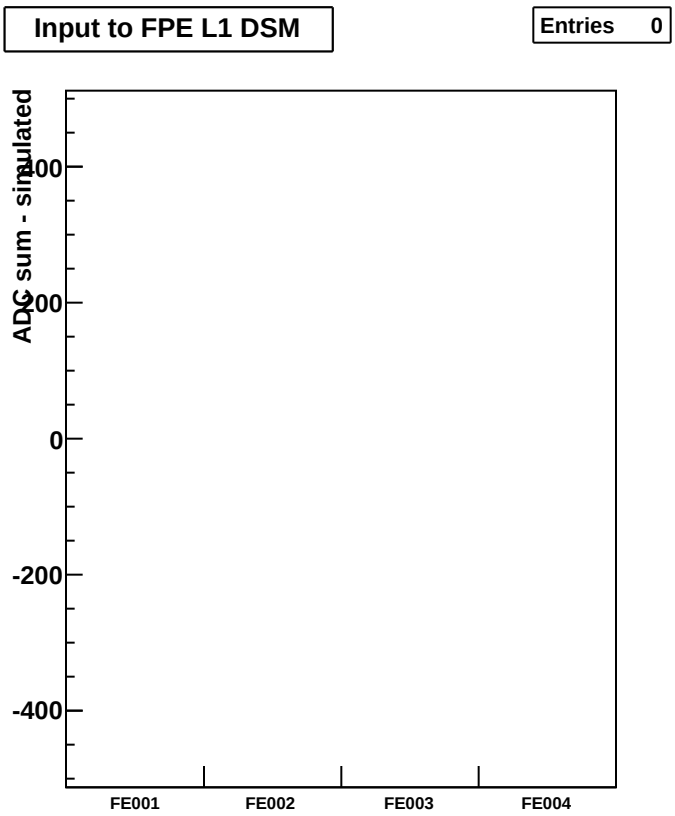

Input to FE001 QT board
Entries 2.4e+07
ADC
channel

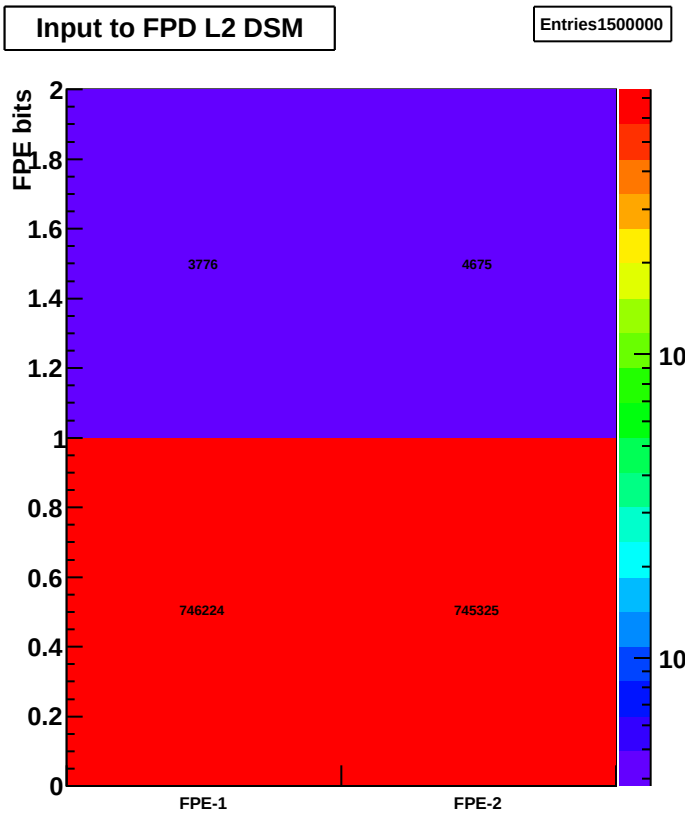

Input to FE002 QT board
Entries 2.4e+07
ADC
channel

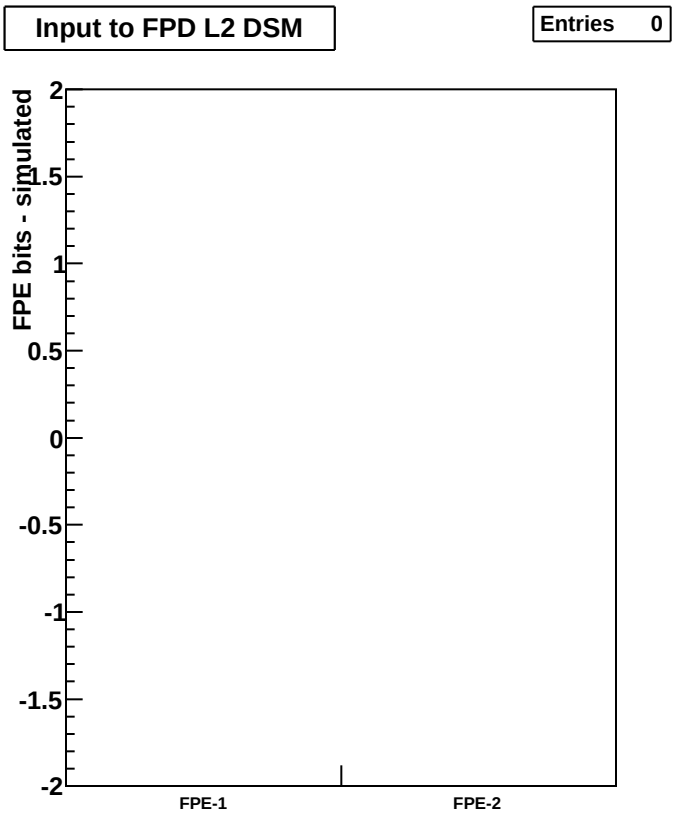

Input to FE003 QT board
Entries 2.4e+07
ADC
channel


Input to FE004 QT board
Entries 2.4e+07
ADC
channel


Input to FPE L1 DSM
Entries3000000
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1500000
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3776
4675
746224
745325
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2