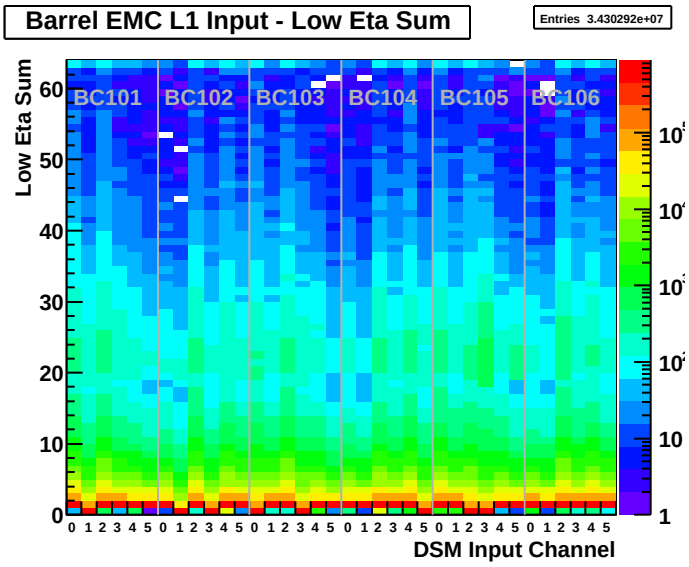
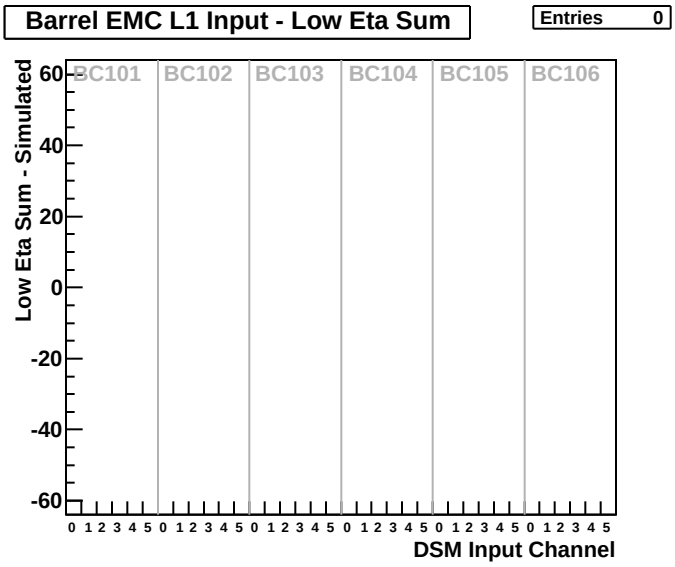
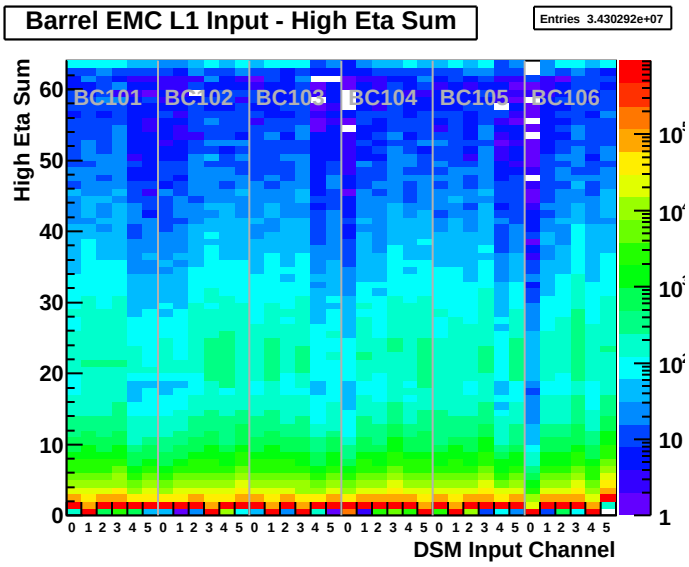
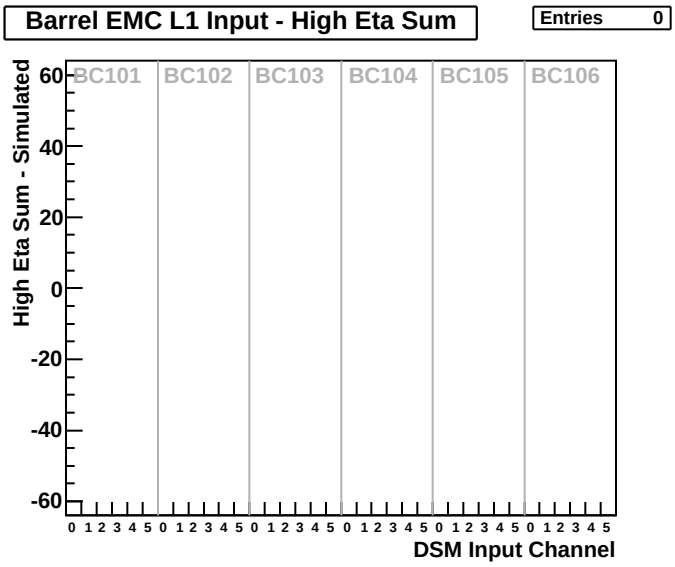
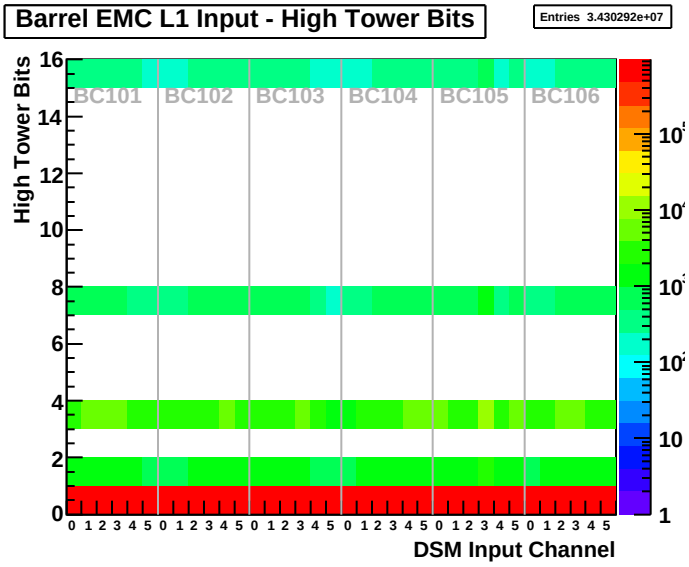
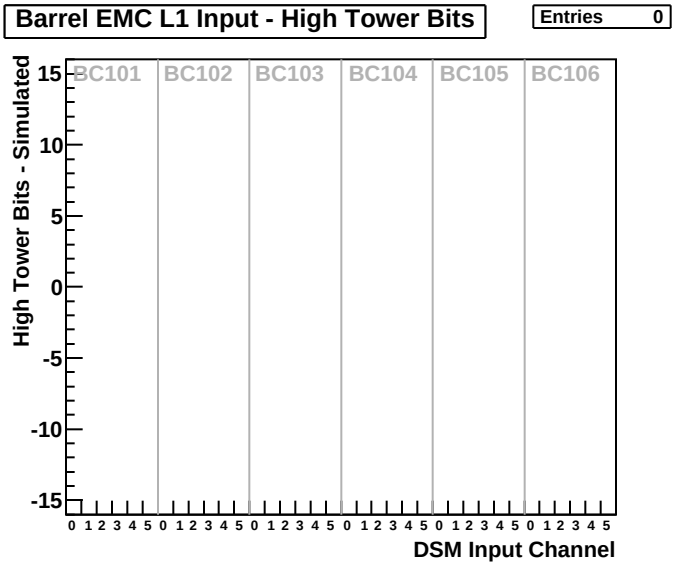

Barrel EMC L1 Input - Low Eta Sum
Entries 3.430292e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

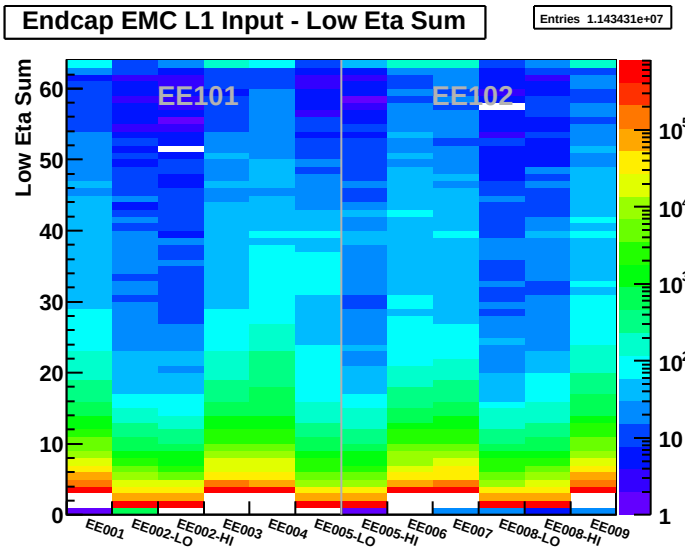

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

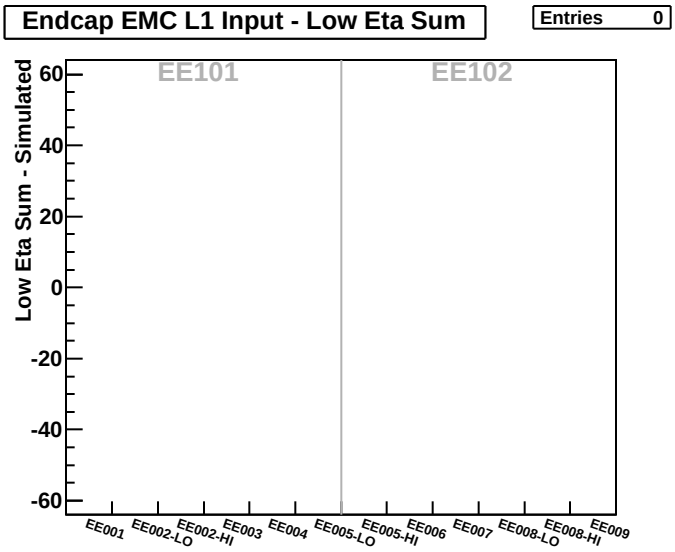

Barrel EMC L1 Input - High Eta Sum
Entries 3.430292e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

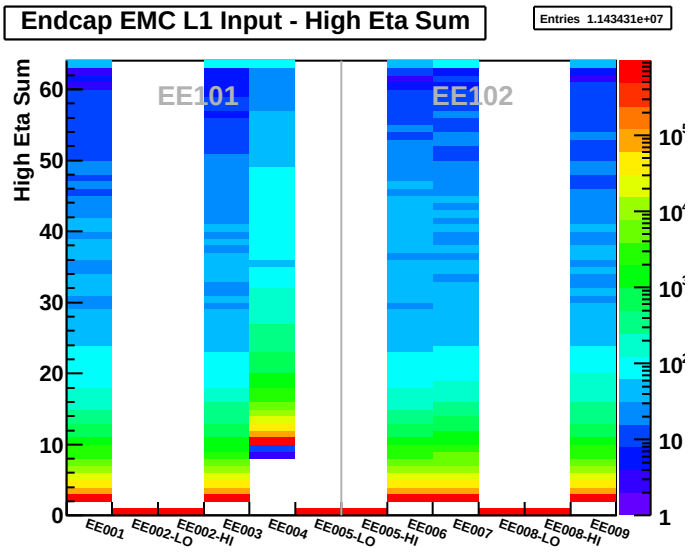

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

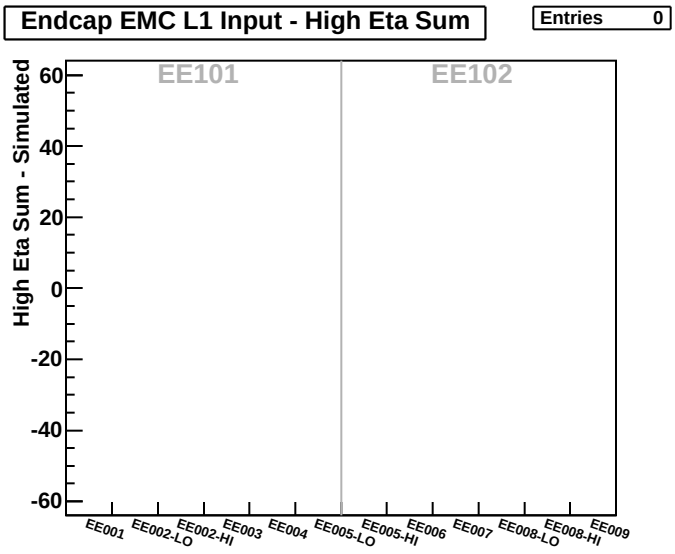

Barrel EMC L1 Input - High Tower Bits
Entries 3.430292e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

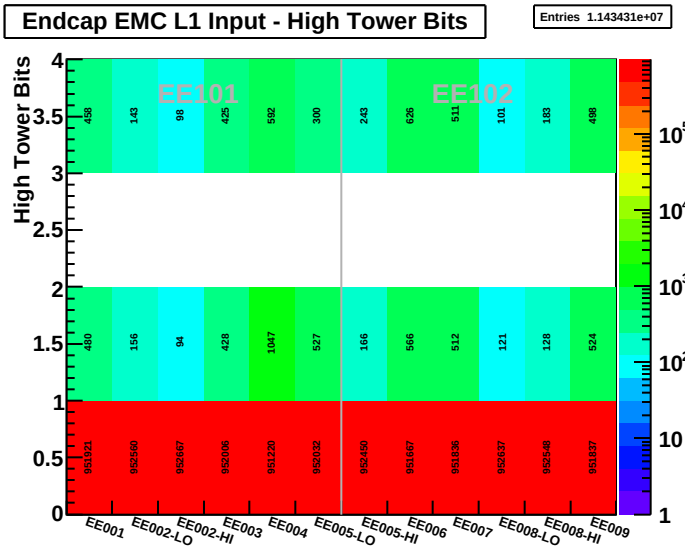

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

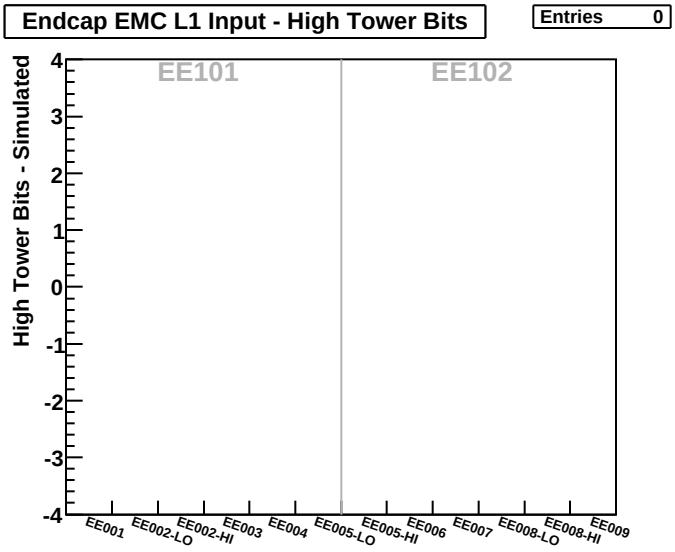

Endcap EMC L1 Input - Low Eta Sum
Entries 1.143431e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

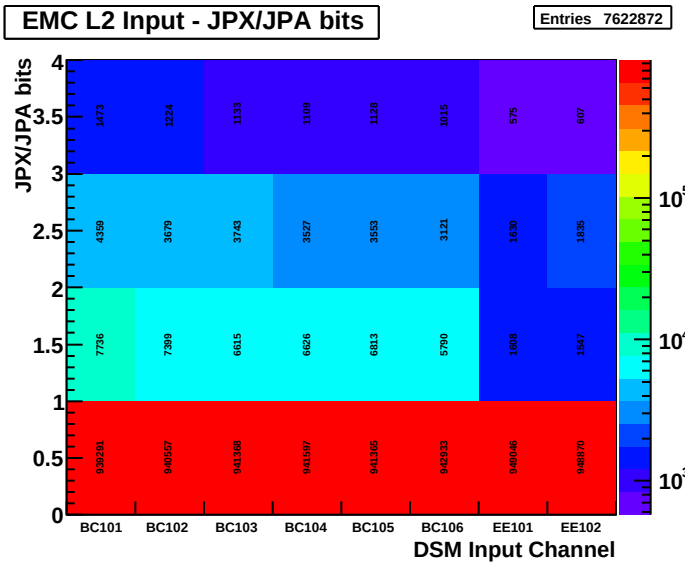

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

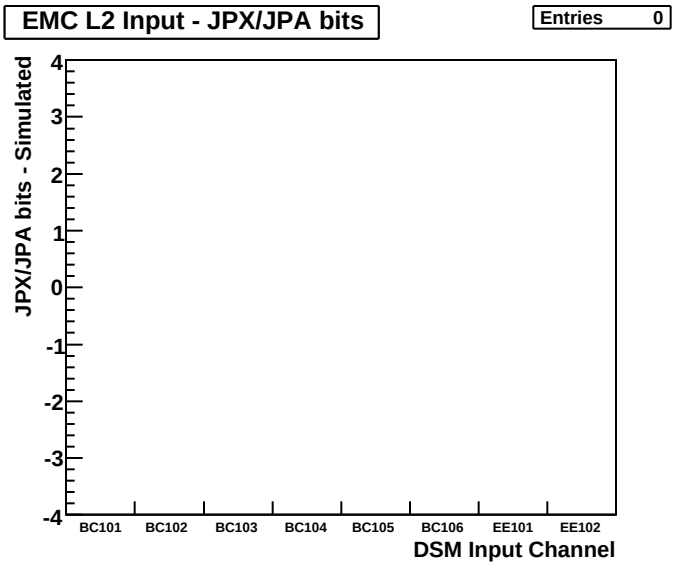

Endcap EMC L1 Input - High Eta Sum
Entries 1.143431e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

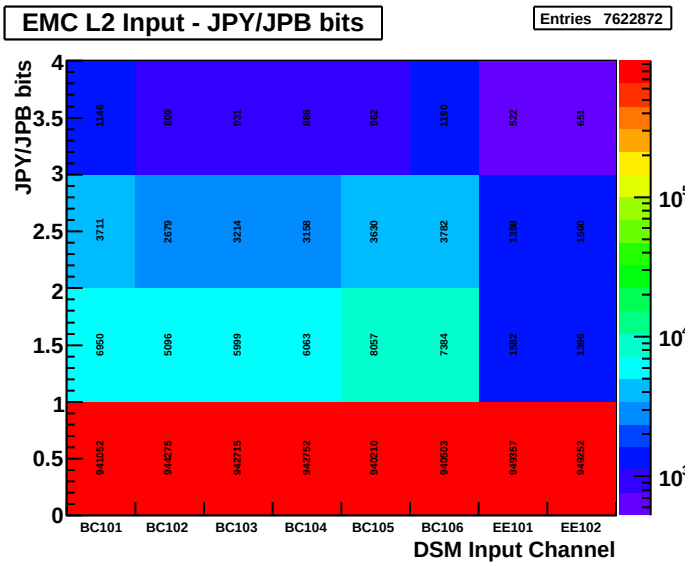

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

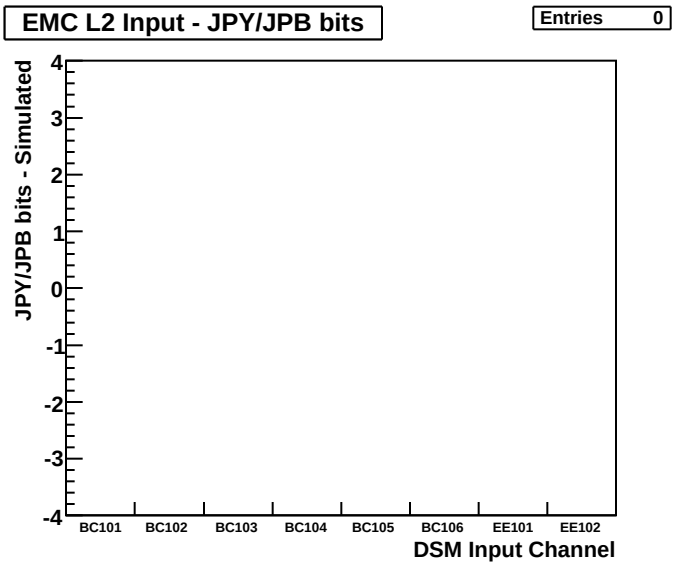

Endcap EMC L1 Input - High Tower Bits
Entries 1.143431e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

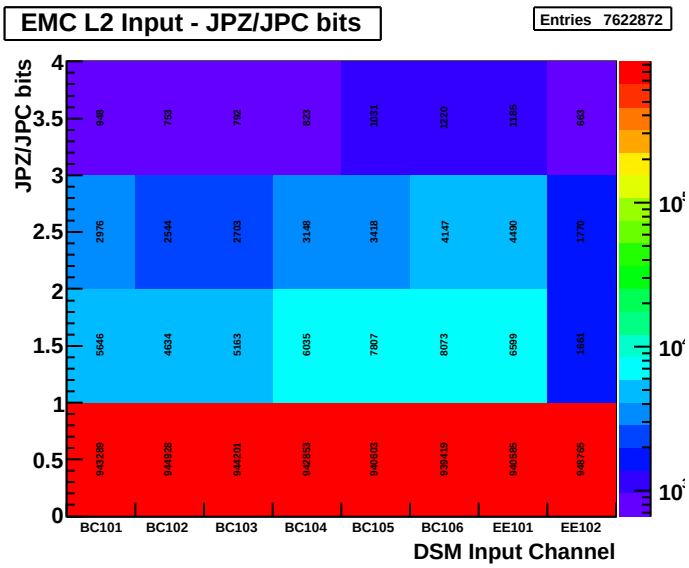

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

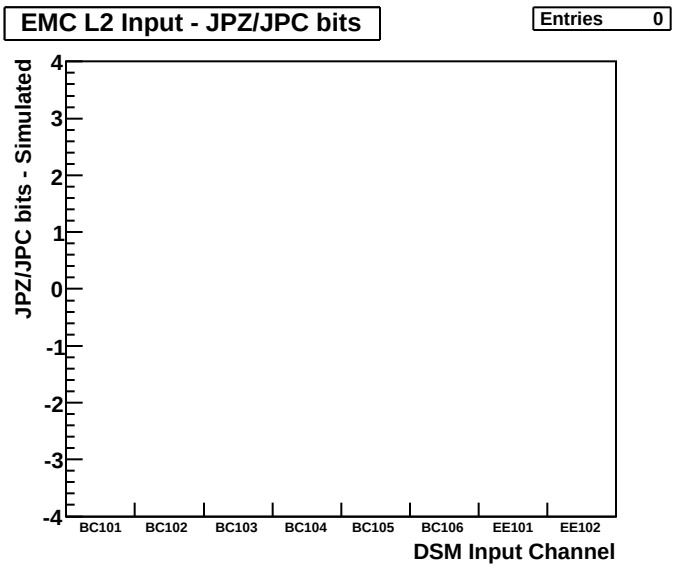

EMC L2 Input - JPX/JPA bits
Entries 7622872
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1473 1224 1133 1109 1128 1015 575 607
4359 3679 3743 3527 3553 3121 1630 1835
7736 7399 6615 6626 6813 5790 1608 1547
939291 940557 941368 941597 941365 942033 948046 948870

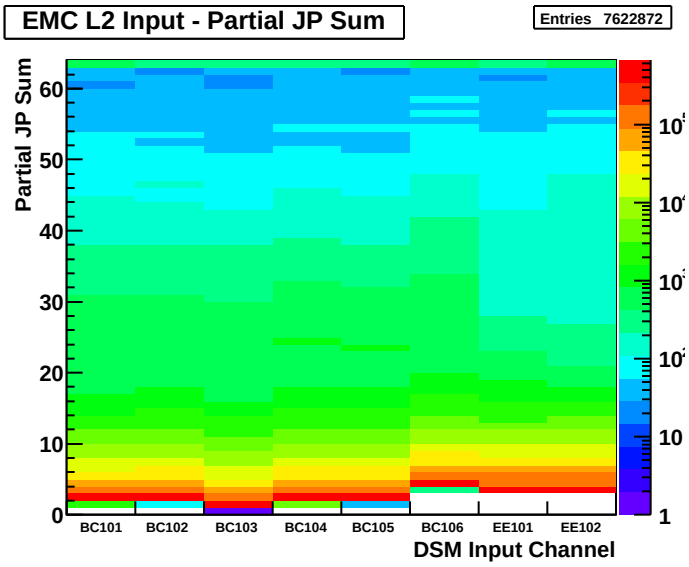

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

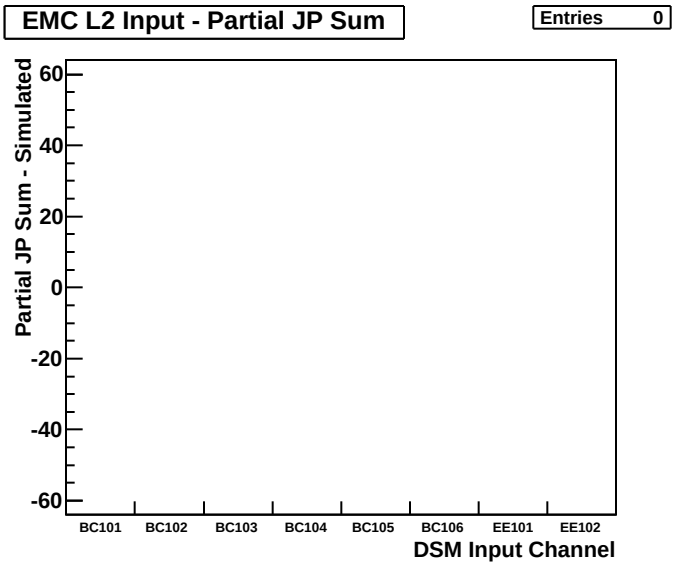

EMC L2 Input - JPY/JPB bits
Entries 7622872
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1146 809 931 886 962 1190 522 651
3711 2679 3214 3158 3630 3782 1398 1560
6950 5096 5999 6063 8057 7384 1832 1396
941052 944275 942715 942752 940210 940593 949357 949252

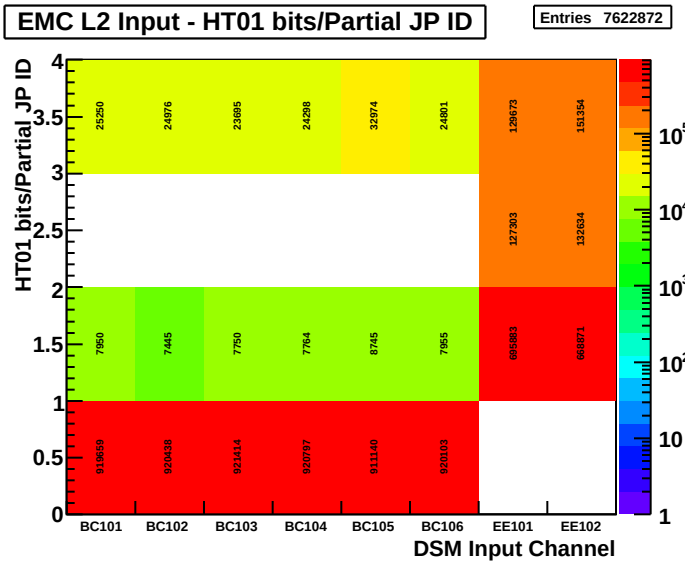

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

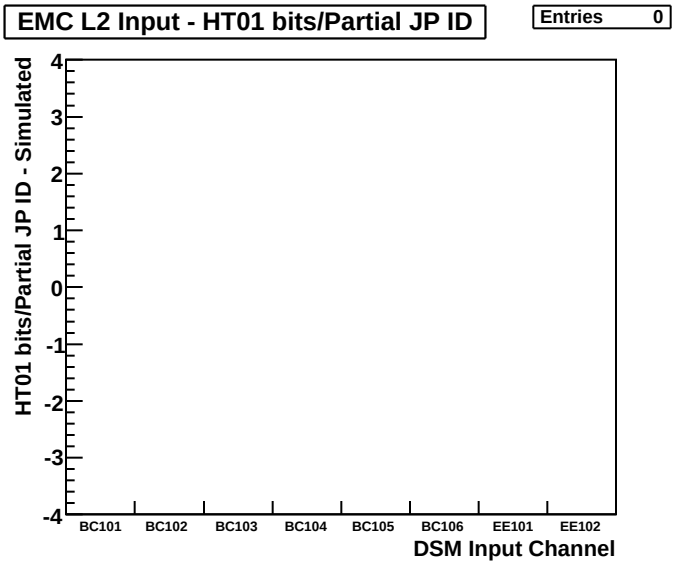

EMC L2 Input - JPZ/JPC bits
Entries 7622872
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
948 753 792 823 1031 1220 1185 663
2976 2544 2703 3148 3418 4147 4490 1770
5646 4634 5163 6035 7807 8073 6599 1661
943289 944928 944201 942853 940603 939419 940585 948765

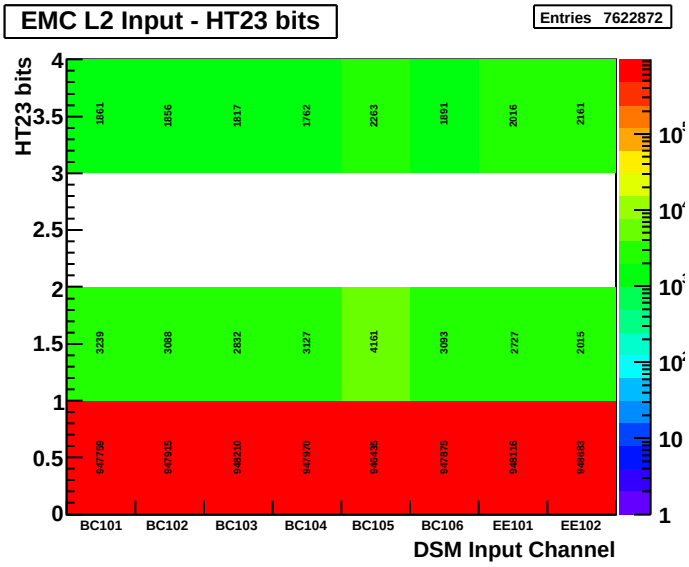

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

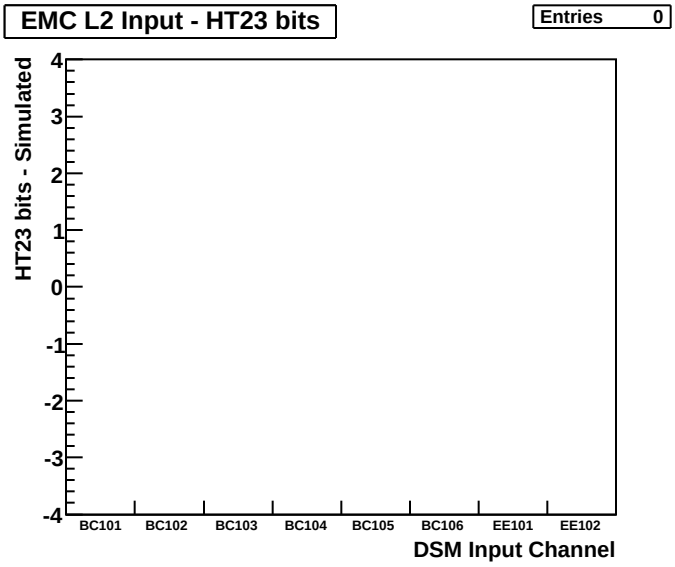

EMC L2 Input - Partial JP Sum
Entries 7622872
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

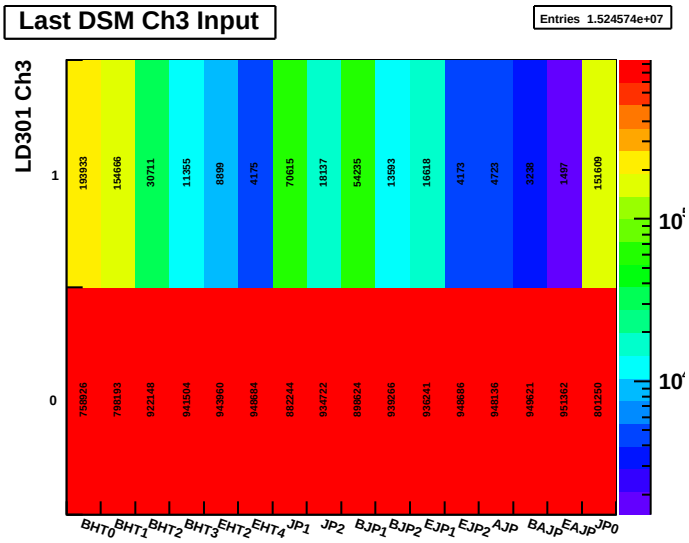

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

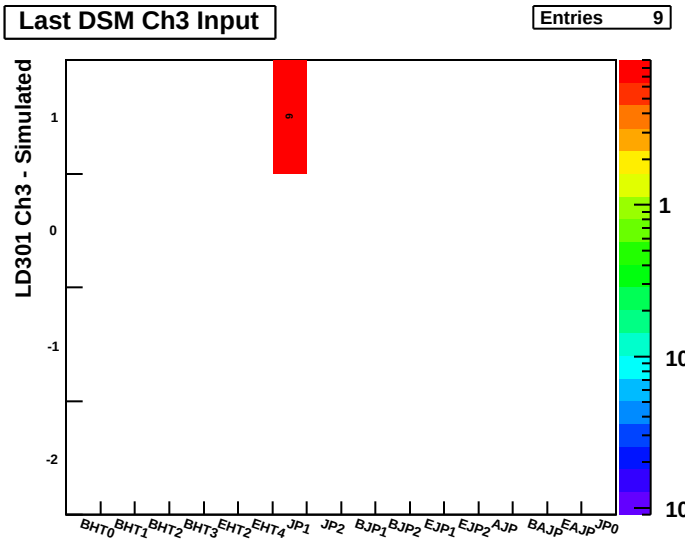

EMC L2 Input - HT01 bits/Partial JP ID
Entries 7622872
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

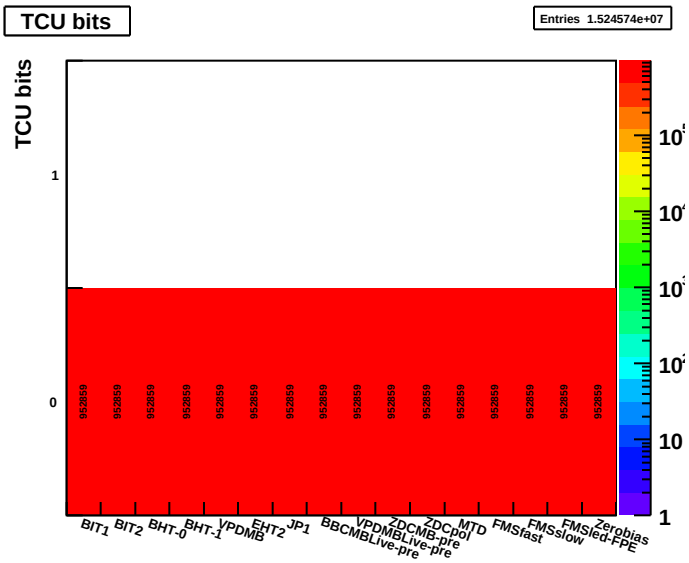

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

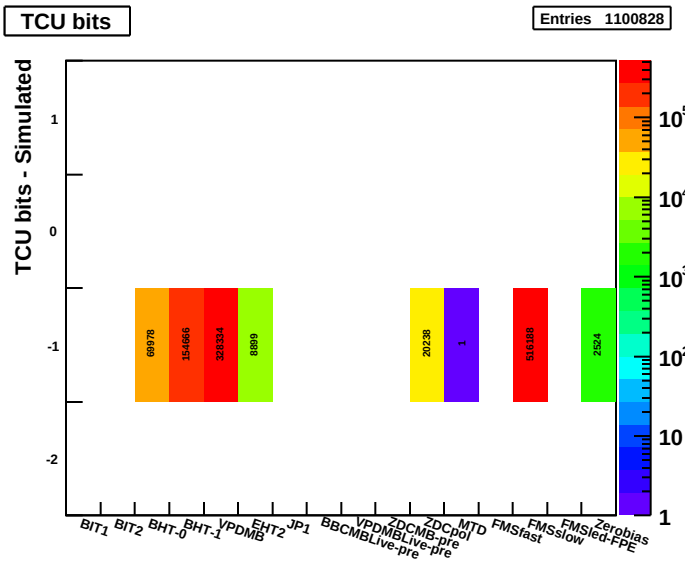

EMC L2 Input - HT23 bits
Entries 7622872
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

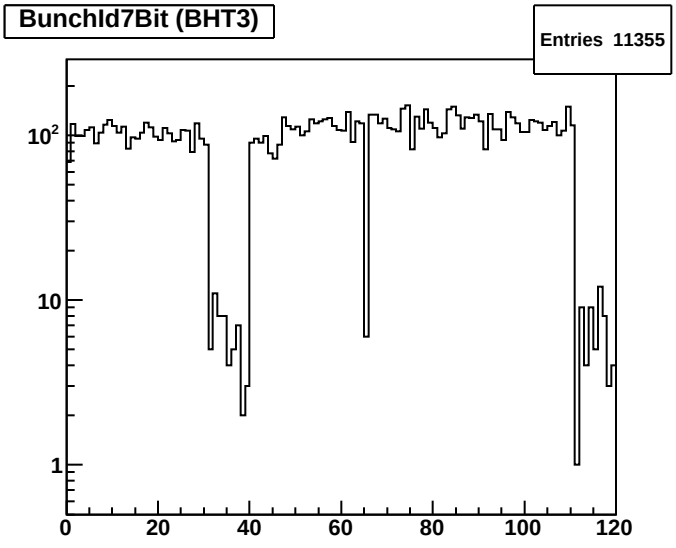

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

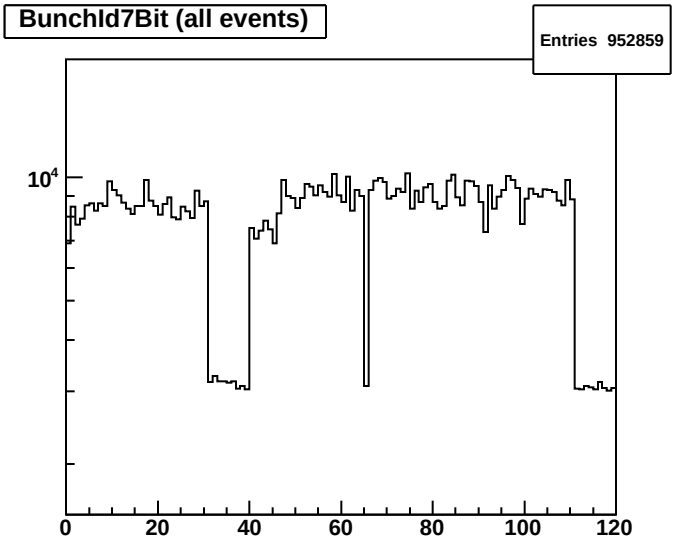

Last DSM Ch3 Input
Entries 1.524574e+07
LD301 Ch3
193933 154666 30711 11355 8899 4175 70615 18137 54235 13593 16618 4173 4723 3238 1497 151609
758826 798193 922148 941504 943960 946684 882244 934722 898624 938266 936241 948686 948136 946621 951362 801250
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

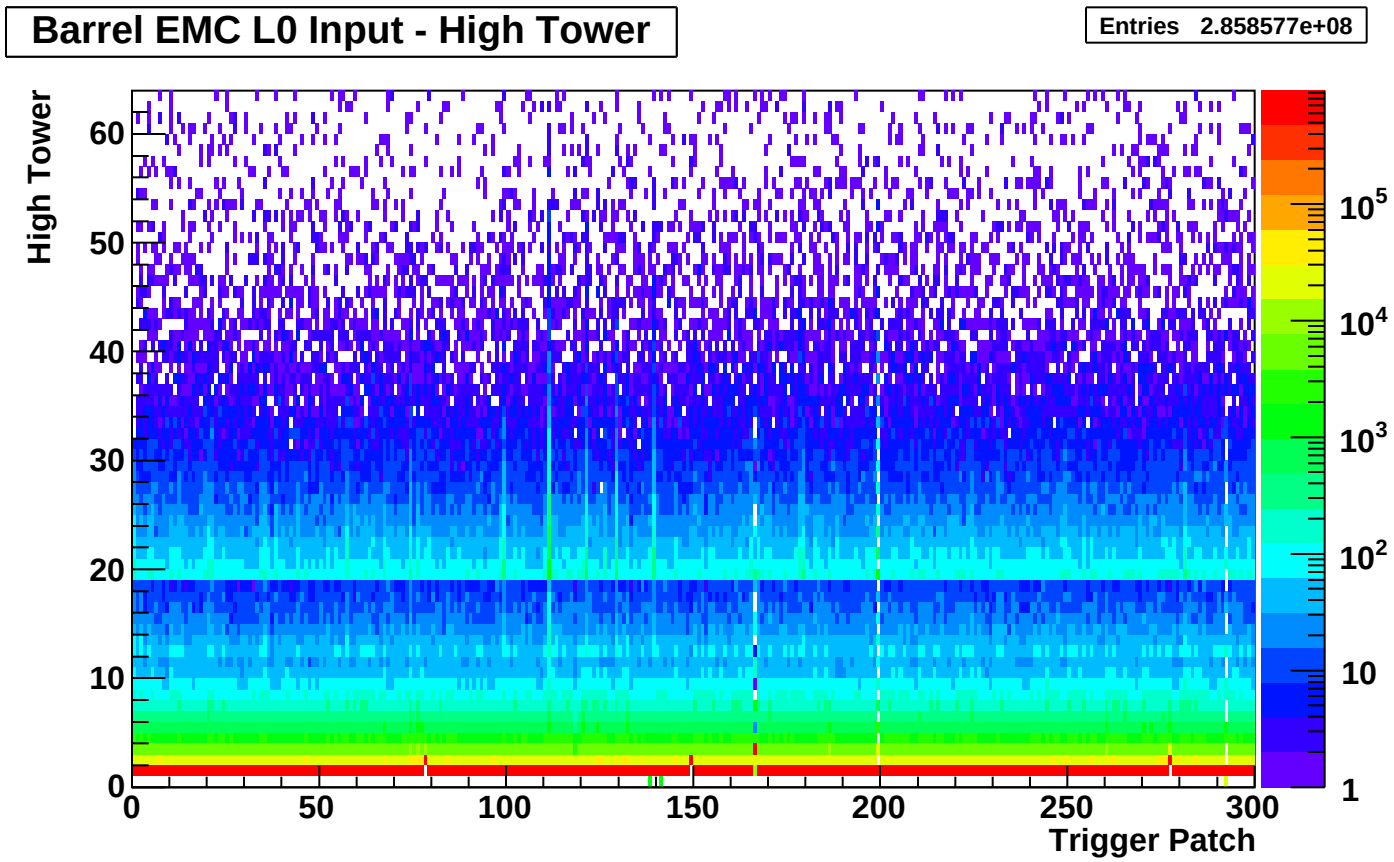

Last DSM Ch3 Input
Entries 9
LD301 Ch3 - Simulated
9
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

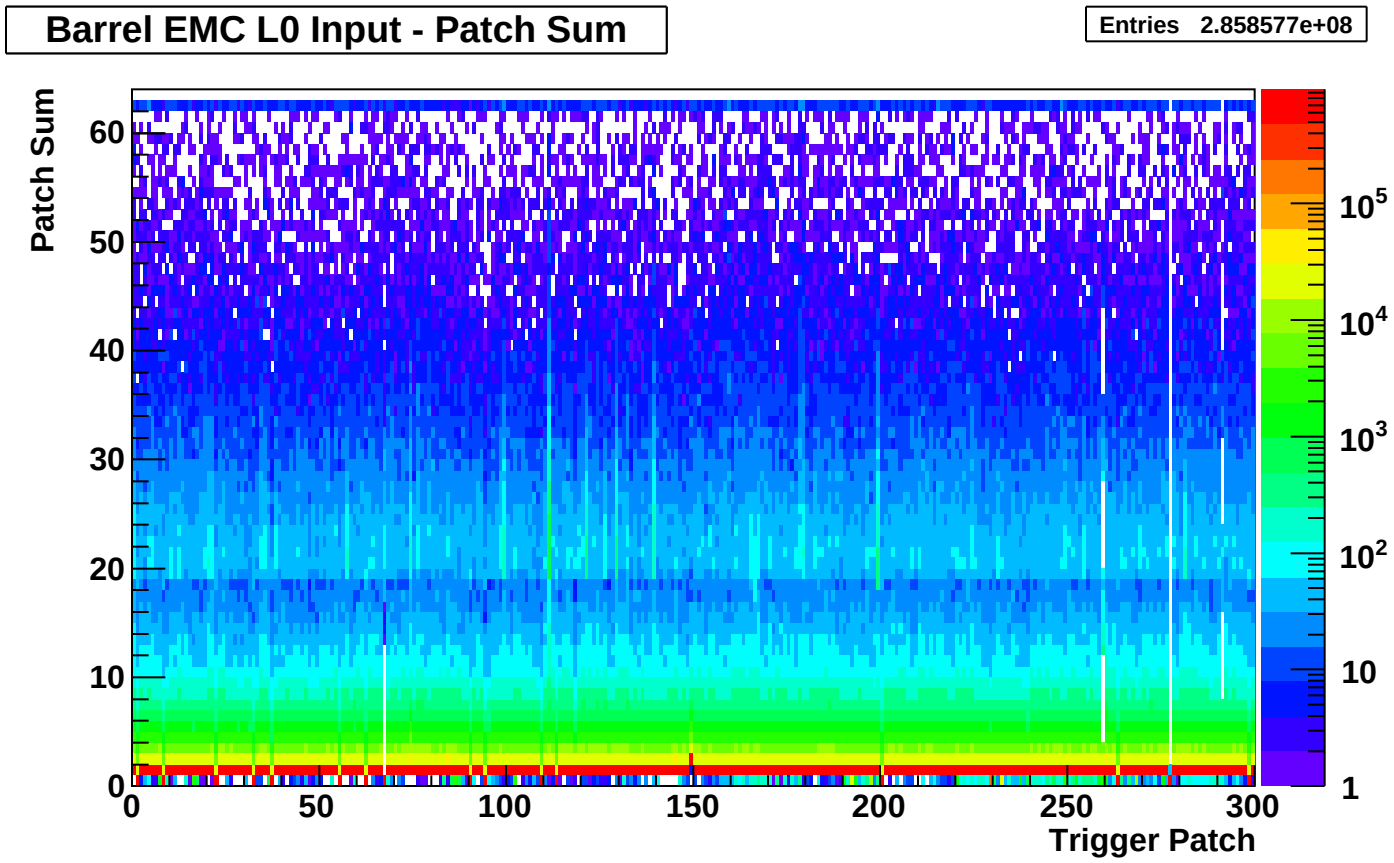

TCU bits
Entries 1.524574e+07
TCU bits
952859
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

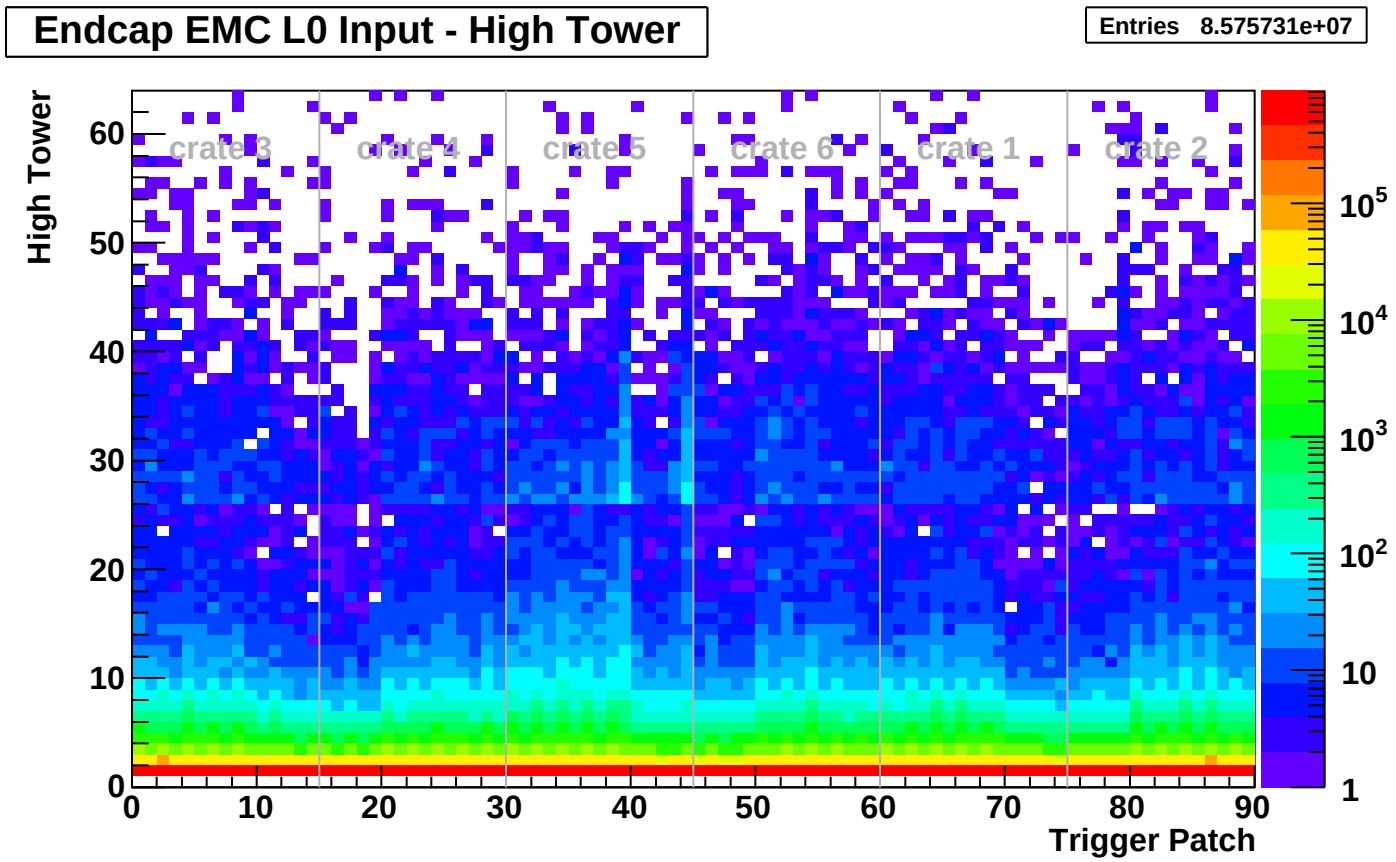

TCU bits
Entries 1100828
TCU bits - Simulated
69978 154666 328334 8899 20238 1 516188 2524
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

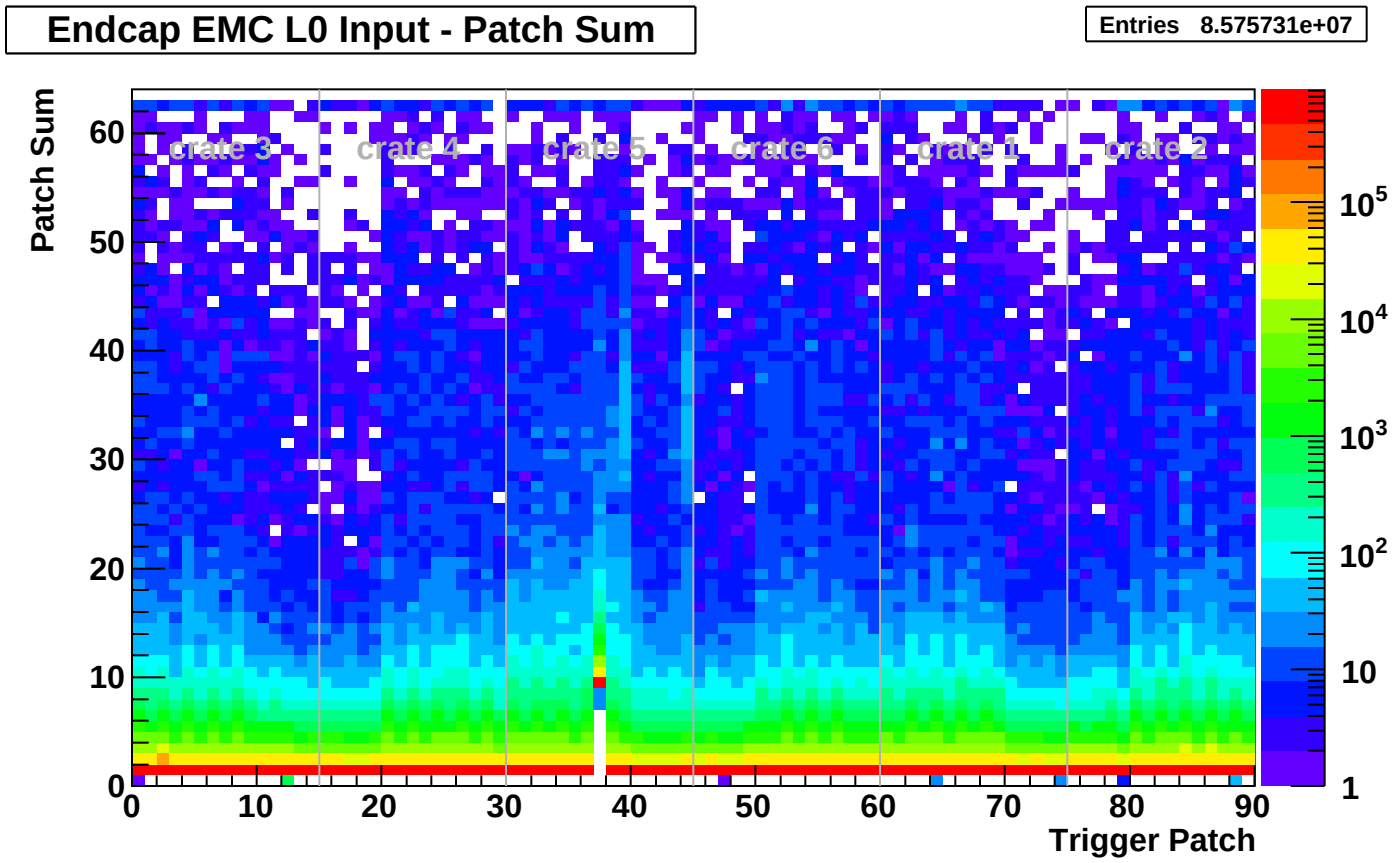

BunchId7Bit (BHT3)
Entries 11355

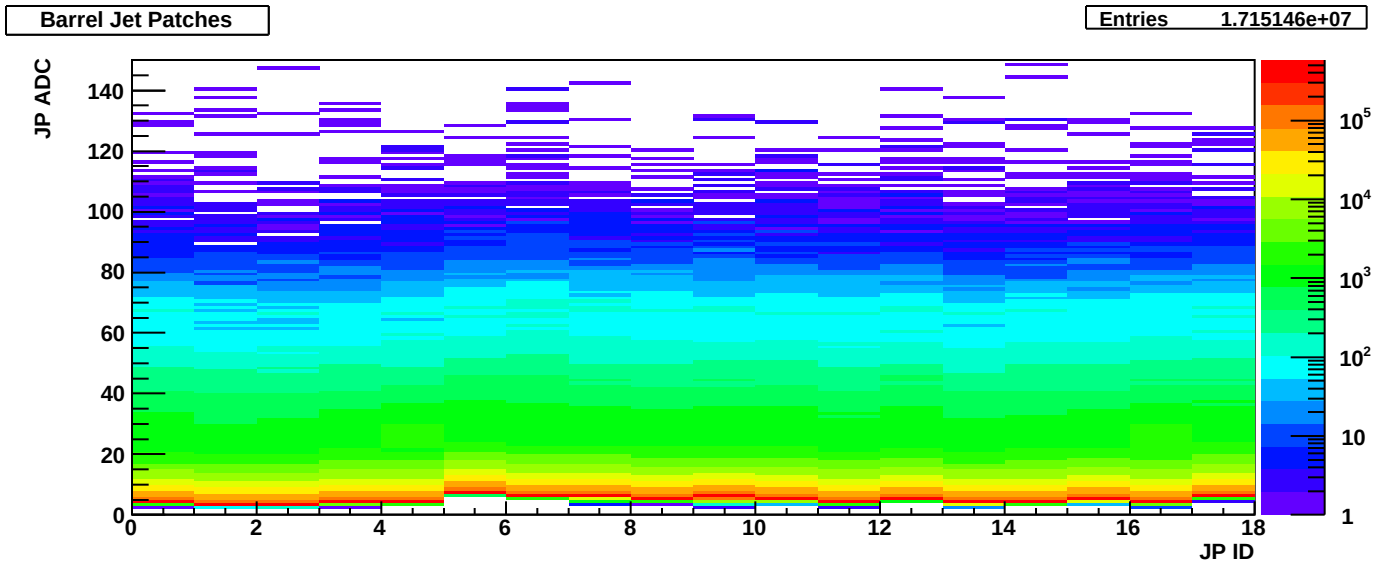

BunchId7Bit (all events)
Entries 952859

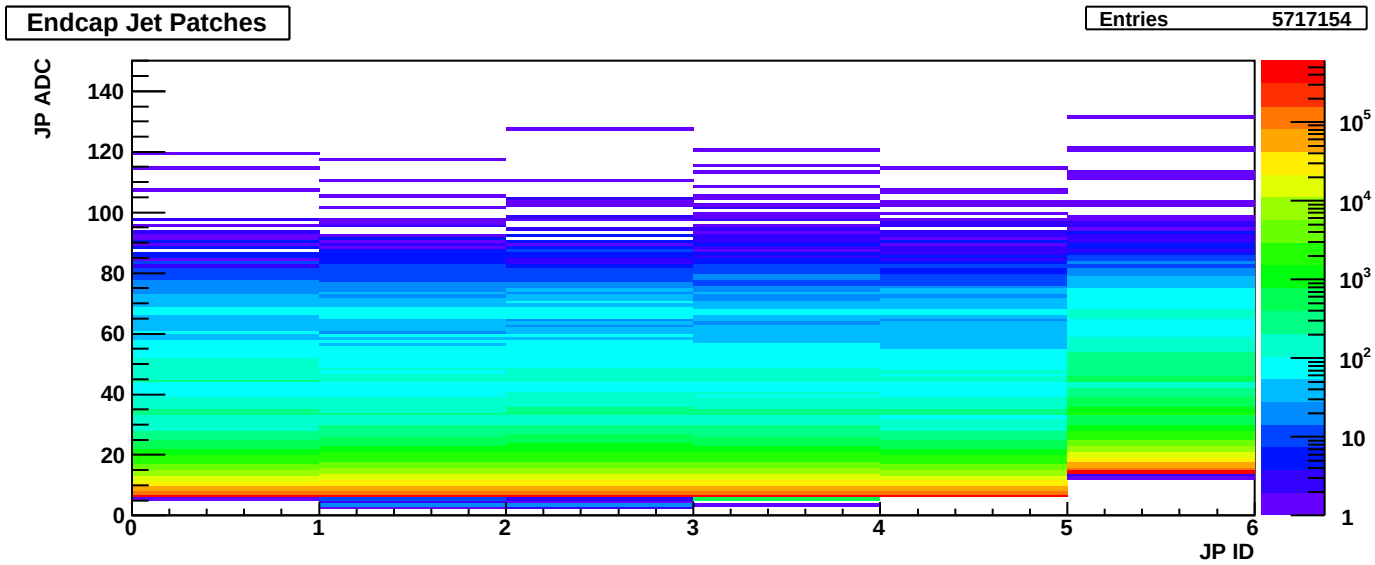

Barrel EMC L0 Input - High Tower
Entries 2.858577e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

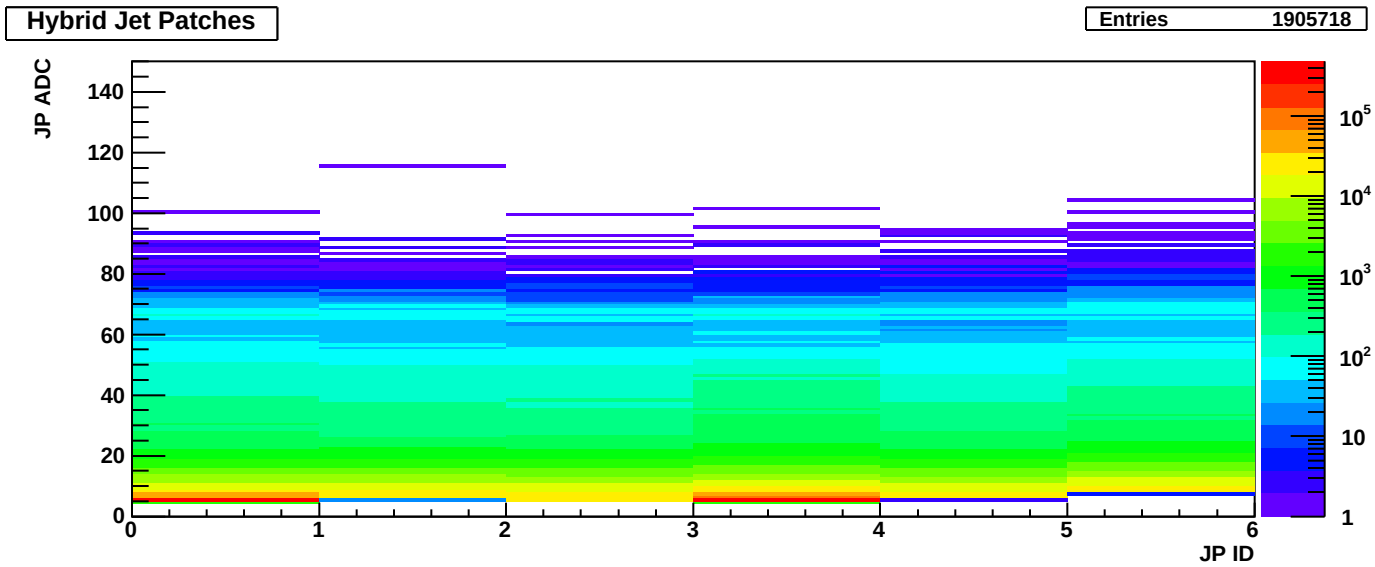

Barrel EMC L0 Input - Patch Sum
Entries 2.858577e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 8.575731e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

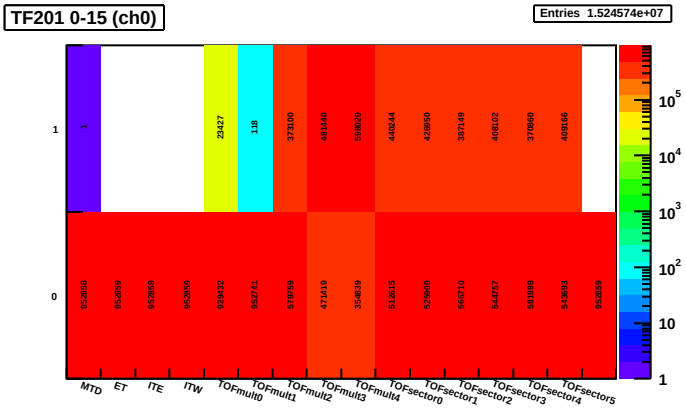

Endcap EMC L0 Input - Patch Sum
Entries 8.575731e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Barrel Jet Patches
Entries 1.715146e+07
JP ADC
JP ID

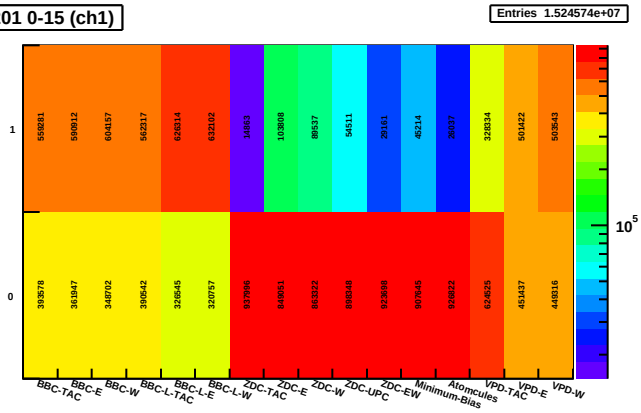

Endcap Jet Patches
Entries 5717154
JP ADC
JP ID


Hybrid Jet Patches
Entries 1905718
JP ADC
JP ID

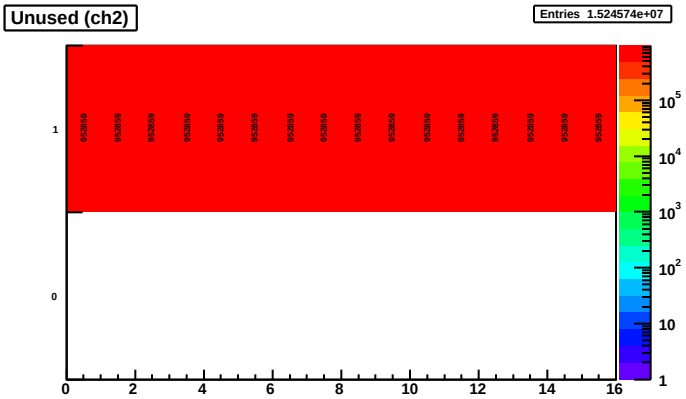
TF201 0-15 (ch0)



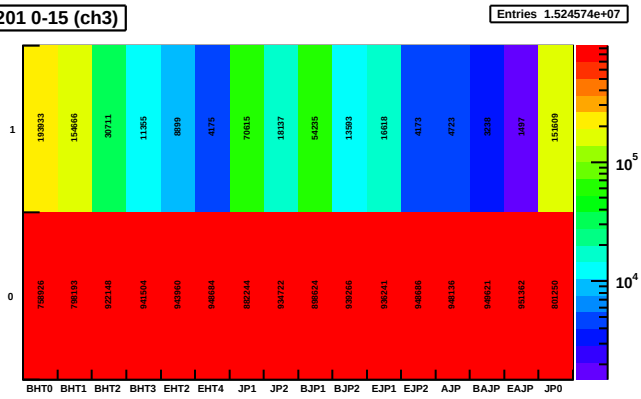
VT201 0-15 (ch1)



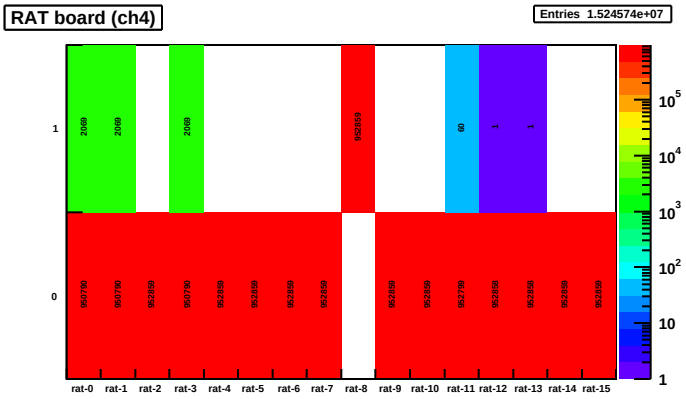
Unused (ch2)



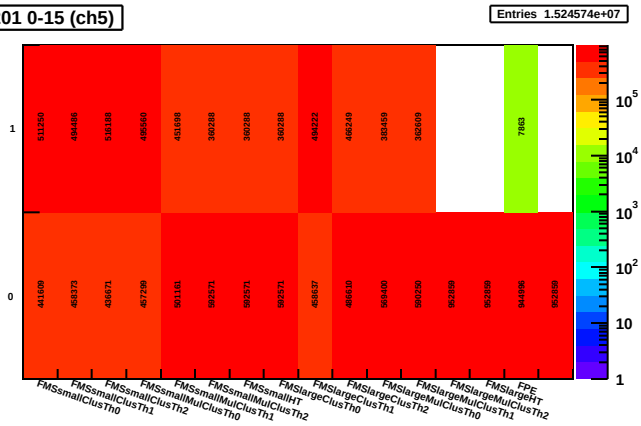
EM201 0-15 (ch3)



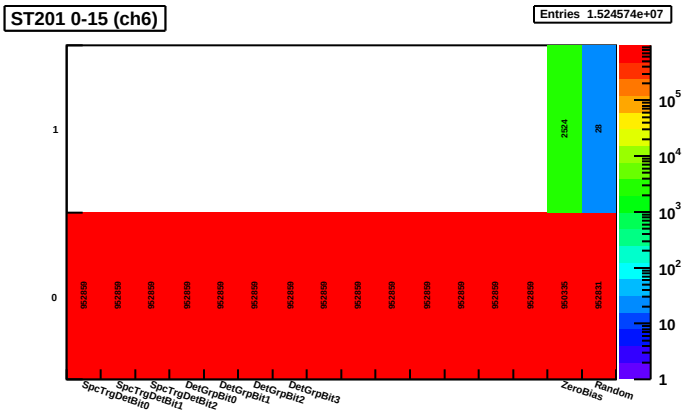
RAT board (ch4)



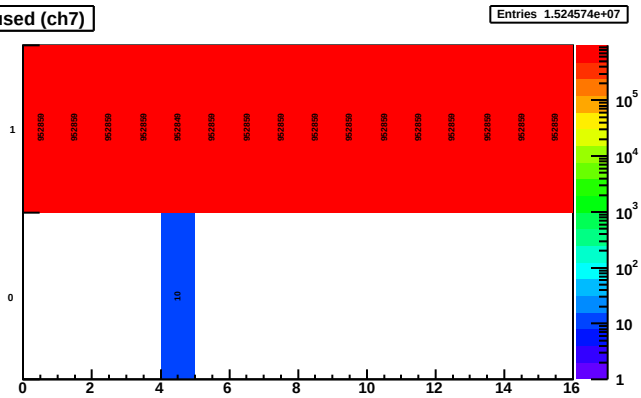
FP201 0-15 (ch5)

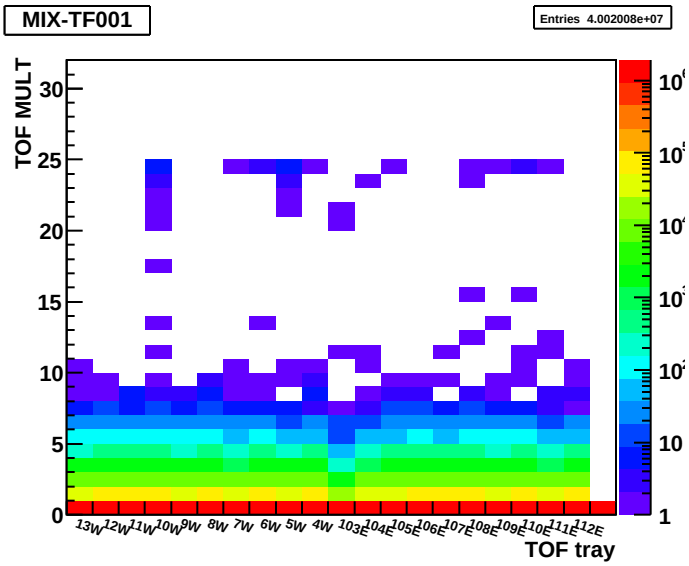


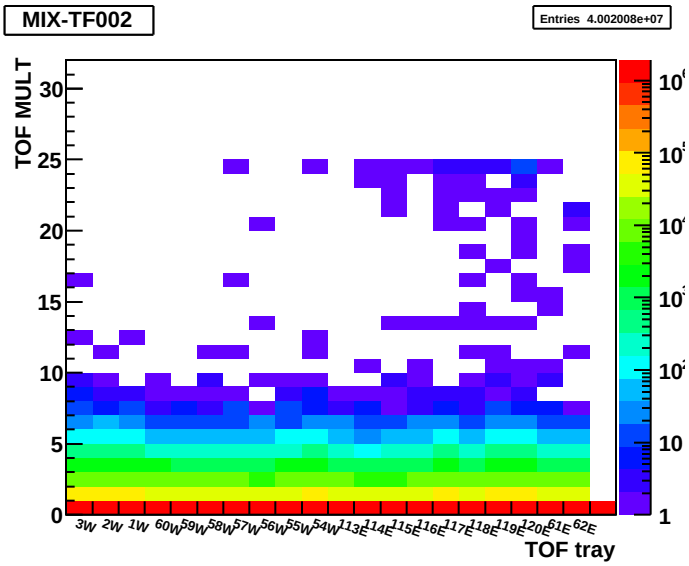
ST201 0-15 (ch6)

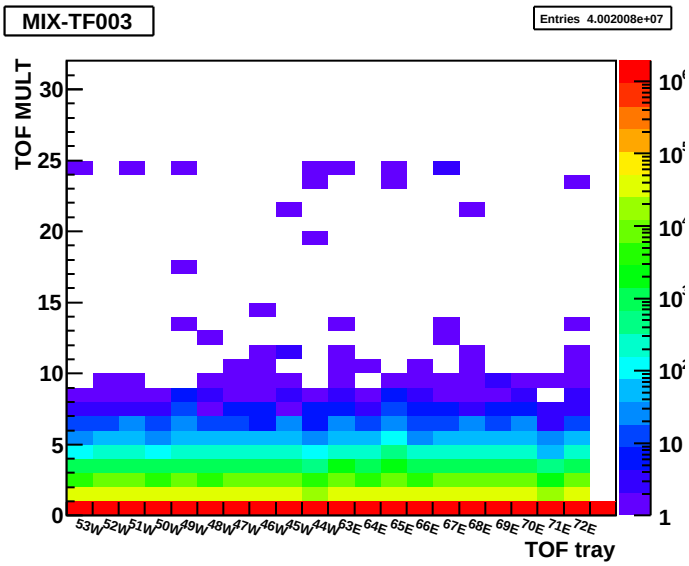


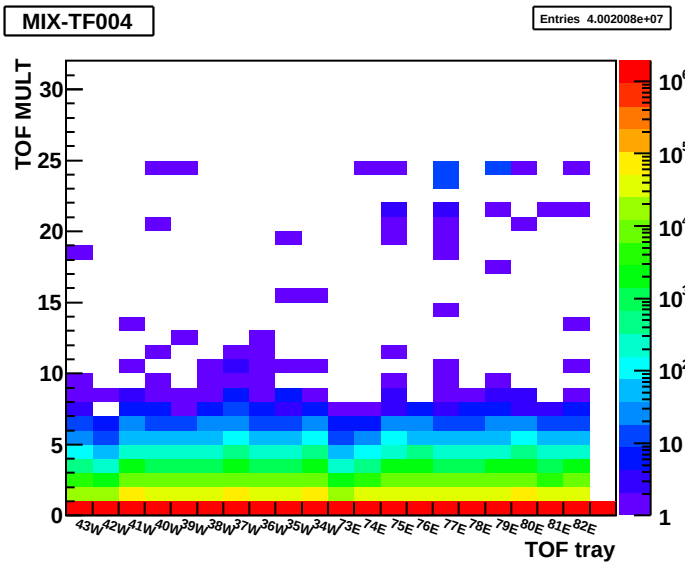
Unused (ch7)

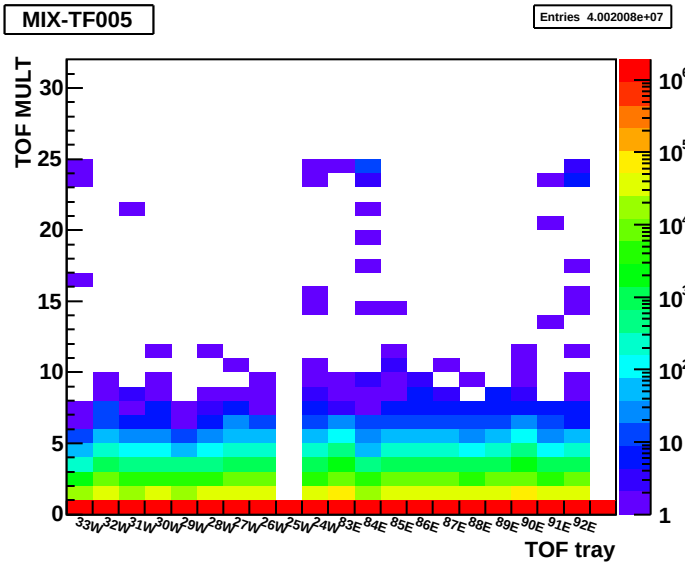


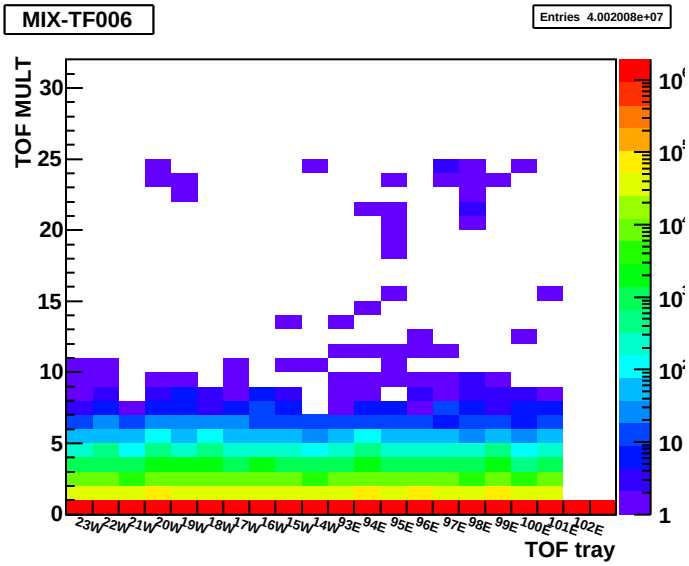

MIX-TF001
Entries 4.002008e+07
TOF MULT
TOF tray

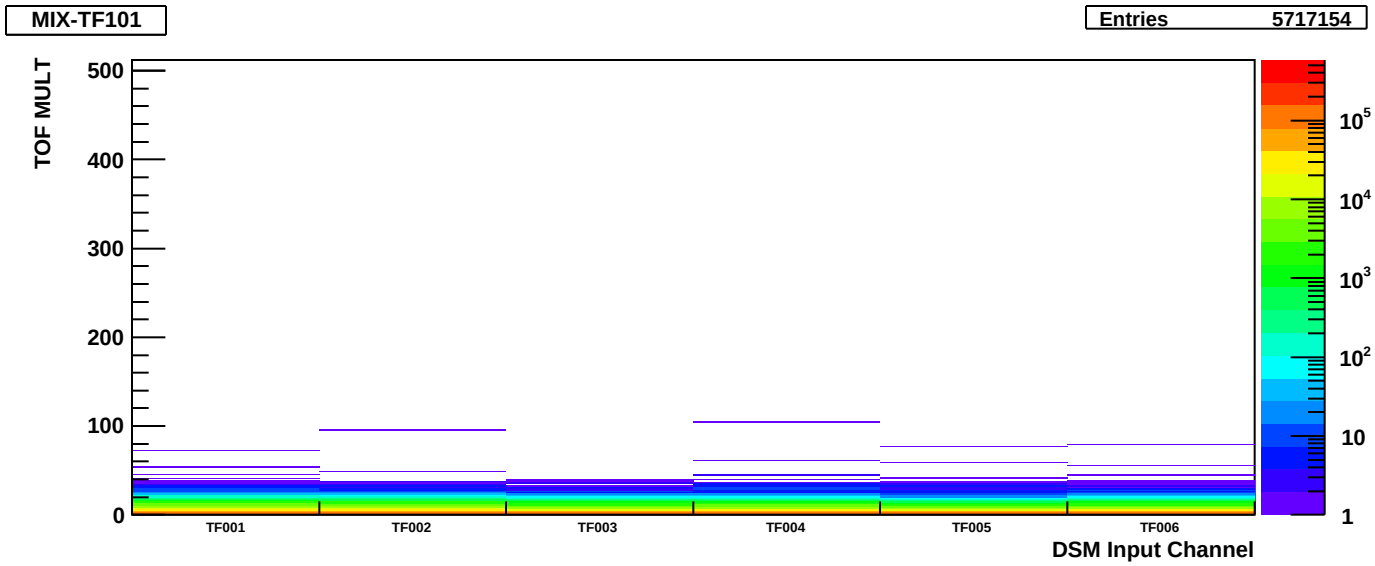

MIX-TF002
Entries 4.002008e+07
TOF MULT
TOF tray

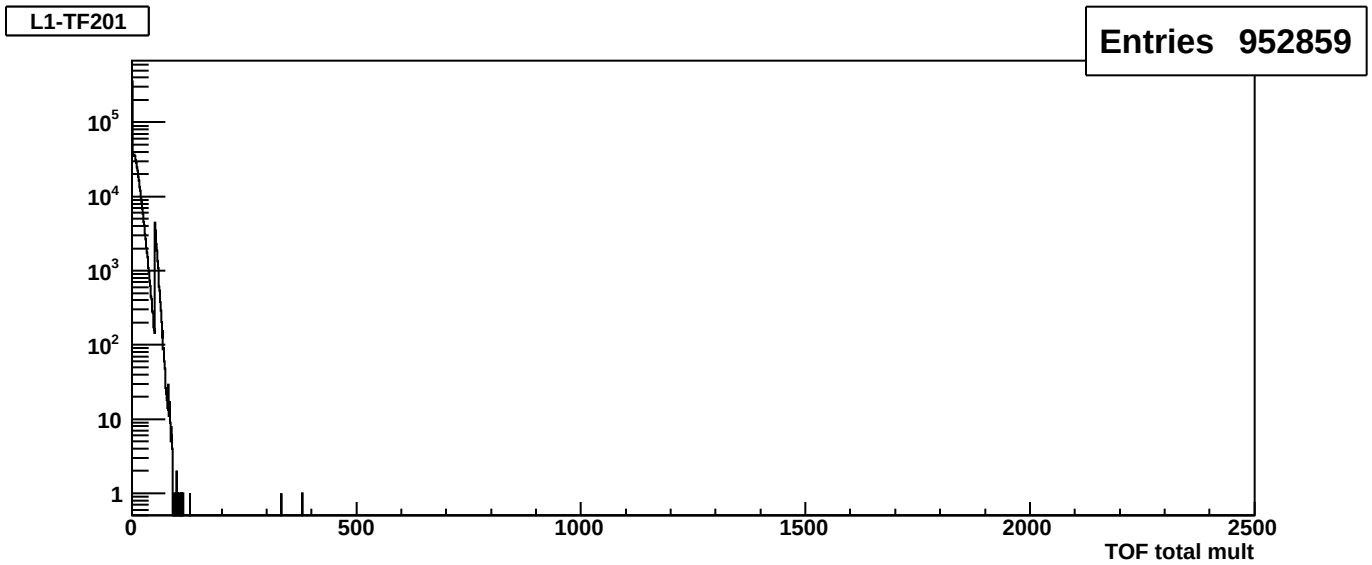

MIX-TF003
Entries 4.002008e+07
TOF MULT
TOF tray

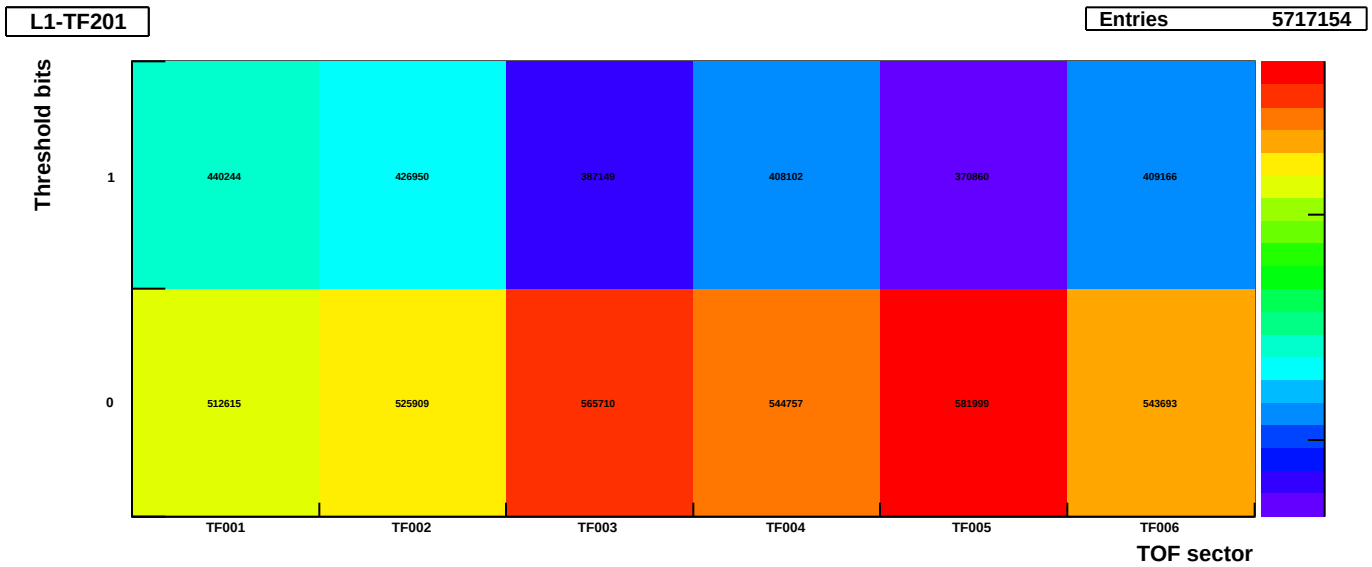

MIX-TF004
Entries 4.002008e+07
TOF MULT
TOF tray

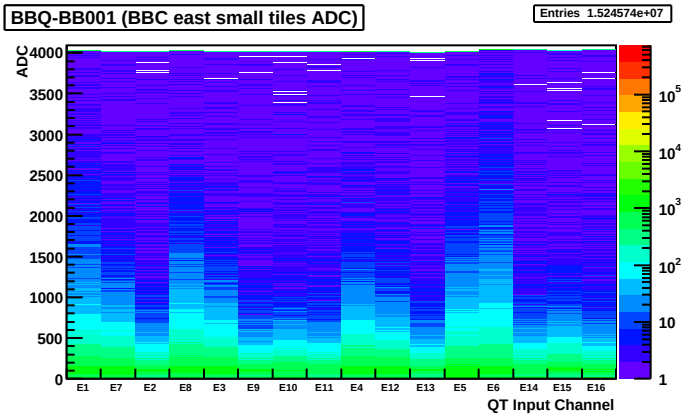

MIX-TF005
Entries 4.002008e+07
TOF MULT
TOF tray

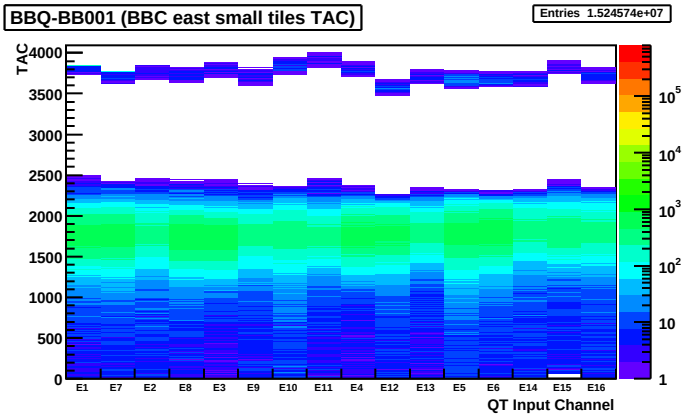

MIX-TF006
Entries 4.002008e+07
TOF MULT
TOF tray

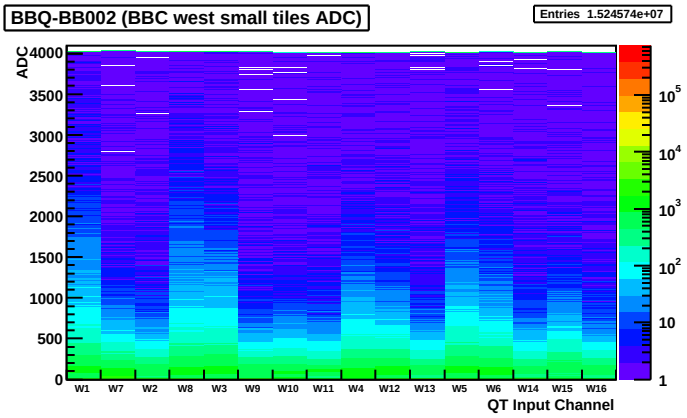

MIX-TF101
Entries 5717154
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

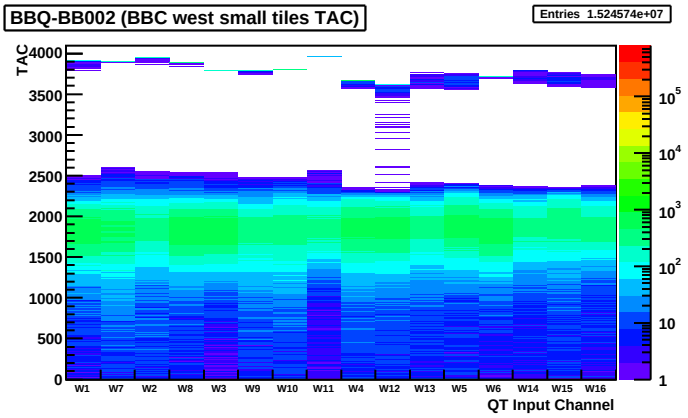

L1-TF201
Entries 952859
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

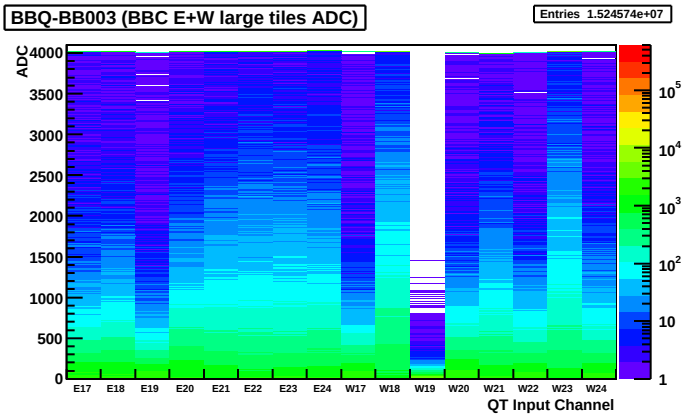

L1-TF201
Entries 5717154
Threshold bits
1
0
440244
426950
387149
408102
370860
409166
512615
525909
565710
544757
581999
543693
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

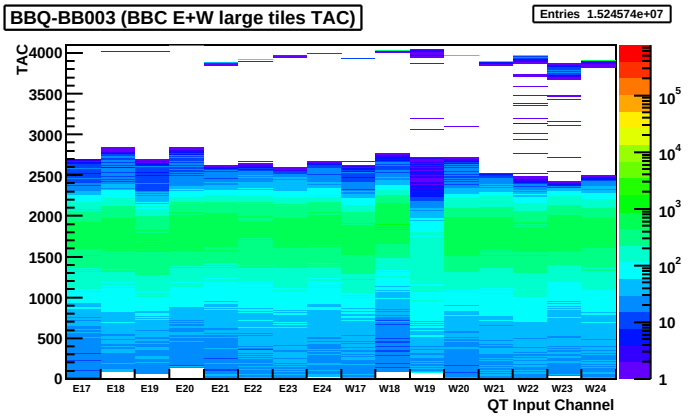

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.524574e+07
ADC
QT Input Channel

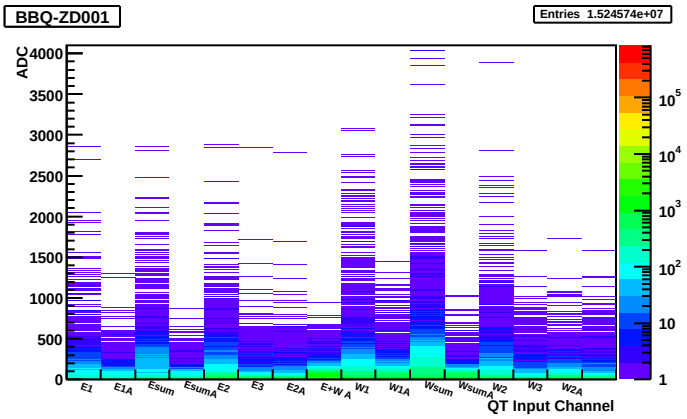

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.524574e+07
TAC
QT Input Channel

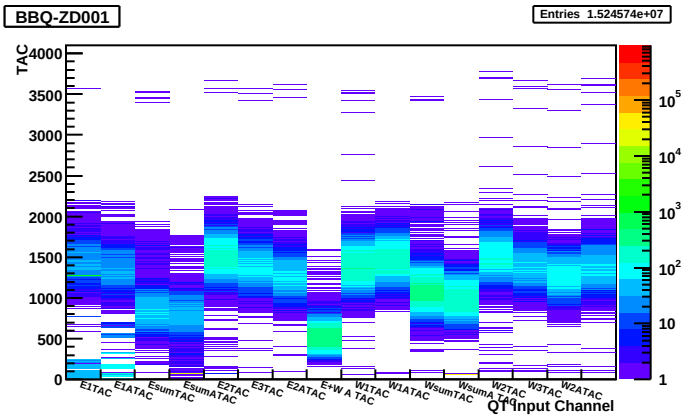

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.524574e+07
ADC
QT Input Channel

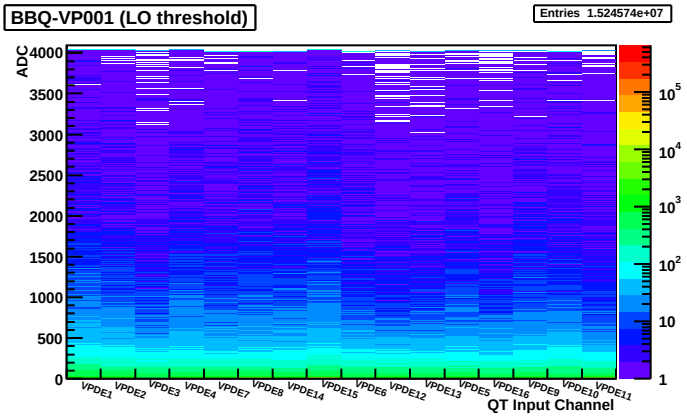

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.524574e+07
TAC
QT Input Channel

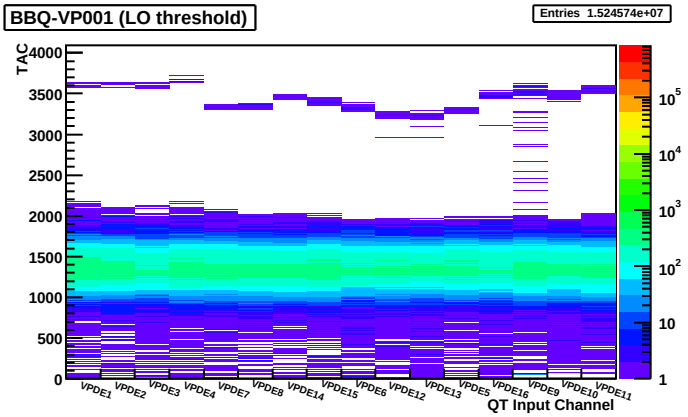

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.524574e+07
ADC
QT Input Channel

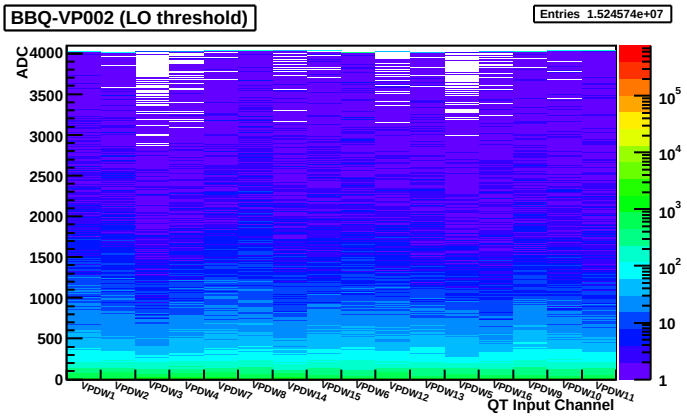

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.524574e+07
TAC
QT Input Channel

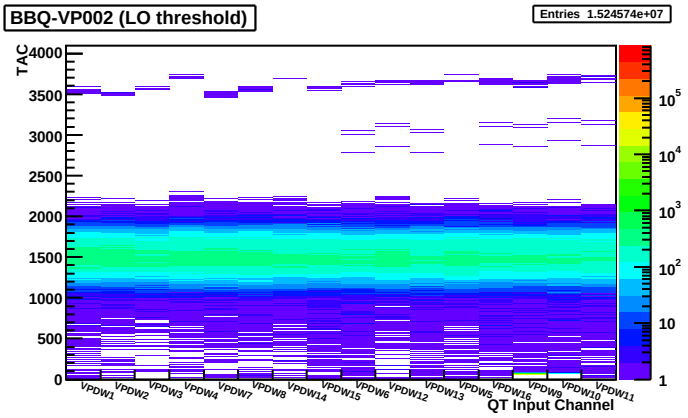

BBQ-ZD001
Entries 1.524574e+07
ADC
QT Input Channel

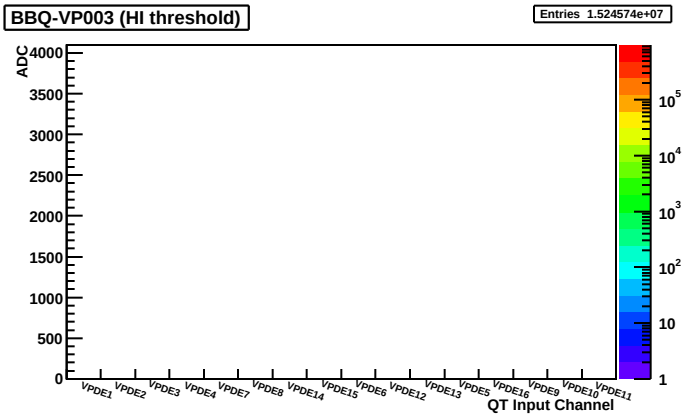

BBQ-ZD001
Entries 1.524574e+07
TAC
QT Input Channel

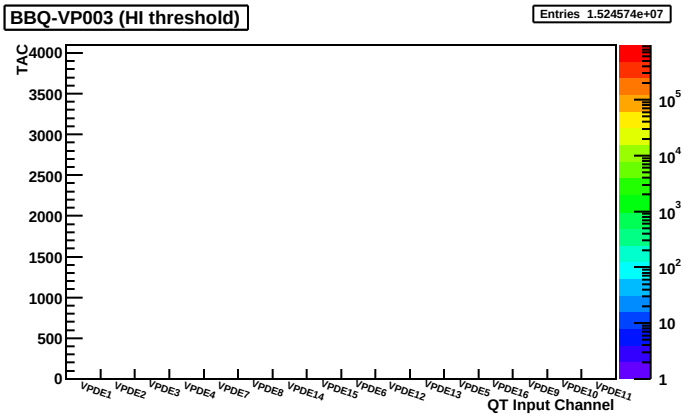

BBQ-VP001 (LO threshold)
Entries 1.524574e+07
ADC
QT Input Channel

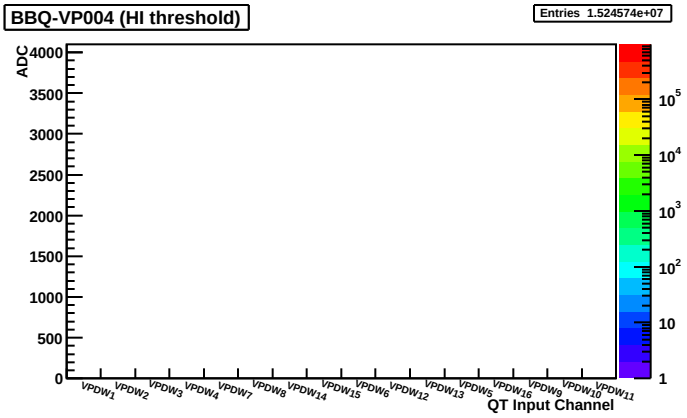

BBQ-VP001 (LO threshold)
Entries 1.524574e+07
TAC
QT Input Channel

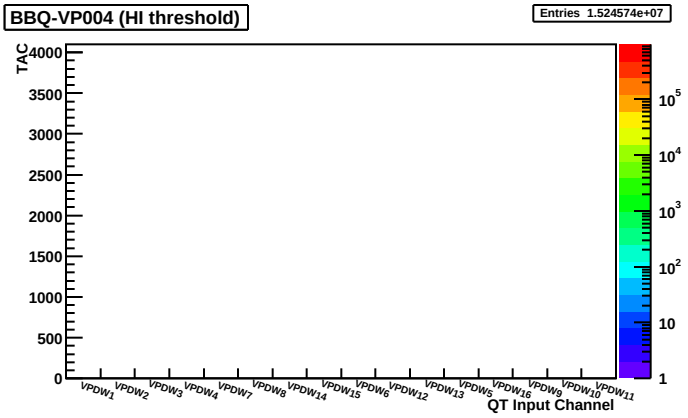

BBQ-VP002 (LO threshold)
Entries 1.524574e+07
ADC
QT Input Channel

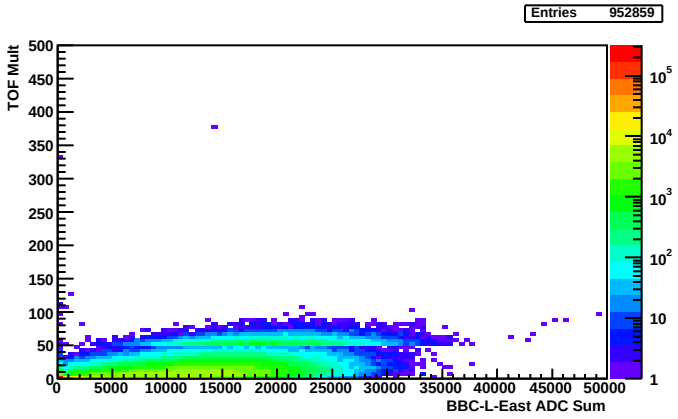

BBQ-VP002 (LO threshold)
Entries 1.524574e+07
TAC
QT Input Channel

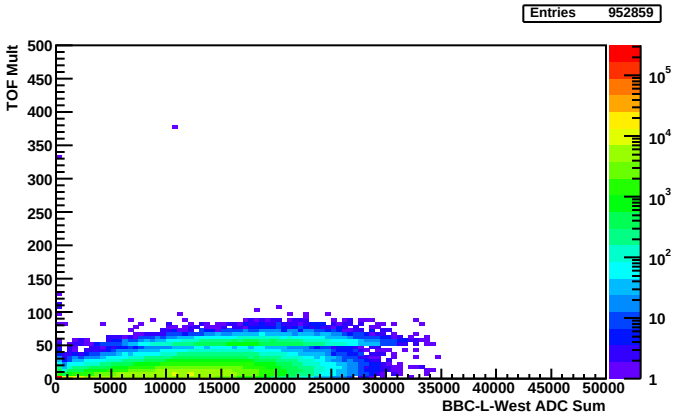

BBQ-VP003 (HI threshold)
Entries 1.524574e+07
ADC
QT Input Channel

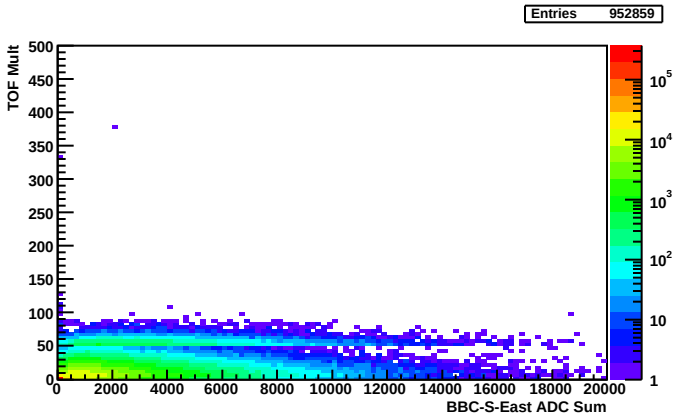

BBQ-VP003 (HI threshold)
Entries 1.524574e+07
TAC
QT Input Channel

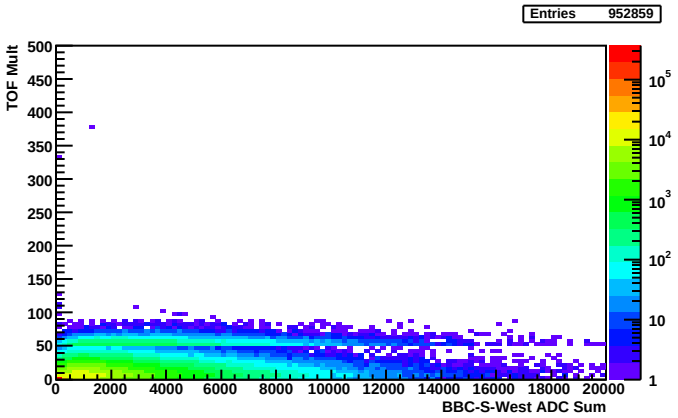

BBQ-VP004 (HI threshold)
Entries 1.524574e+07
ADC
QT Input Channel

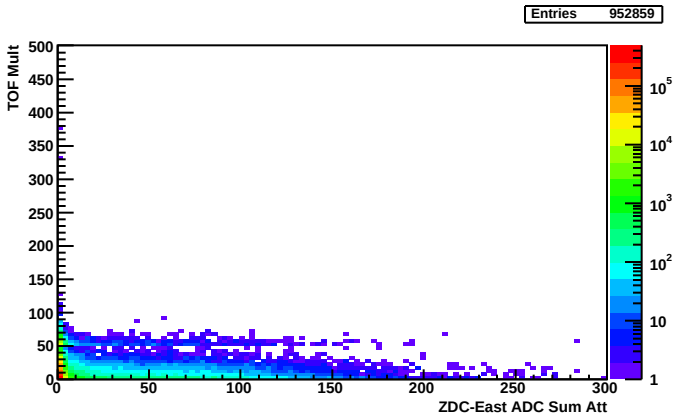

BBQ-VP004 (HI threshold)
Entries 1.524574e+07
TAC
QT Input Channel

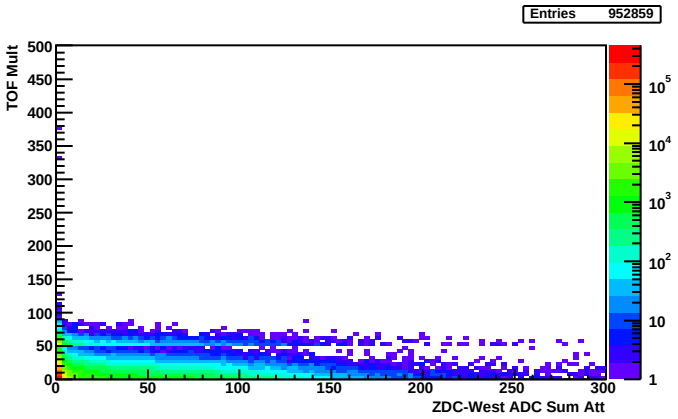

Entries 952859
TOF Mult
BBC-L-East ADC Sum

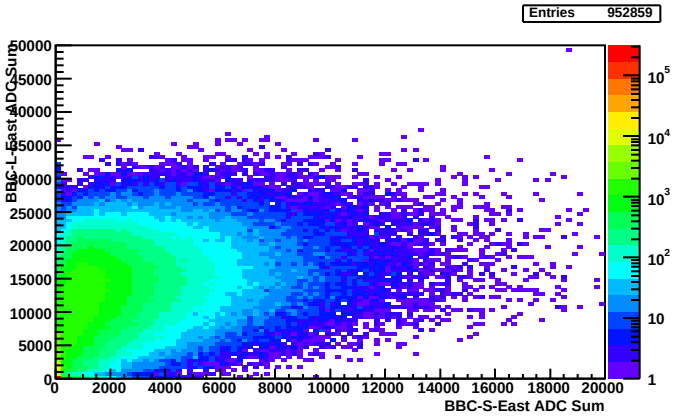

Entries 952859
TOF Mult
BBC-L-West ADC Sum

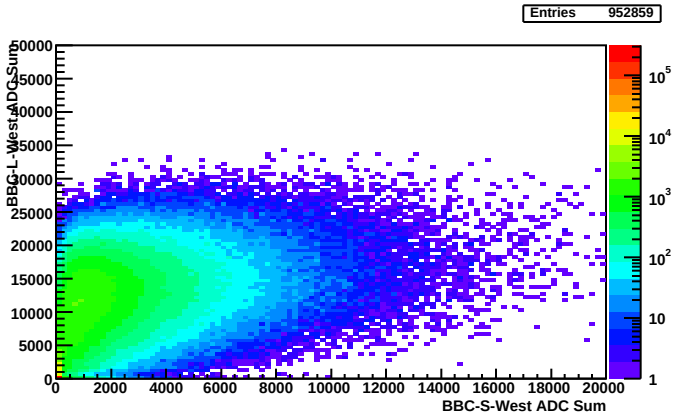

Entries 952859
TOF Mult
BBC-S-East ADC Sum

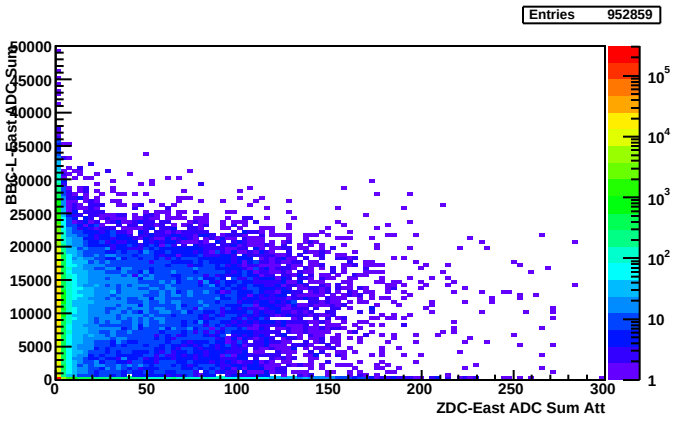

Entries 952859
TOF Mult
BBC-S-West ADC Sum

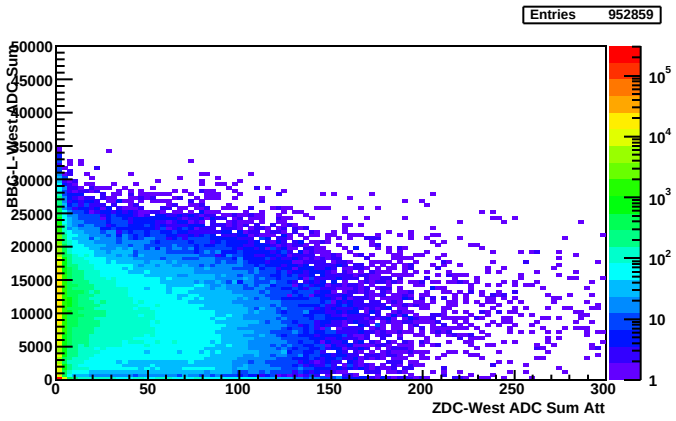

Entries 952859
TOF Mult
ZDC-East ADC Sum Att

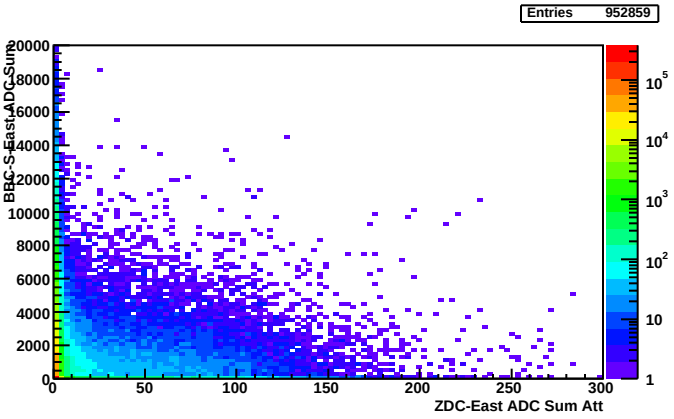

Entries 952859
TOF Mult
ZDC-West ADC Sum Att

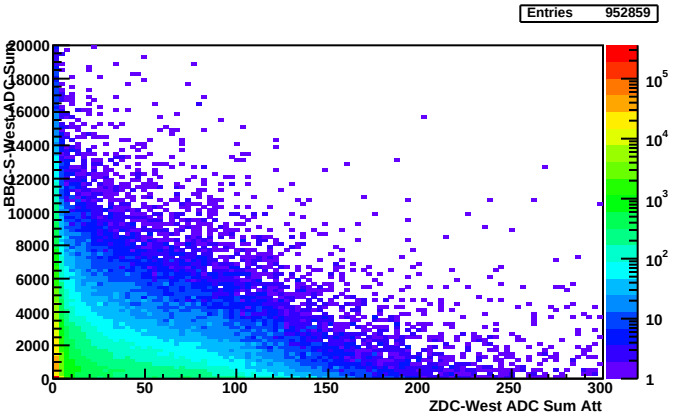

Entries 952859
BBC-L-East ADC Sum
BBC-S-East ADC Sum

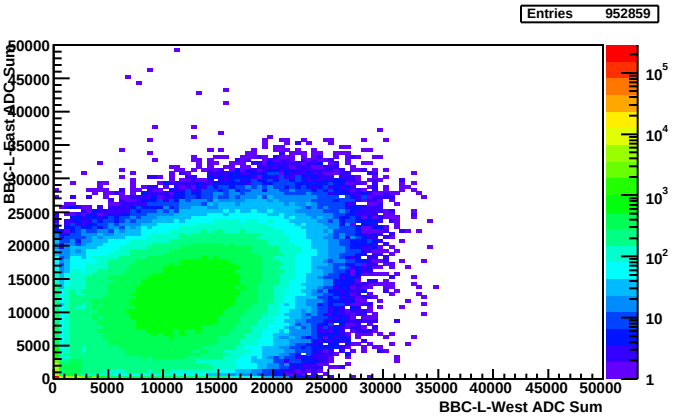

Entries 952859
BBC-L-West ADC Sum
BBC-S-West ADC Sum

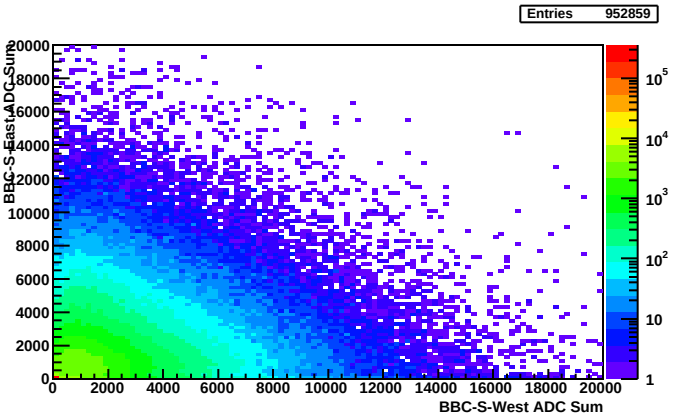

Entries 952859
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

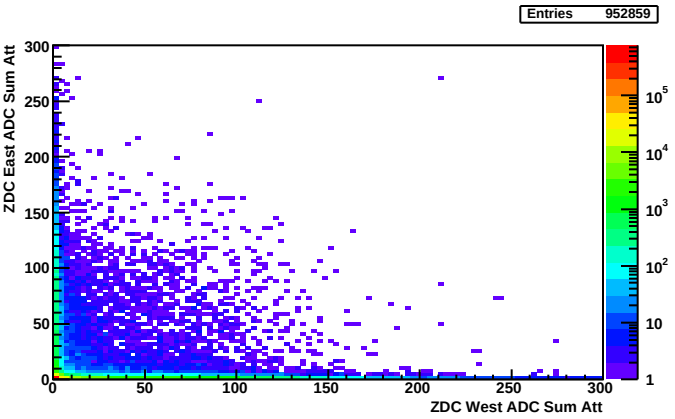

Entries 952859
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

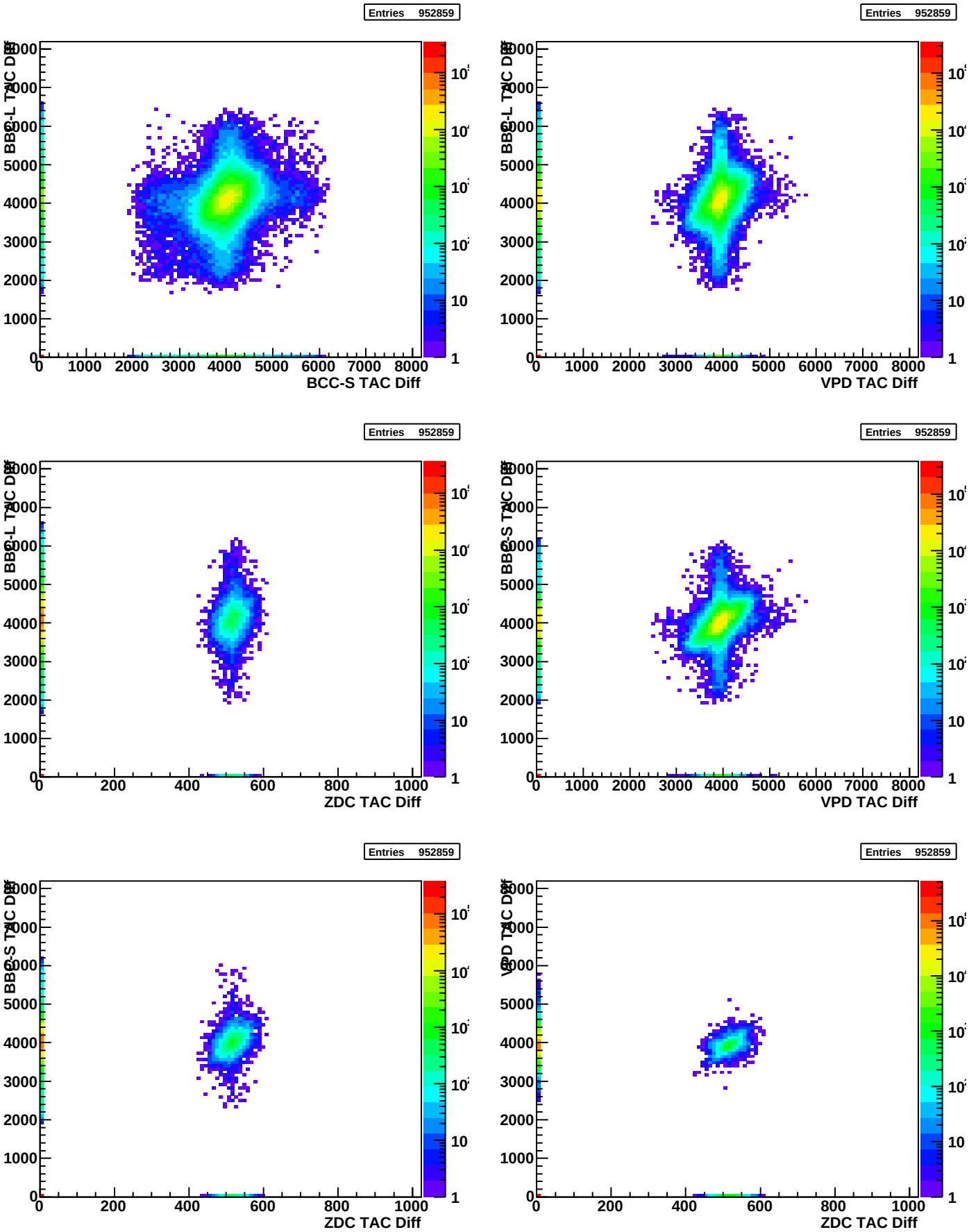

Entries 952859
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

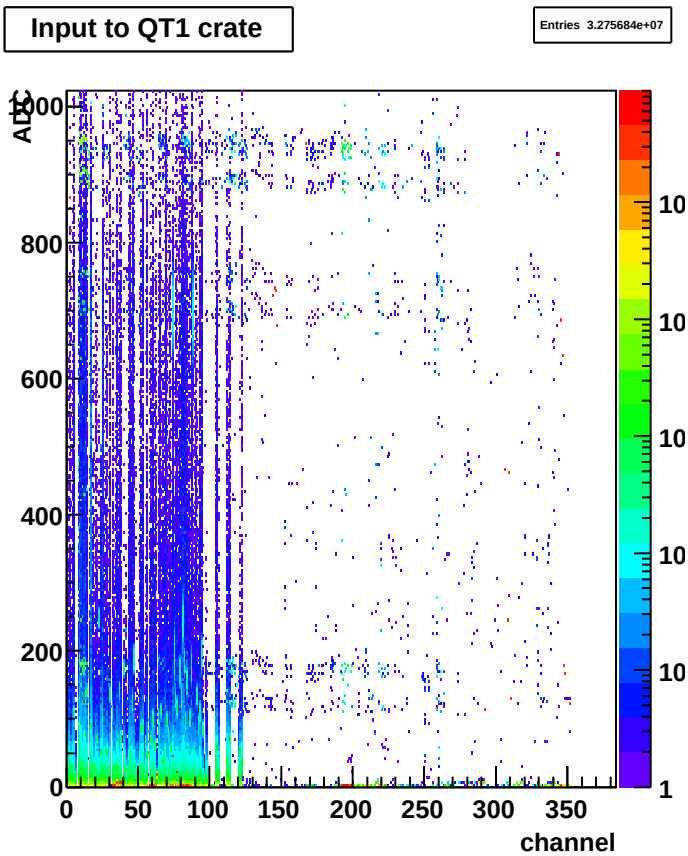

Entries 952859
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

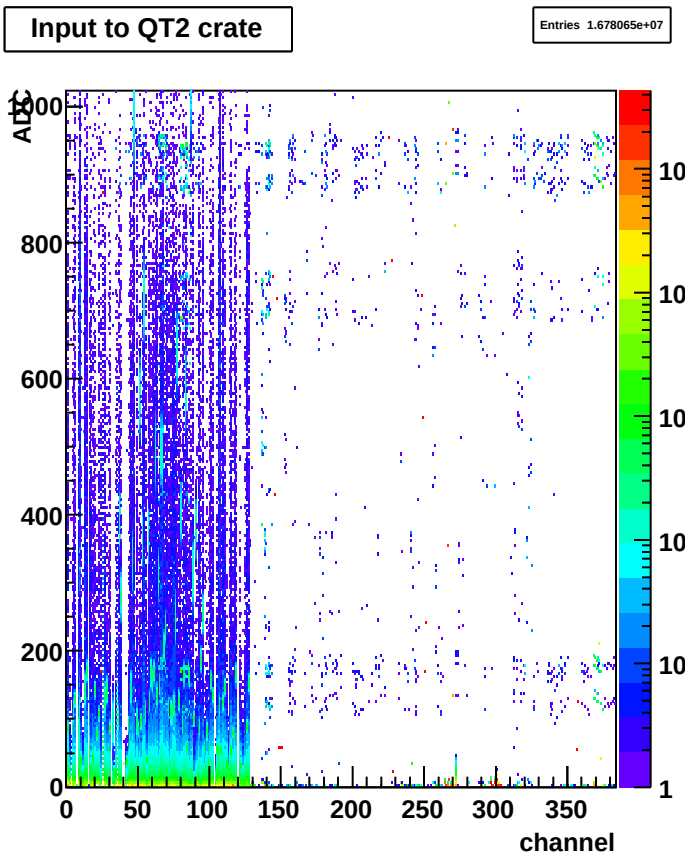

Entries 952859
BBC-L-East ADC Sum
BBC-L-West ADC Sum

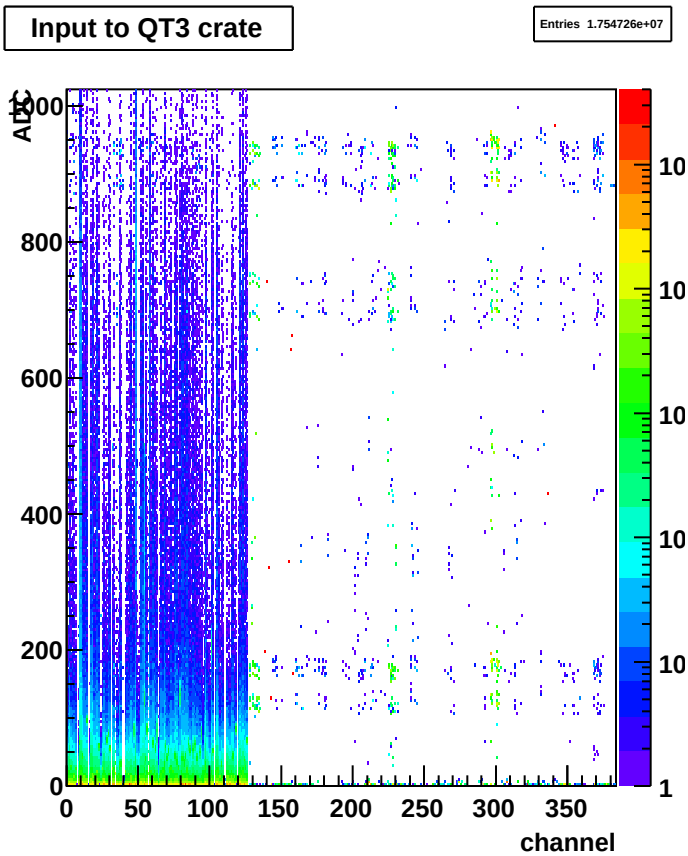

Entries 952859
BBC-S-East ADC Sum
BBC-S-West ADC Sum

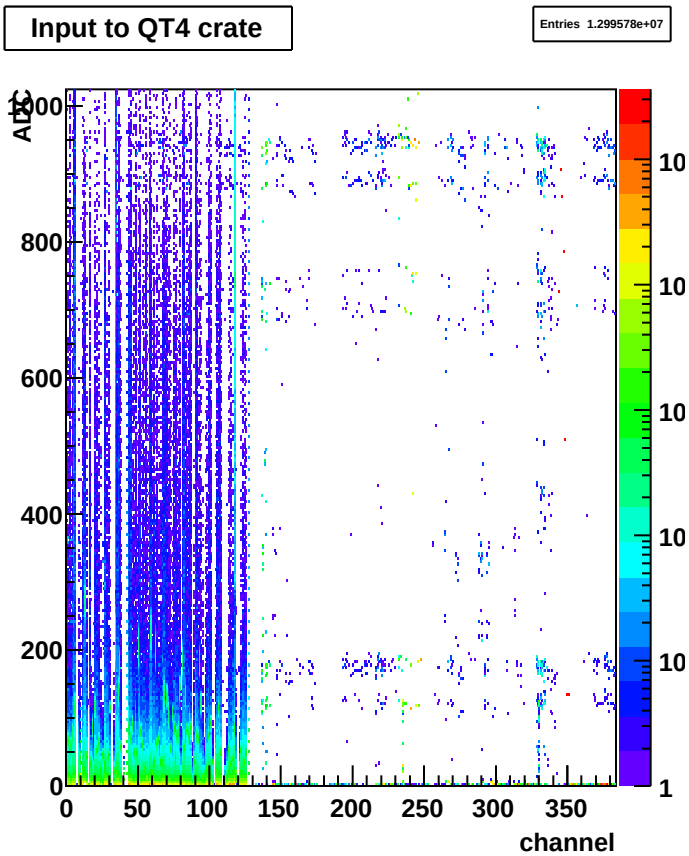

Entries 952859
ZDC East ADC Sum Att
ZDC West ADC Sum Att

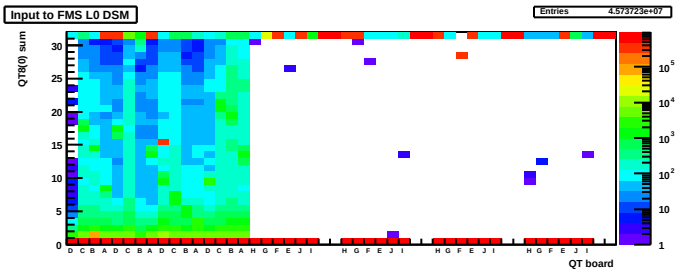

Entries 952859
BBC-L TAC Diff
BCC-S TAC Diff
Entries 952859
BBC-L TAC Diff
VPD TAC Diff
Entries 952859
BBC-L TAC Diff
ZDC TAC Diff
Entries 952859
BBC-S TAC Diff
VPD TAC Diff
Entries 952859
BBC-S TAC Diff
ZDC TAC Diff
Entries 952859
VPD TAC Diff
ZDC TAC Diff

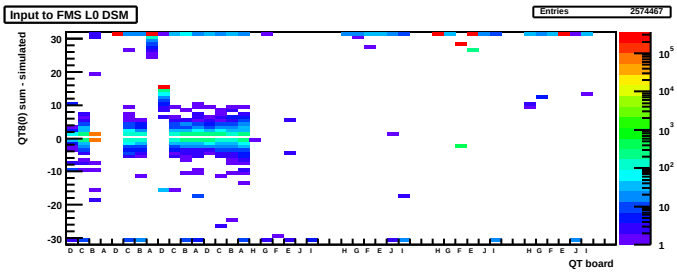

Input to QT1 crate
Entries 3.275684e+07
ADC
channel

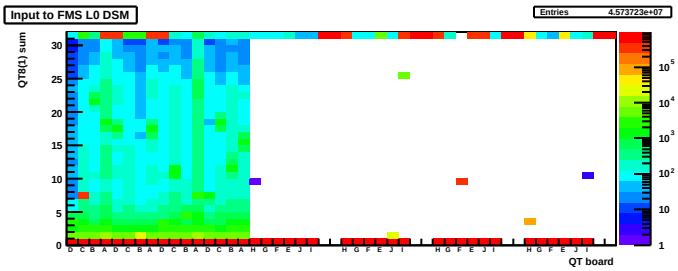

Input to QT2 crate
Entries 1.678065e+07
ADC
channel

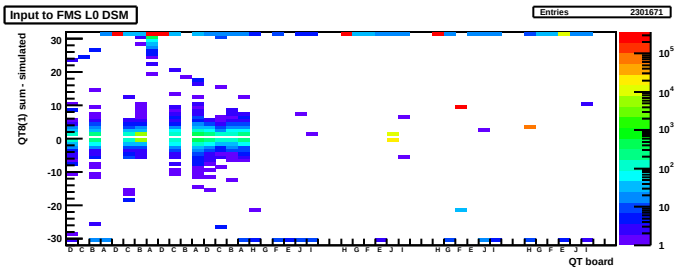

Input to QT3 crate
Entries 1.754726e+07
ADC
channel

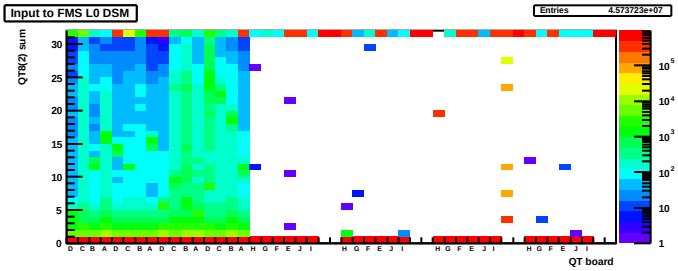

Input to QT4 crate
Entries 1.299578e+07
ADC
channel

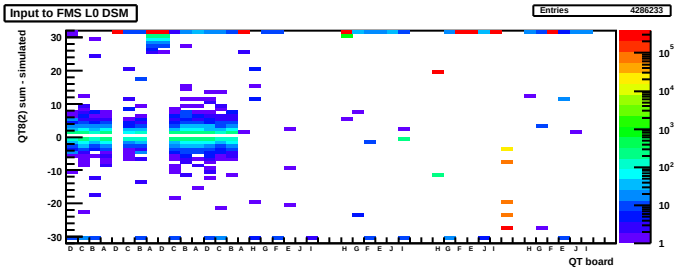

Input to FMS L0 DSM
Entries 4.573723e+07
QT8(0) sum
QT board

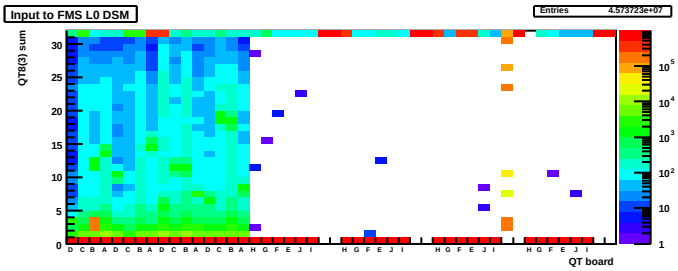

Input to FMS L0 DSM
Entries 2574467
QT8(0) sum - simulated
QT board

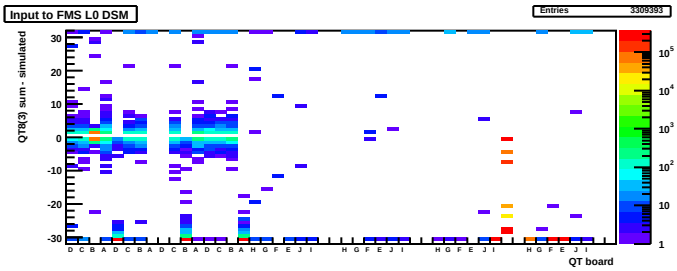

Input to FMS L0 DSM
Entries 4.573723e+07
QT8(1) sum
QT board

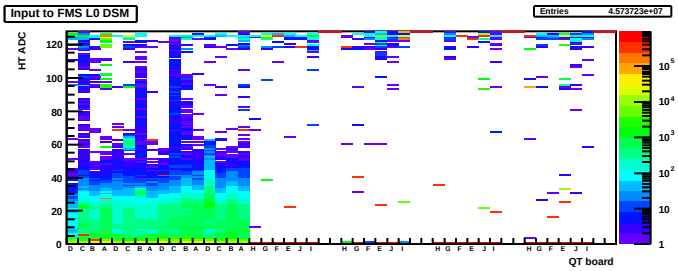

Input to FMS L0 DSM
Entries 2301671
QT8(1) sum - simulated
QT board

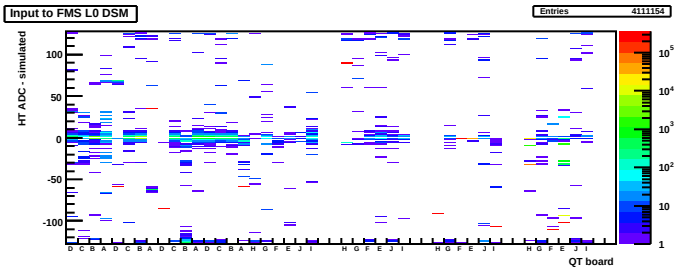

Input to FMS L0 DSM
Entries 4.573723e+07
QT8(2) sum
QT board

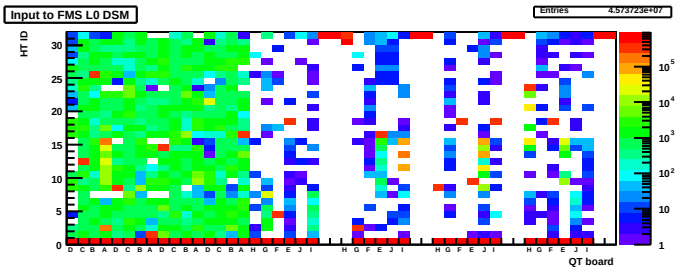

Input to FMS L0 DSM
Entries 4286233
QT8(2) sum - simulated
QT board

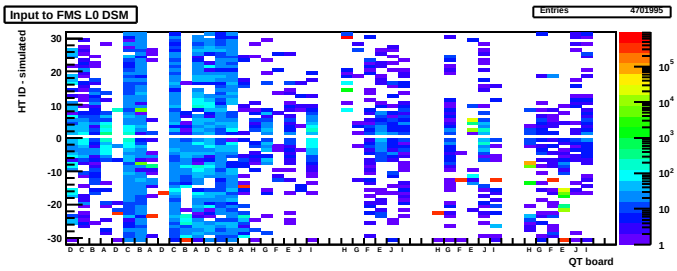

Input to FMS L0 DSM
Entries 4.573723e+07
QT8(3) sum
QT board

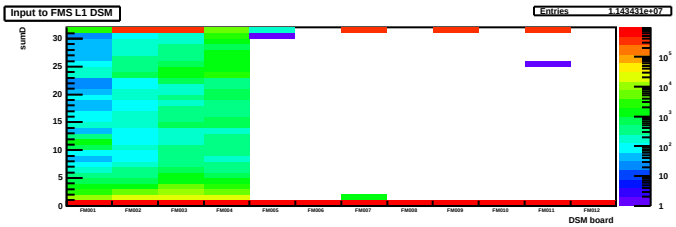

Input to FMS L0 DSM
Entries 3309393
QT8(3) sum - simulated
QT board

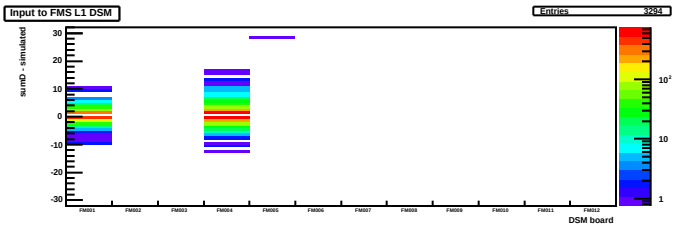

Input to FMS L0 DSM
Entries 4.573723e+07
HT ADC
QT board

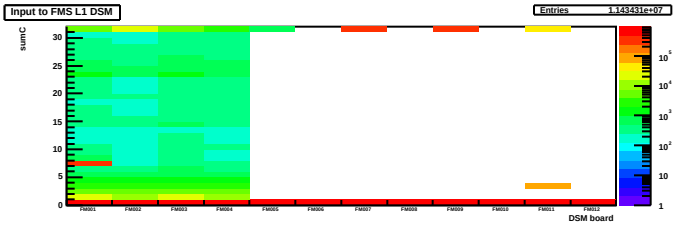

Input to FMS L0 DSM
Entries 4111154
HT ADC - simulated
QT board

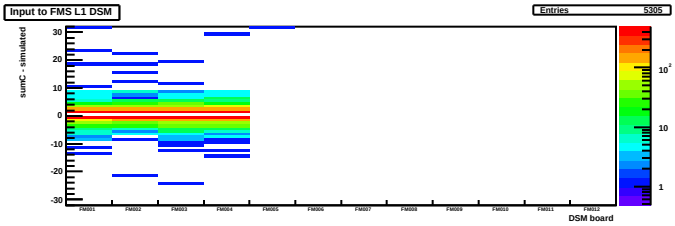

Input to FMS L0 DSM
Entries 4.573723e+07
HT ID
QT board

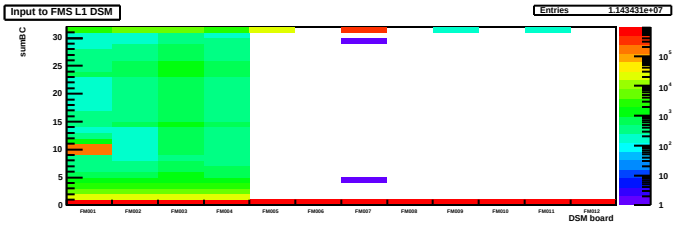

Input to FMS L0 DSM
Entries 4701995
HT ID - simulated
QT board

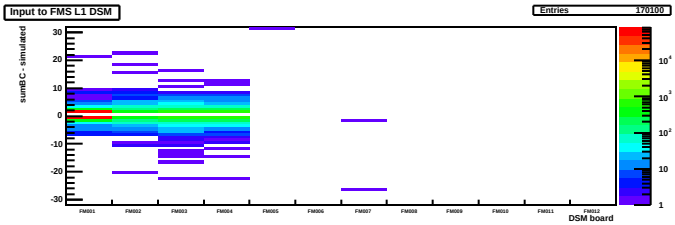

Input to FMS L1 DSM
Entries 1.143431e+07
sumD
DSM board

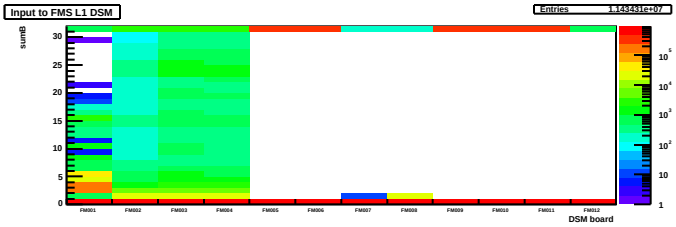

Input to FMS L1 DSM
Entries 3294
sumD - simulated
DSM board

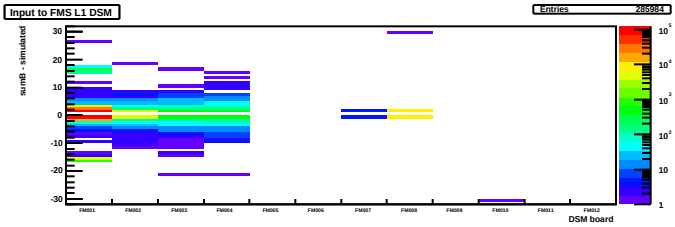

Input to FMS L1 DSM
Entries 1.143431e+07
sumC
DSM board

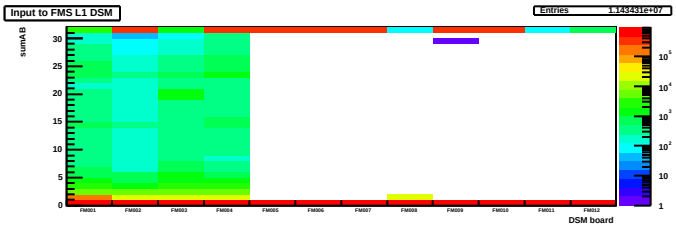

Input to FMS L1 DSM
Entries 5305
sumC - simulated
DSM board

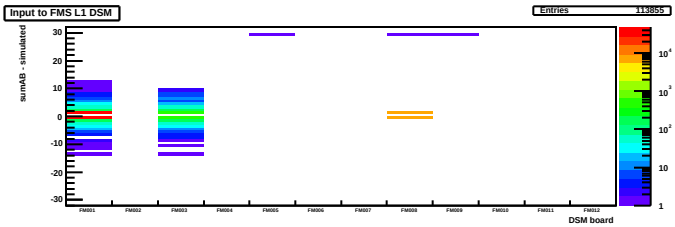

Input to FMS L1 DSM
Entries 1.143431e+07
sumBC
DSM board

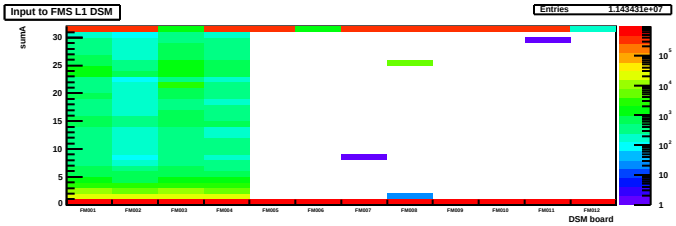

Input to FMS L1 DSM
Entries 170100
sumBC - simulated
DSM board

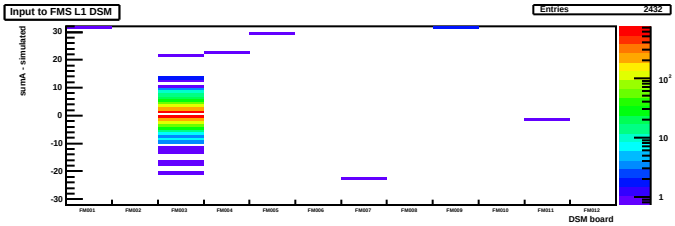

Input to FMS L1 DSM
Entries 1.143431e+07
sumB
DSM board

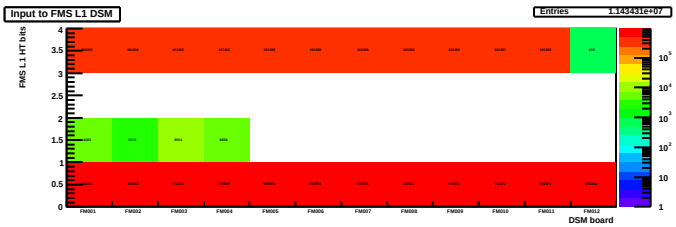

Input to FMS L1 DSM
Entries 285984
sumB - simulated
DSM board

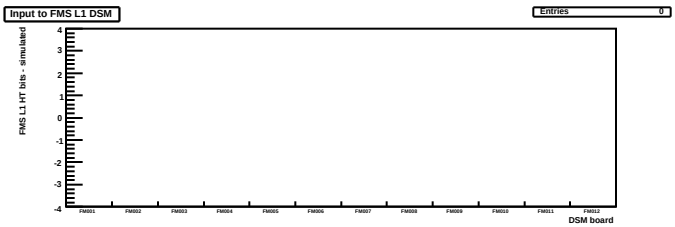

Input to FMS L1 DSM
Entries 1.143431e+07
sumAB
DSM board

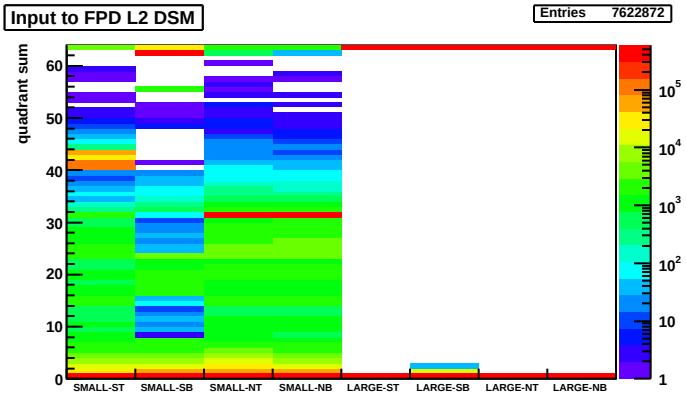

Input to FMS L1 DSM
Entries 113855
sumAB - simulated
DSM board

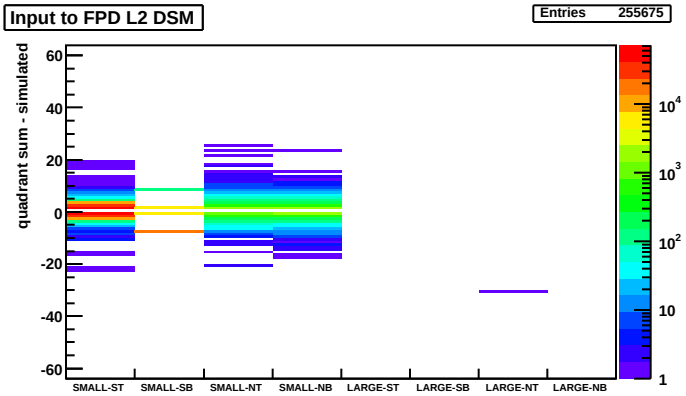

Input to FMS L1 DSM
Entries 1.143431e+07
sumA
DSM board

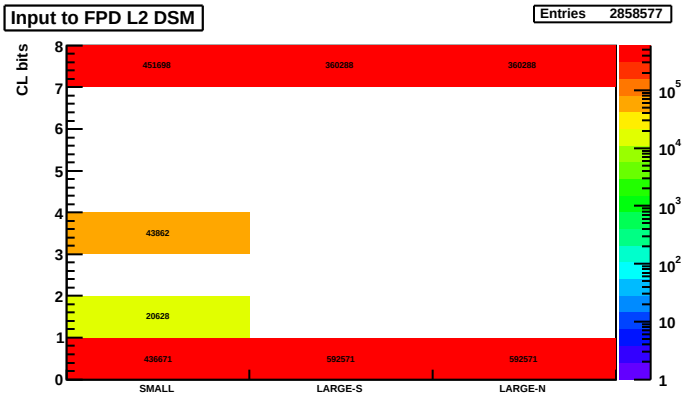

Input to FMS L1 DSM
Entries 2432
sumA - simulated
DSM board

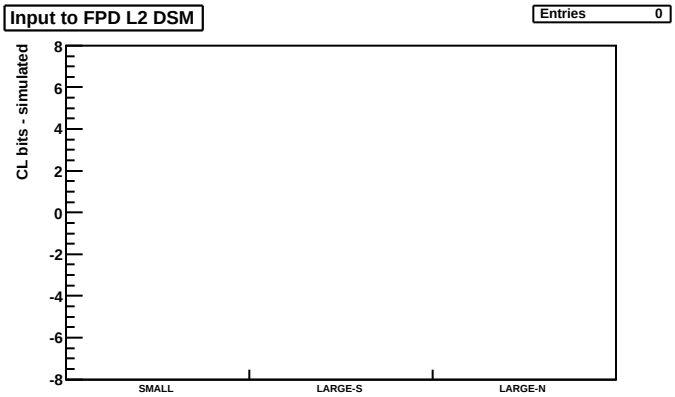

Input to FMS L1 DSM
Entries 1.143431e+07
FMS L1 HT bits
DSM board

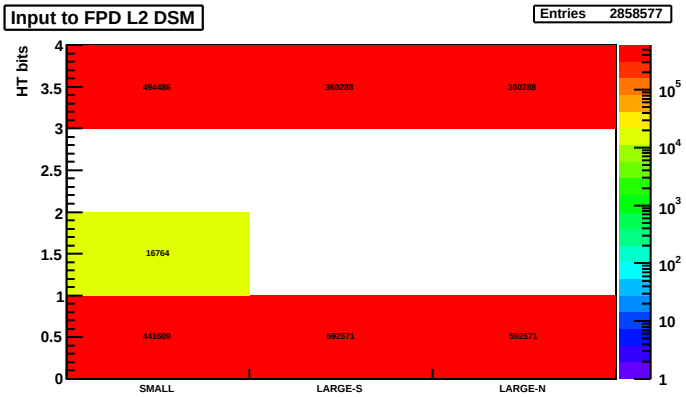

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

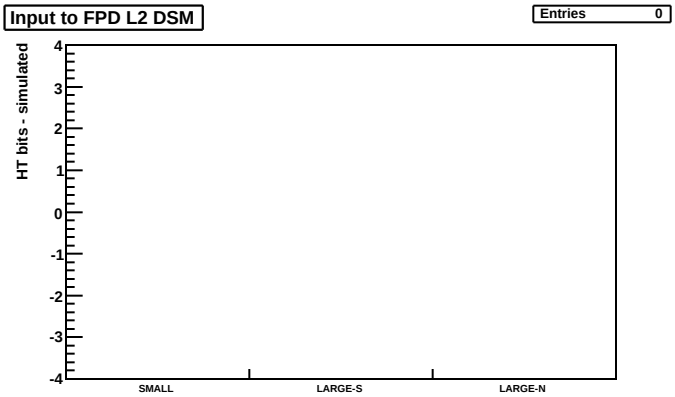

Input to FPD L2 DSM
Entries 7622872
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

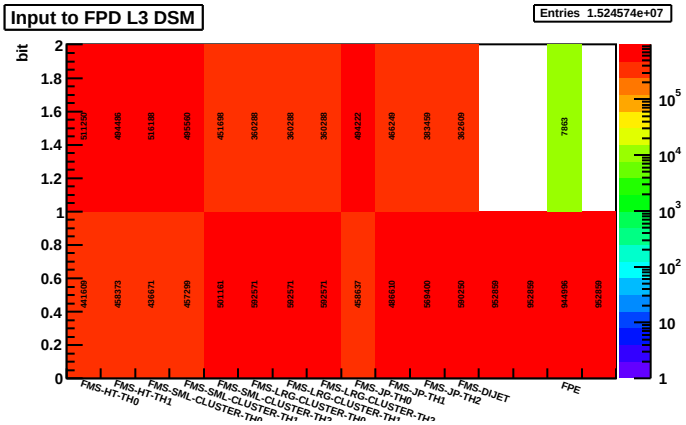

Input to FPD L2 DSM
Entries 255675
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

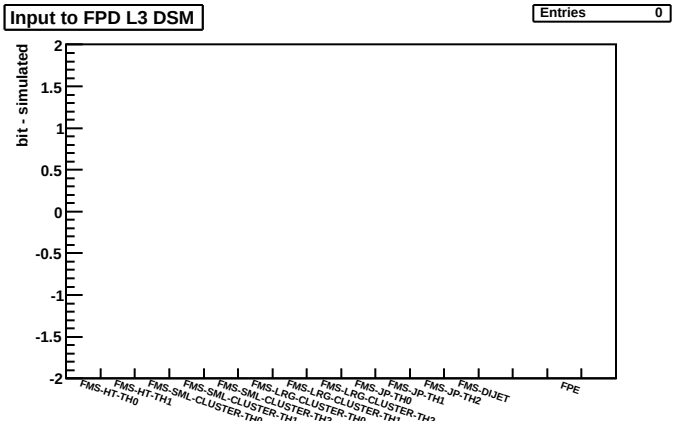

Input to FPD L2 DSM
Entries 2858577
CL bits
451698
360288
360288
43862
20628
436671
592571
592571
SMALL
LARGE-S
LARGE-N

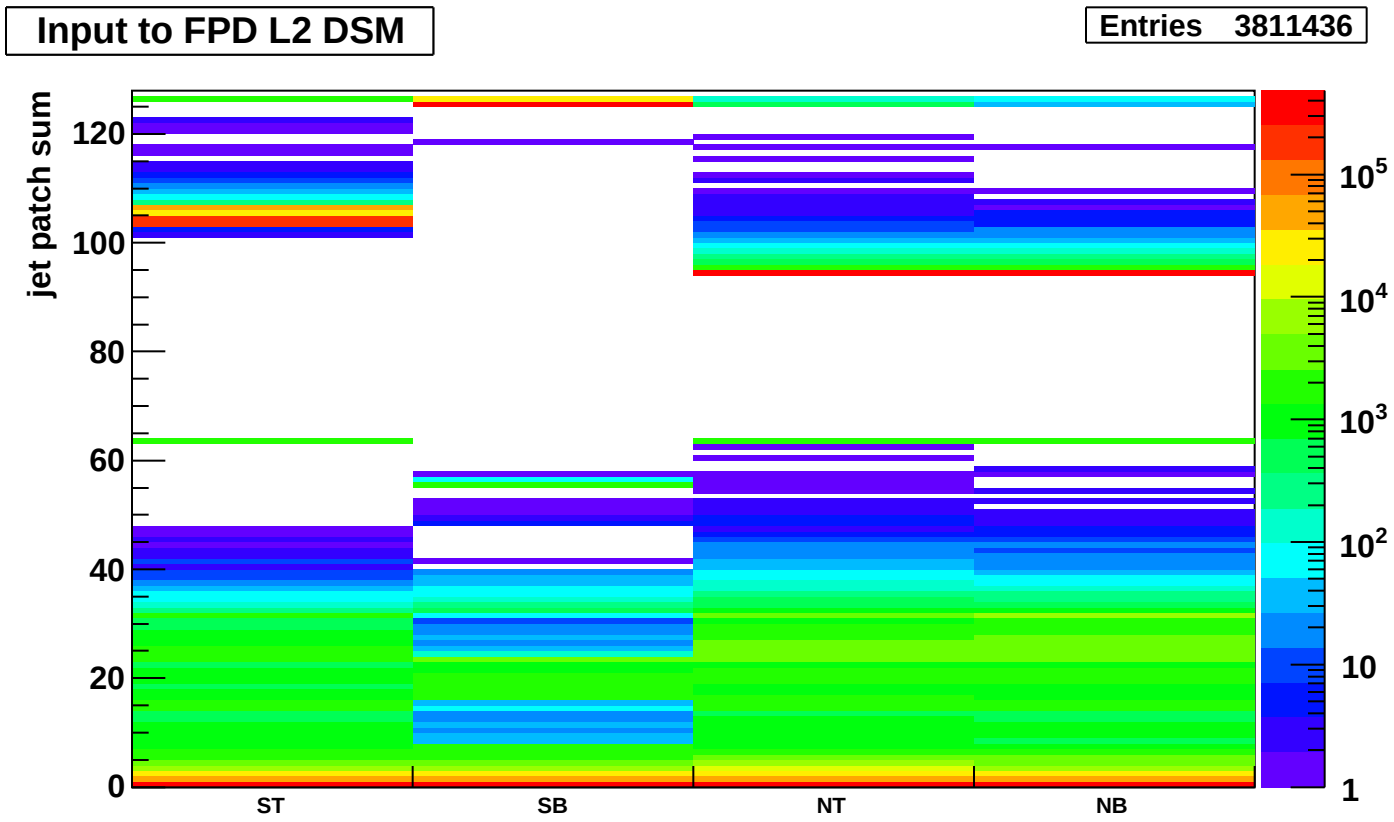

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

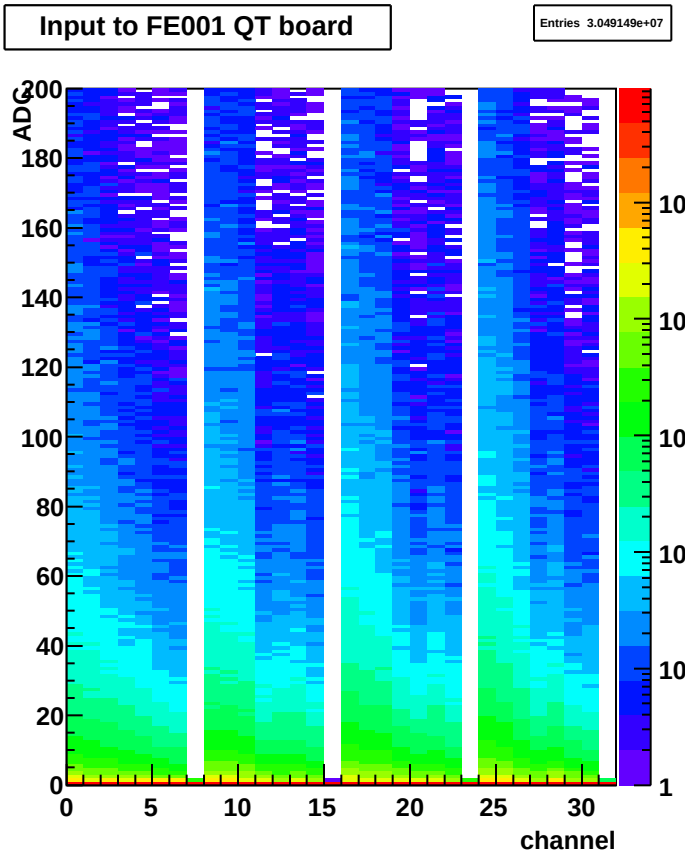

Input to FPD L2 DSM
Entries 2858577
HT bits
494486
360288
360288
16764
441609
592571
592571
SMALL
LARGE-S
LARGE-N

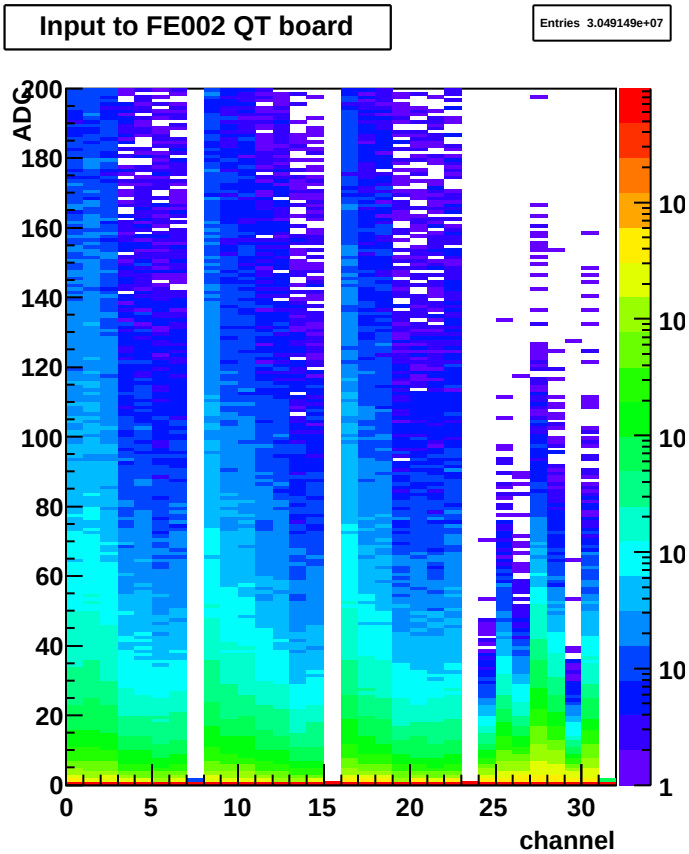

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

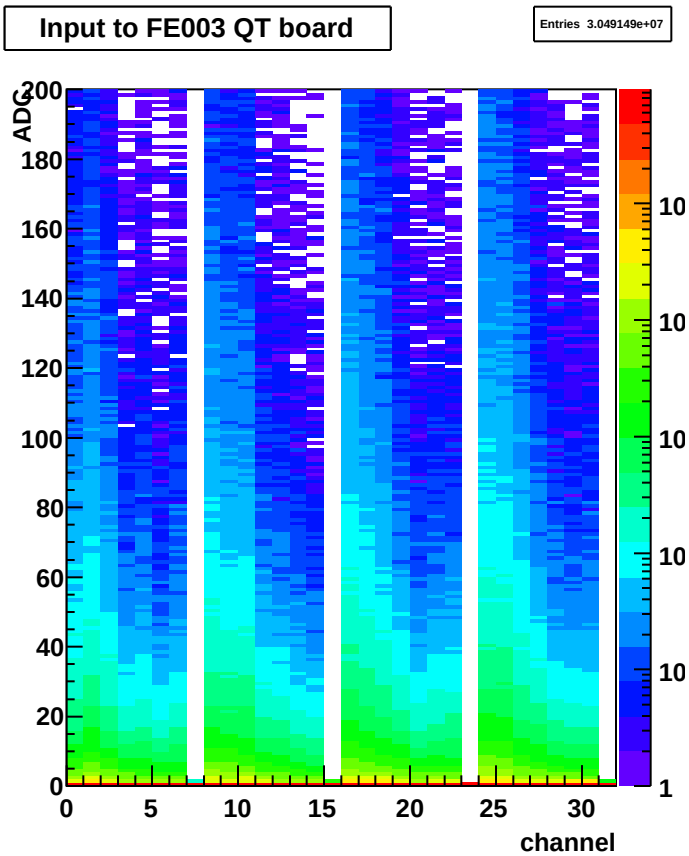

Input to FPD L3 DSM
Entries 1.524574e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

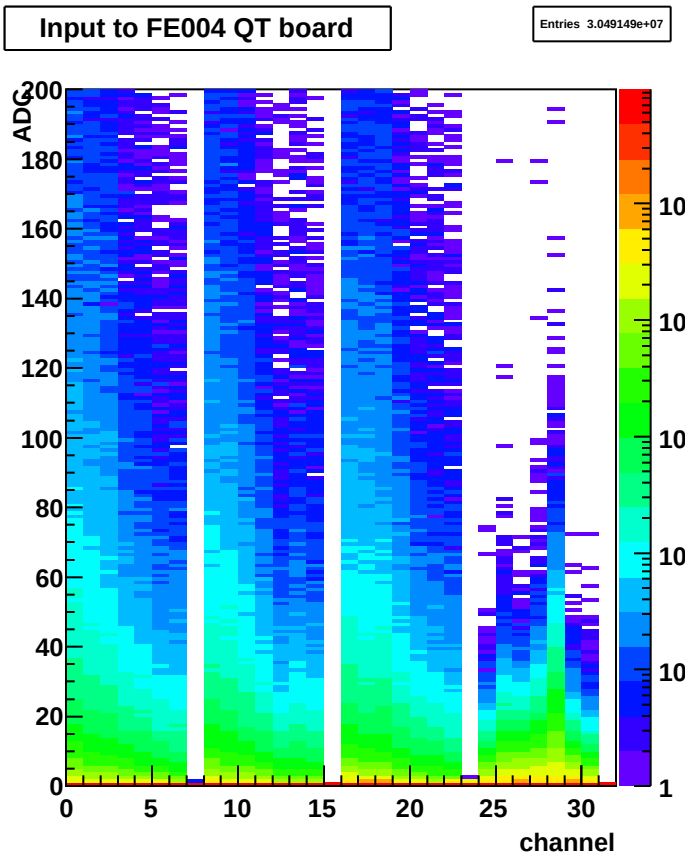

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

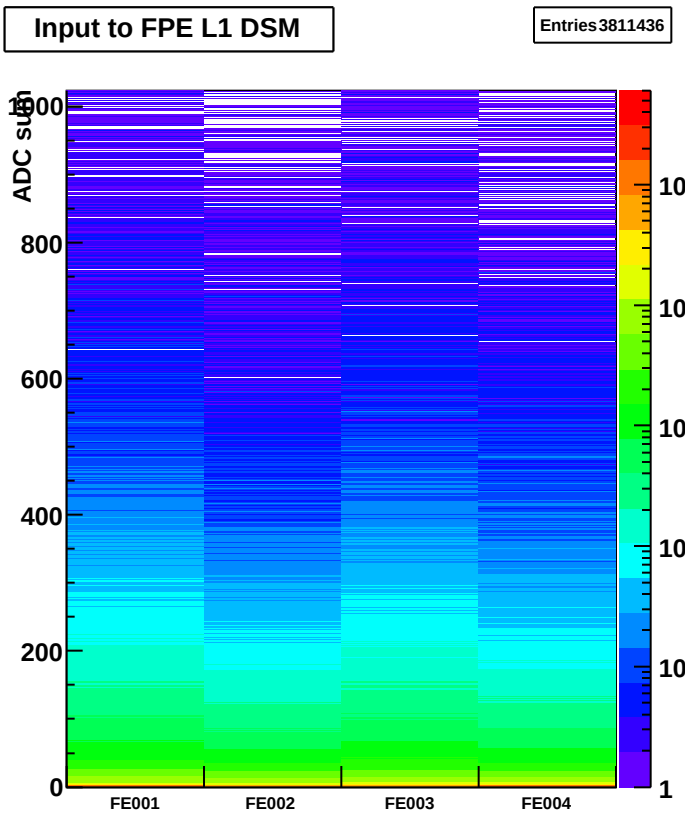

Input to FPD L2 DSM
Entries 3811436
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

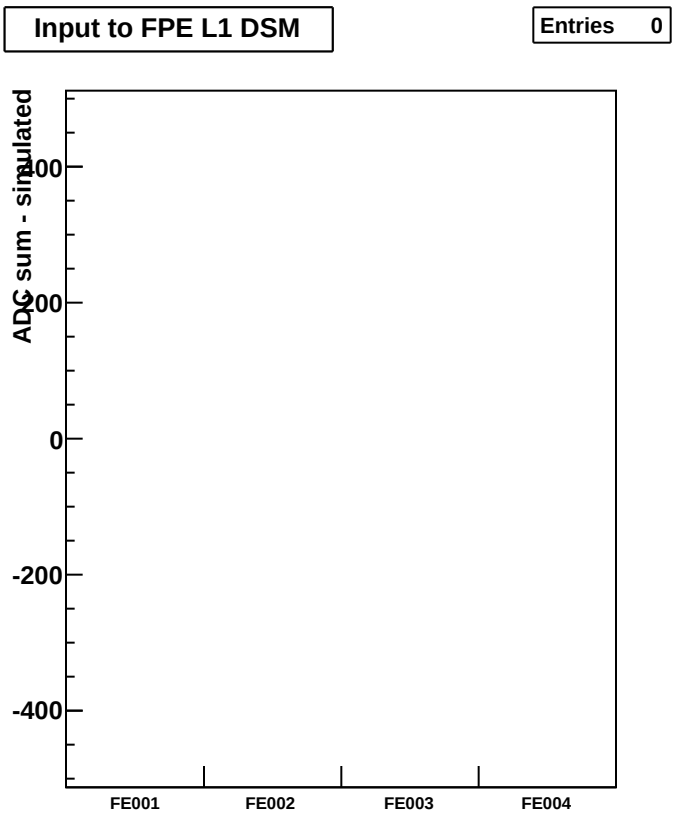

Input to FE001 QT board
Entries 3.049149e+07
ADC
channel

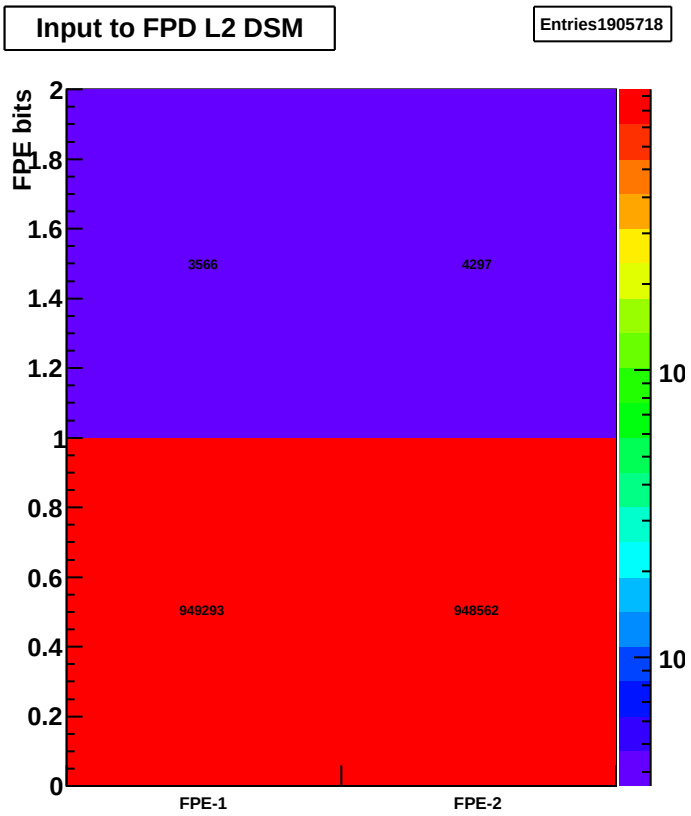

Input to FE002 QT board
Entries 3.049149e+07
ADC
channel

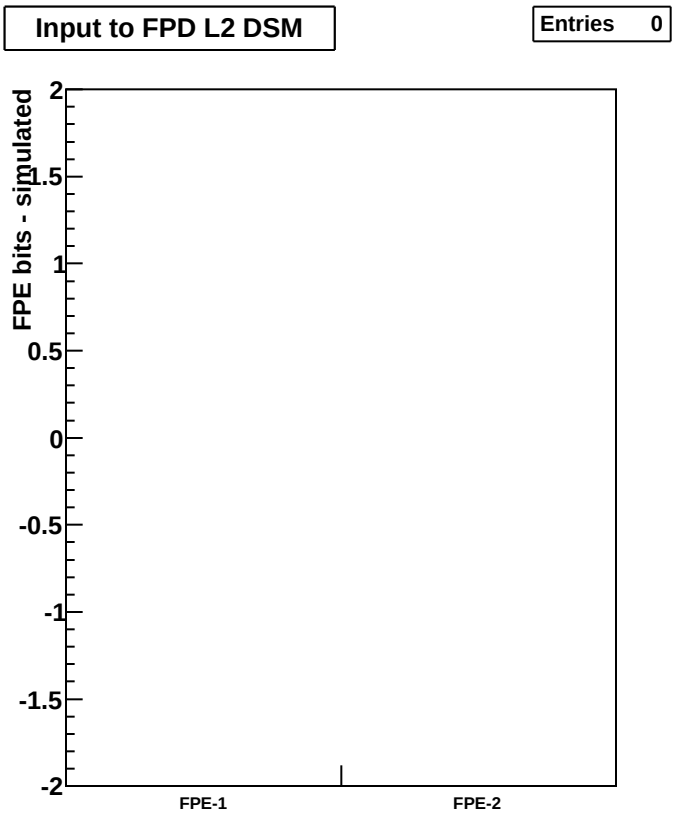

Input to FE003 QT board
Entries 3.049149e+07
ADC
channel


Input to FE004 QT board
Entries 3.049149e+07
ADC
channel


Input to FPE L1 DSM
Entries 3811436
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1905718
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3566
4297
949293
948562
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2