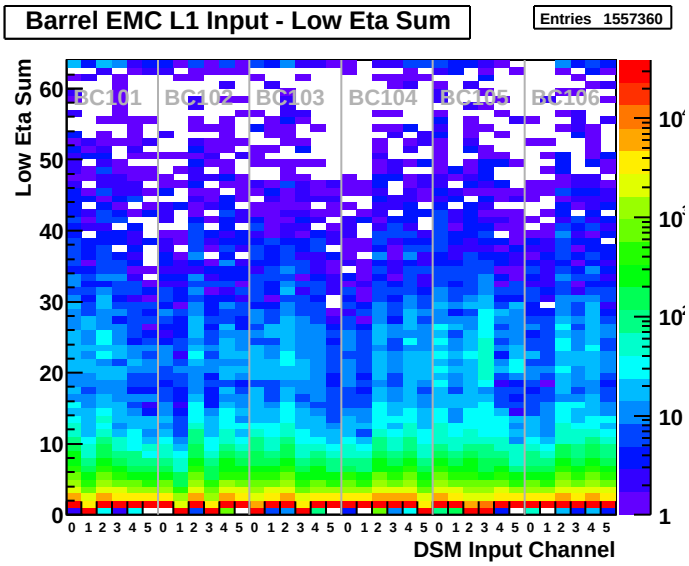
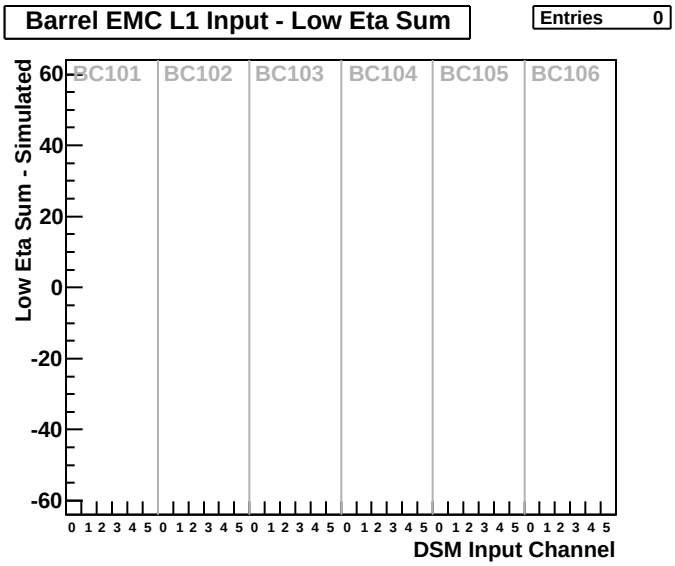
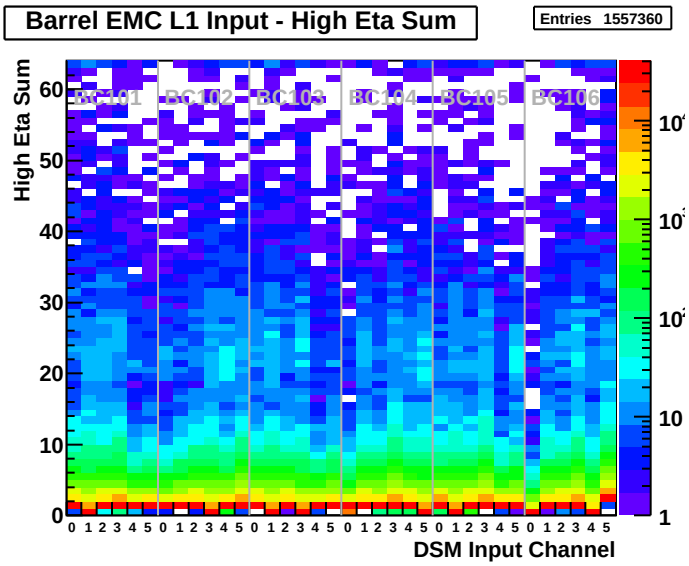
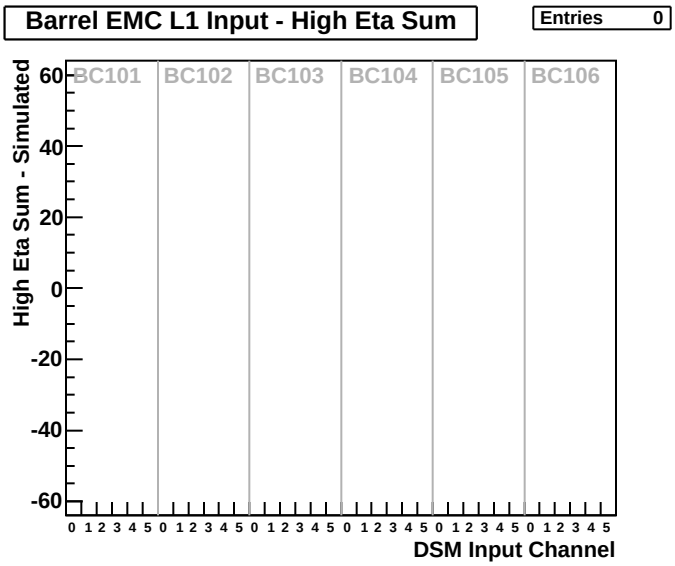
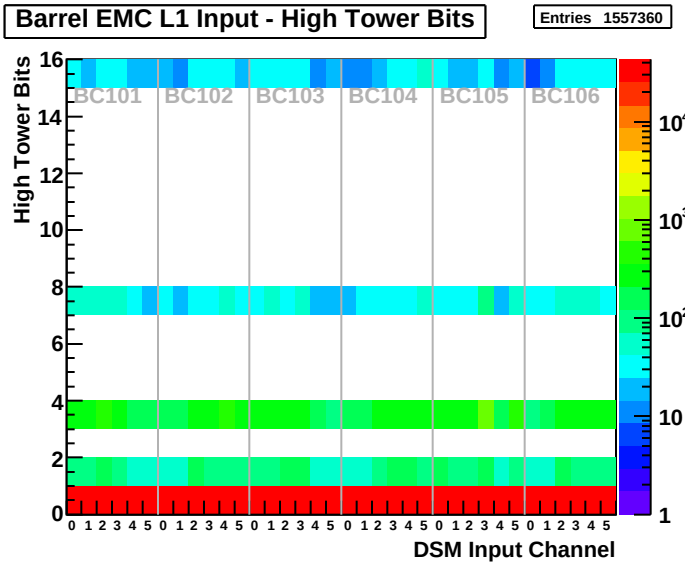
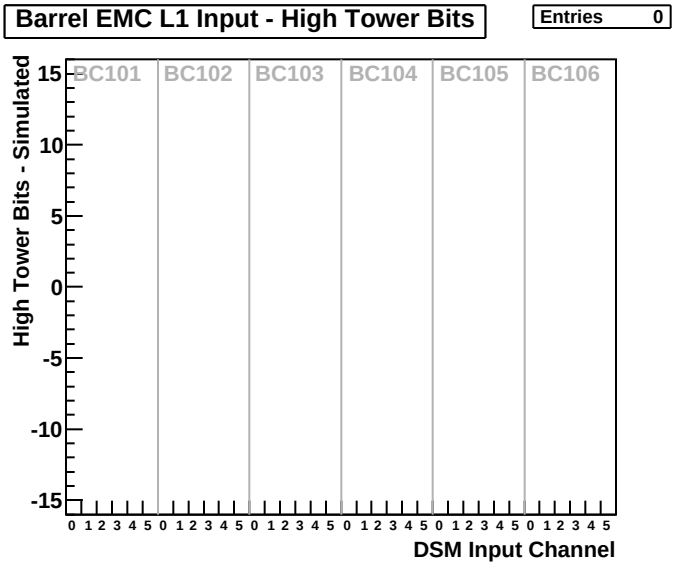

Barrel EMC L1 Input - Low Eta Sum
Entries 1557360
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

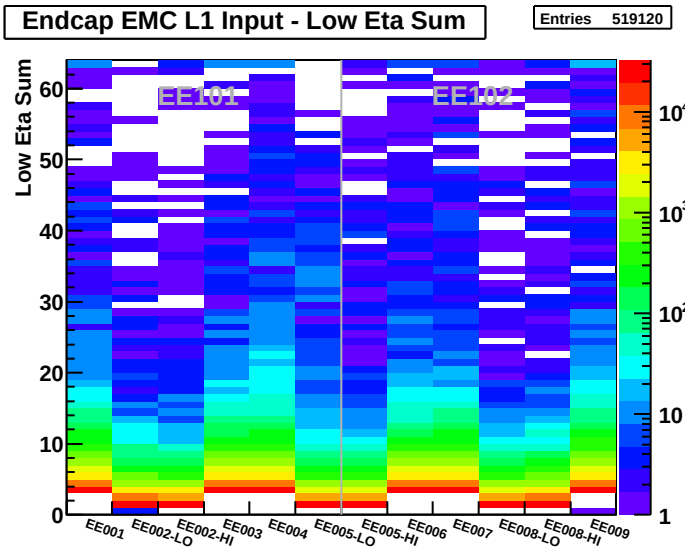

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

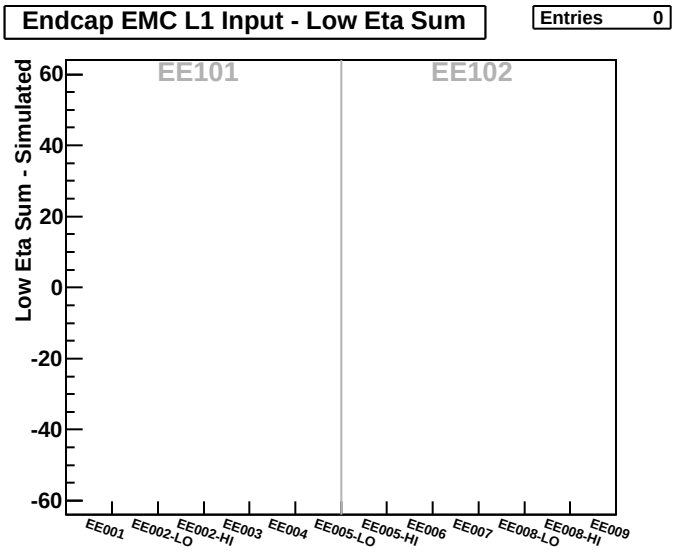

Barrel EMC L1 Input - High Eta Sum
Entries 1557360
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

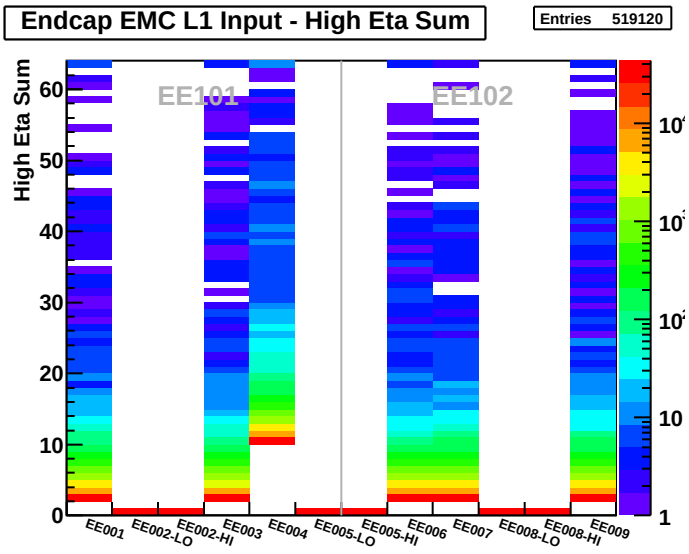

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

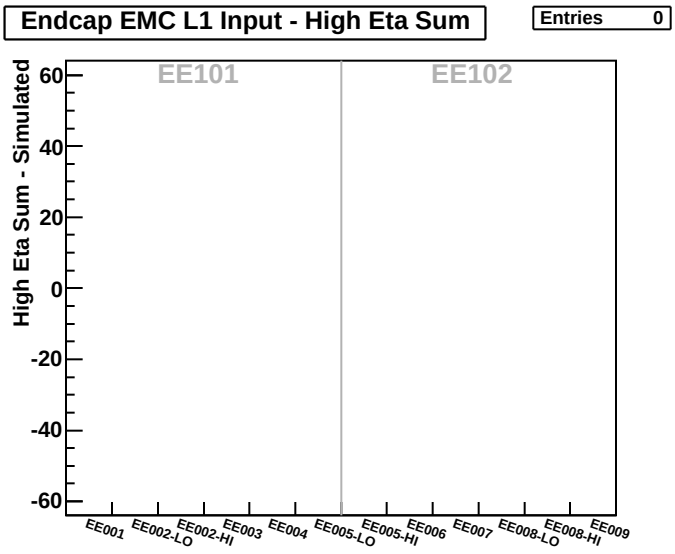

Barrel EMC L1 Input - High Tower Bits
Entries 1557360
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

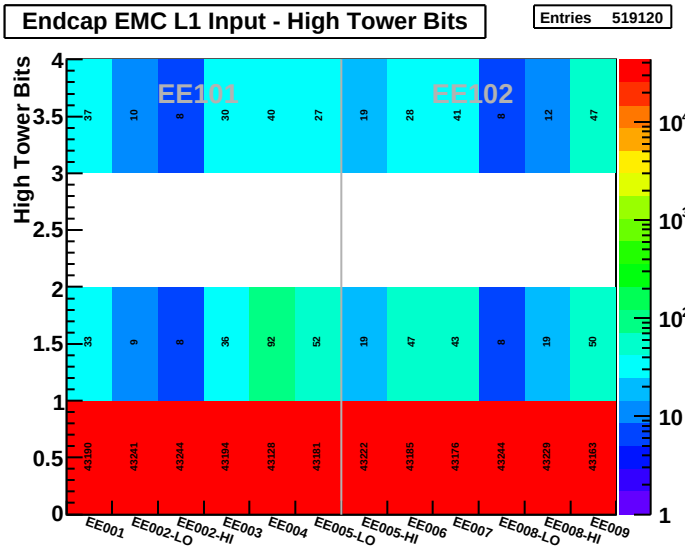

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

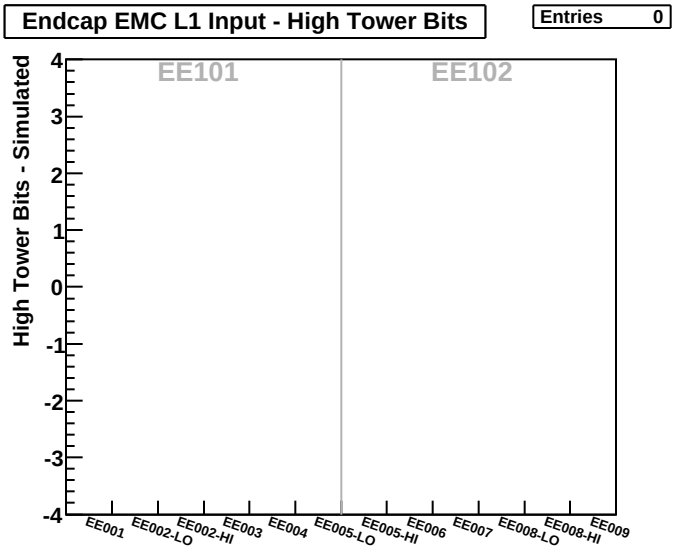

Endcap EMC L1 Input - Low Eta Sum
Entries 519120
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

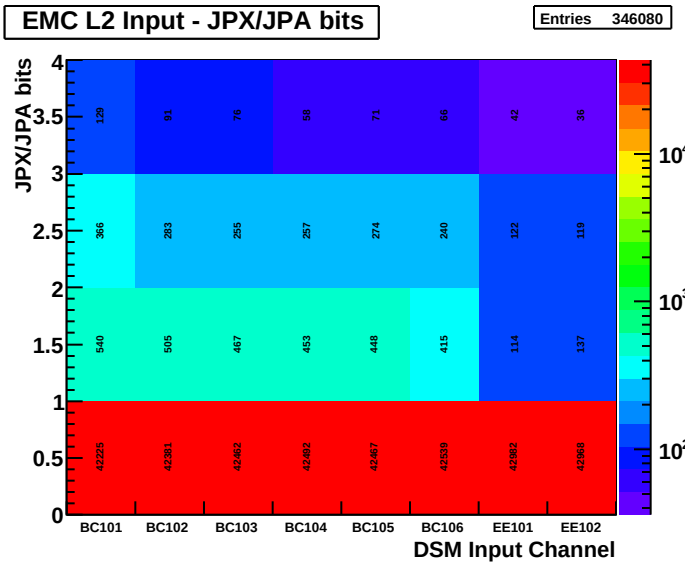

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

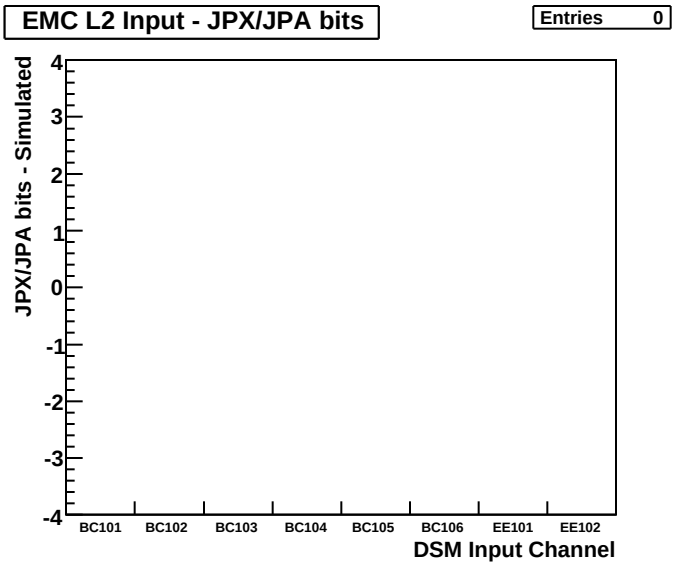

Endcap EMC L1 Input - High Eta Sum
Entries 519120
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

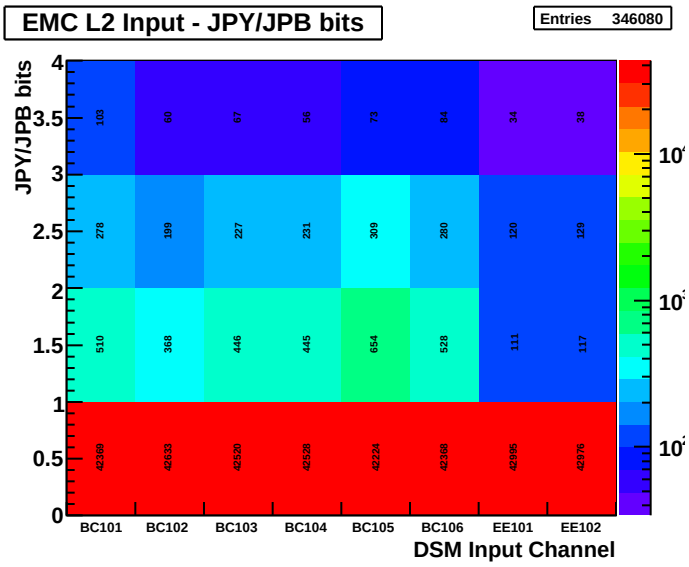

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

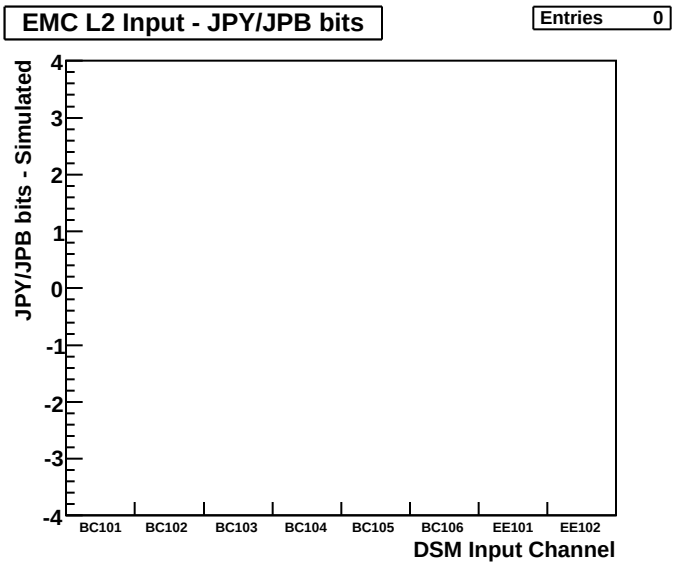

Endcap EMC L1 Input - High Tower Bits
Entries 519120
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

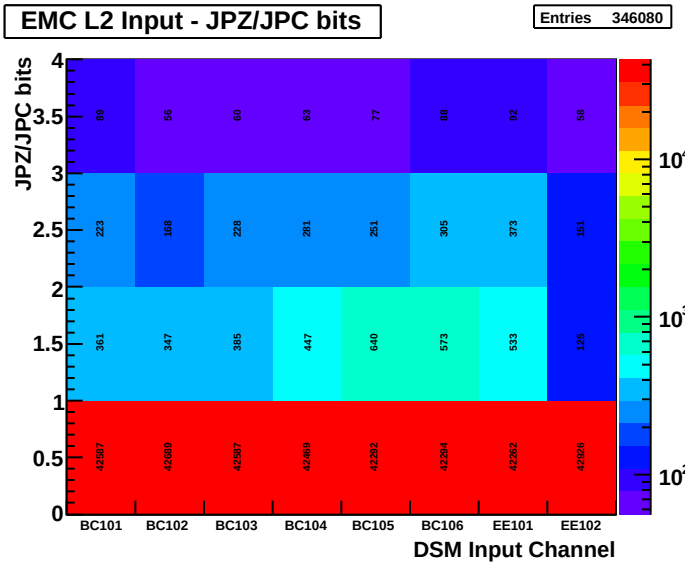

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

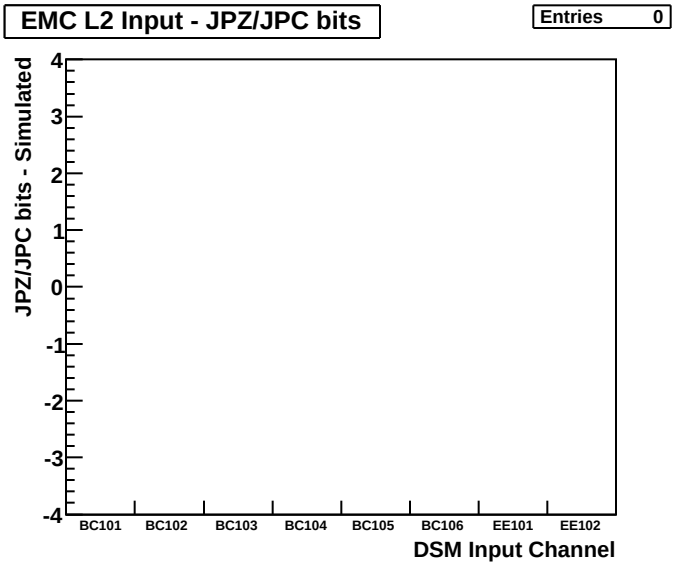

EMC L2 Input - JPX/JPA bits
Entries 346080
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
129 91 76 58 71 66 42 36
366 283 255 257 274 240 122 119
540 505 467 453 448 415 114 137
42225 42381 42462 42492 42467 42539 42982 42968

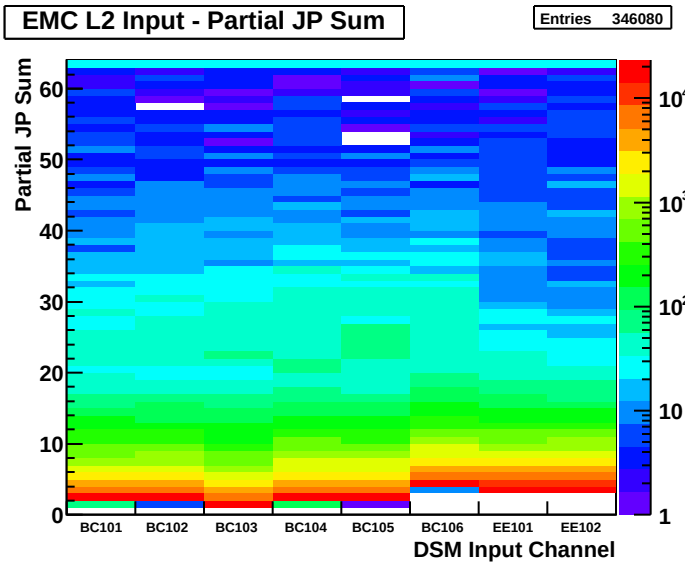

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

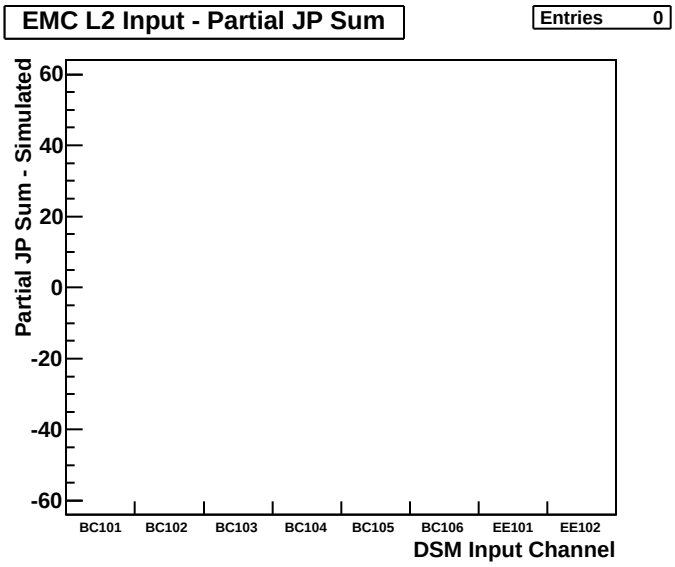

EMC L2 Input - JPY/JPB bits
Entries 346080
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
103 60 67 56 73 84 34 38
278 199 227 231 309 280 120 129
510 368 446 445 654 528 111 117
42369 42533 42520 42528 42224 42368 42995 42976

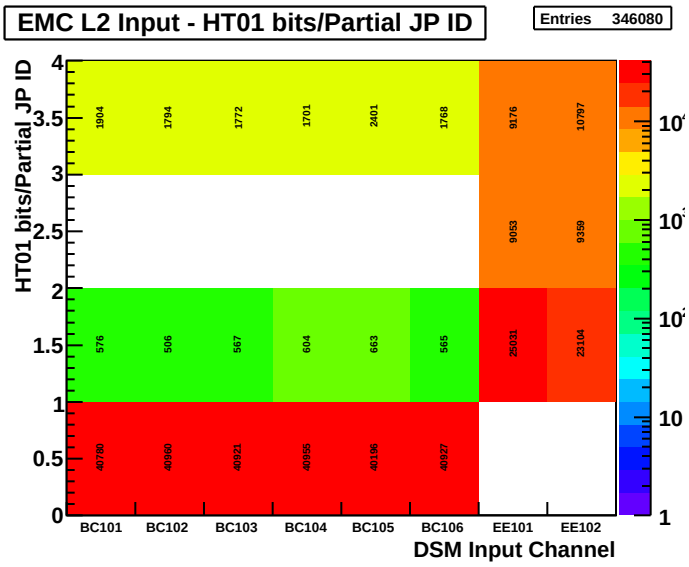

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

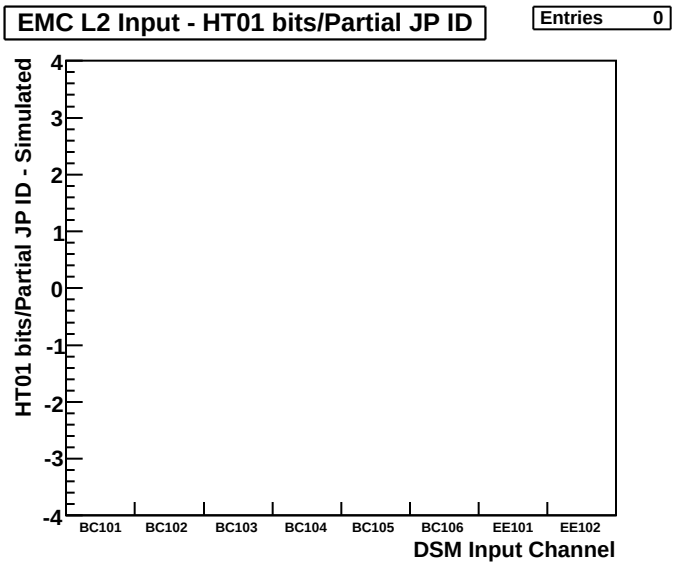

EMC L2 Input - JPZ/JPC bits
Entries 346080
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
89 56 60 63 77 88 92 58
223 168 228 281 251 305 373 151
361 347 385 447 640 573 533 125
42387 42389 42387 42469 42392 42394 42352 42326

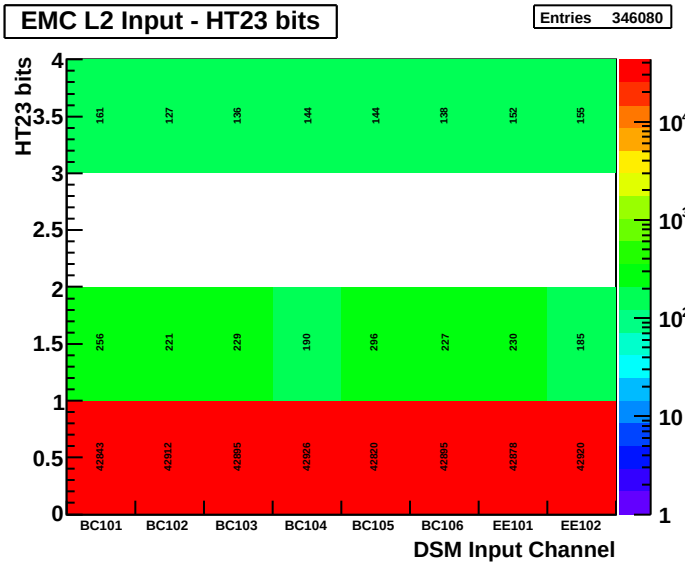

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

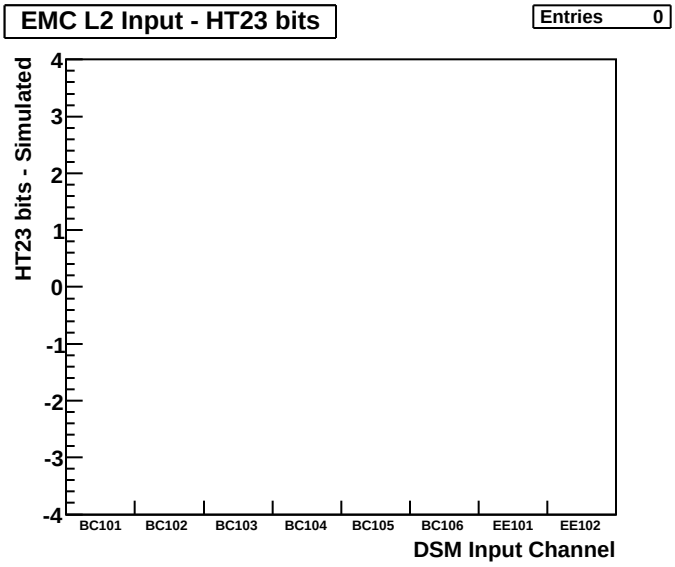

EMC L2 Input - Partial JP Sum
Entries 346080
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

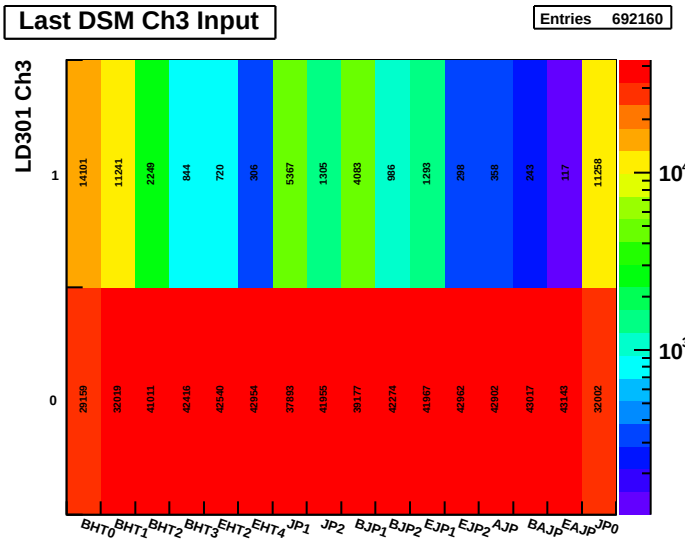

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

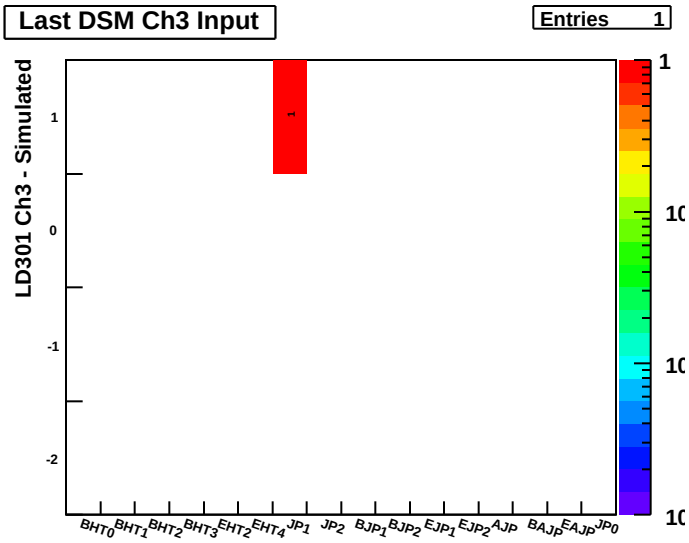

EMC L2 Input - HT01 bits/Partial JP ID
Entries 346080
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

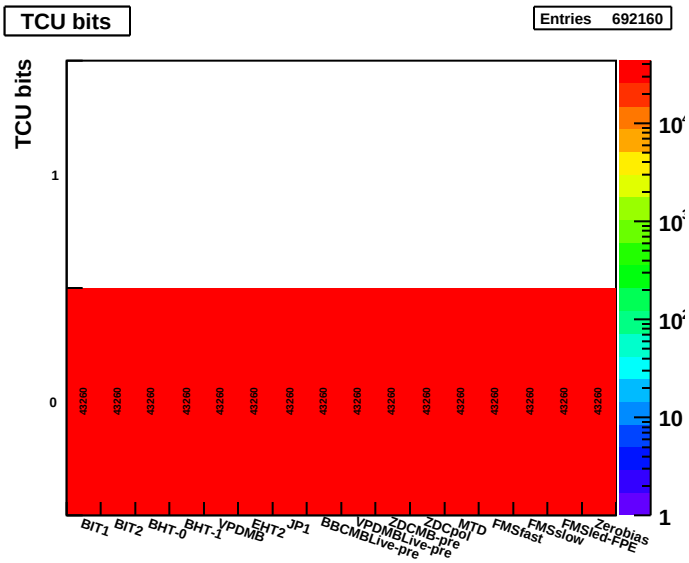

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

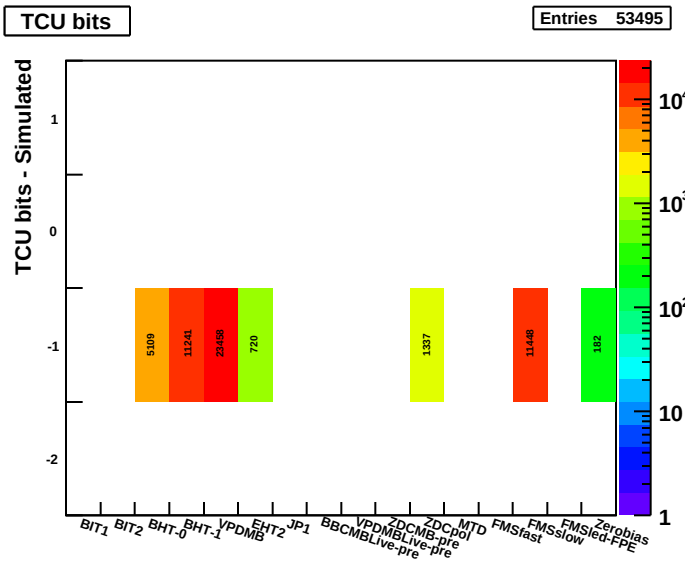

EMC L2 Input - HT23 bits
Entries 346080
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

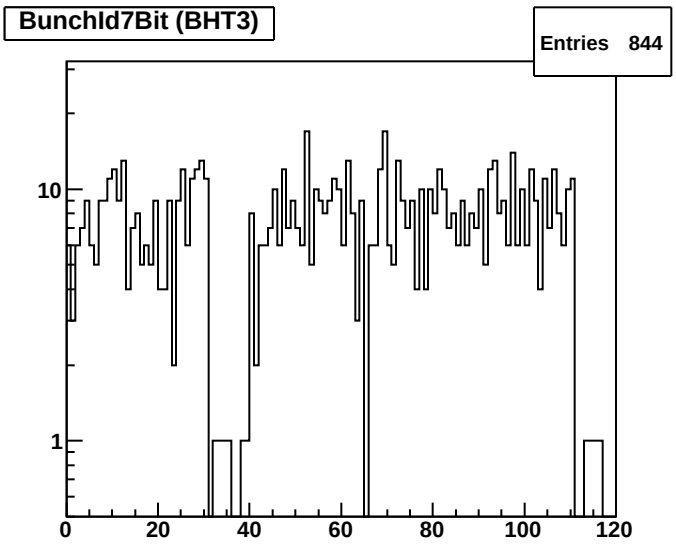

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

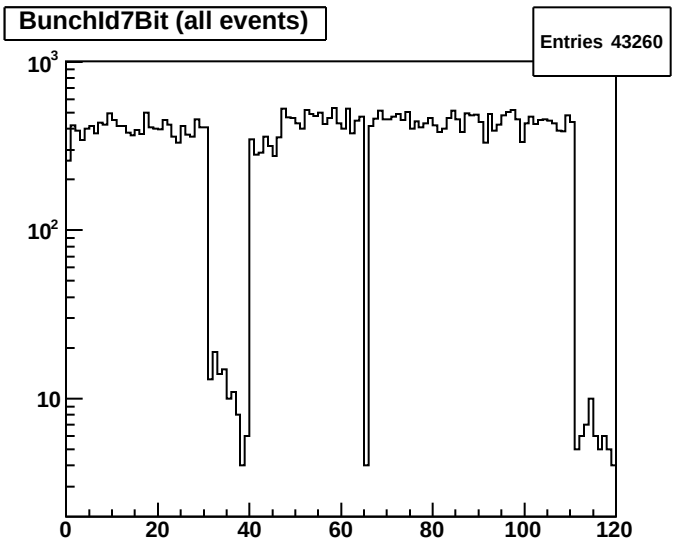

Last DSM Ch3 Input
Entries 692160
LD301 Ch3
14101 11241 2249 844 720 306 5367 1305 4083 986 1293 298 358 243 117 11258
29159 32019 41011 42416 42540 42954 37893 41955 39177 42274 41967 42062 42902 43017 43143 32002
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

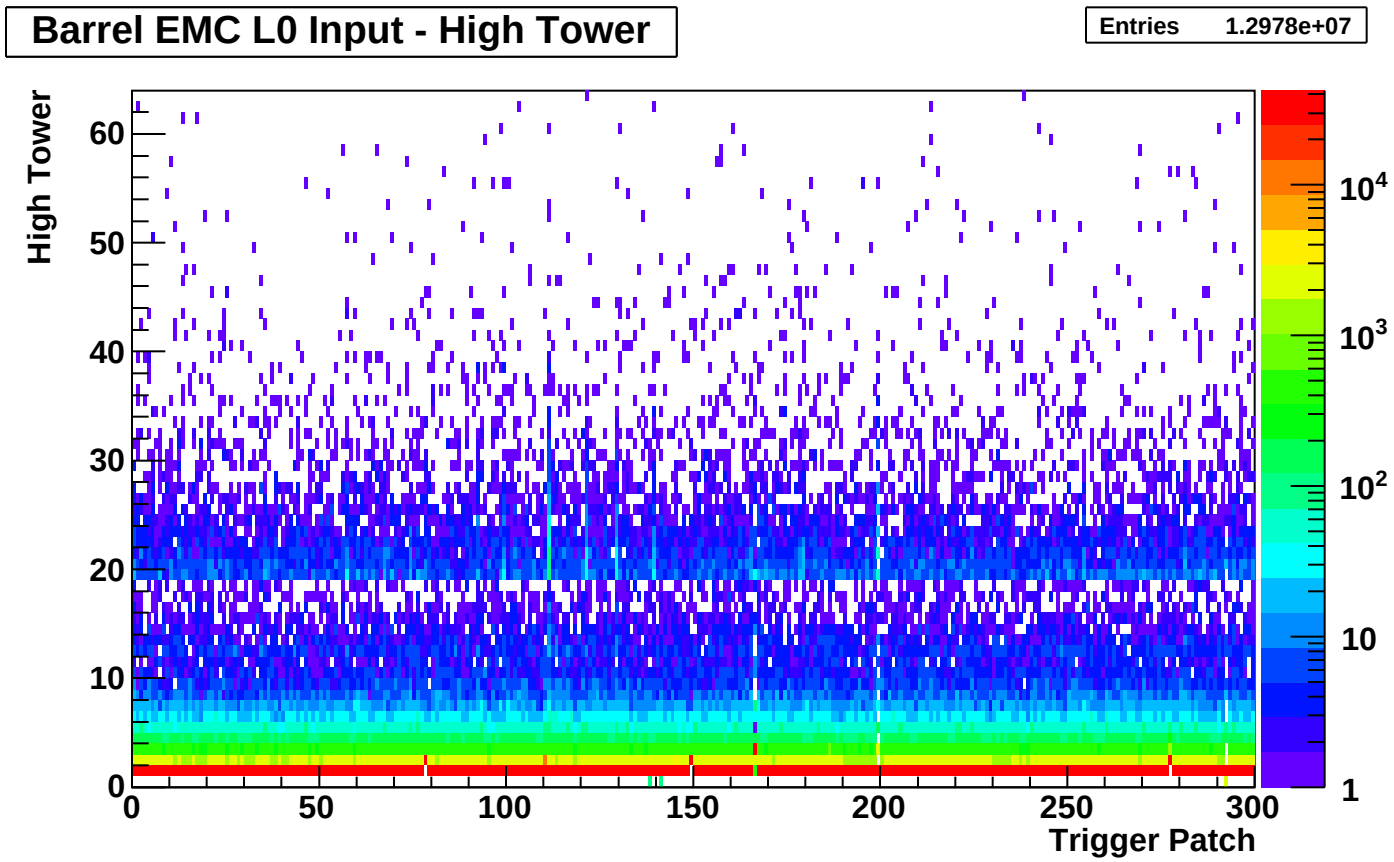

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

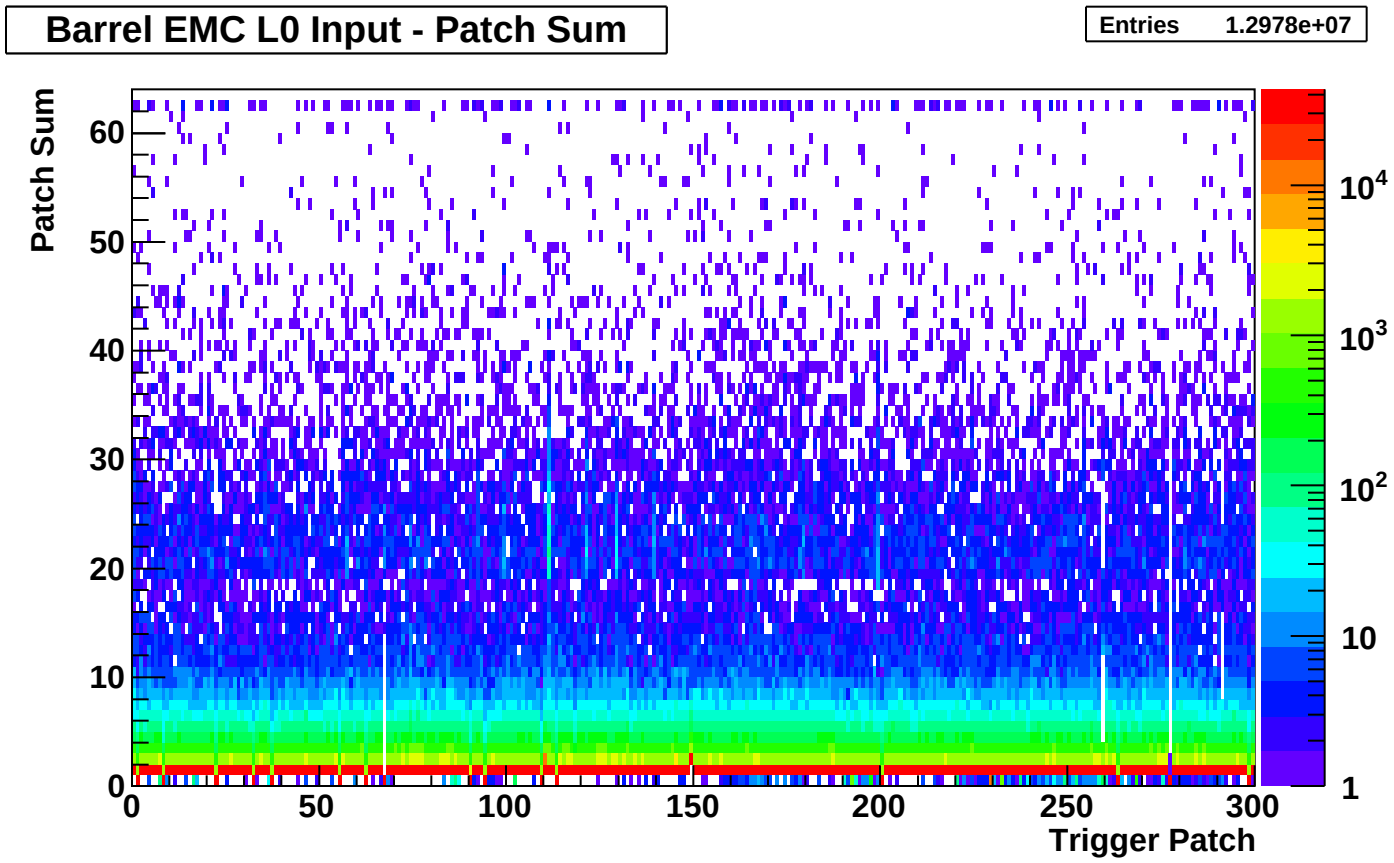

TCU bits
Entries 692160
TCU bits
43260 43260 43260 43260 43260 43260 43260 43260 43260 43260 43260 43260 43260 43260 43260 43260
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

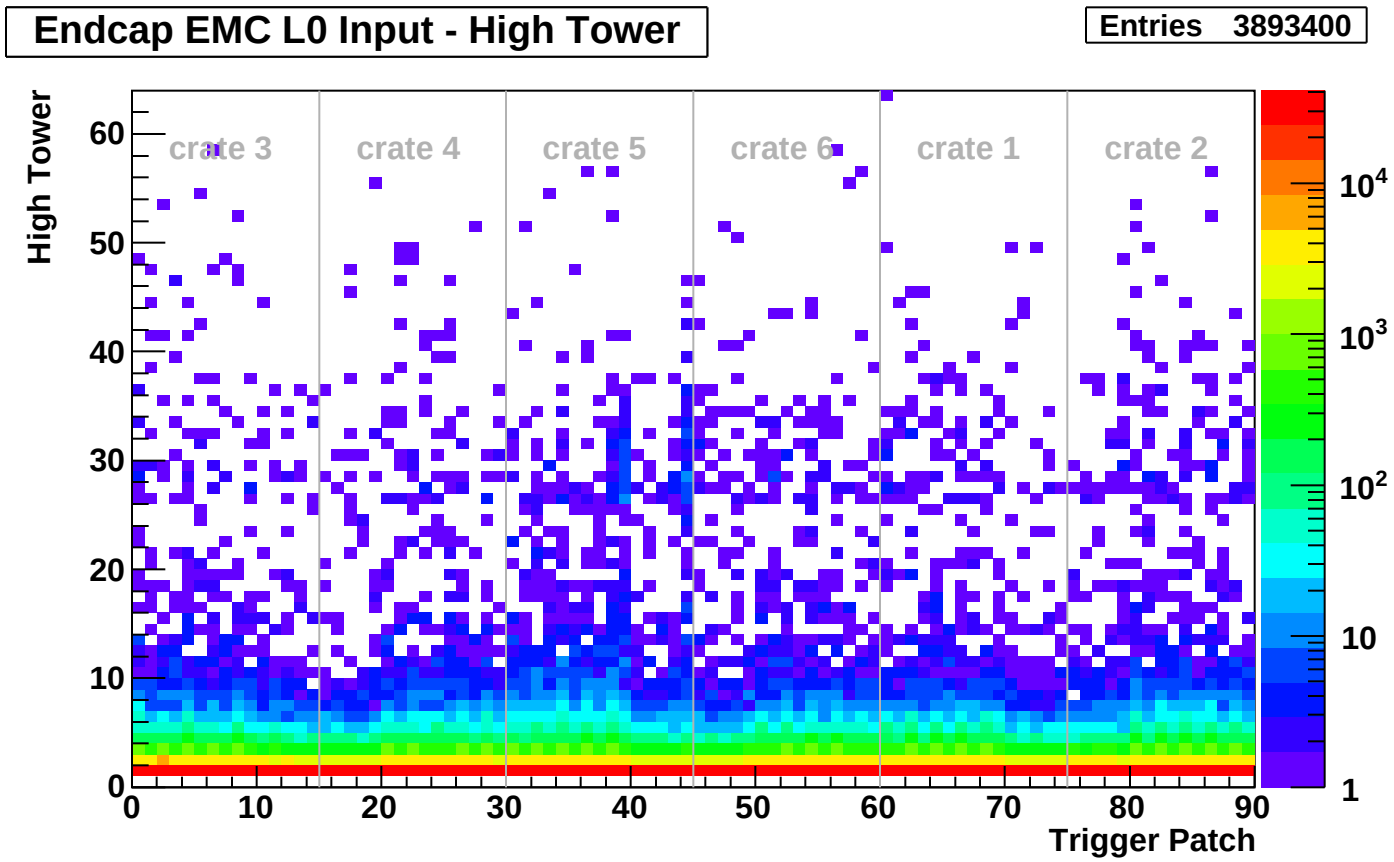

TCU bits
Entries 53495
TCU bits - Simulated
5109 11241 23458 720 1337 11448 182
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

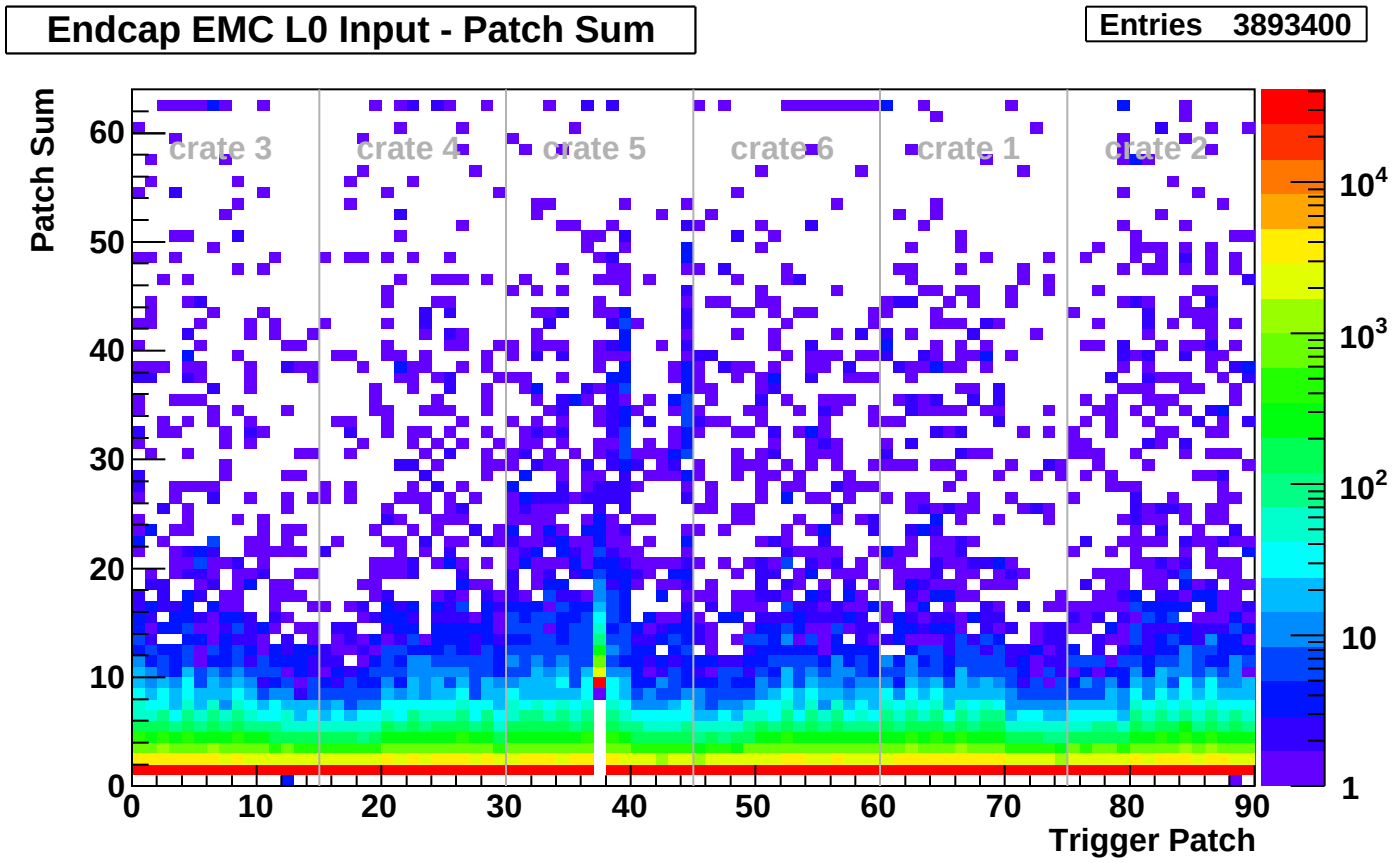

BunchId7Bit (BHT3)
Entries 844

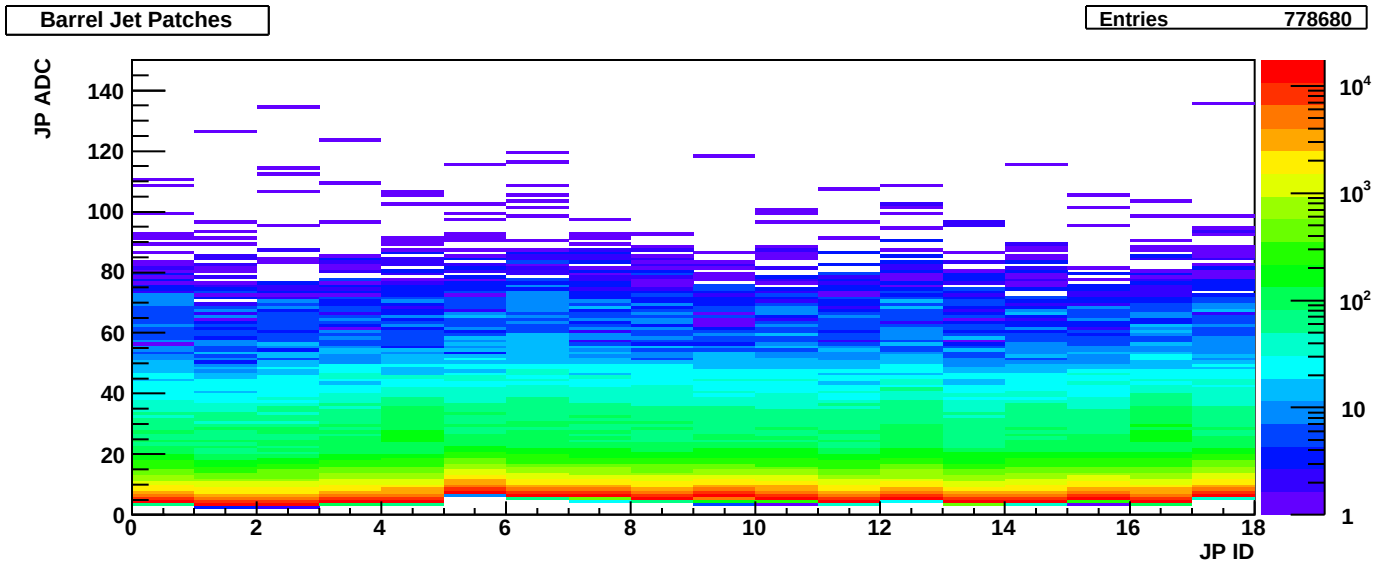

BunchId7Bit (all events)
Entries 43260

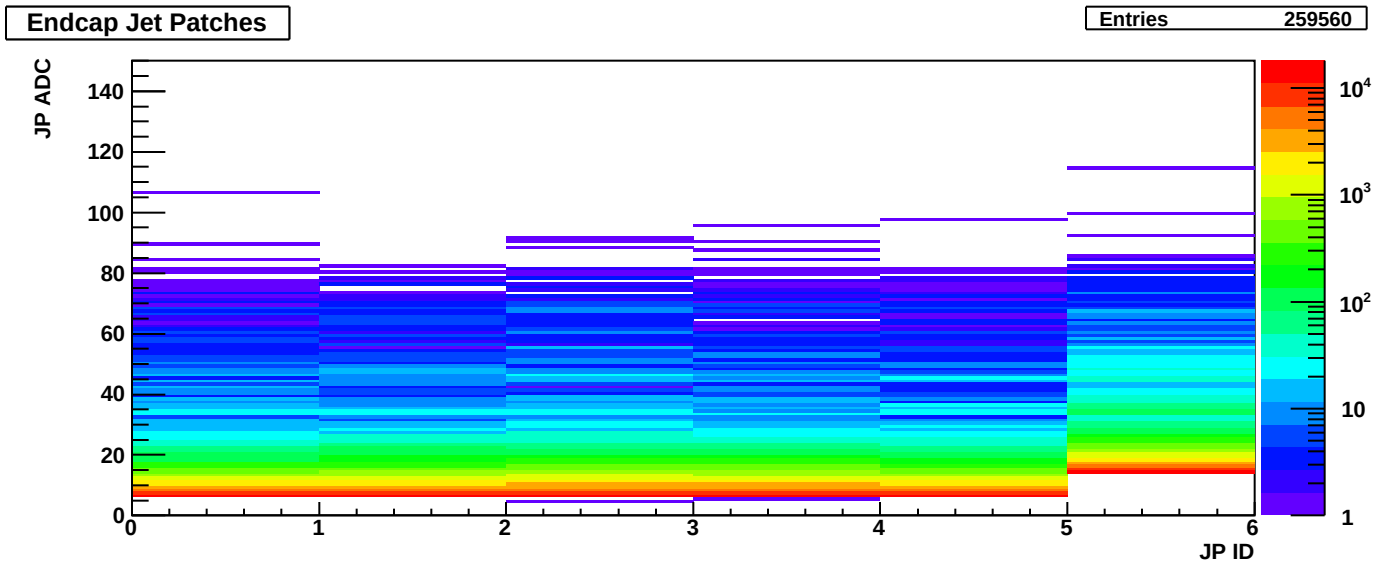

Barrel EMC L0 Input - High Tower
Entries 1.2978e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

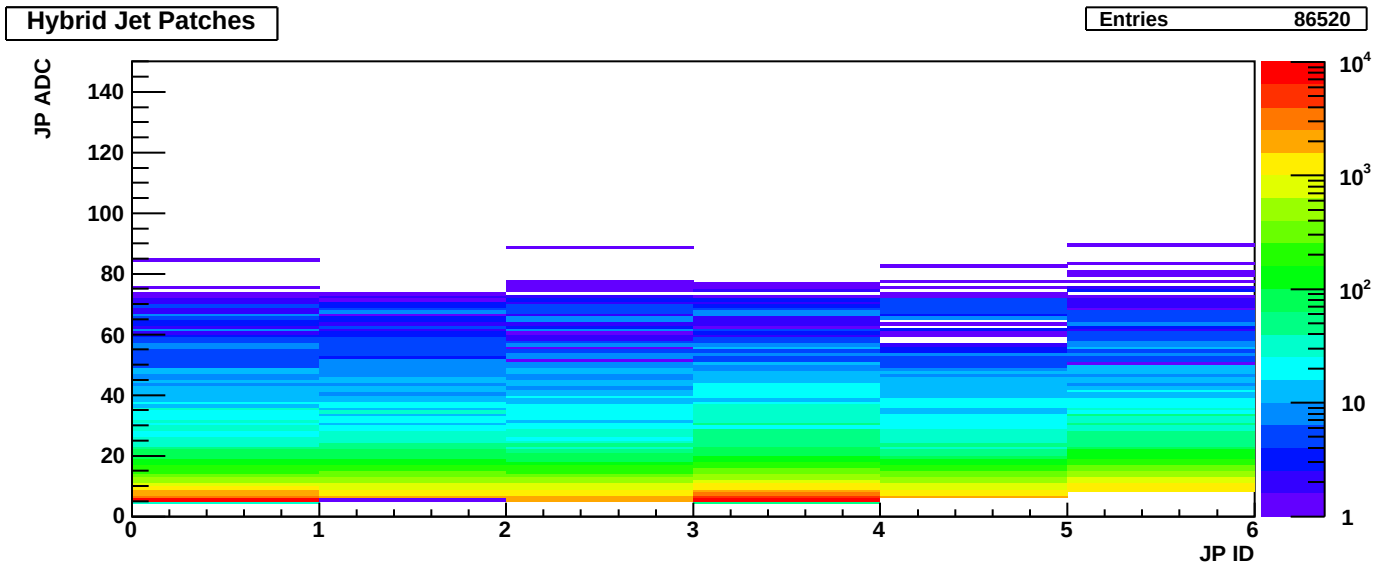

Barrel EMC L0 Input - Patch Sum
Entries 1.2978e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

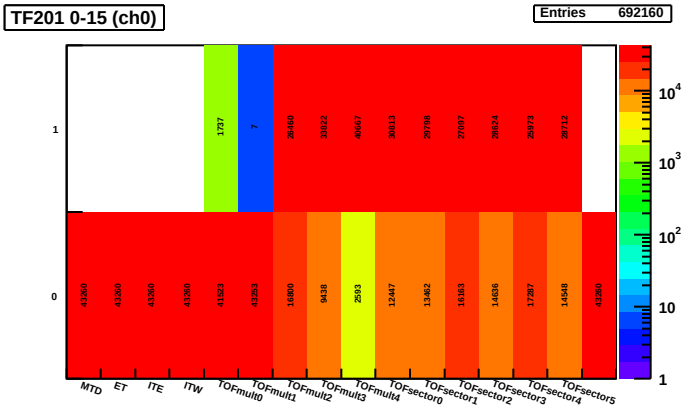

Endcap EMC L0 Input - High Tower
Entries 3893400
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

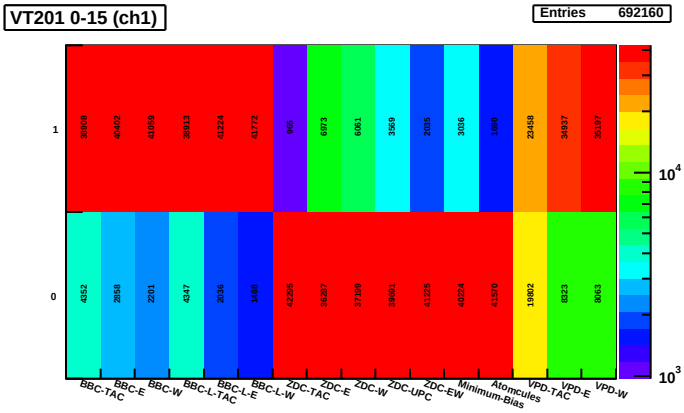

Endcap EMC L0 Input - Patch Sum
Entries 3893400
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

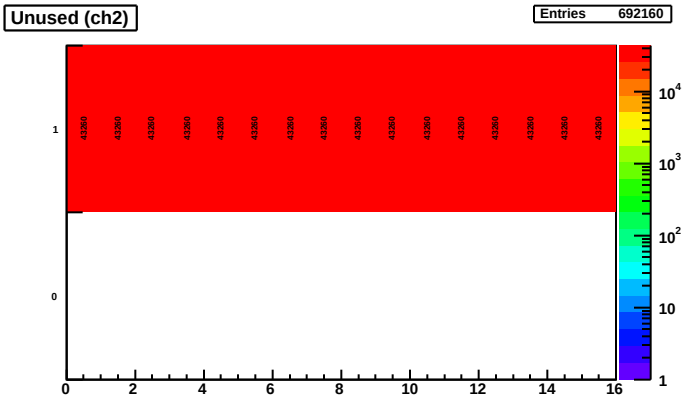

Barrel Jet Patches
Entries 778680
JP ADC
JP ID

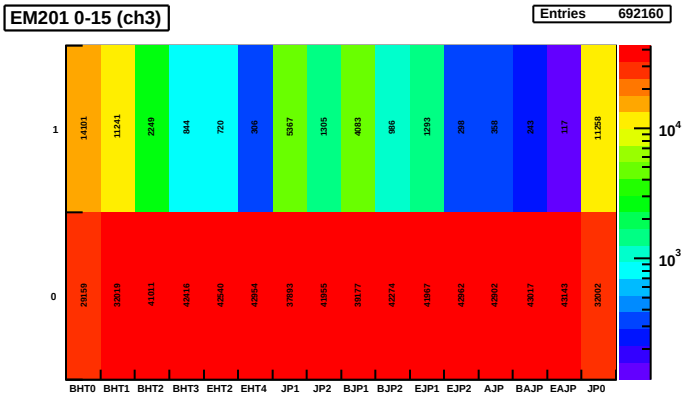

Endcap Jet Patches
Entries 259560
JP ADC
JP ID

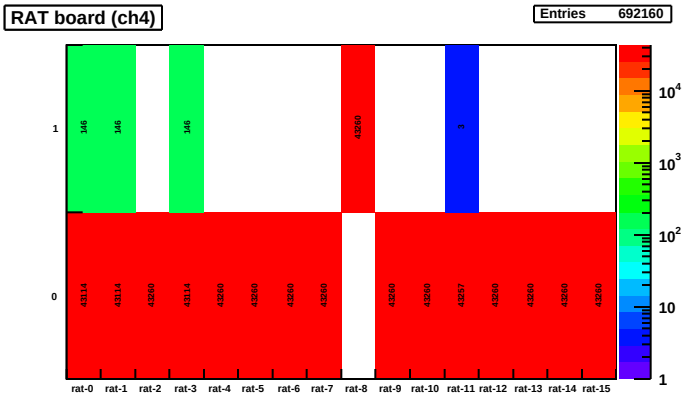

Hybrid Jet Patches
Entries 86520
JP ADC
JP ID

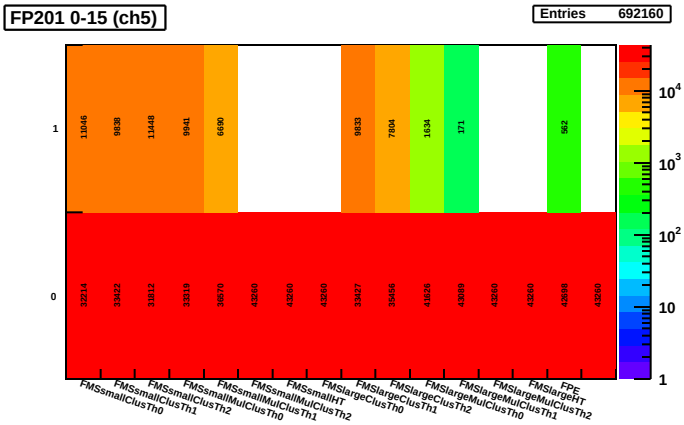

TF201 0-15 (ch0)
Entries 692160
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

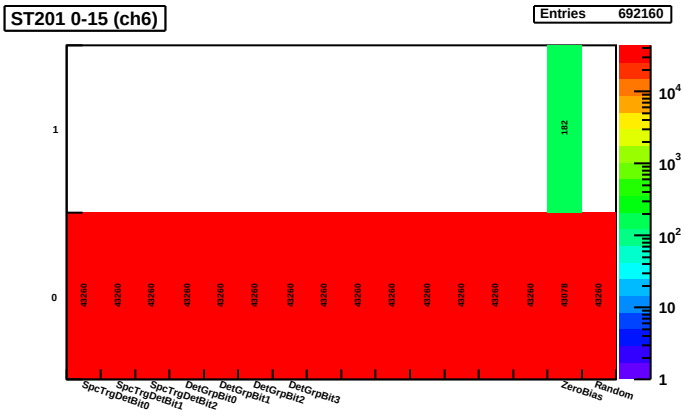

VT201 0-15 (ch1)
Entries 692160
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

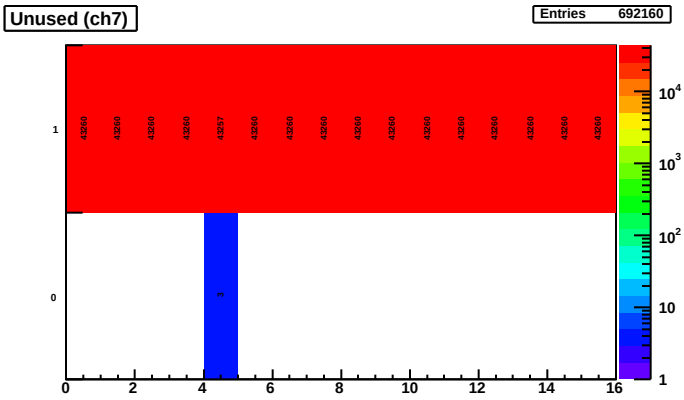

Unused (ch2)
Entries 692160

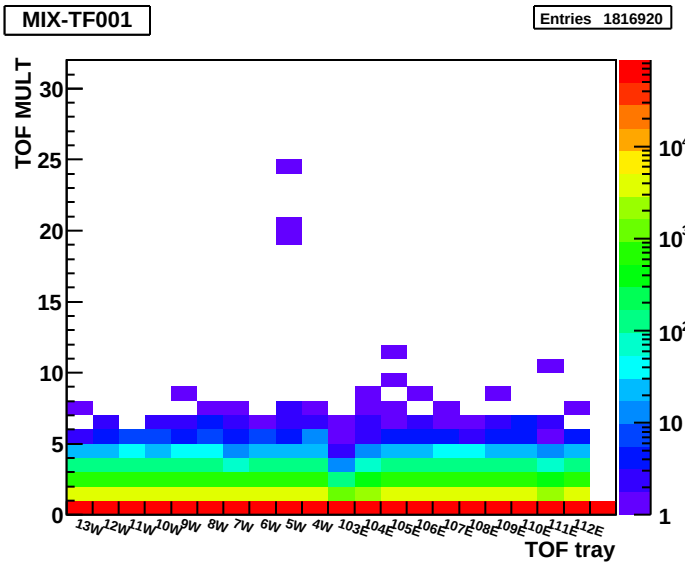

EM201 0-15 (ch3)
Entries 692160
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

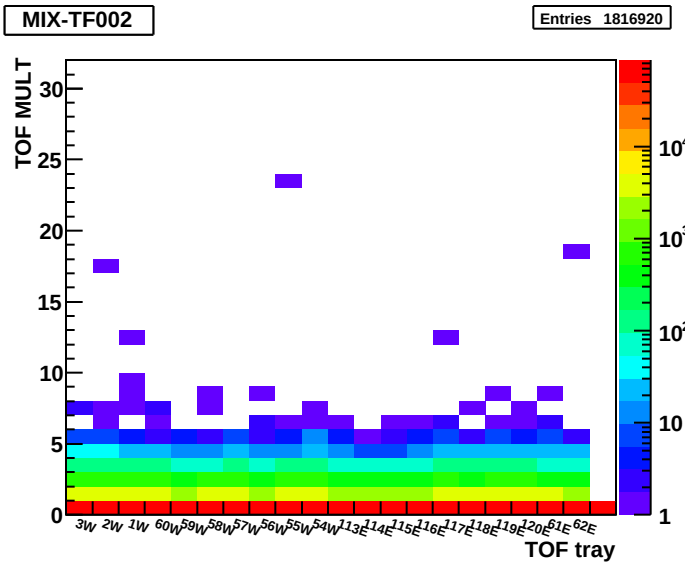

RAT board (ch4)
Entries 692160
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

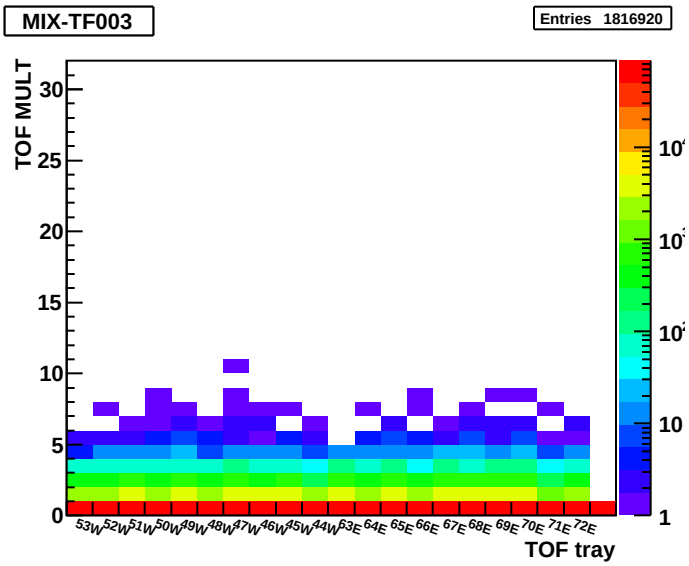

FP201 0-15 (ch5)
Entries 692160
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

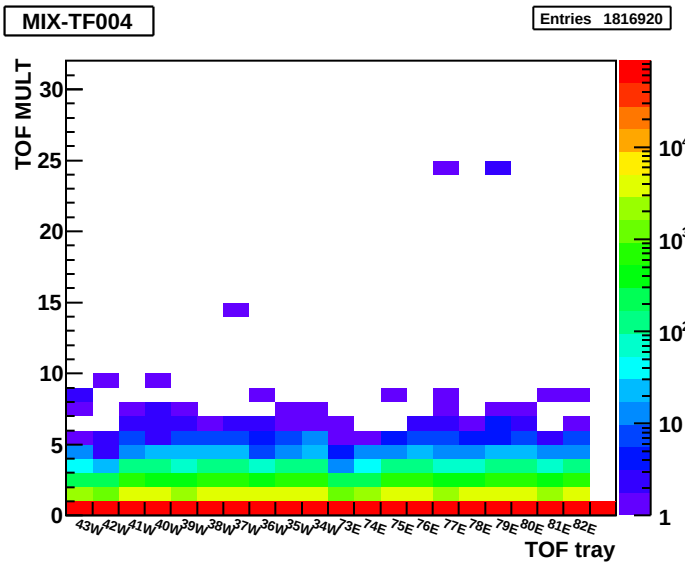

ST201 0-15 (ch6)
Entries 692160
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

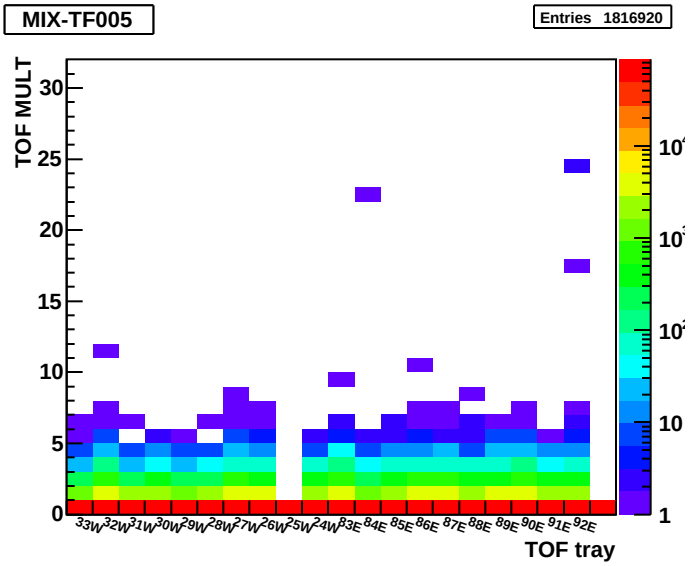

Unused (ch7)
Entries 692160

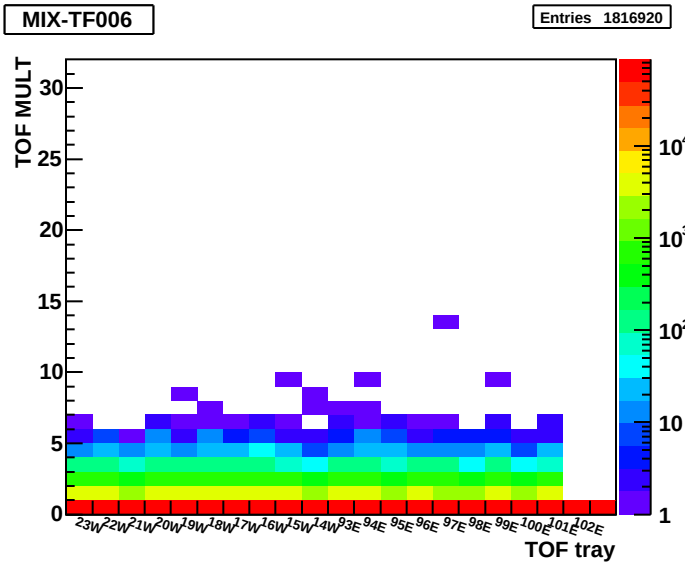

MIX-TF001
Entries 1816920
TOF MULT
TOF tray

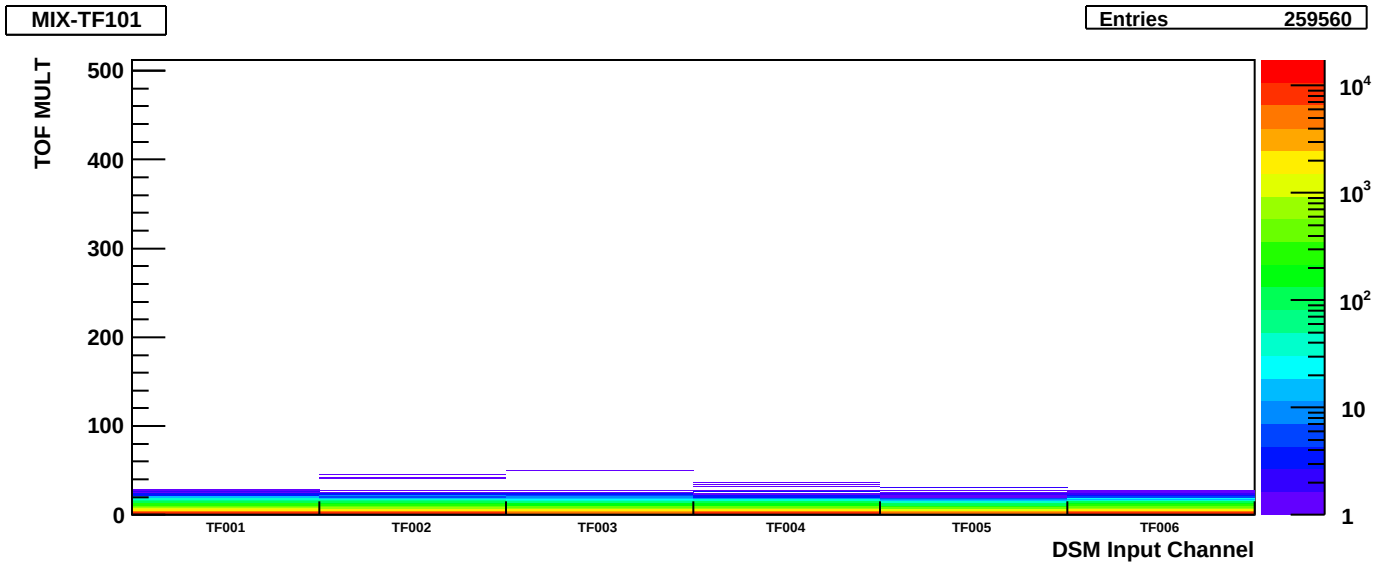

MIX-TF002
Entries 1816920
TOF MULT
TOF tray

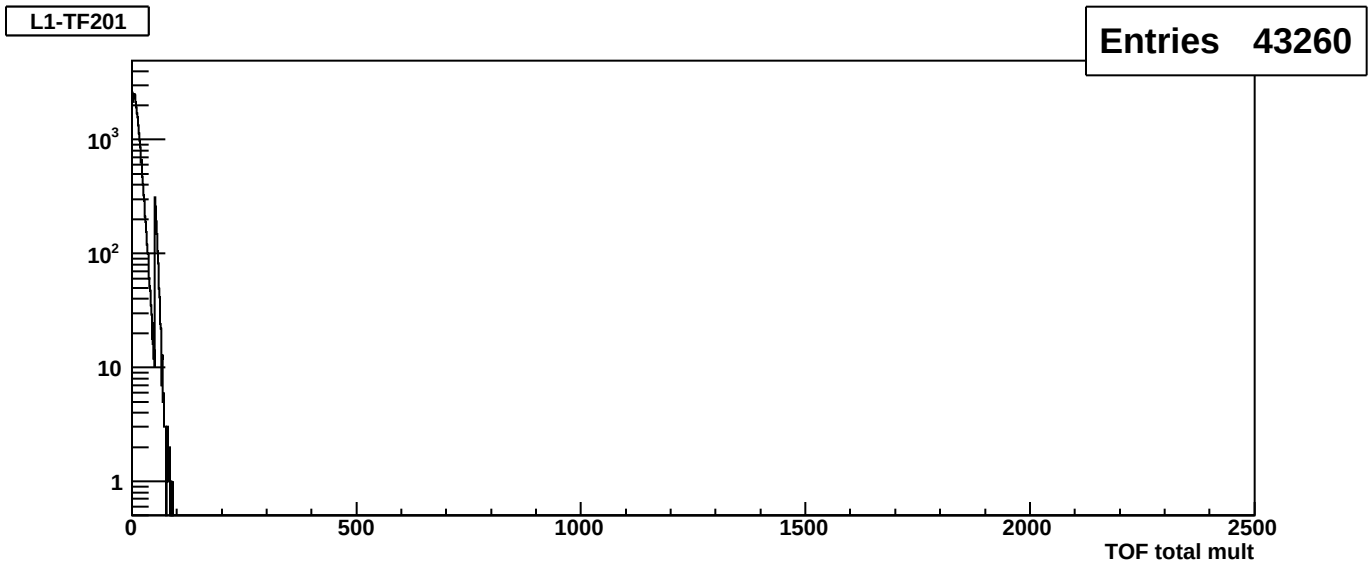

MIX-TF003
Entries 1816920
TOF MULT
TOF tray

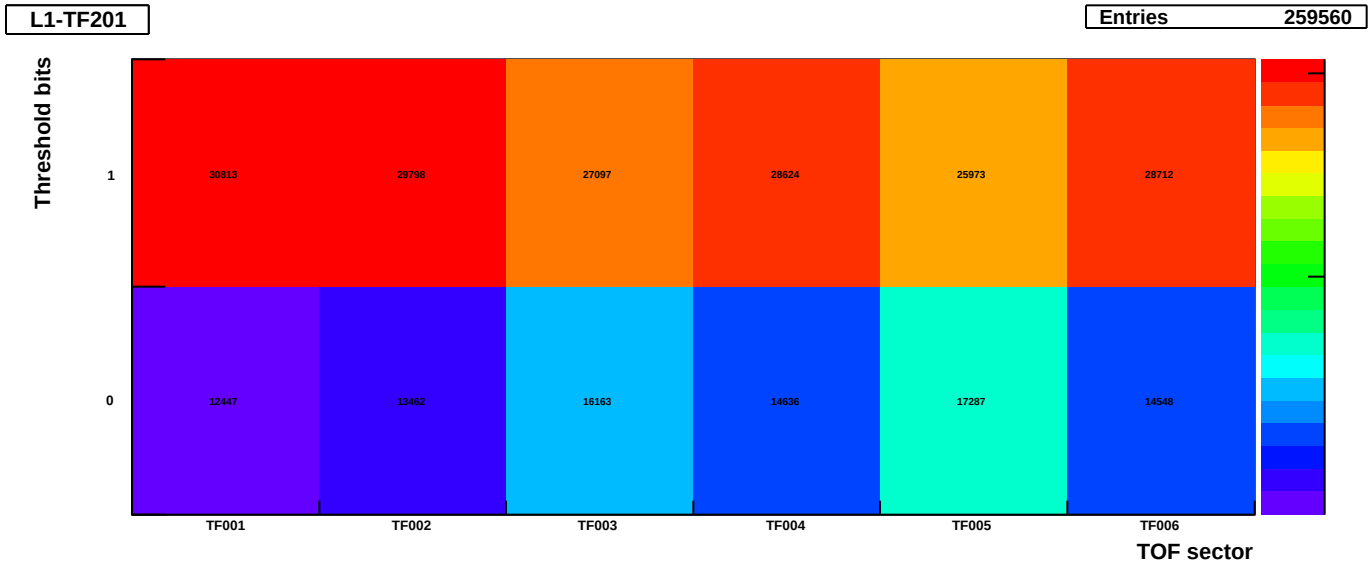

MIX-TF004
Entries 1816920
TOF MULT
TOF tray

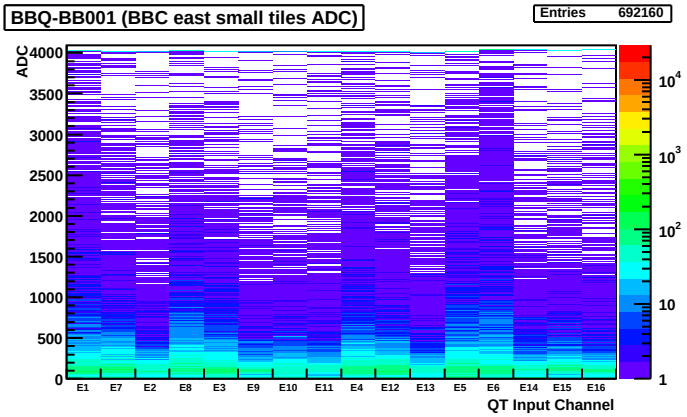

MIX-TF005
Entries 1816920
TOF MULT
TOF tray

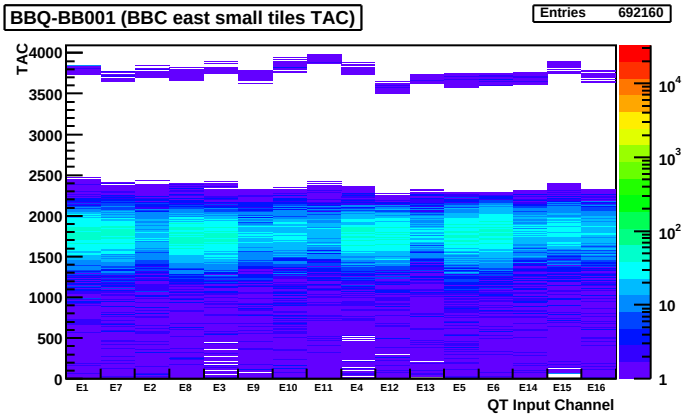

MIX-TF006
Entries 1816920
TOF MULT
TOF tray

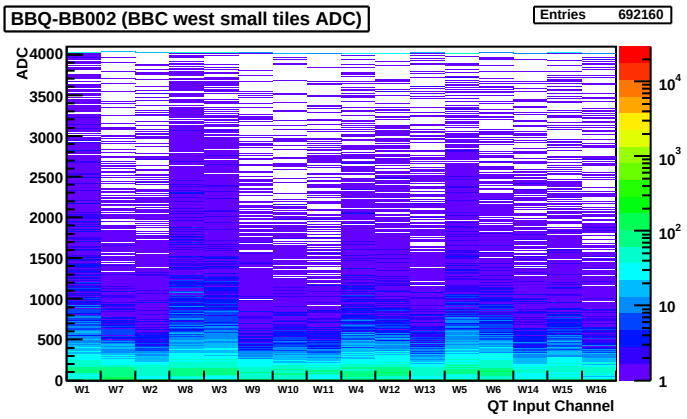

MIX-TF101
Entries 259560
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

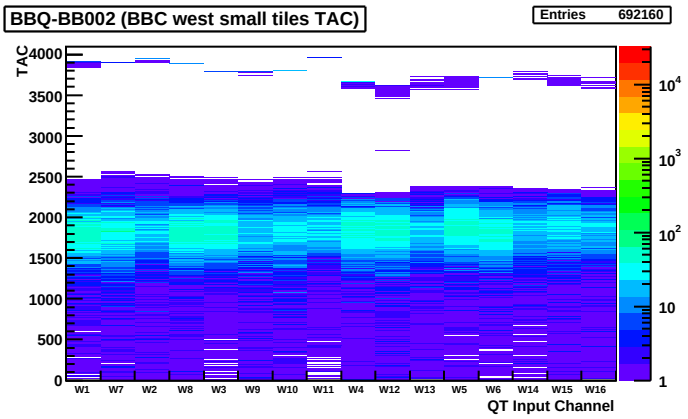

L1-TF201
Entries 43260
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

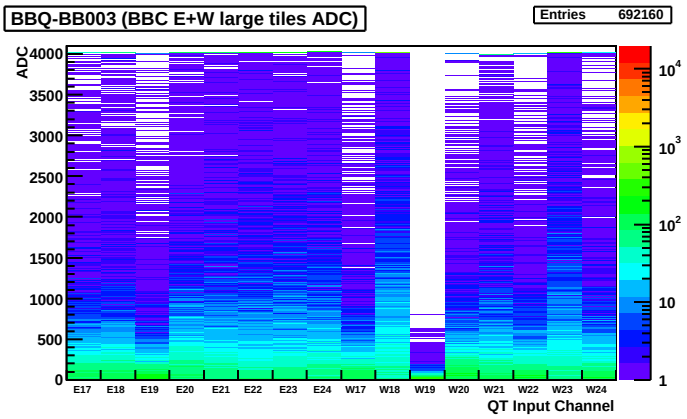

L1-TF201
Entries 259560
Threshold bits
1
0
30813
29798
27097
28624
25973
28712
12447
13462
16163
14636
17287
14548
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

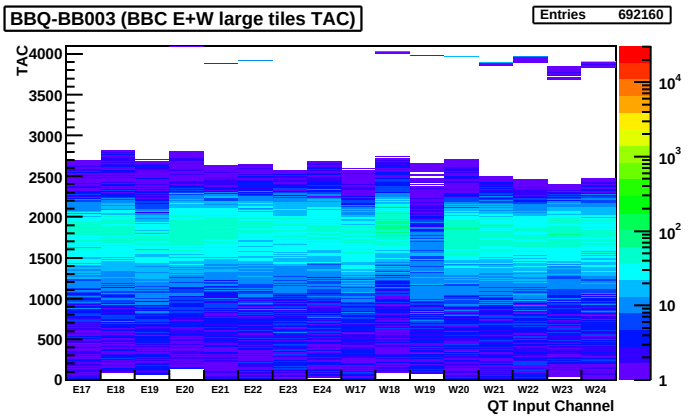

BBQ-BB001 (BBC east small tiles ADC)
Entries 692160
ADC
QT Input Channel

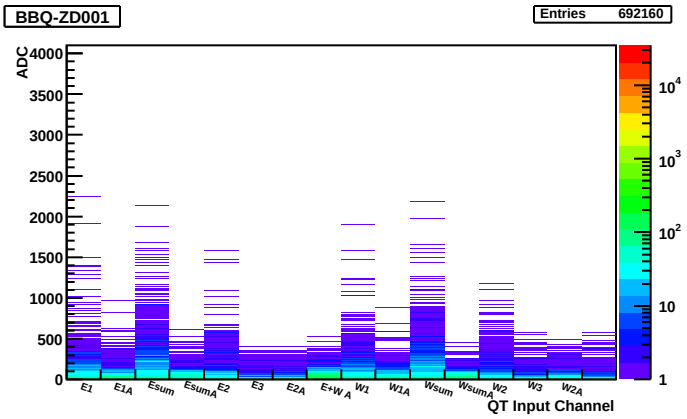

BBQ-BB001 (BBC east small tiles TAC)
Entries 692160
TAC
QT Input Channel

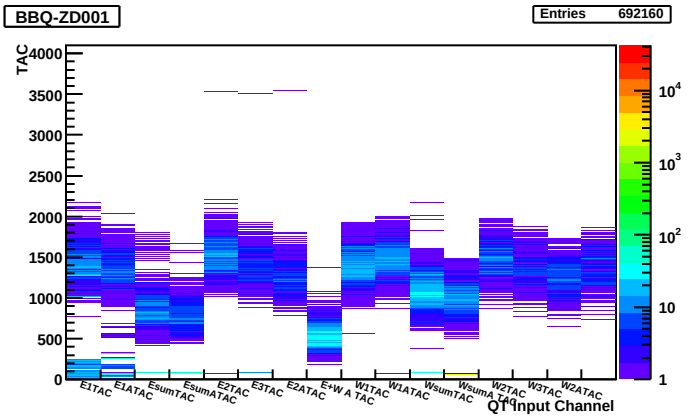

BBQ-BB002 (BBC west small tiles ADC)
Entries 692160
ADC
QT Input Channel

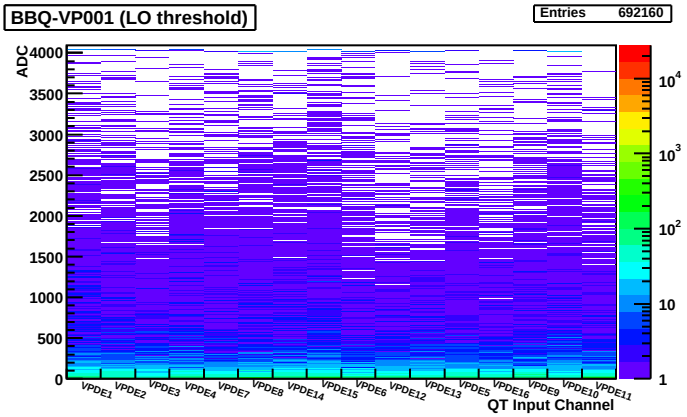

BBQ-BB002 (BBC west small tiles TAC)
Entries 692160
TAC
QT Input Channel

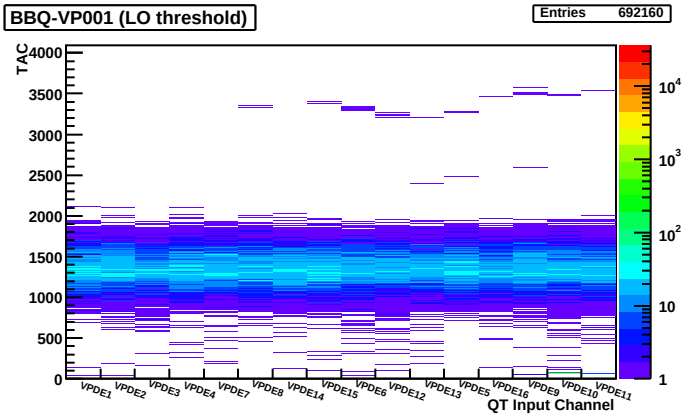

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 692160
ADC
QT Input Channel

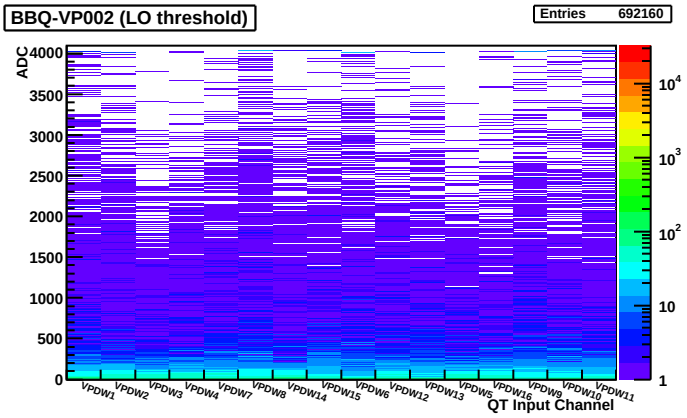

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 692160
TAC
QT Input Channel

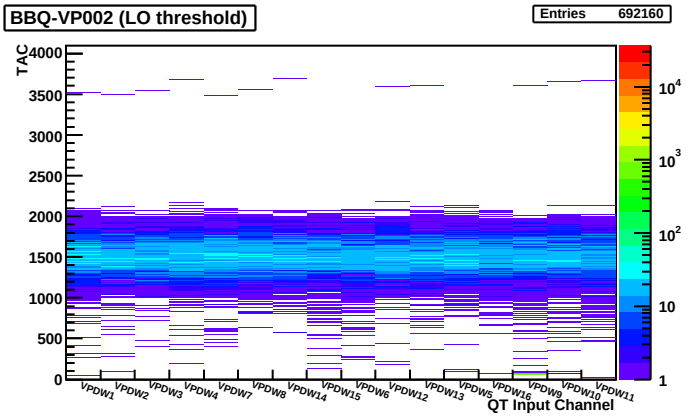

BBQ-ZD001
Entries 692160
ADC
QT Input Channel

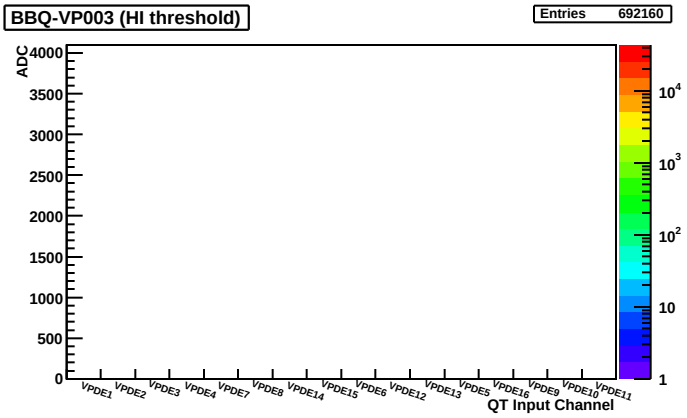

BBQ-ZD001
Entries 692160
TAC
QT Input Channel

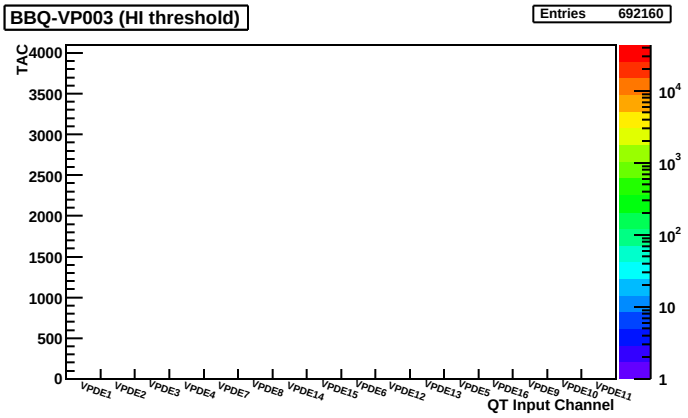

BBQ-VP001 (LO threshold)
Entries 692160
ADC
QT Input Channel

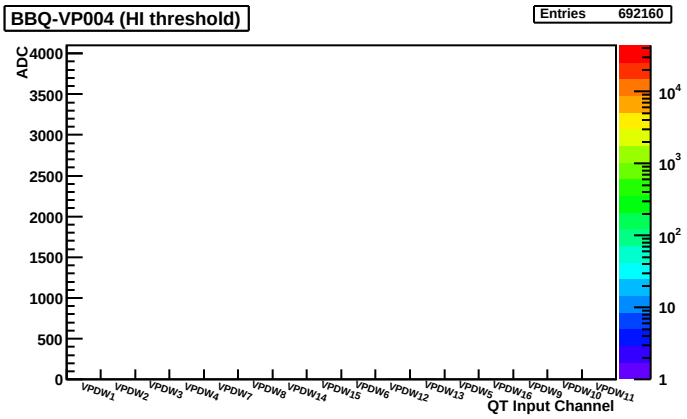

BBQ-VP001 (LO threshold)
Entries 692160
TAC
QT Input Channel

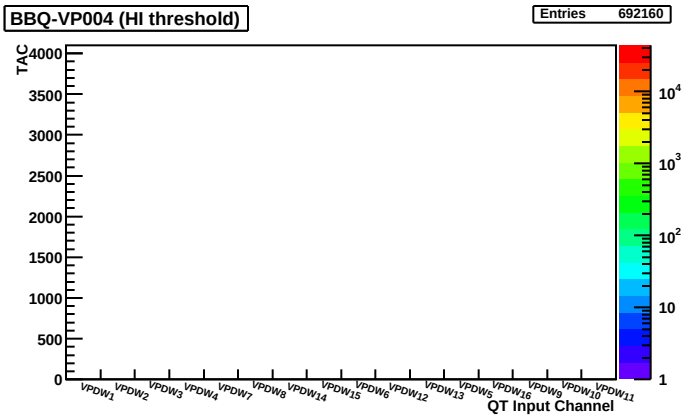

BBQ-VP002 (LO threshold)
Entries 692160
ADC
QT Input Channel

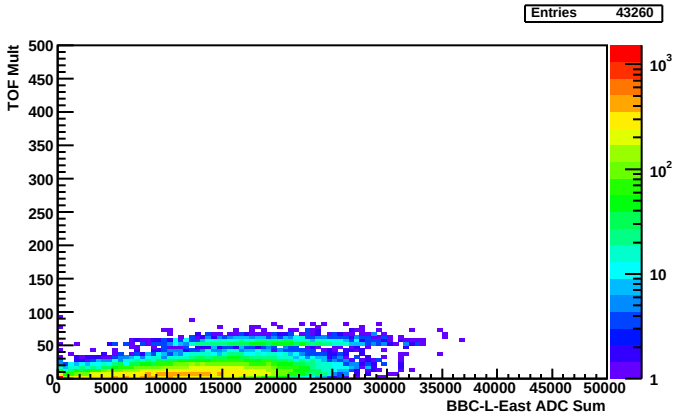

BBQ-VP002 (LO threshold)
Entries 692160
TAC
QT Input Channel

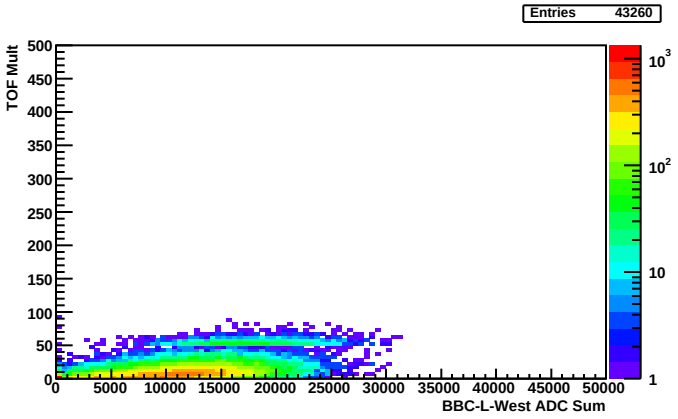

BBQ-VP003 (HI threshold)
Entries 692160
ADC
QT Input Channel

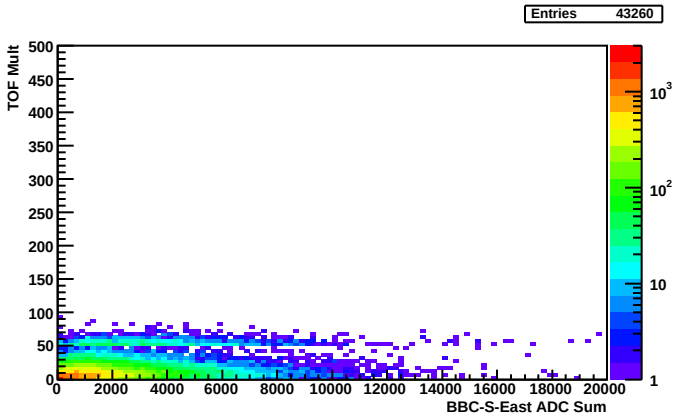

BBQ-VP003 (HI threshold)
Entries 692160
TAC
QT Input Channel

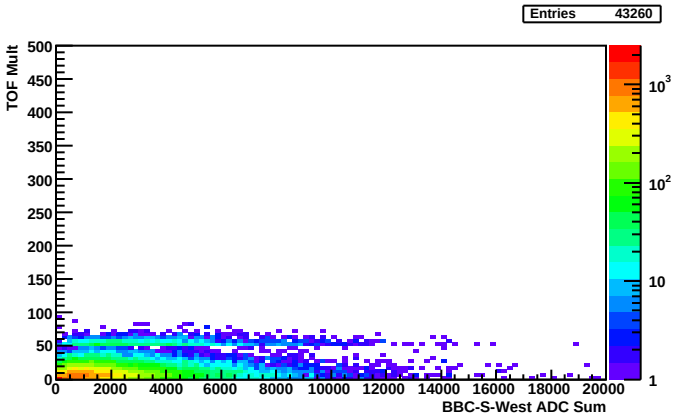

BBQ-VP004 (HI threshold)
Entries 692160
ADC
QT Input Channel

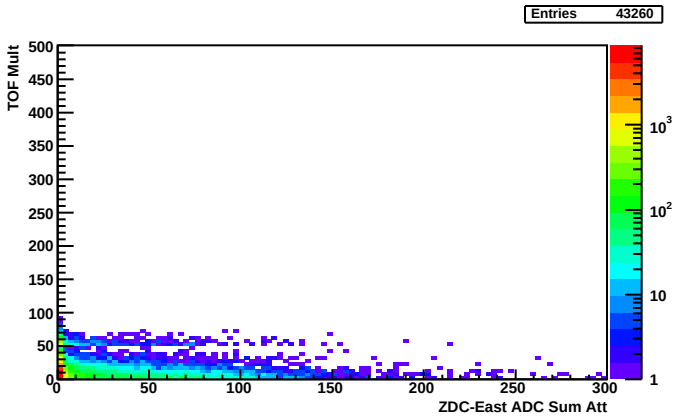

BBQ-VP004 (HI threshold)
Entries 692160
TAC
QT Input Channel

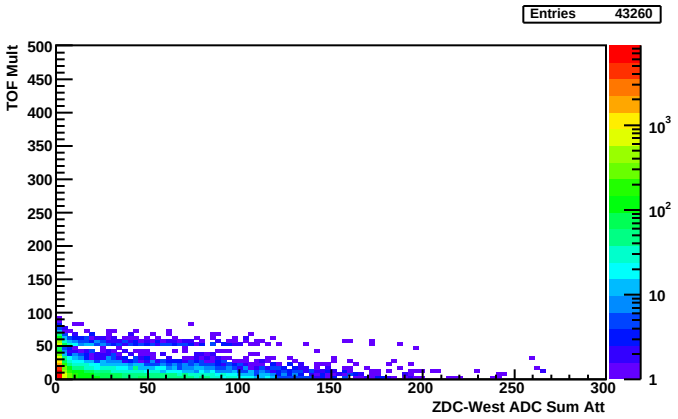

Entries 43260
TOF Mult
BBC-L-East ADC Sum

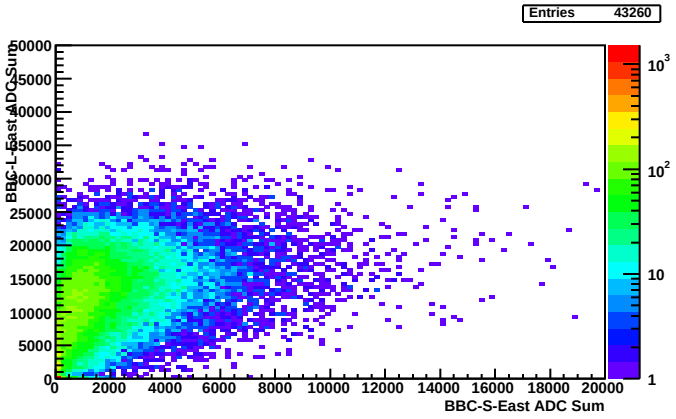

Entries 43260
TOF Mult
BBC-L-West ADC Sum

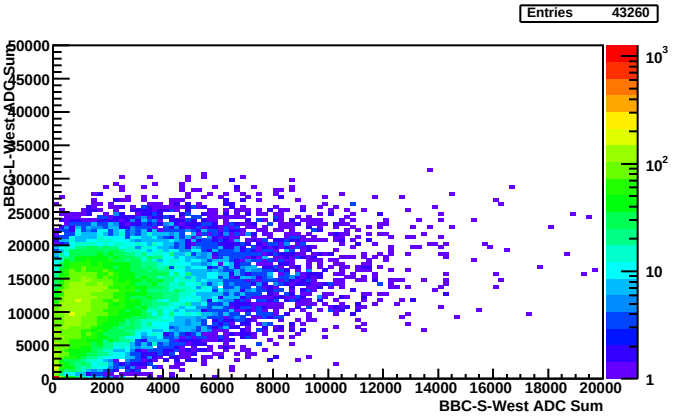

Entries 43260
TOF Mult
BBC-S-East ADC Sum

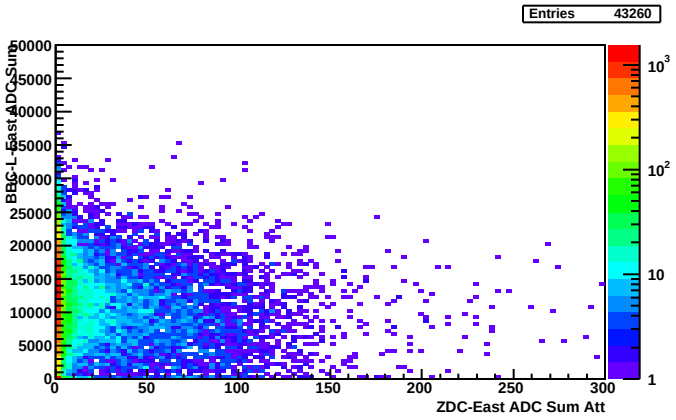

Entries 43260
TOF Mult
BBC-S-West ADC Sum

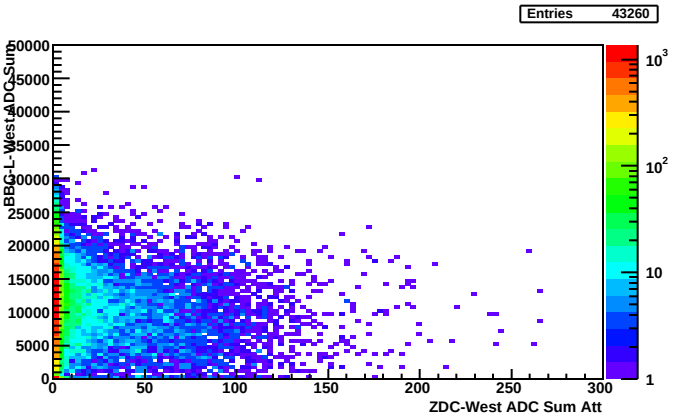

Entries 43260
TOF Mult
ZDC-East ADC Sum Att

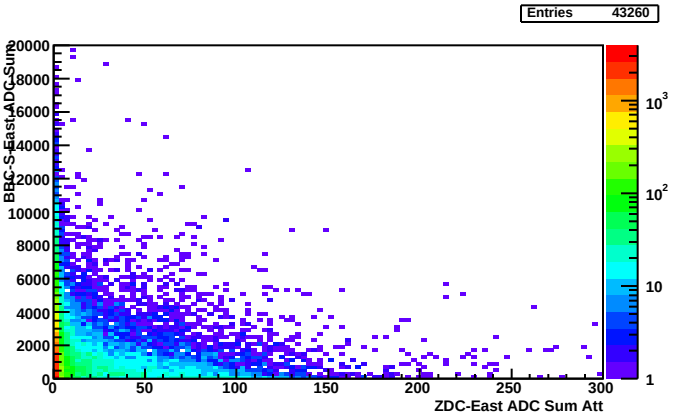

Entries 43260
TOF Mult
ZDC-West ADC Sum Att

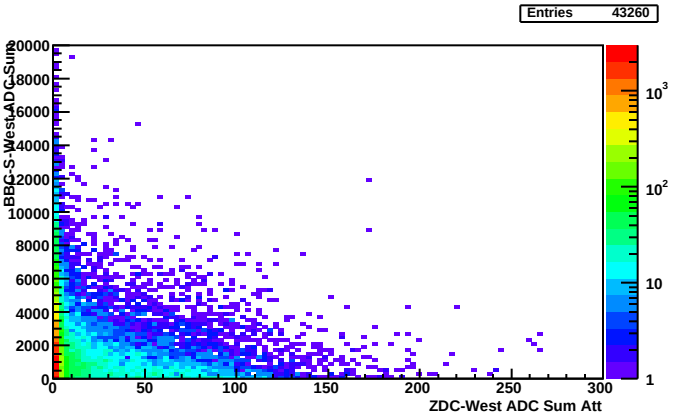

Entries 43260
BBC-L-East ADC Sum
BBC-S-East ADC Sum

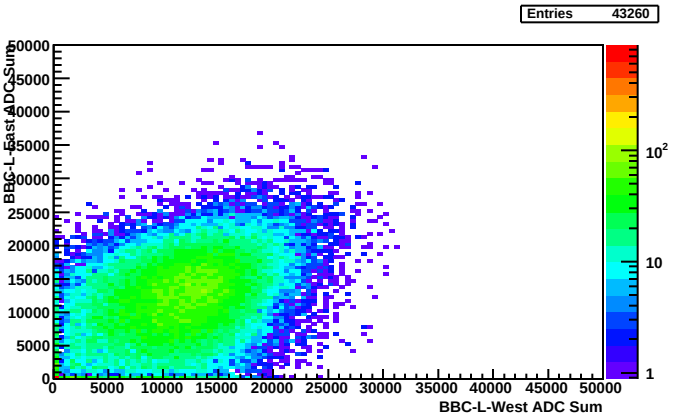

Entries 43260
BBC-L-West ADC Sum
BBC-S-West ADC Sum

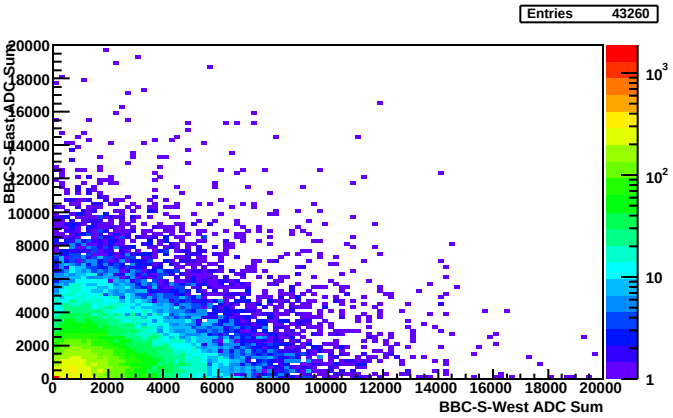

Entries 43260
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

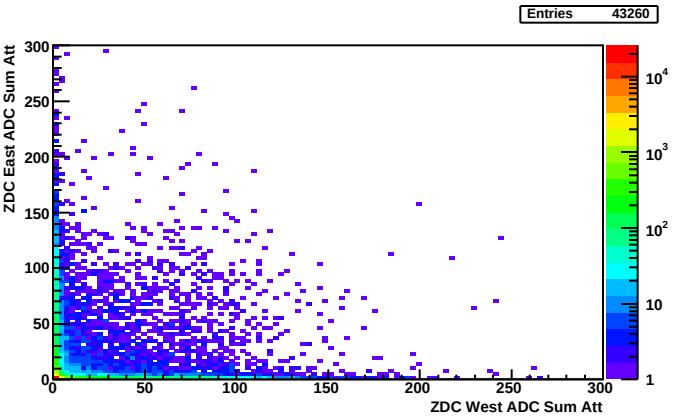

Entries 43260
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

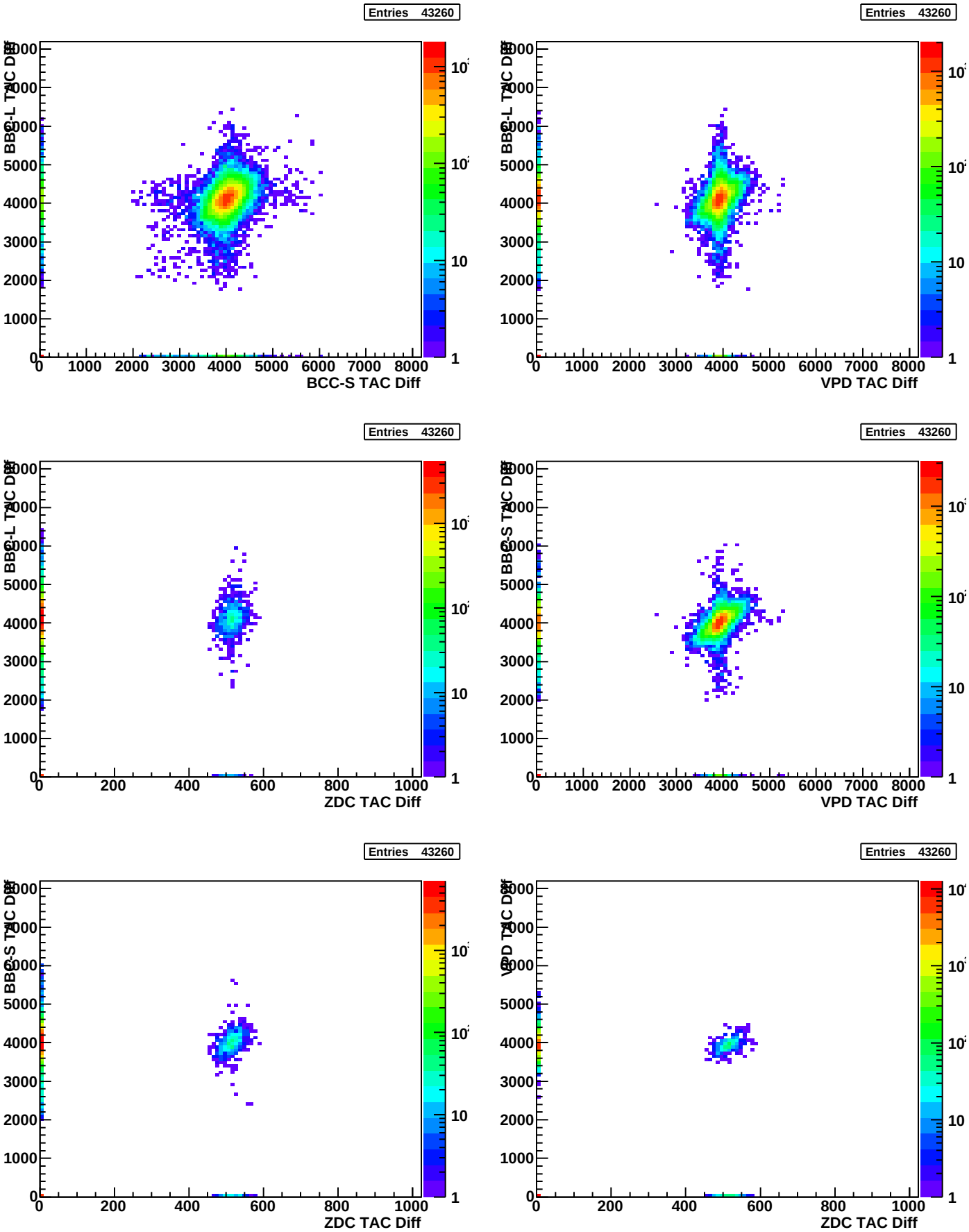

Entries 43260
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

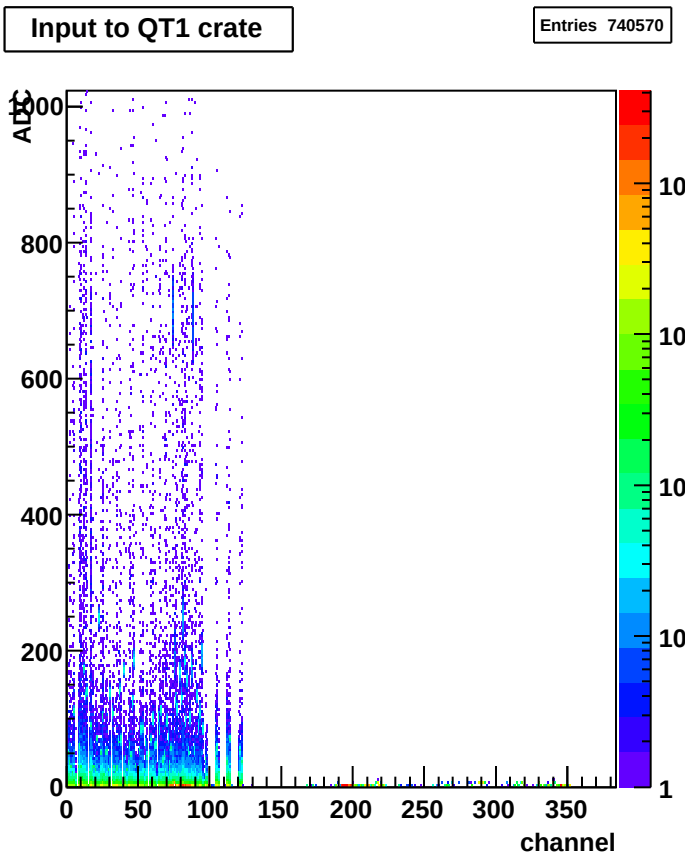

Entries 43260
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

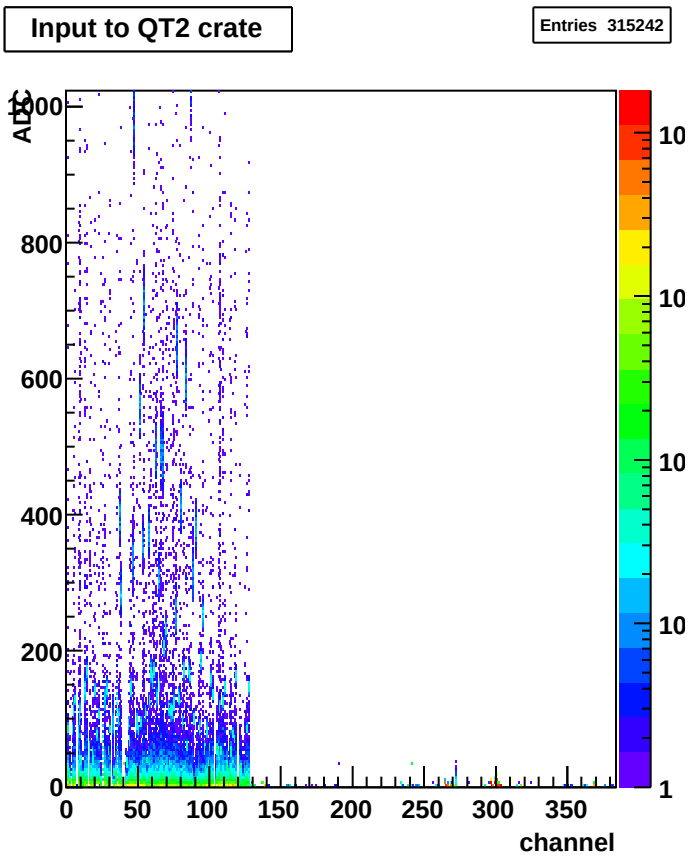

Entries 43260
BBC-L-East ADC Sum
BBC-L-West ADC Sum

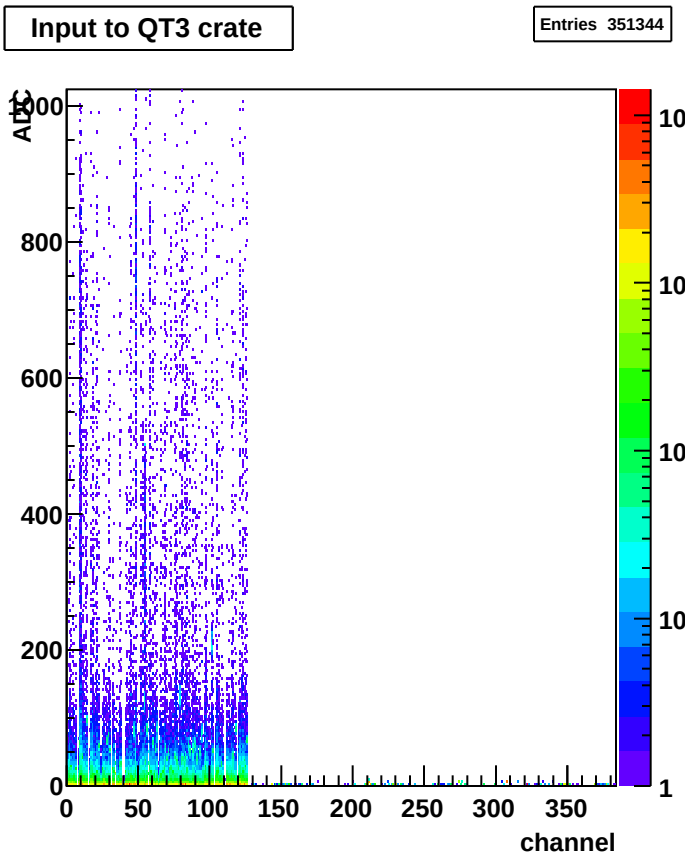

Entries 43260
BBC-S-East ADC Sum
BBC-S-West ADC Sum

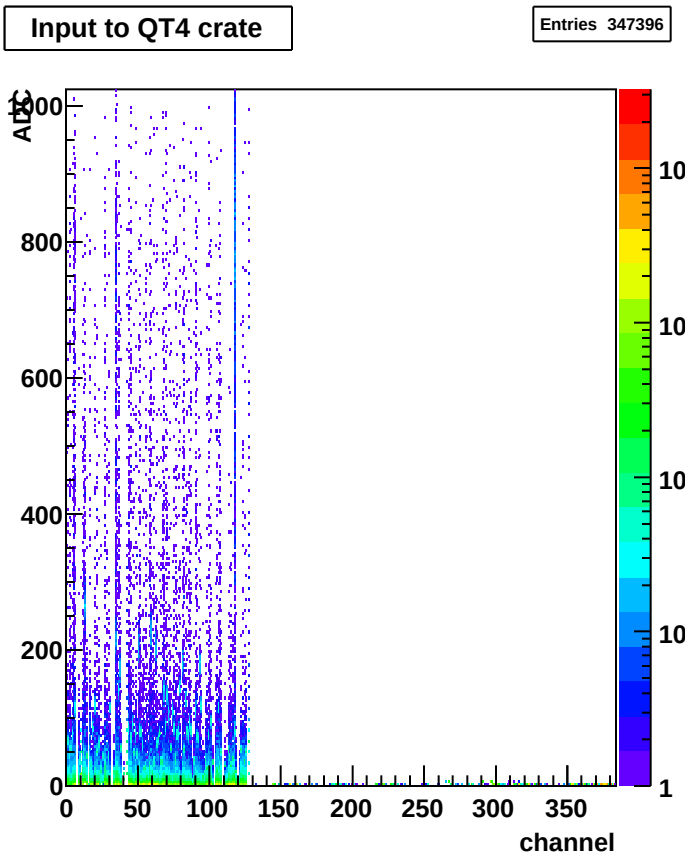

Entries 43260
ZDC East ADC Sum Att
ZDC West ADC Sum Att

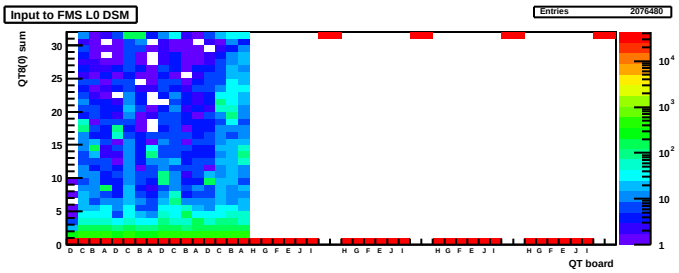

Entries 43260
BBC-L TAC Diff
BCC-S TAC Diff
Entries 43260
BBC-L TAC Diff
VPD TAC Diff
Entries 43260
BBC-L TAC Diff
ZDC TAC Diff
Entries 43260
BBC-S TAC Diff
VPD TAC Diff
Entries 43260
BBC-S TAC Diff
ZDC TAC Diff
Entries 43260
VPD TAC Diff
ZDC TAC Diff

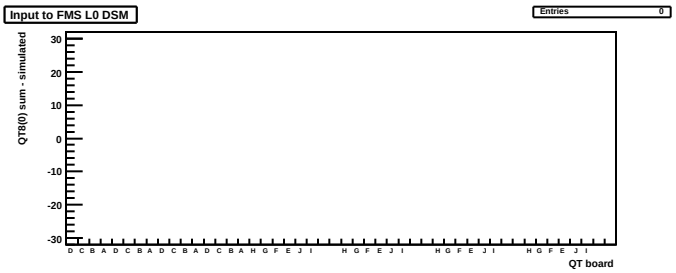

Input to QT1 crate
Entries 740570
ADC
channel

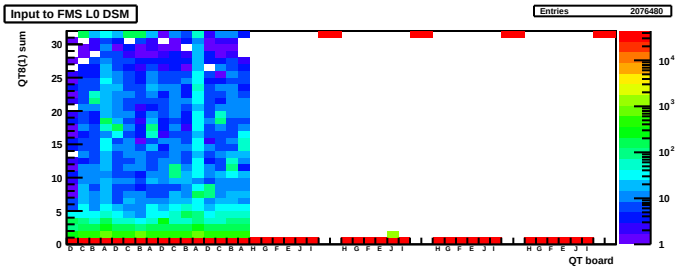

Input to QT2 crate
Entries 315242
ADC
channel

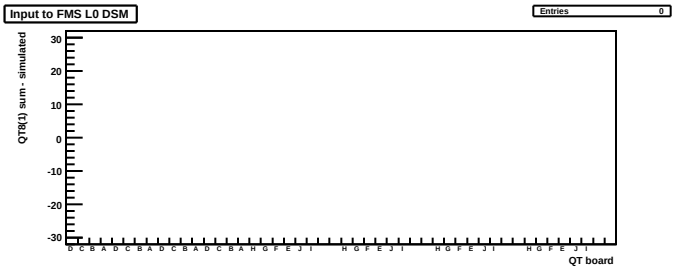

Input to QT3 crate
Entries 351344
ADC
channel

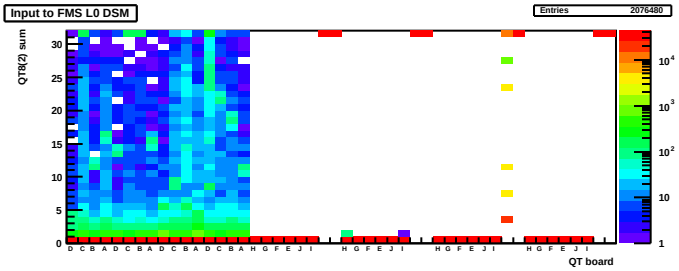

Input to QT4 crate
Entries 347396
ADC
channel

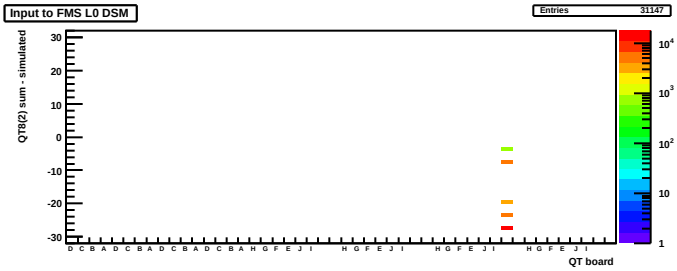

Input to FMS L0 DSM
Entries 2076480
QT8(0) sum
QT board

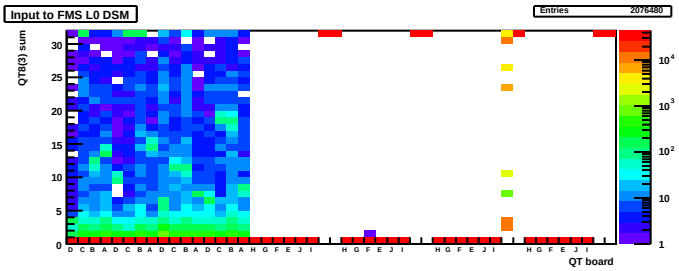

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

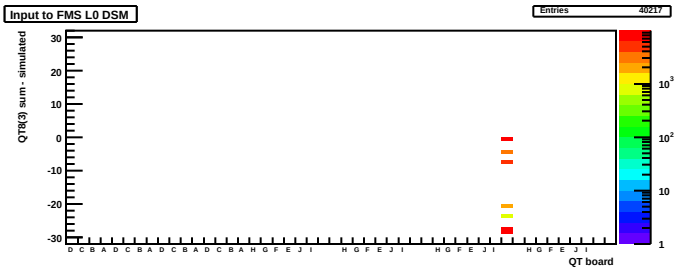

Input to FMS L0 DSM
Entries 2076480
QT8(1) sum
QT board

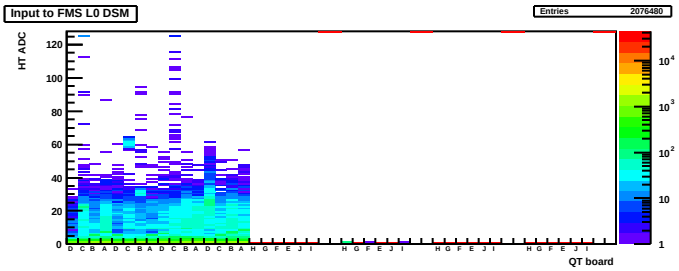

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

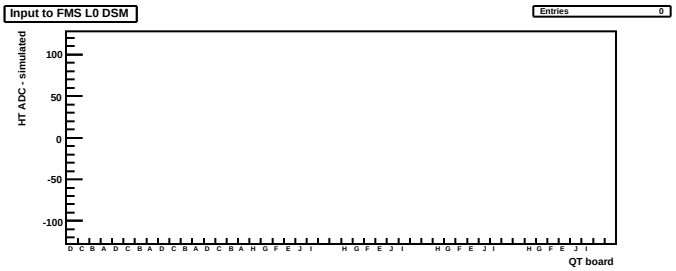

Input to FMS L0 DSM
Entries 2076480
QT8(2) sum
QT board

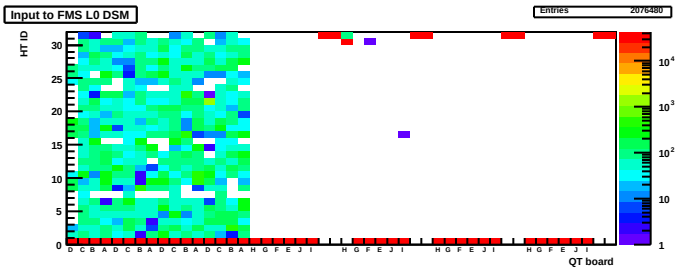

Input to FMS L0 DSM
Entries 31147
QT8(2) sum - simulated
QT board

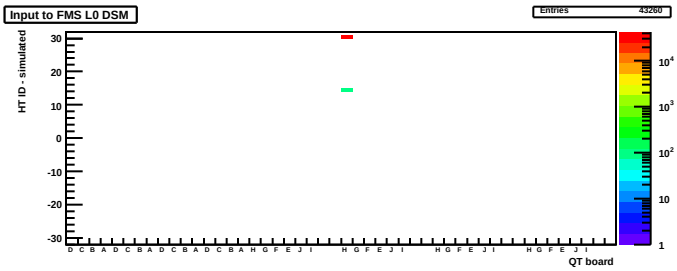

Input to FMS L0 DSM
Entries 2076480
QT8(3) sum
QT board

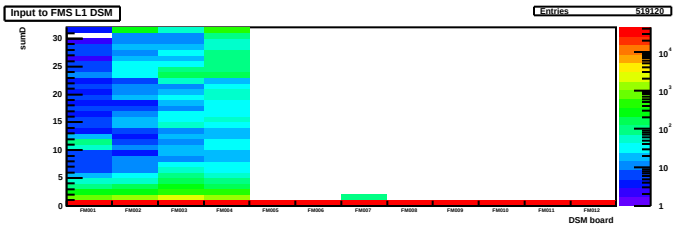

Input to FMS L0 DSM
Entries 40217
QT8(3) sum - simulated
QT board

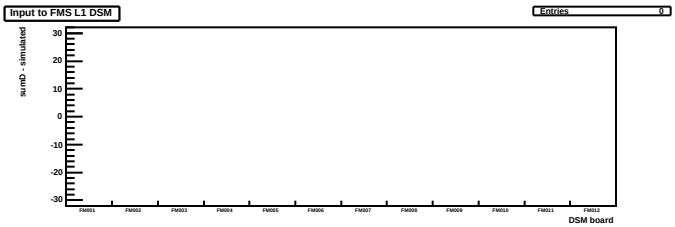

Input to FMS L0 DSM
Entries 2076480
HT ADC
QT board

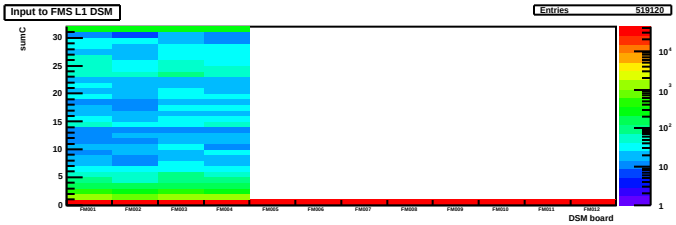

Input to FMS L0 DSM
Entries 0
HT ADC - simulated
QT board

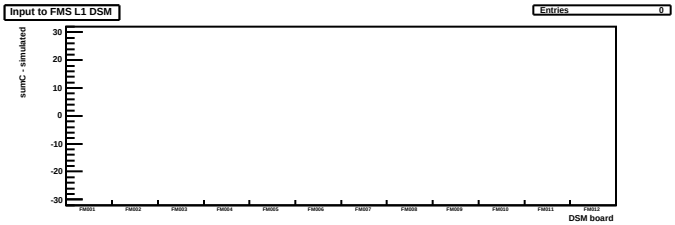

Input to FMS L0 DSM
Entries 2076480
HT ID
QT board

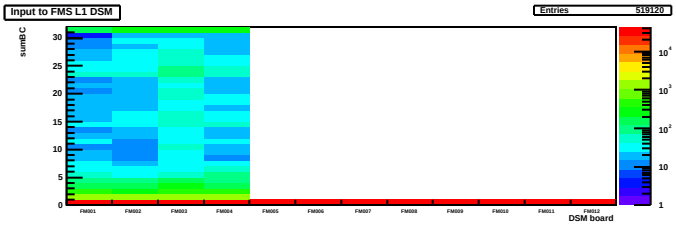

Input to FMS L0 DSM
Entries 43260
HT ID - simulated
QT board

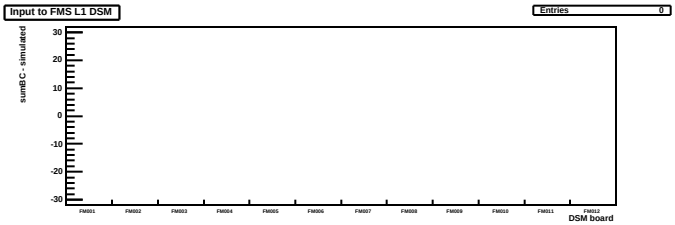

Input to FMS L1 DSM
Entries 519120
sumD
DSM board

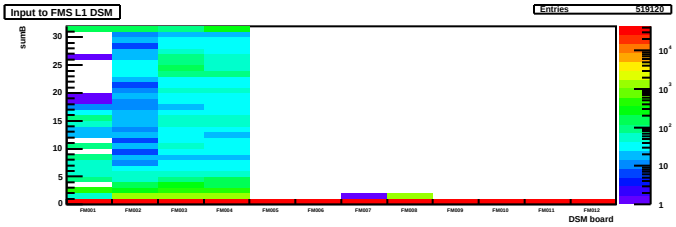

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

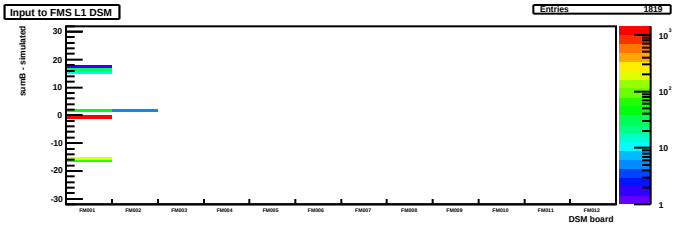

Input to FMS L1 DSM
Entries 519120
sumC
DSM board

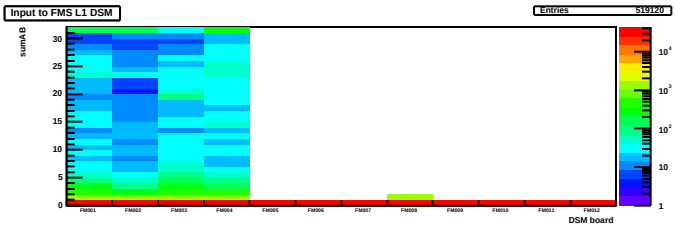

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

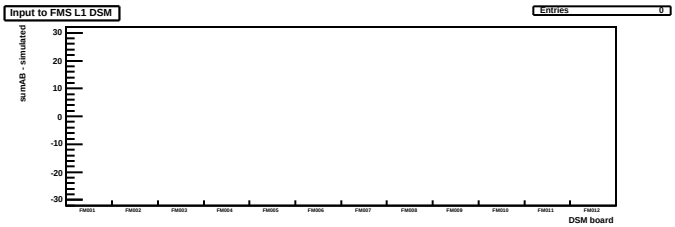

Input to FMS L1 DSM
Entries 519120
sumBC
DSM board

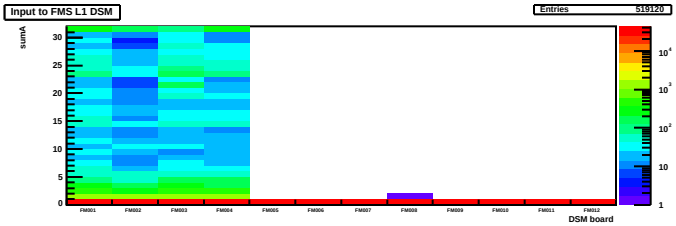

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

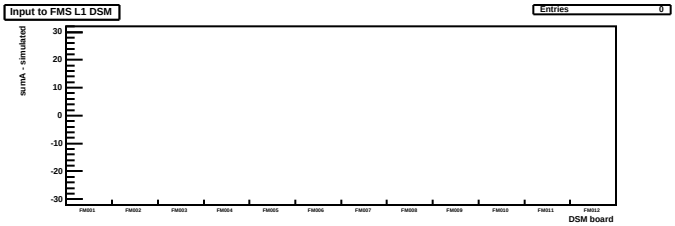

Input to FMS L1 DSM
Entries 519120
sumB
DSM board

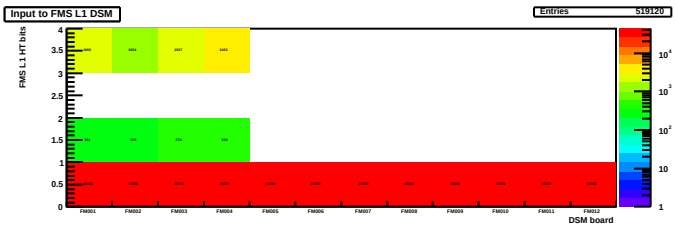

Input to FMS L1 DSM
Entries 1819
sumB - simulated
DSM board

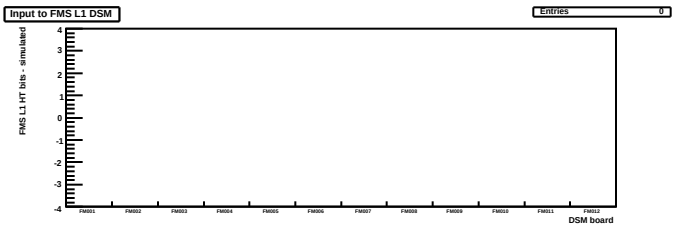

Input to FMS L1 DSM
Entries 519120
sumAB
DSM board

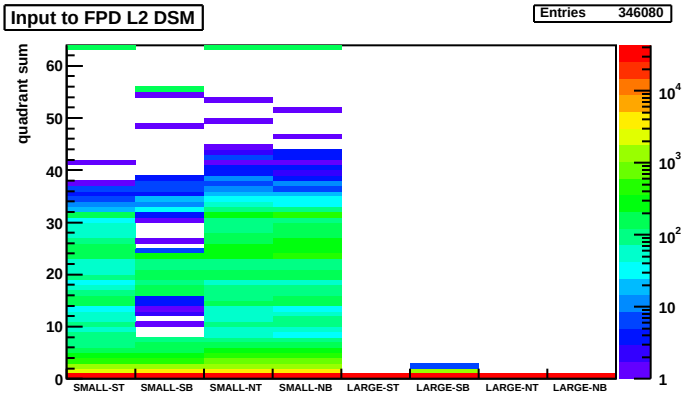

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

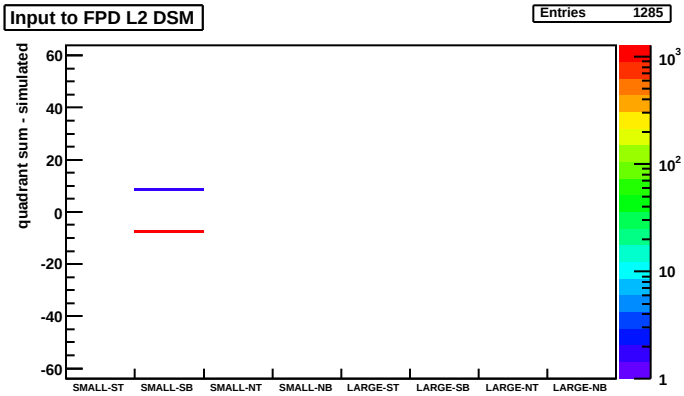

Input to FMS L1 DSM
Entries 519120
sumA
DSM board

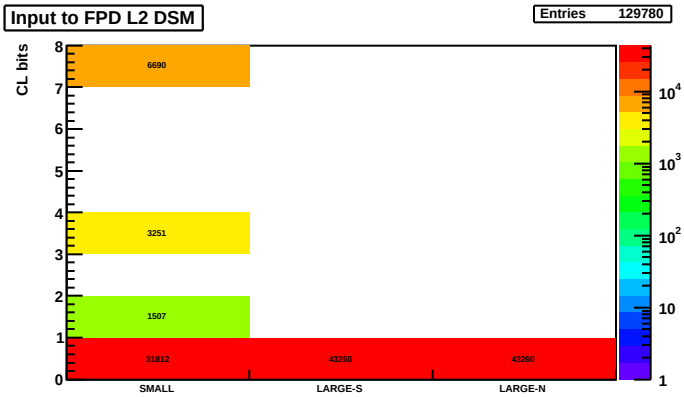

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

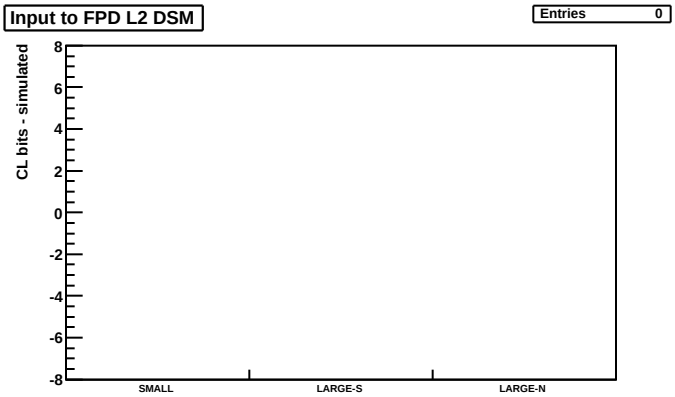

Input to FMS L1 DSM
Entries 519120
FMS L1 HT bits
DSM board

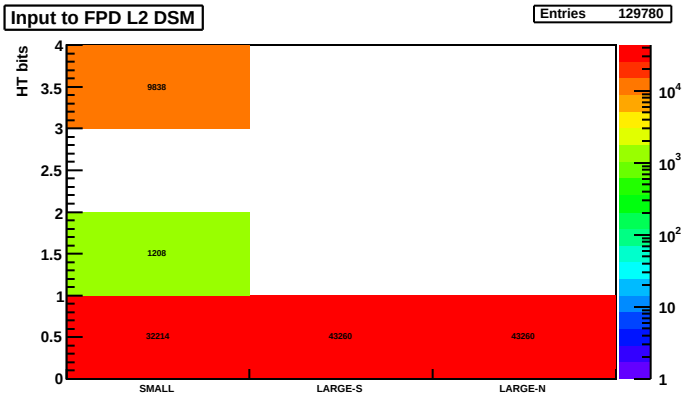

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

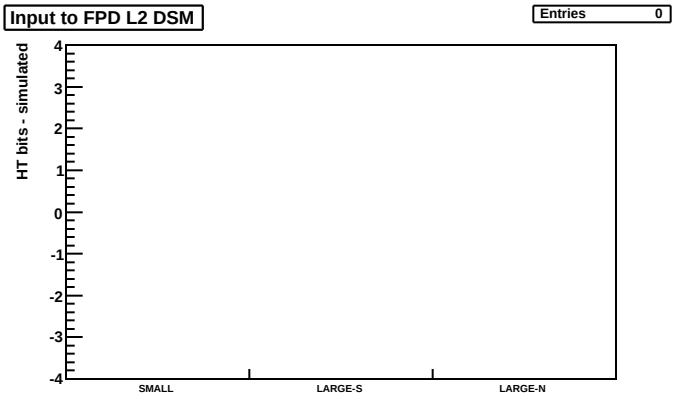

Input to FPD L2 DSM
Entries 346080
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

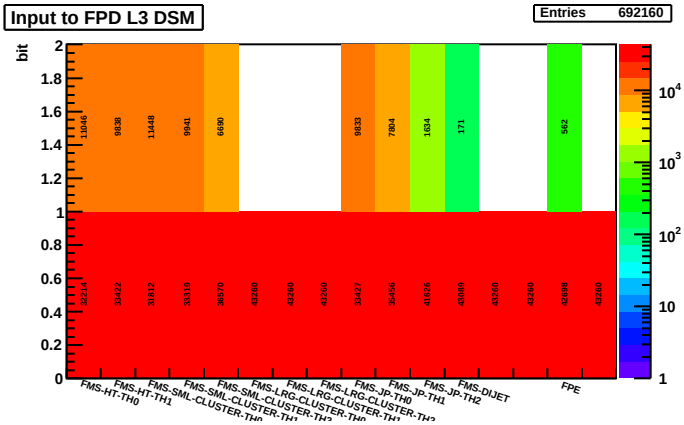

Input to FPD L2 DSM
Entries 1285
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

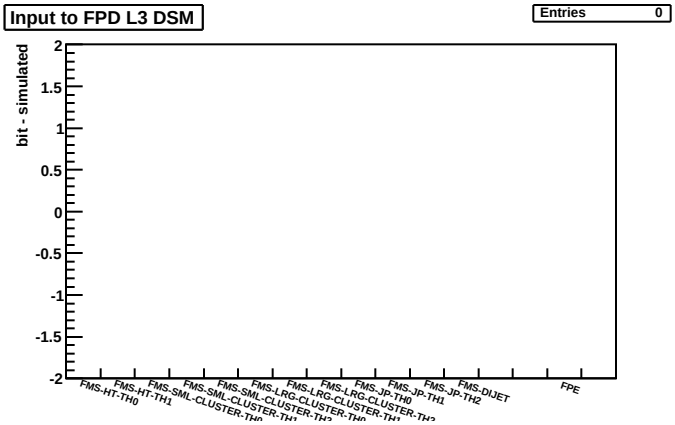

Input to FPD L2 DSM
Entries 129780
CL bits
6690
3251
1507
31812
43260
43260
SMALL
LARGE-S
LARGE-N

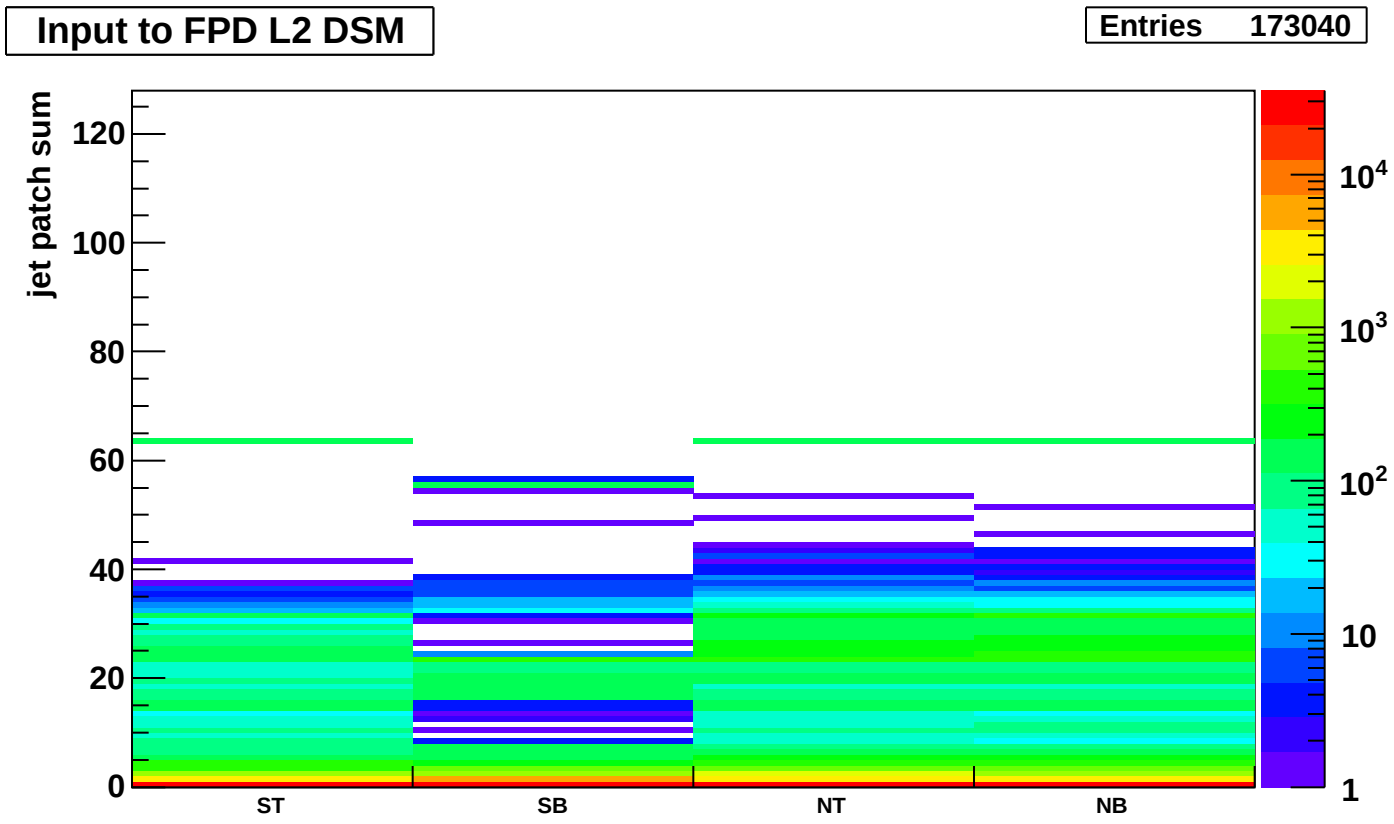

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

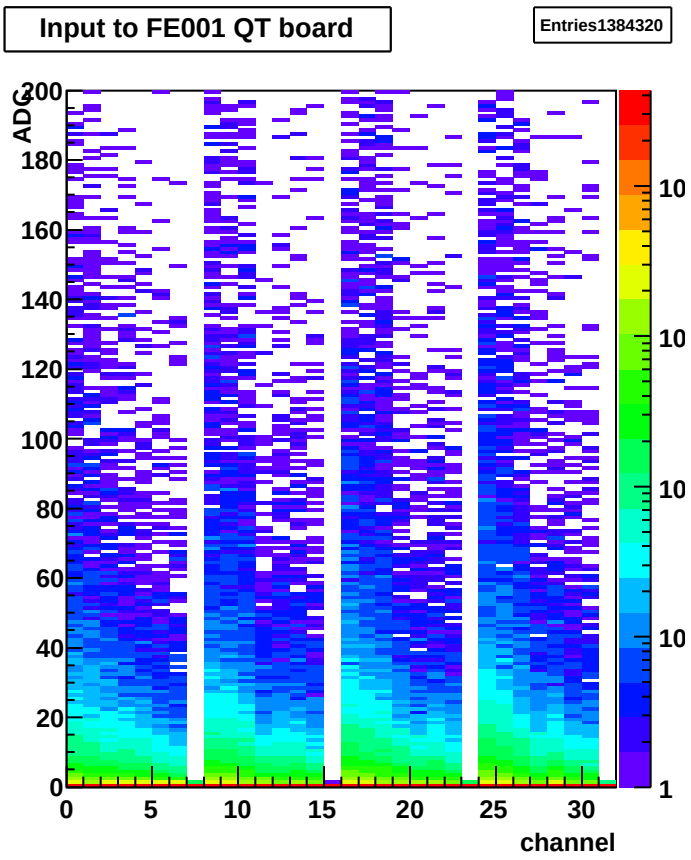

Input to FPD L2 DSM
Entries 129780
HT bits
9838
1208
32214
43260
43260
SMALL
LARGE-S
LARGE-N

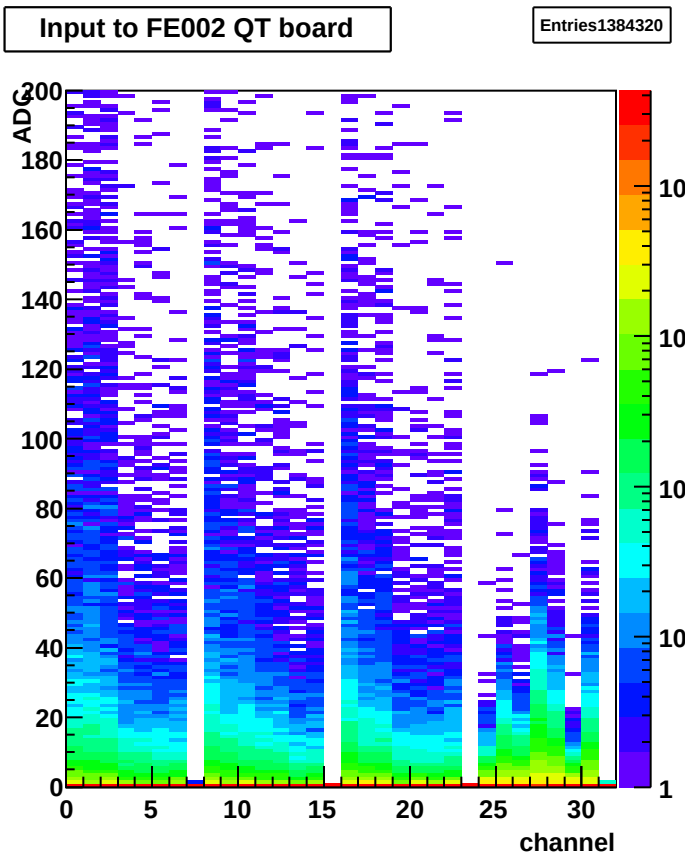

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

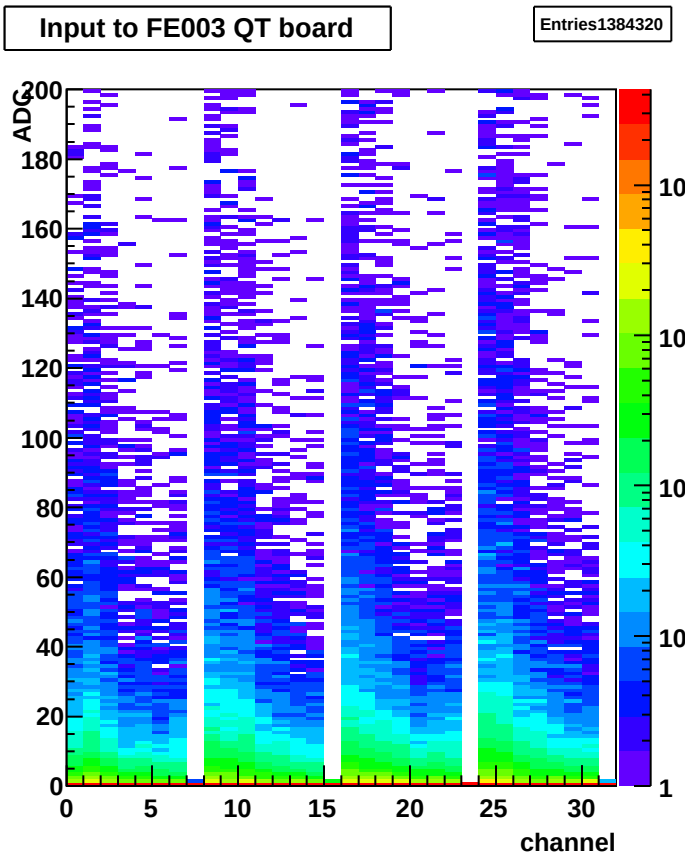

Input to FPD L3 DSM
Entries 692160
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

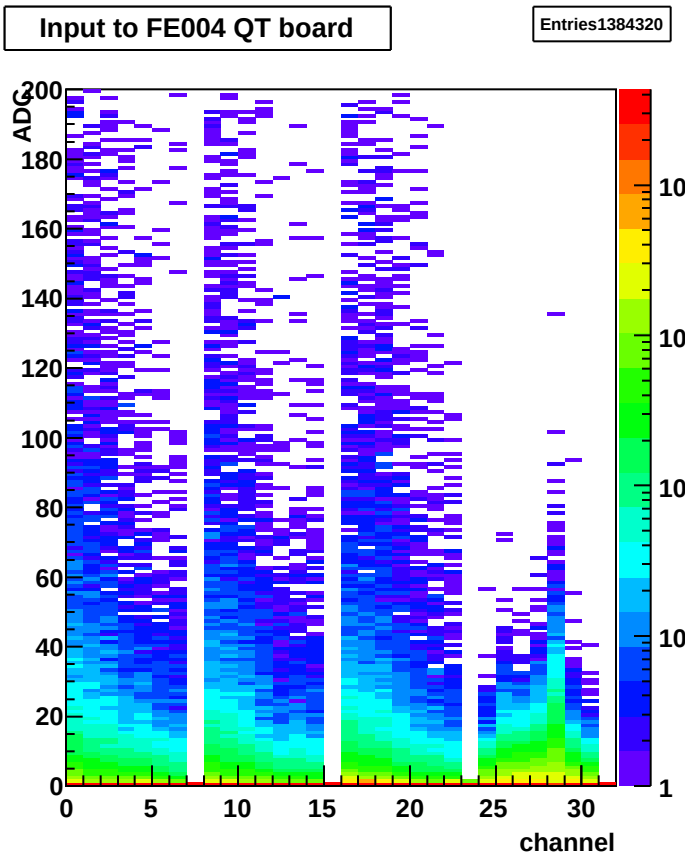

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

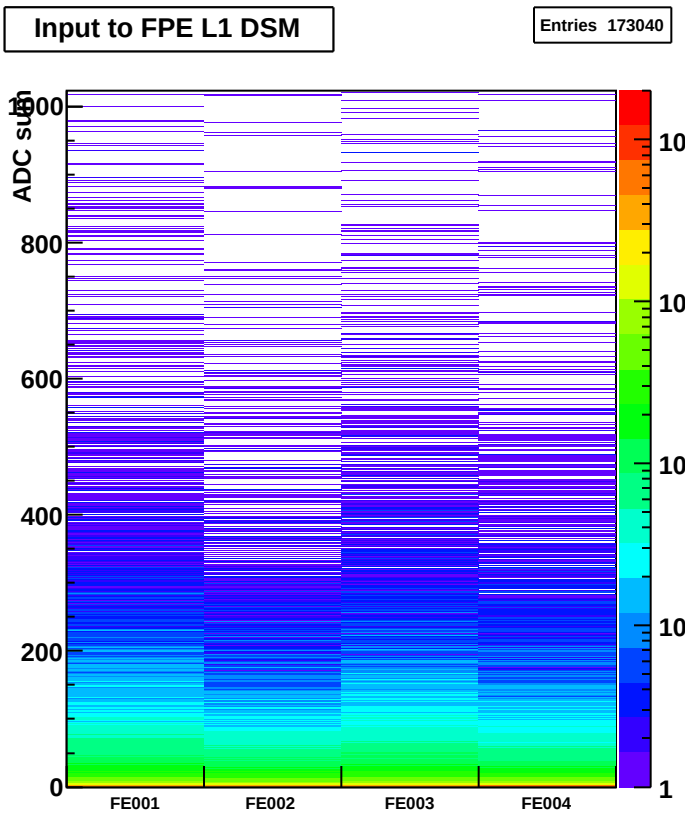

Input to FPD L2 DSM
Entries 173040
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

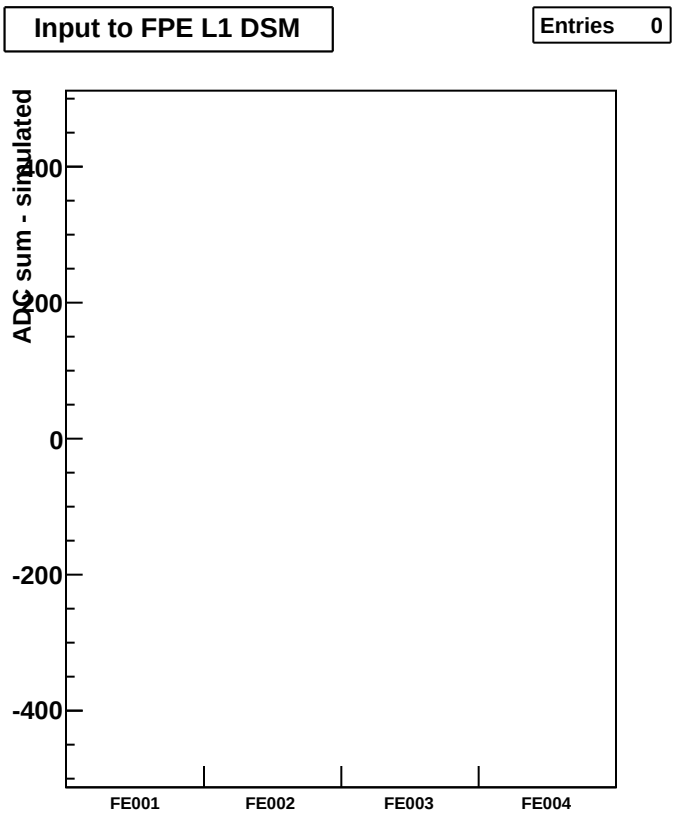

Input to FE001 QT board
Entries1384320
ADC
channel

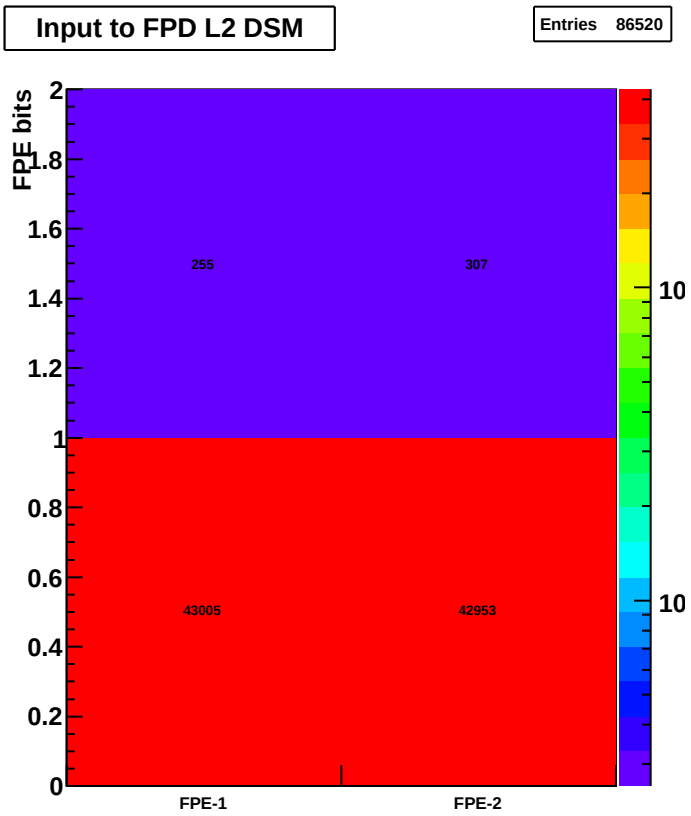

Input to FE002 QT board
Entries1384320
ADC
channel

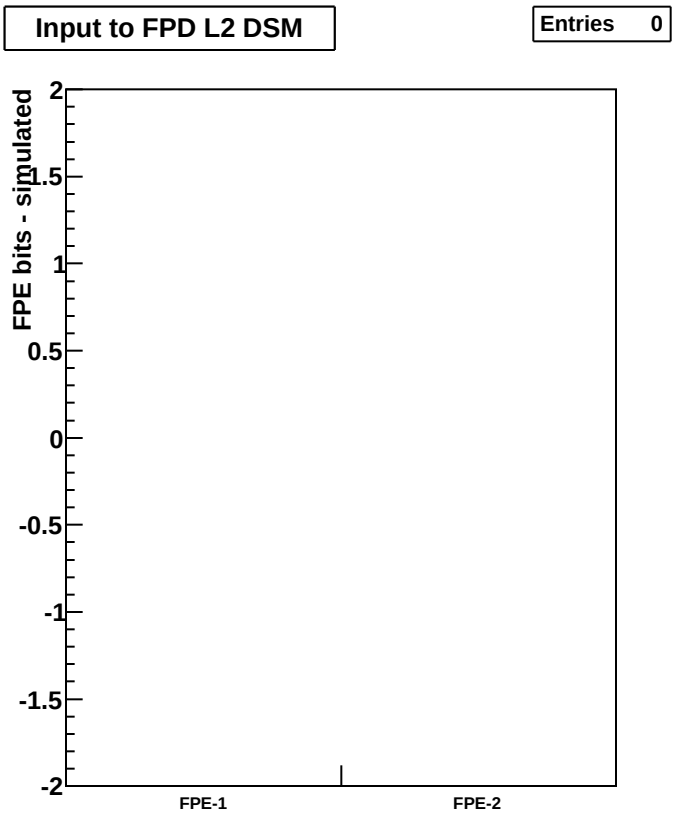

Input to FE003 QT board
Entries1384320
ADC
channel


Input to FE004 QT board
Entries1384320
ADC
channel


Input to FPE L1 DSM
Entries 173040
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 86520
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
255
307
43005
42953
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2