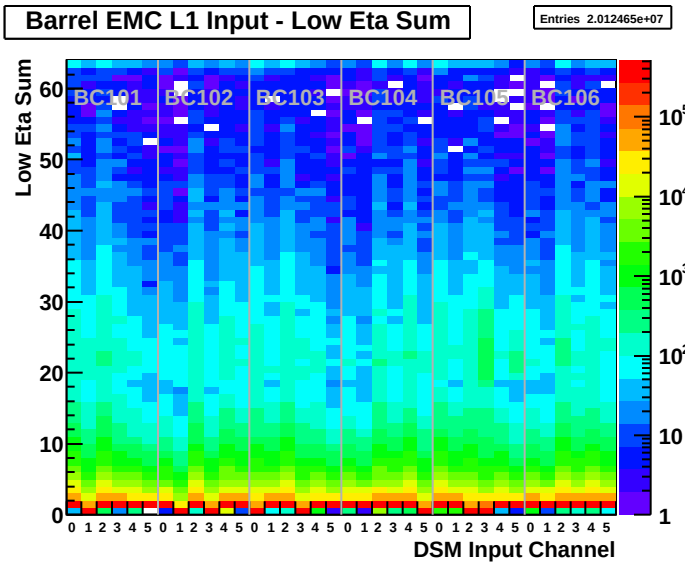
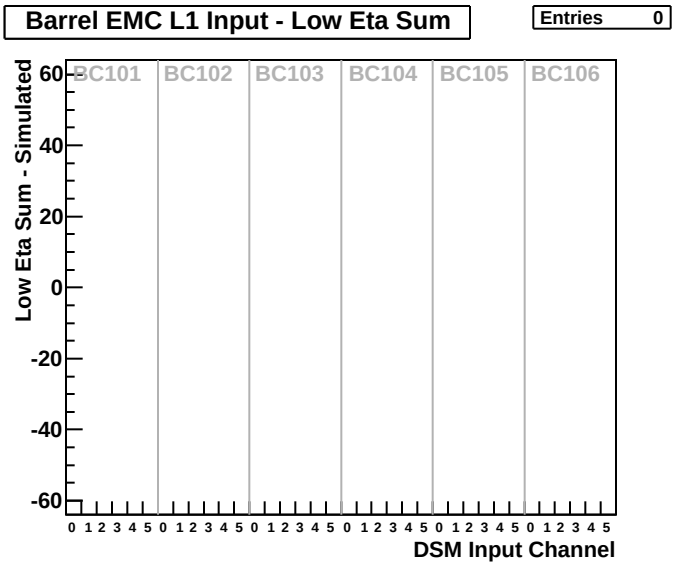
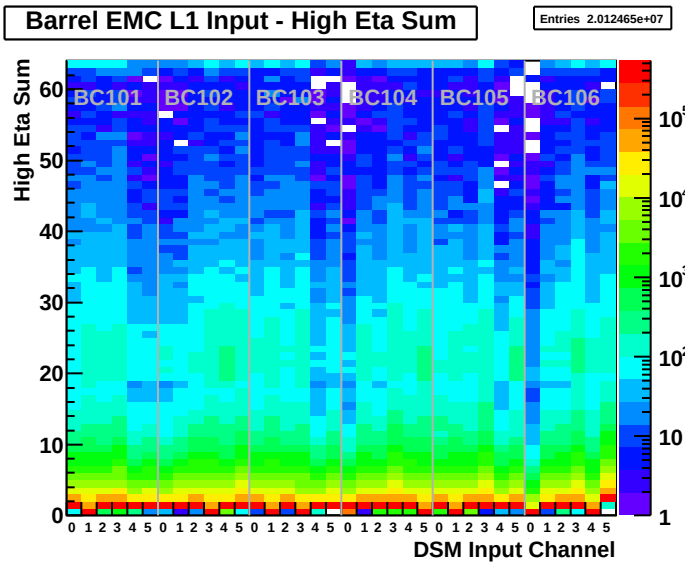
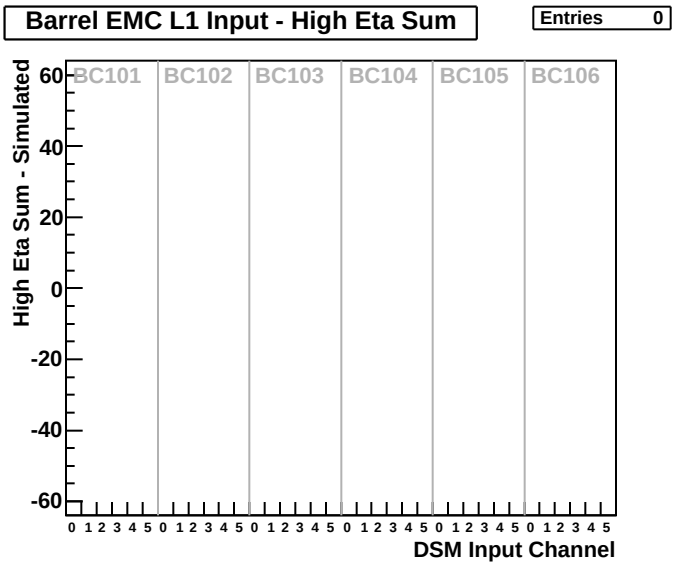
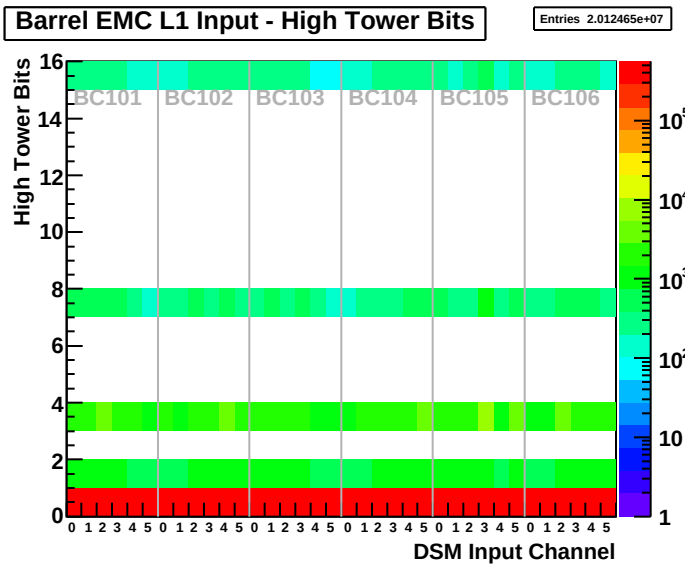
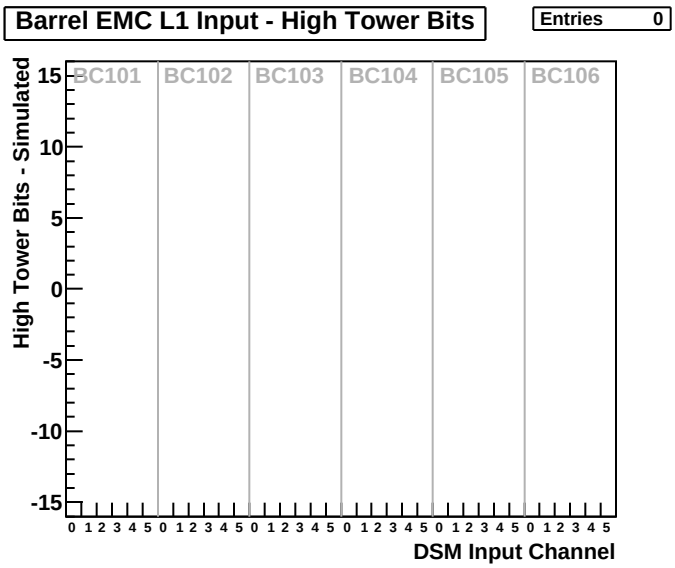

Barrel EMC L1 Input - Low Eta Sum
Entries 2.012465e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

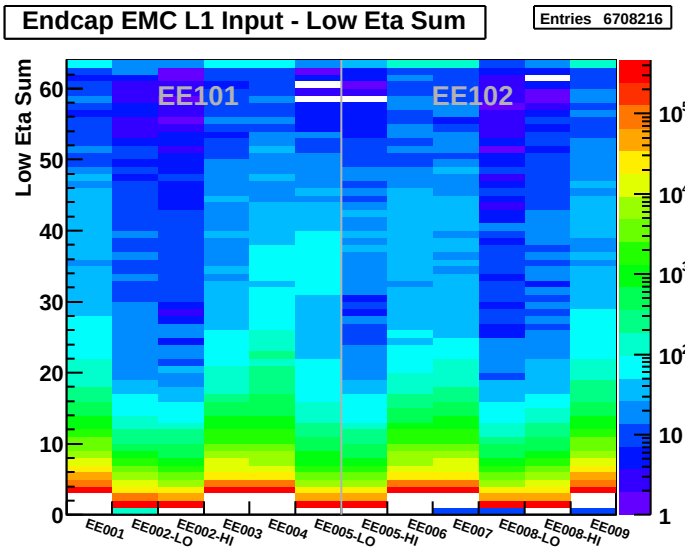

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

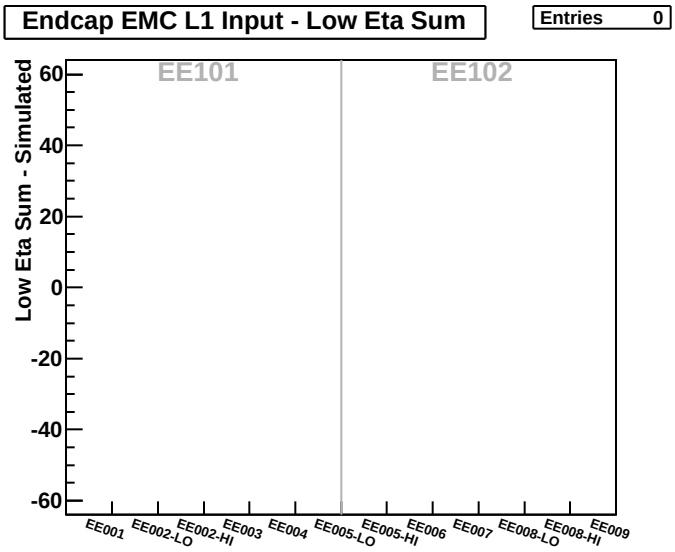

Barrel EMC L1 Input - High Eta Sum
Entries 2.012465e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

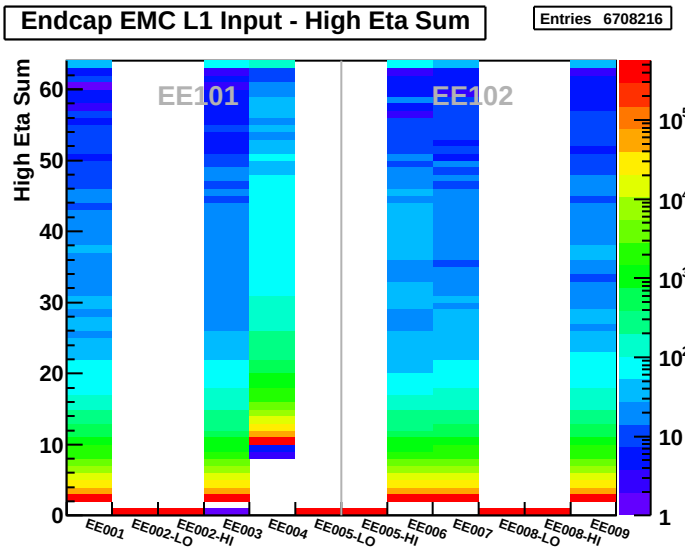

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

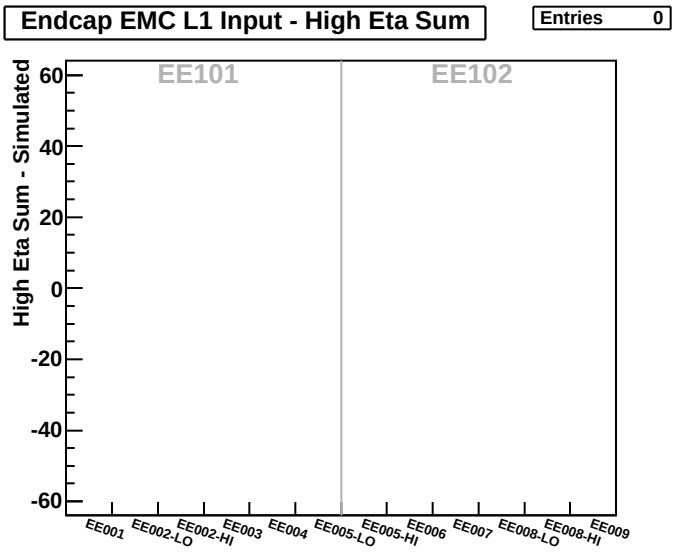

Barrel EMC L1 Input - High Tower Bits
Entries 2.012465e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

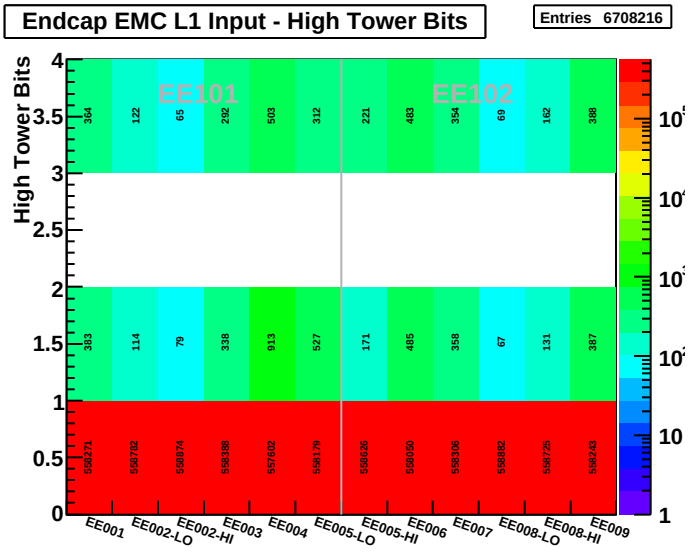

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

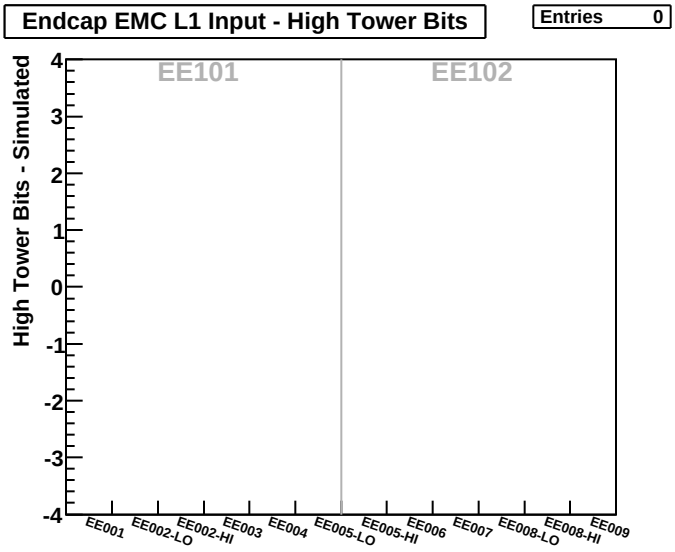

Endcap EMC L1 Input - Low Eta Sum
Entries 6708216
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

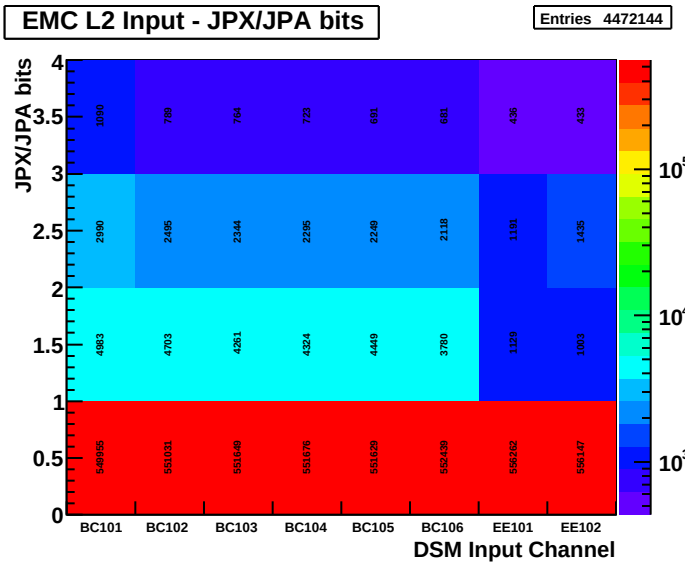

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

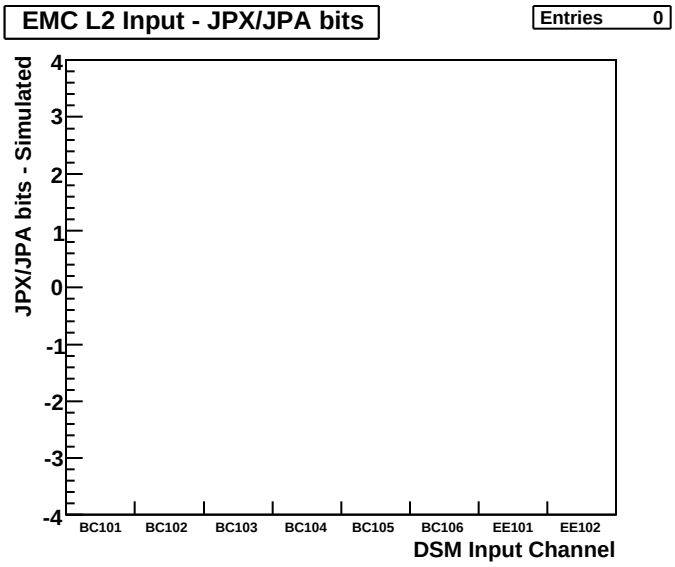

Endcap EMC L1 Input - High Eta Sum
Entries 6708216
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

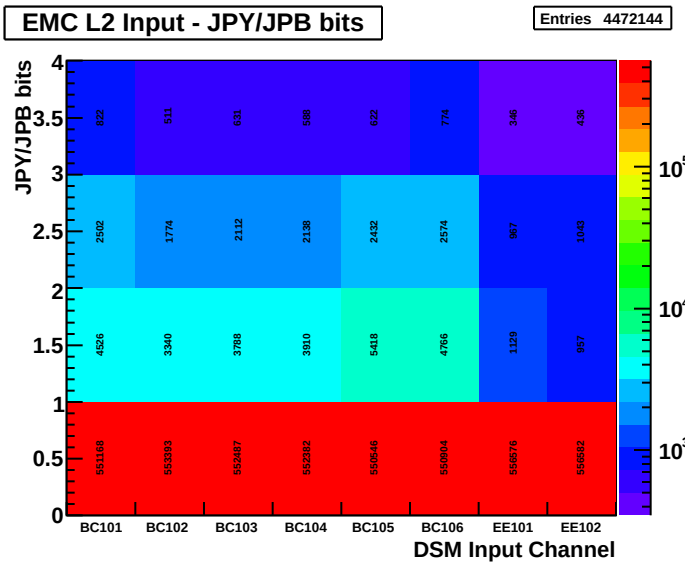

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

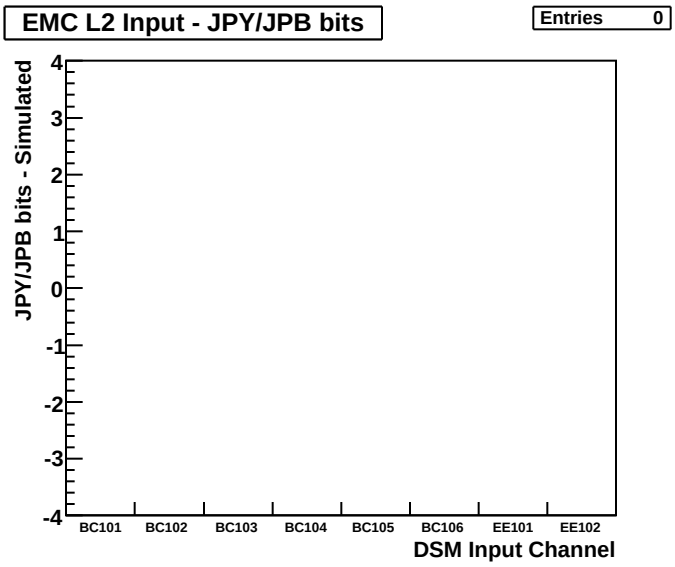

Endcap EMC L1 Input - High Tower Bits
Entries 6708216
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

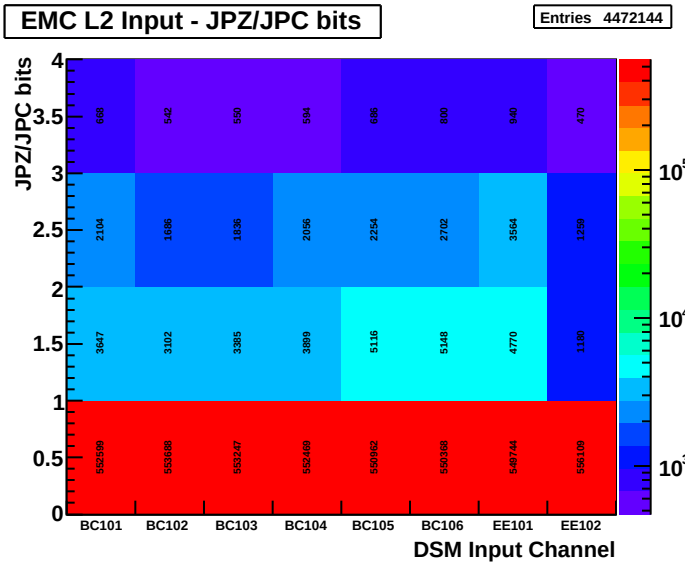

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

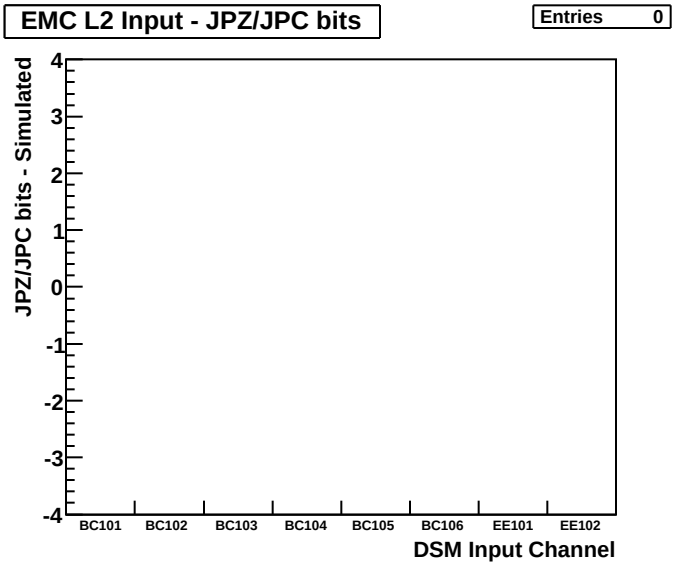

EMC L2 Input - JPX/JPA bits
Entries 4472144
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1090 789 764 723 691 681 436 433
2990 2495 2344 2295 2249 2118 1191 1435
4963 4703 4261 4324 4449 3780 1129 1003
548955 551031 551649 551676 551629 552439 558262 556147

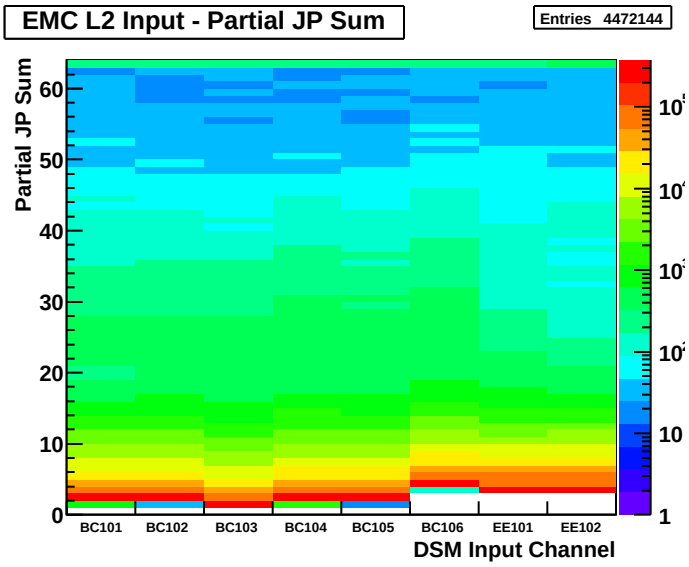

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

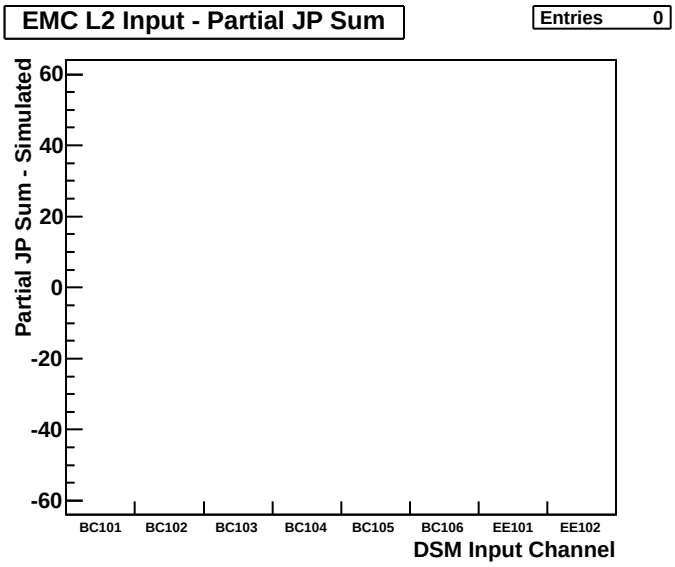

EMC L2 Input - JPY/JPB bits
Entries 4472144
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
622 511 631 588 622 774 346 436
2502 1774 2112 2138 2432 2574 967 1043
4526 3340 3788 3910 5418 4766 1129 957
551168 553393 553487 553282 550546 550004 556576 556582

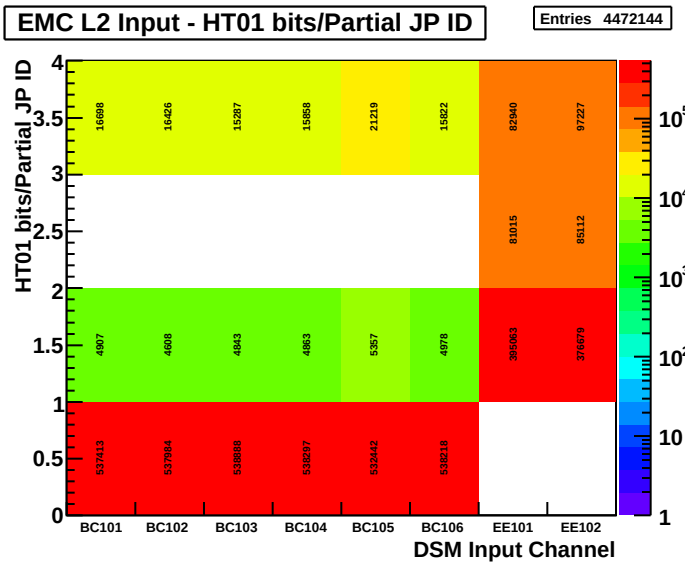

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

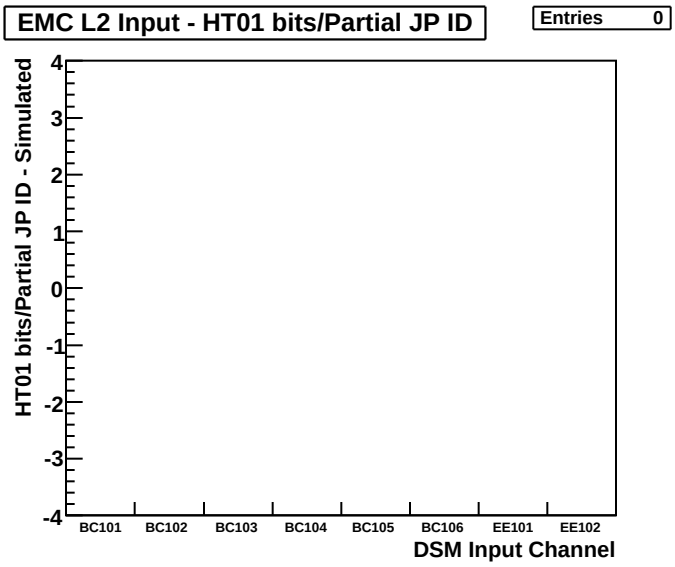

EMC L2 Input - JPZ/JPC bits
Entries 4472144
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
688 542 550 594 686 800 940 470
2104 1686 1836 2056 2254 2702 3564 1259
3647 3102 3385 3899 5116 5148 4770 1180
552599 553588 553247 552469 550962 550368 549744 556109

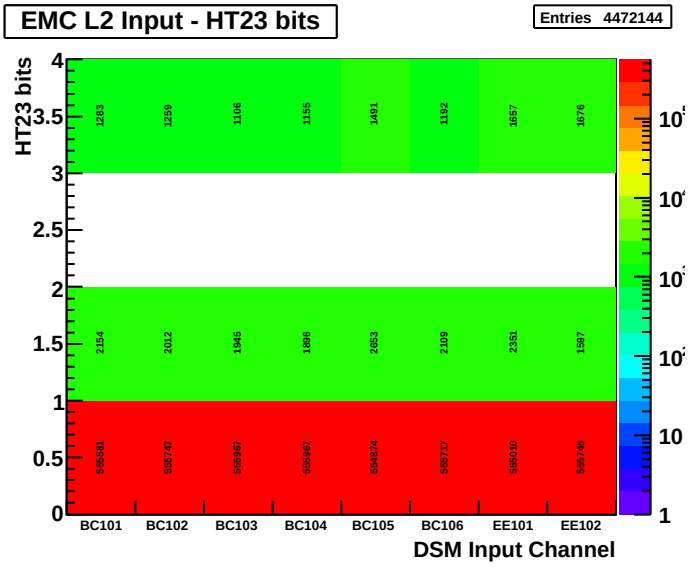

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

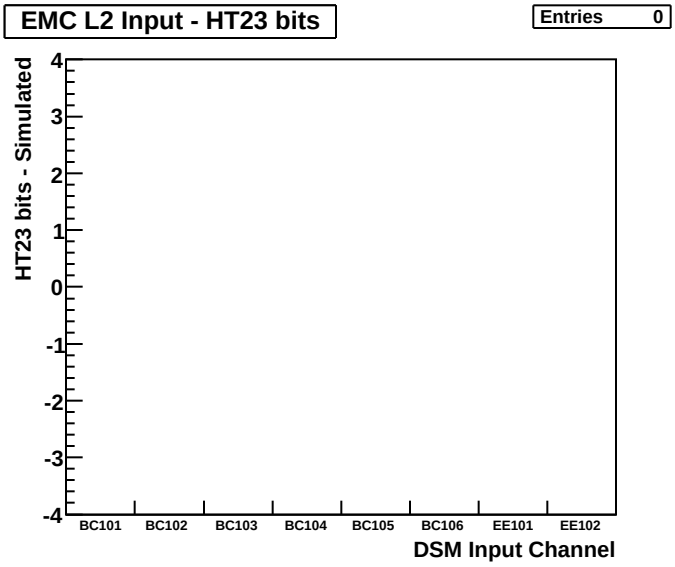

EMC L2 Input - Partial JP Sum
Entries 4472144
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

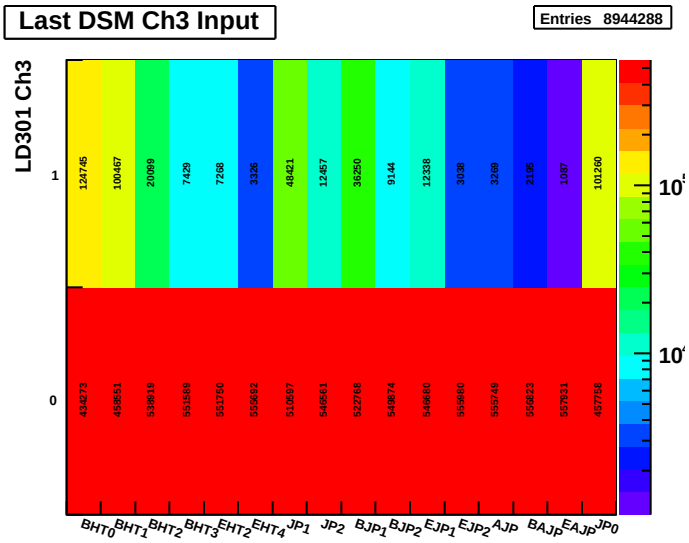

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

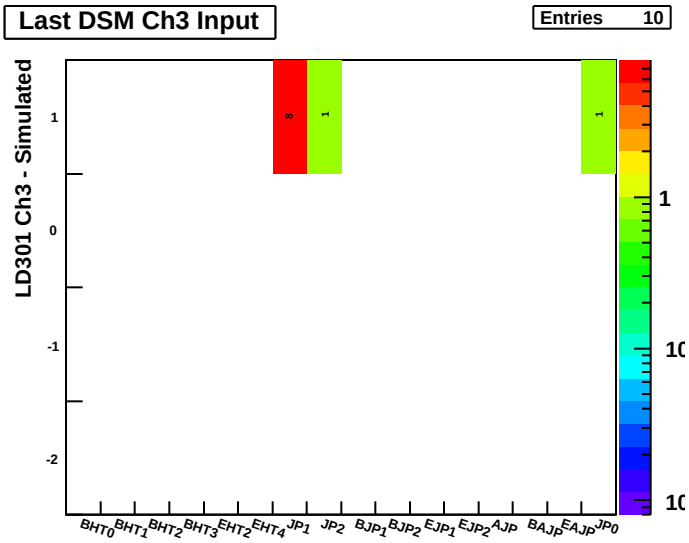

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4472144
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

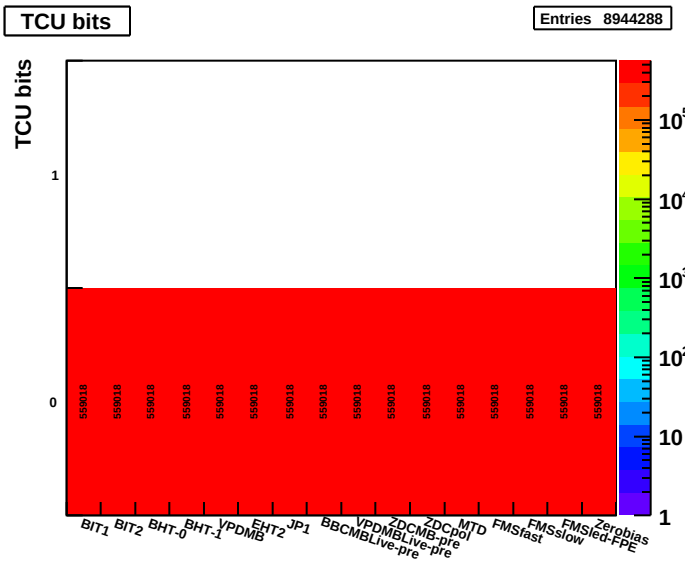

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

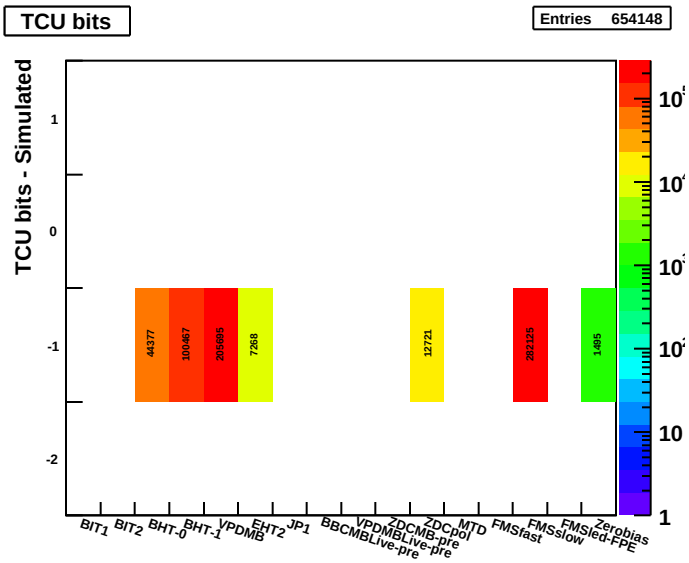

EMC L2 Input - HT23 bits
Entries 4472144
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

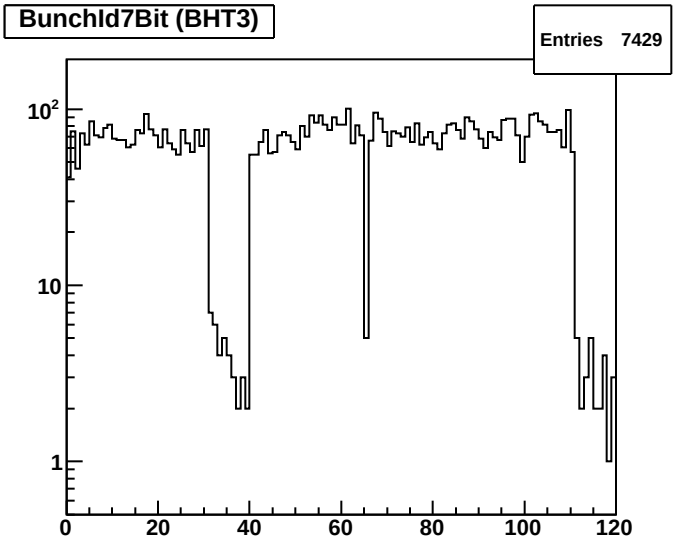

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

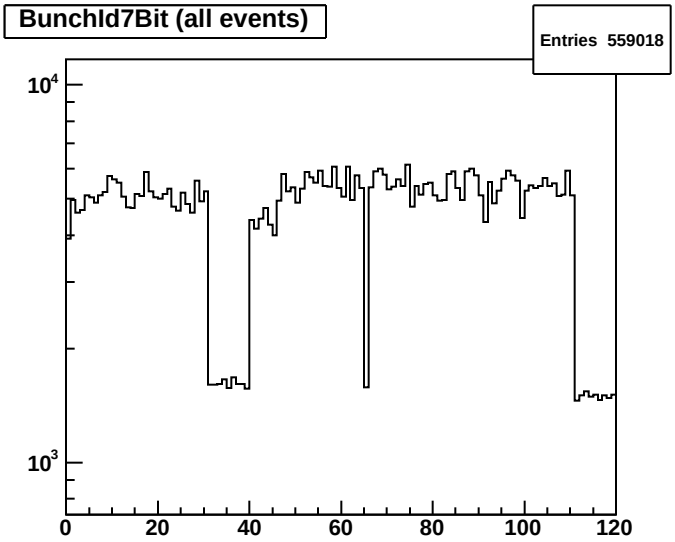

Last DSM Ch3 Input
Entries 8944288
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

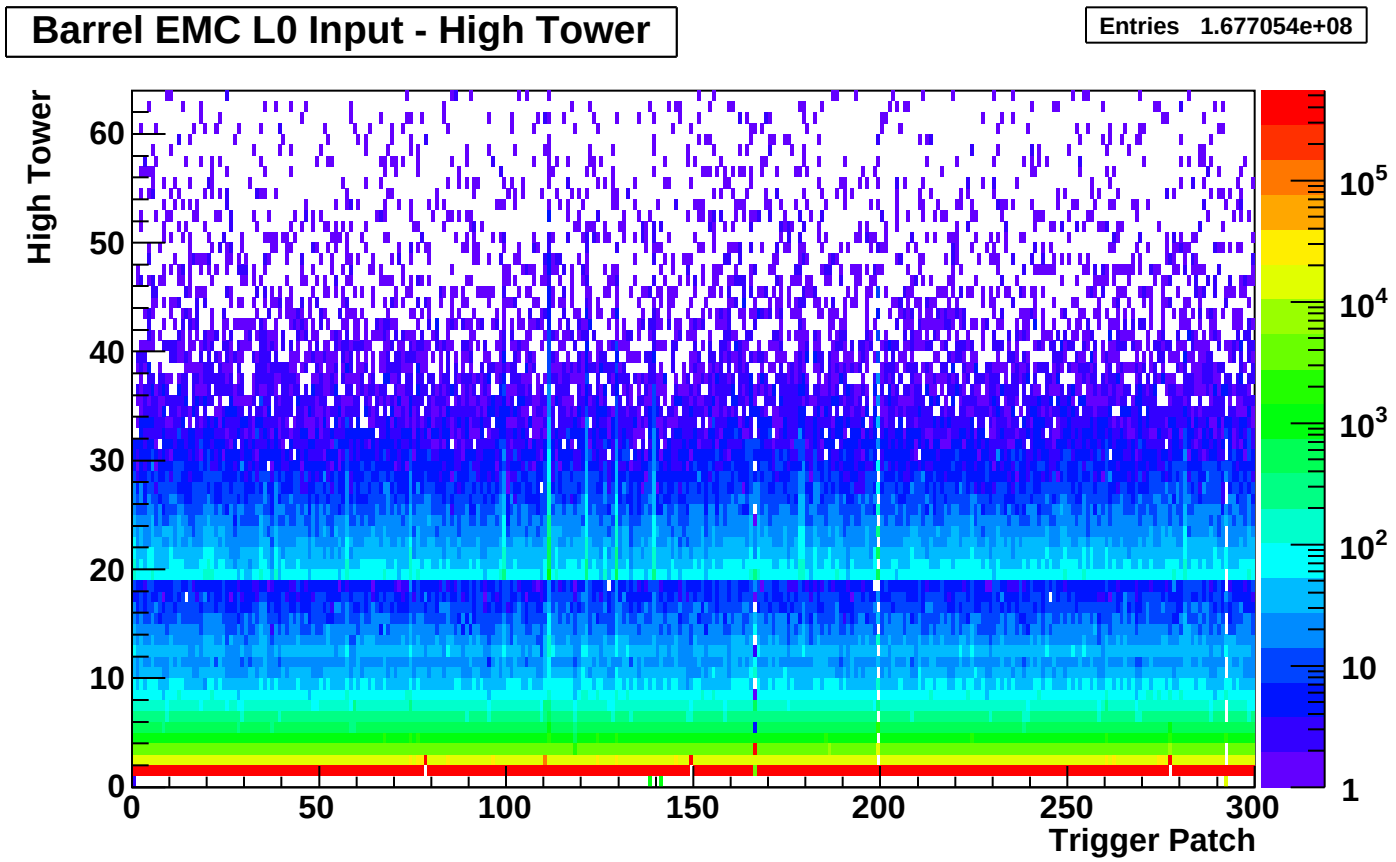

Last DSM Ch3 Input
Entries 10
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

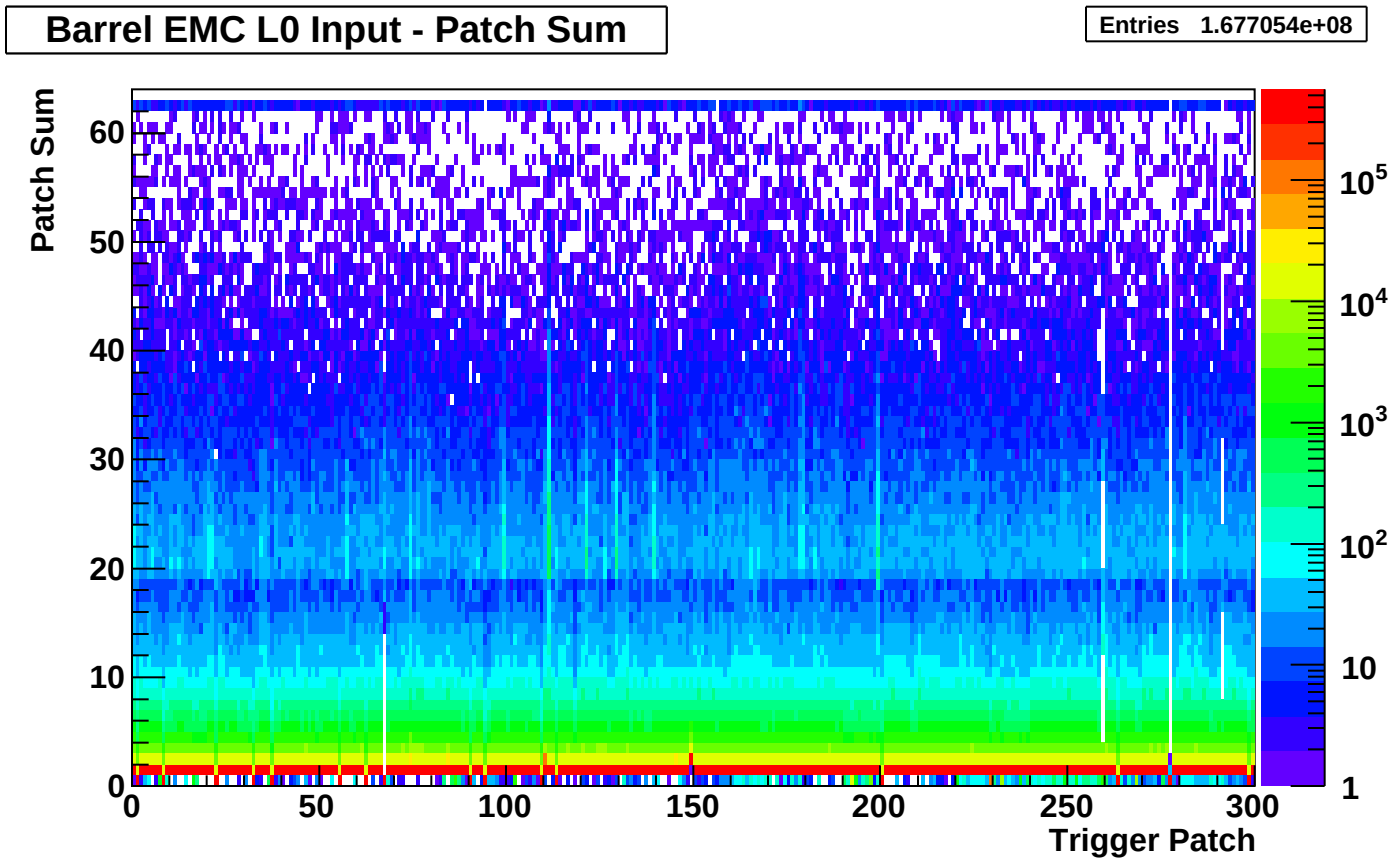

TCU bits
Entries 8944288
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

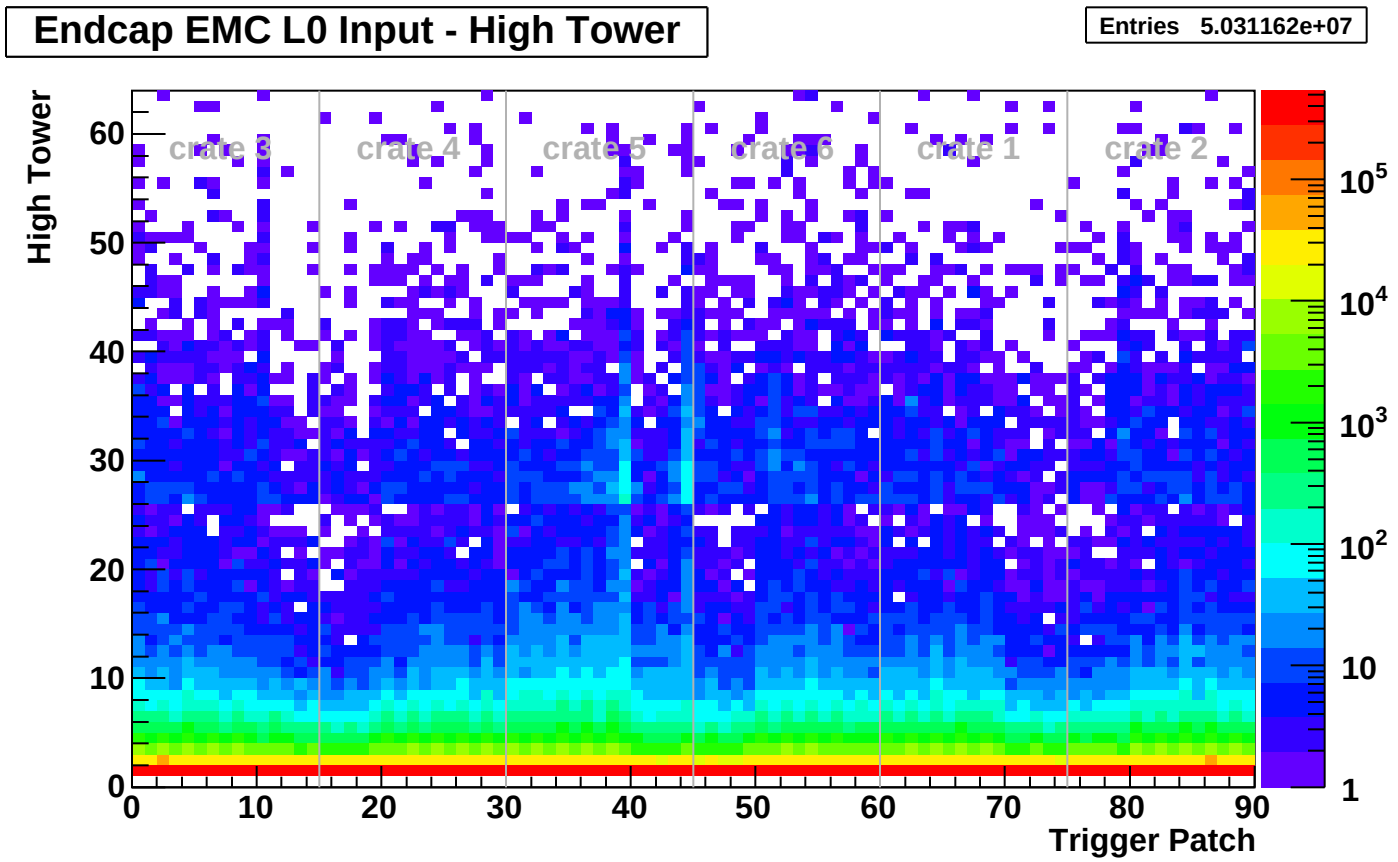

TCU bits
Entries 654148
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

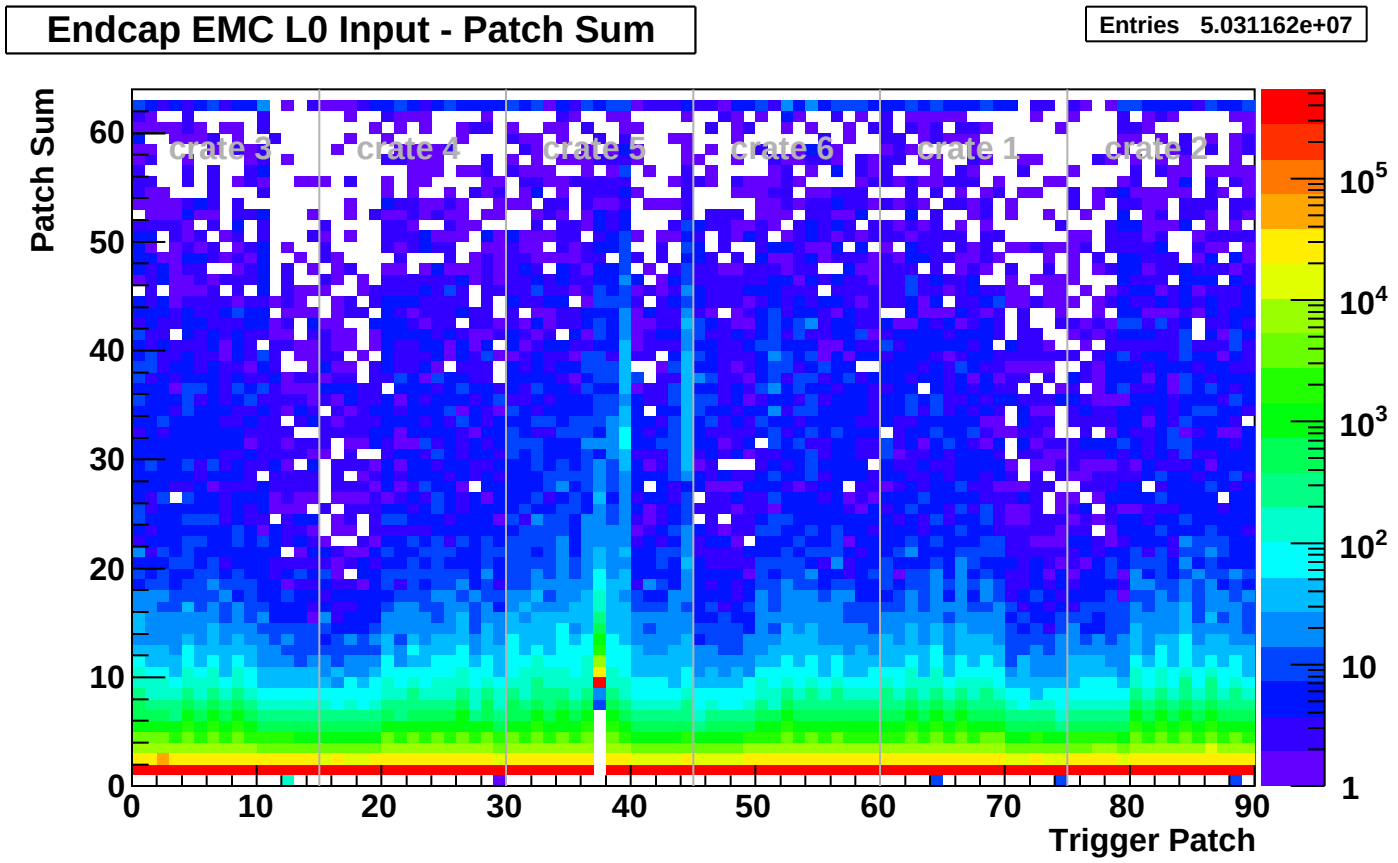

BunchId7Bit (BHT3)
Entries 7429

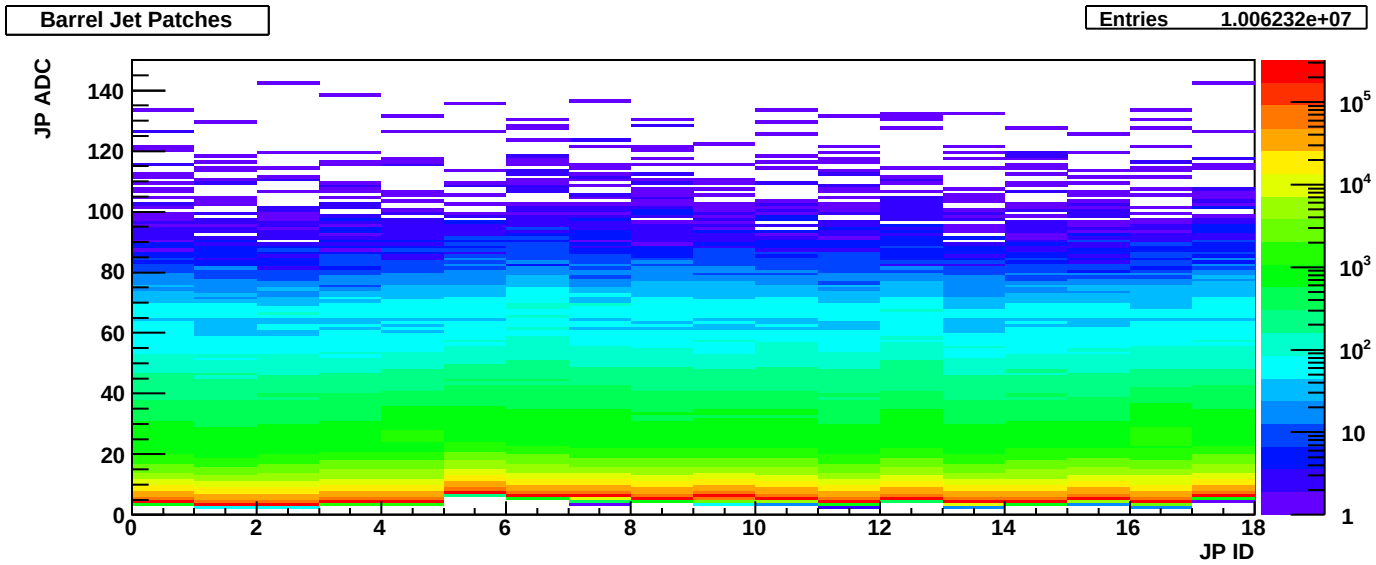

BunchId7Bit (all events)
Entries 559018

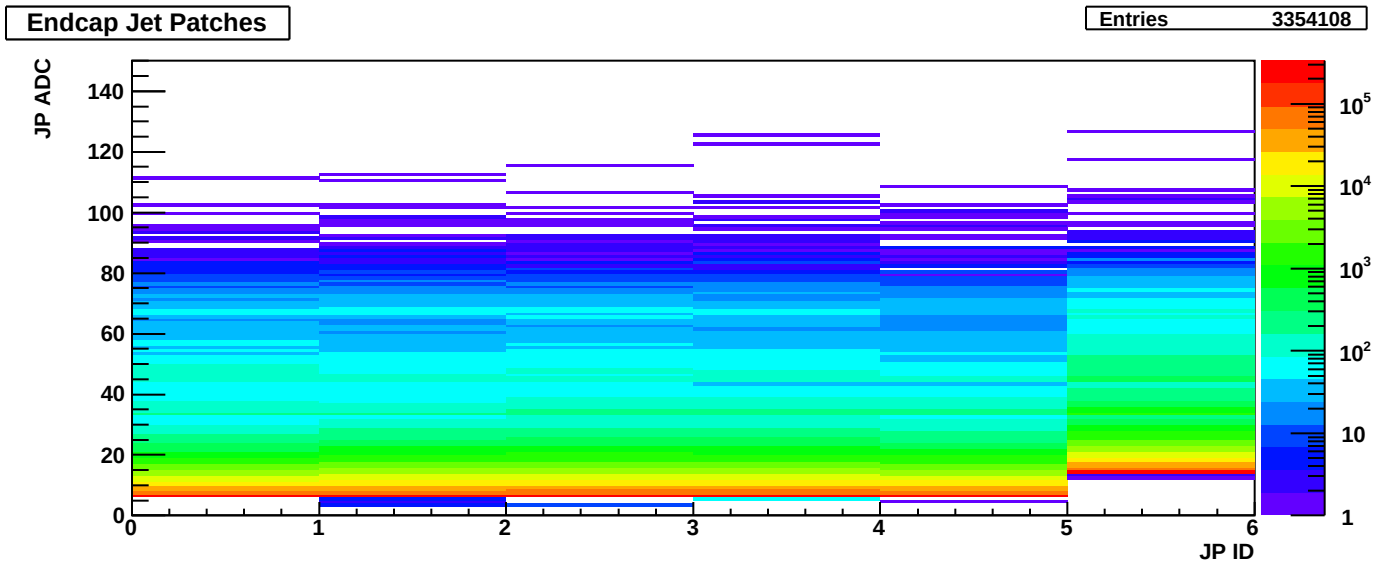

Barrel EMC L0 Input - High Tower
Entries 1.677054e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

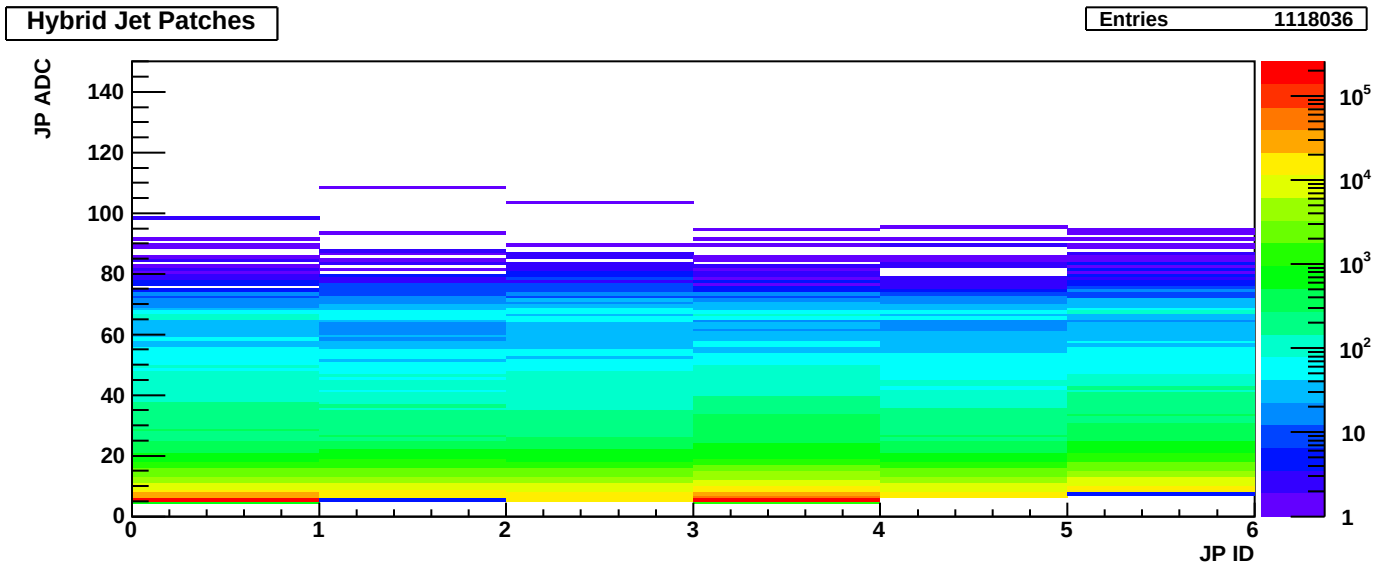

Barrel EMC L0 Input - Patch Sum
Entries 1.677054e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

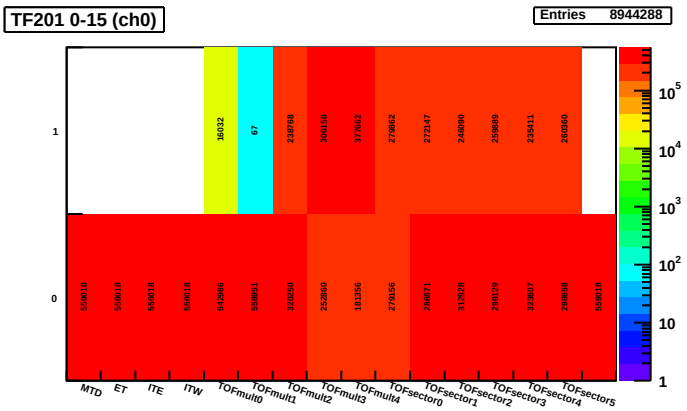

Endcap EMC L0 Input - High Tower
Entries 5.031162e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

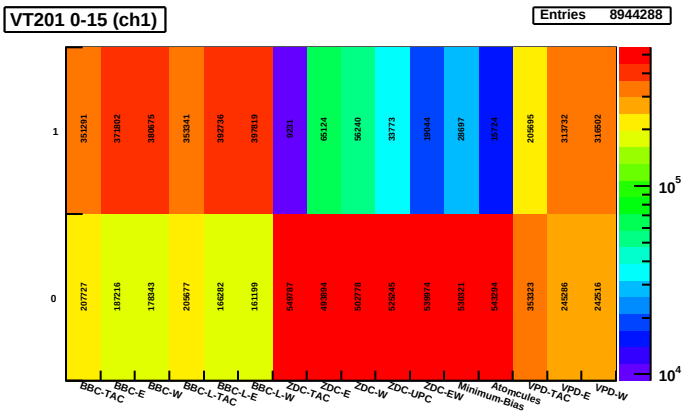

Endcap EMC L0 Input - Patch Sum
Entries 5.031162e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

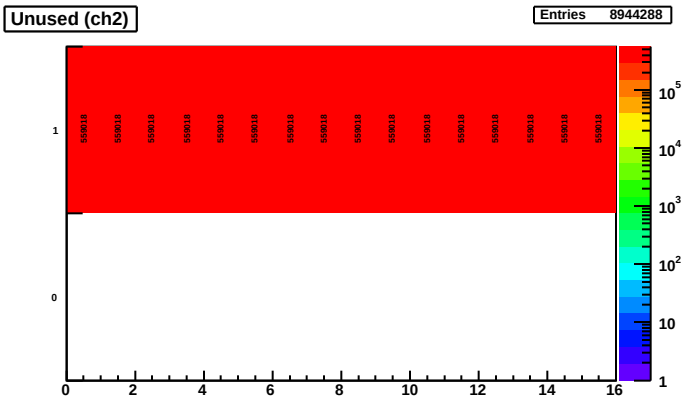

Barrel Jet Patches
Entries 1.006232e+07
JP ADC
JP ID

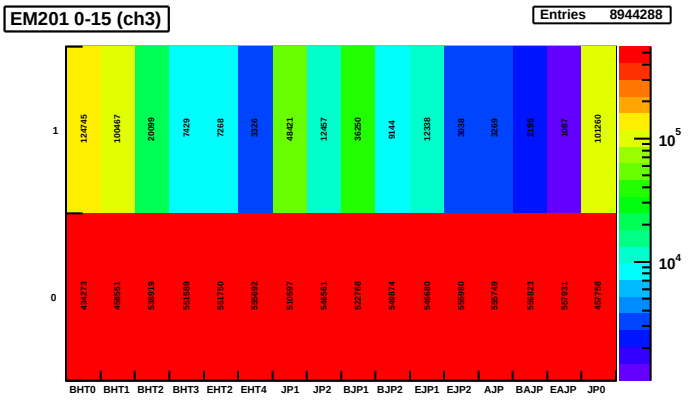

Endcap Jet Patches
Entries 3354108
JP ADC
JP ID

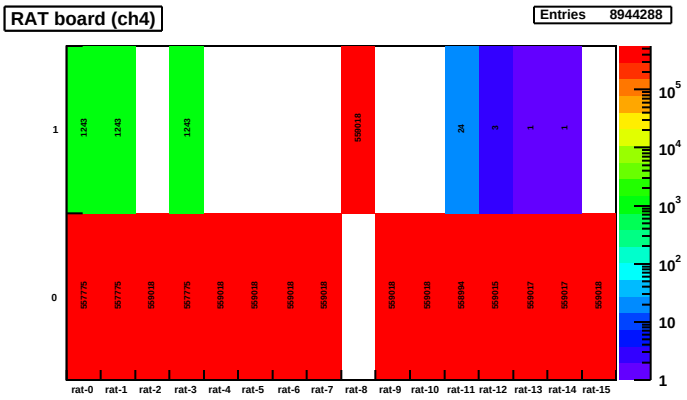

Hybrid Jet Patches
Entries 1118036
JP ADC
JP ID

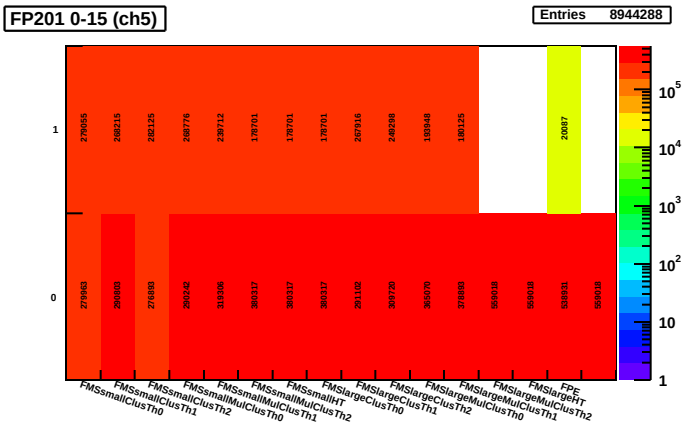

TF201 0-15 (ch0)
Entries 8944288
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

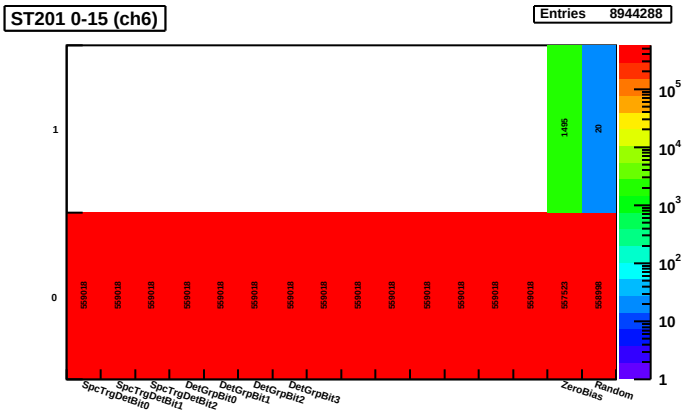

VT201 0-15 (ch1)
Entries 8944288
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

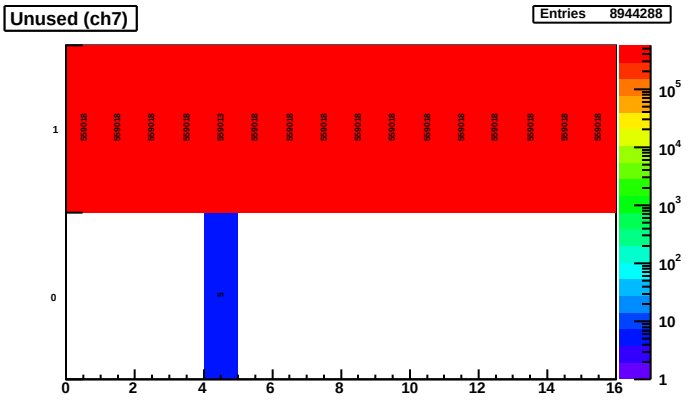

Unused (ch2)
Entries 8944288

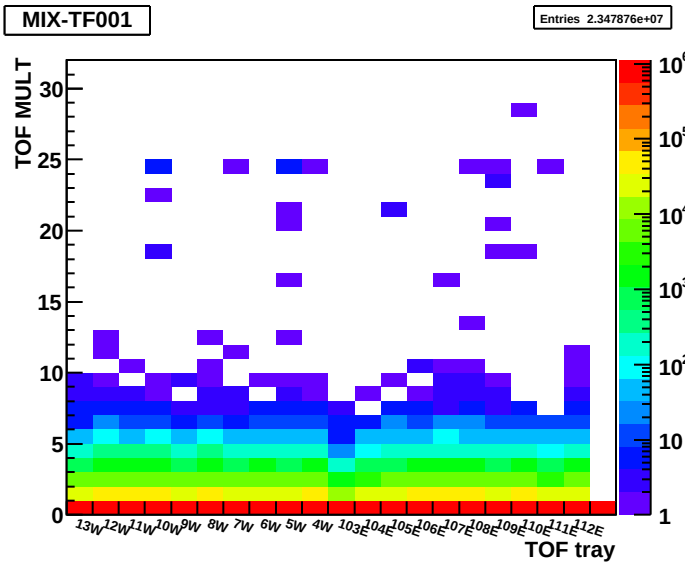

EM201 0-15 (ch3)
Entries 8944288
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

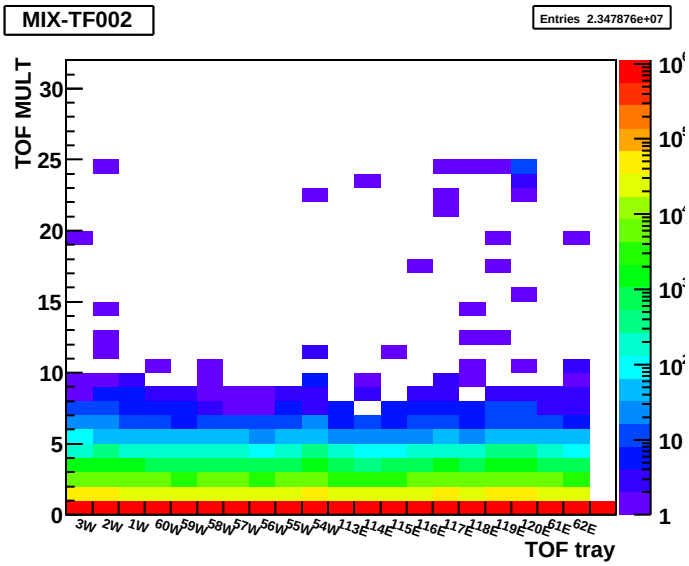

RAT board (ch4)
Entries 8944288
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

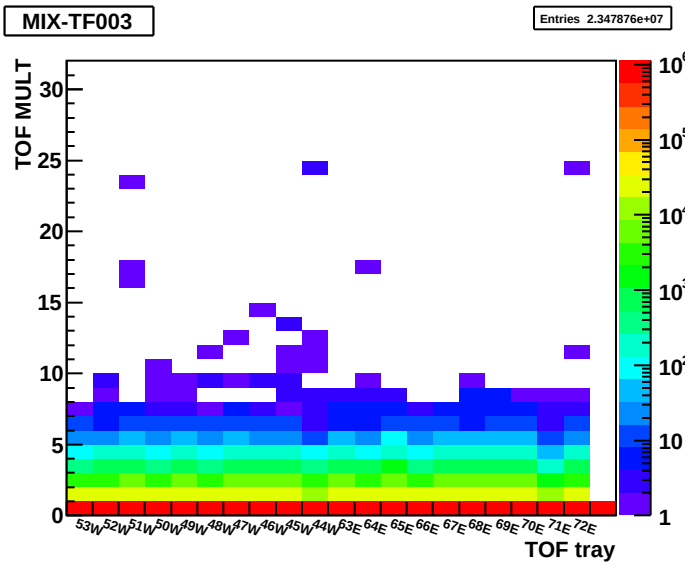

FP201 0-15 (ch5)
Entries 8944288
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

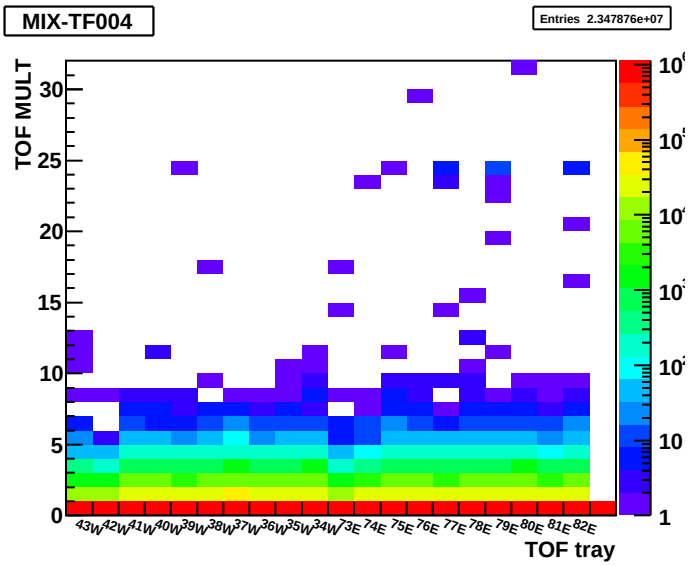

ST201 0-15 (ch6)
Entries 8944288
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

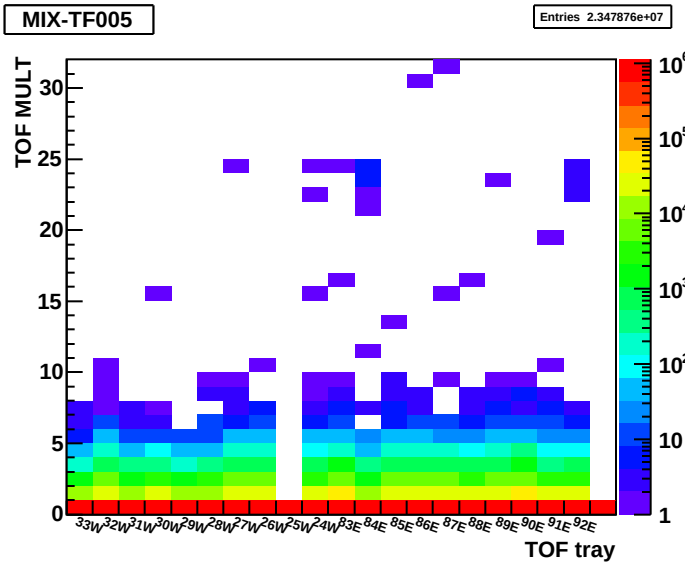

Unused (ch7)
Entries 8944288

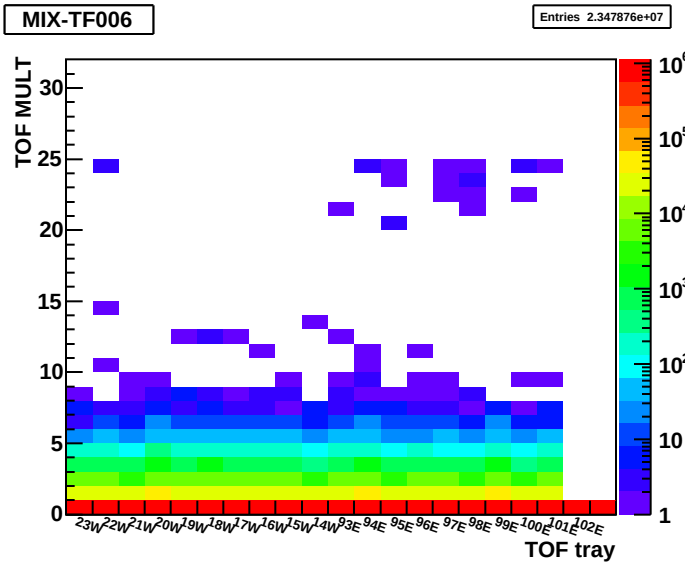

MIX-TF001
Entries 2.347876e+07
TOF MULT
TOF tray

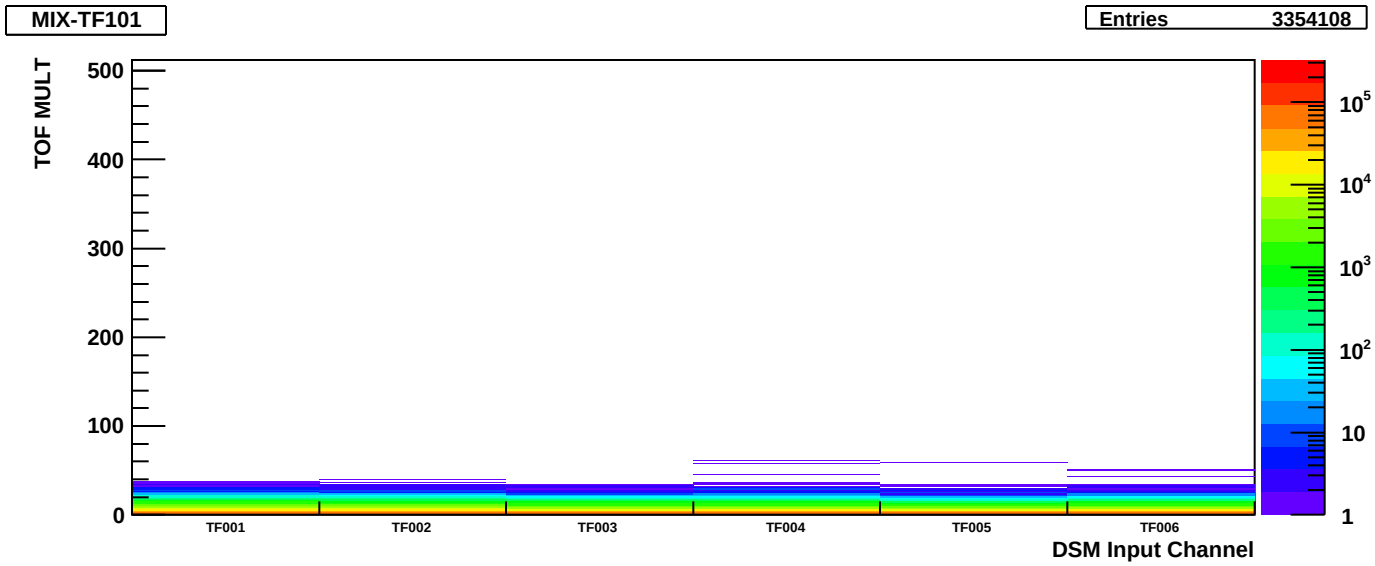

MIX-TF002
Entries 2.347876e+07
TOF MULT
TOF tray

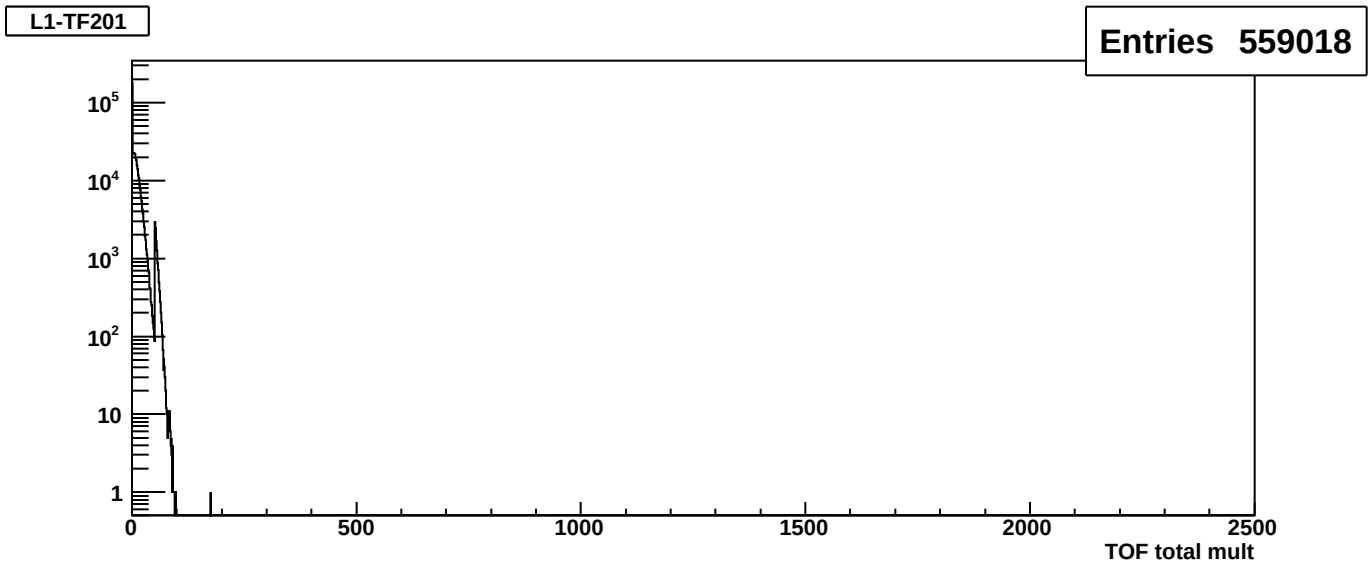

MIX-TF003
Entries 2.347876e+07
TOF MULT
TOF tray

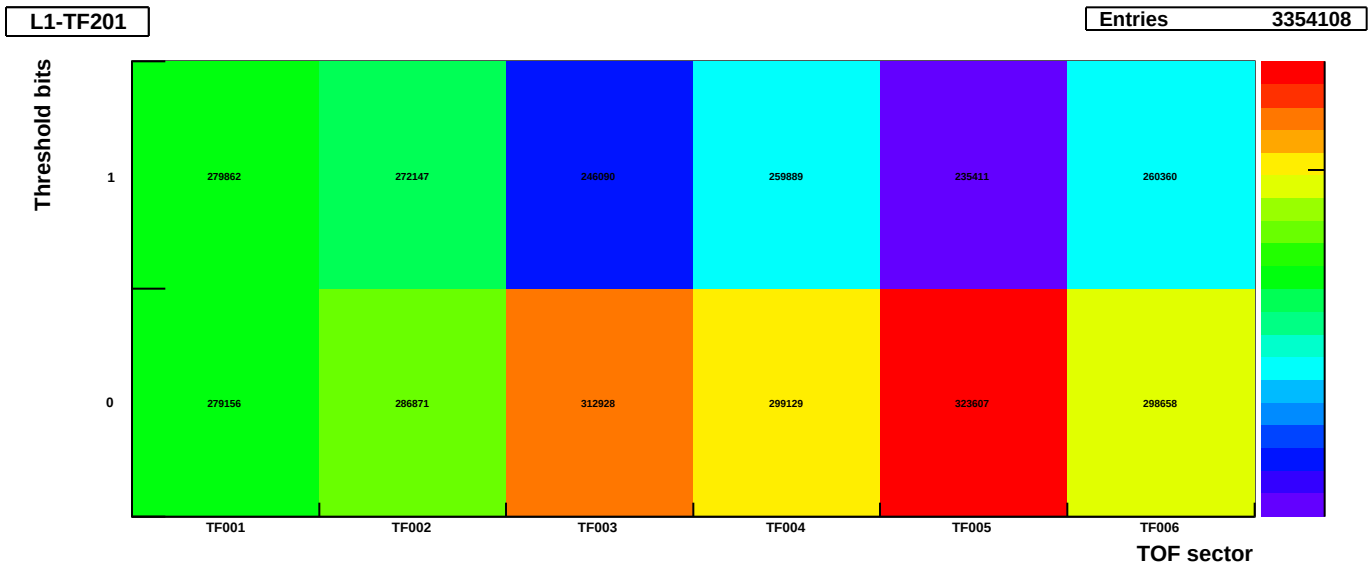

MIX-TF004
Entries 2.347876e+07
TOF MULT
TOF tray

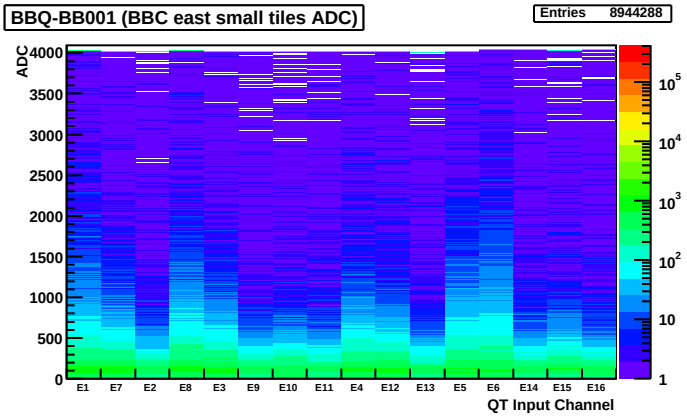

MIX-TF005
Entries 2.347876e+07
TOF MULT
TOF tray

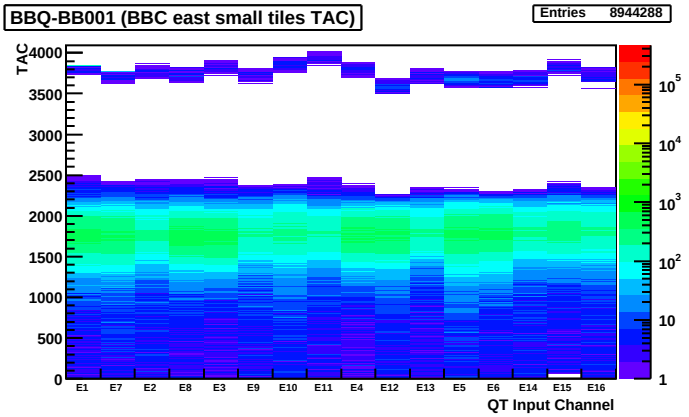

MIX-TF006
Entries 2.347876e+07
TOF MULT
TOF tray

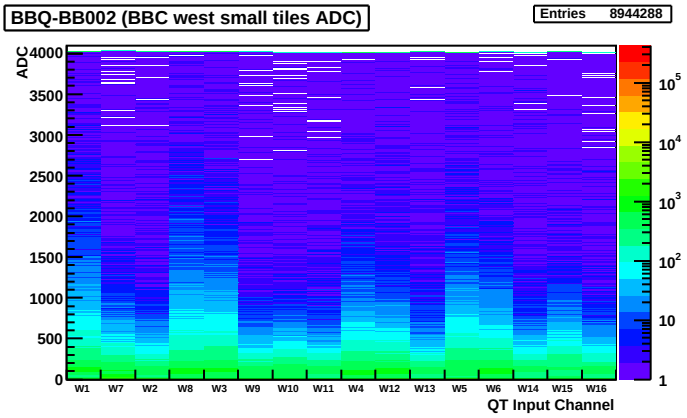

MIX-TF101
Entries 3354108
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

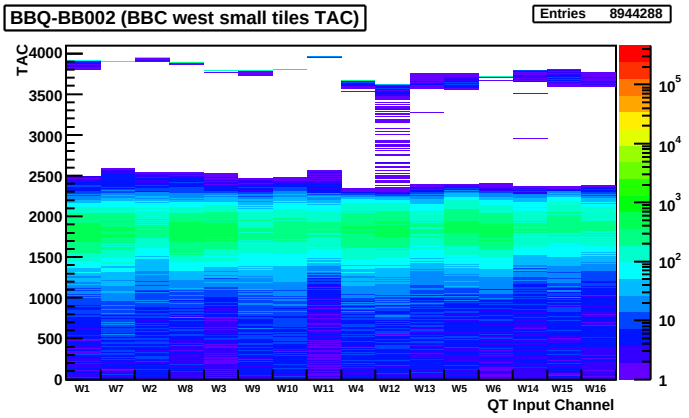

L1-TF201
Entries 559018
10^5
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

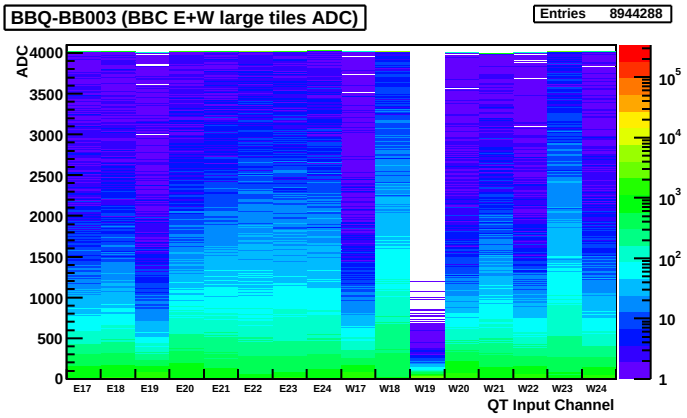

L1-TF201
Entries 3354108
Threshold bits
1
0
279862
272147
246090
259889
235411
260360
279156
286871
312928
299129
323607
298658
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

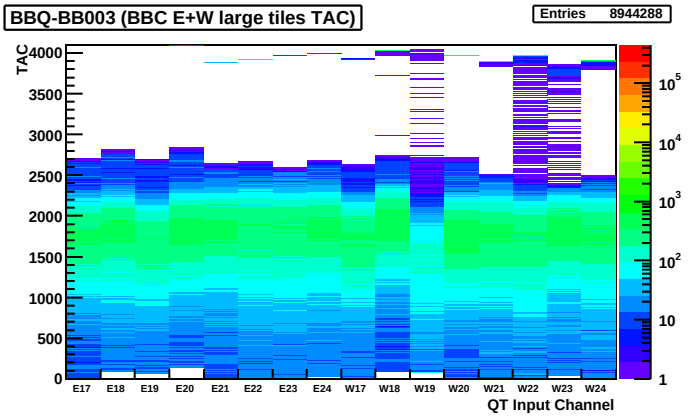

BBQ-BB001 (BBC east small tiles ADC)
Entries 8944288
ADC
QT Input Channel

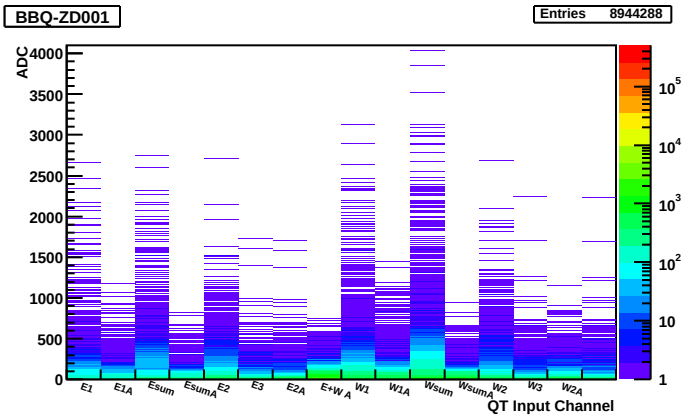

BBQ-BB001 (BBC east small tiles TAC)
Entries 8944288
TAC
QT Input Channel

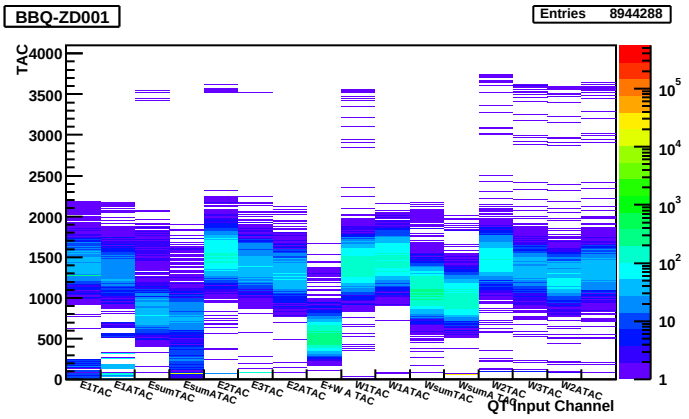

BBQ-BB002 (BBC west small tiles ADC)
Entries 8944288
ADC
QT Input Channel

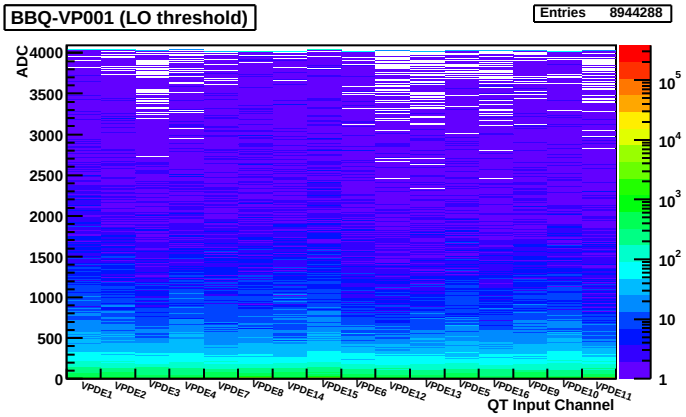

BBQ-BB002 (BBC west small tiles TAC)
Entries 8944288
TAC
QT Input Channel

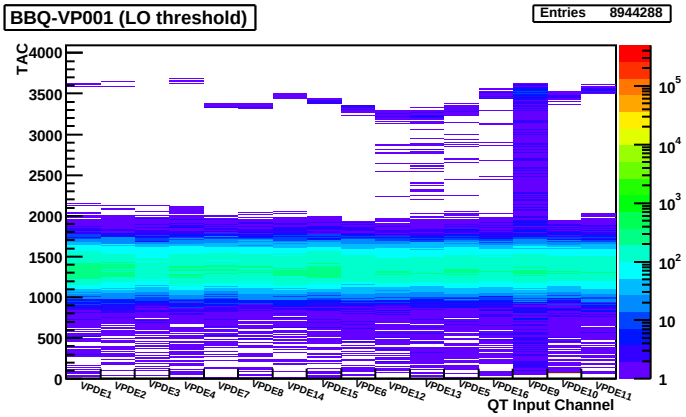

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8944288
ADC
QT Input Channel

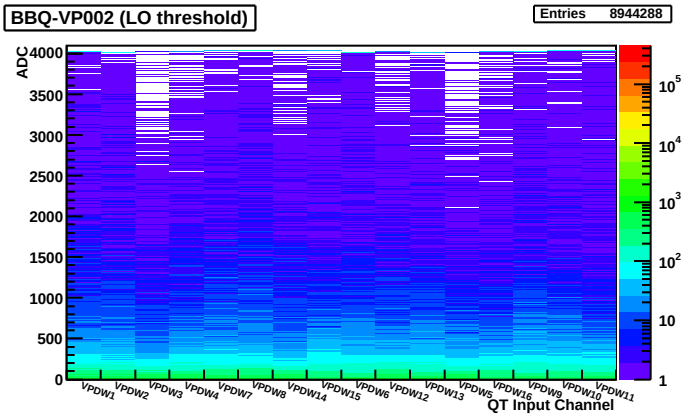

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8944288
TAC
QT Input Channel

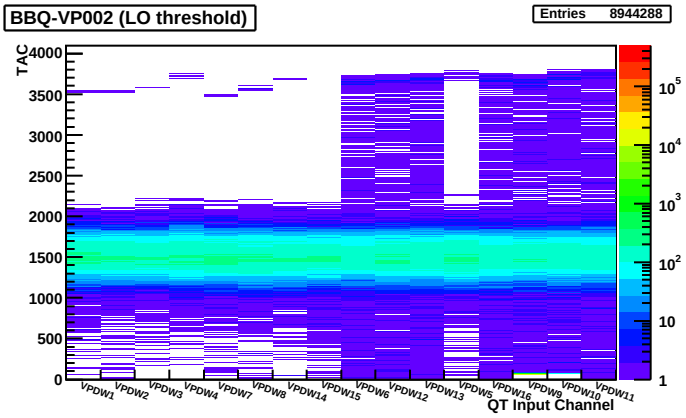

BBQ-ZD001
Entries 8944288
ADC
QT Input Channel

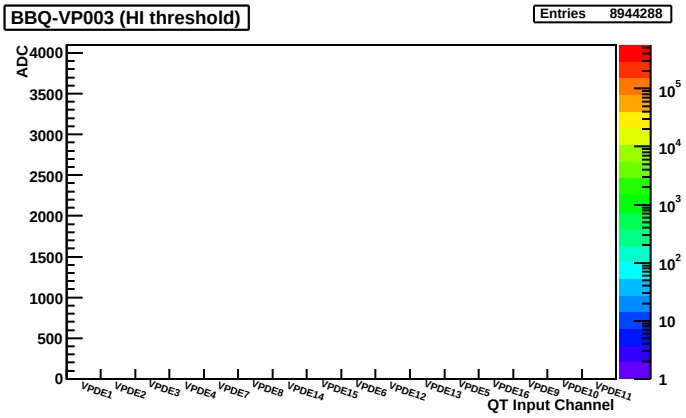

BBQ-ZD001
Entries 8944288
TAC
QT Input Channel

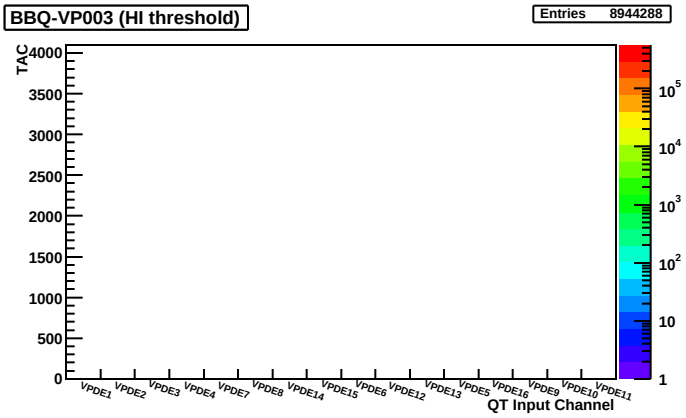

BBQ-VP001 (LO threshold)
Entries 8944288
ADC
QT Input Channel

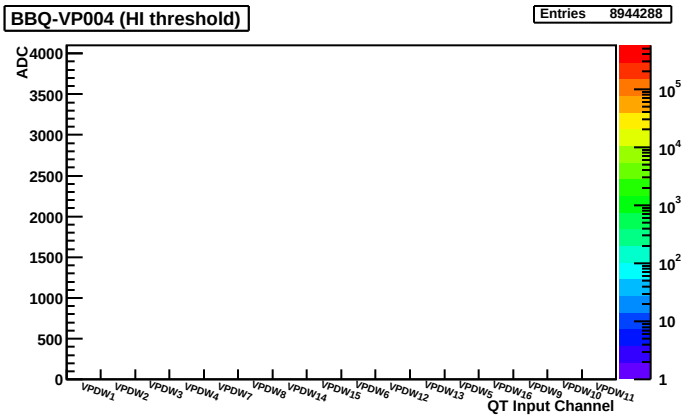

BBQ-VP001 (LO threshold)
Entries 8944288
TAC
QT Input Channel

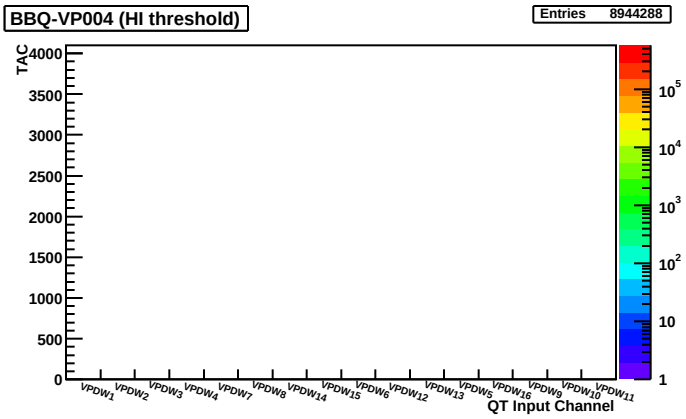

BBQ-VP002 (LO threshold)
Entries 8944288
ADC
QT Input Channel

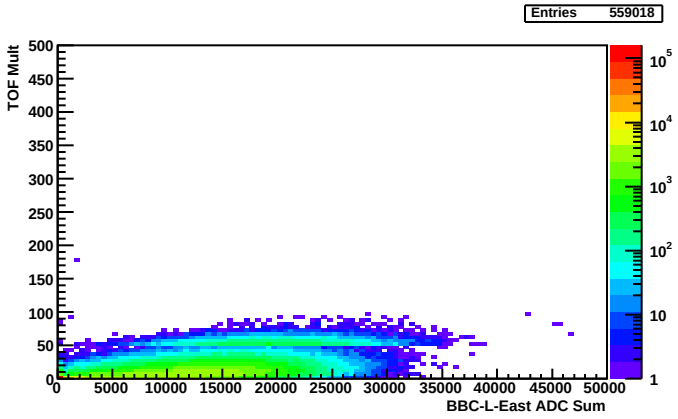

BBQ-VP002 (LO threshold)
Entries 8944288
TAC
QT Input Channel

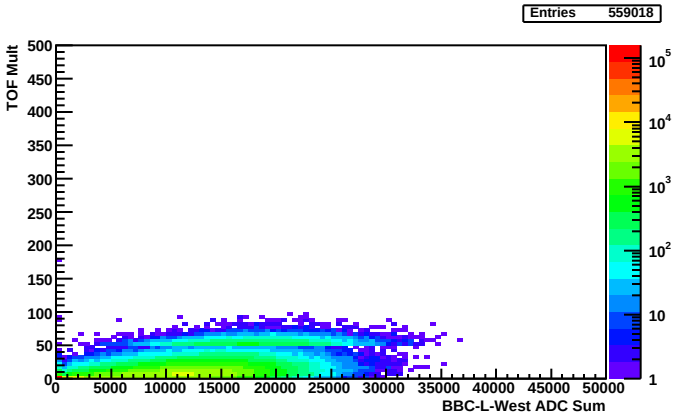

BBQ-VP003 (HI threshold)
Entries 8944288
ADC
QT Input Channel

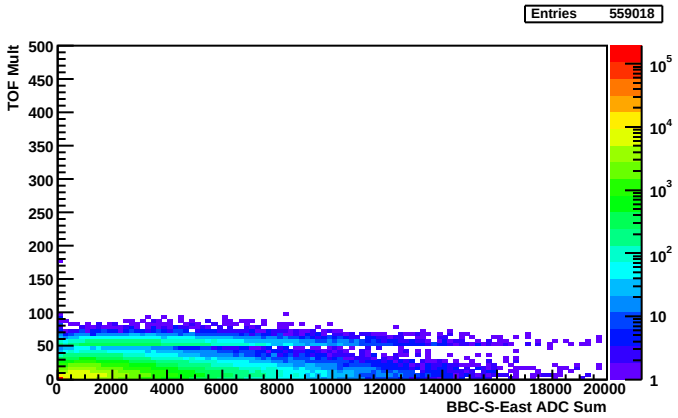

BBQ-VP003 (HI threshold)
Entries 8944288
TAC
QT Input Channel

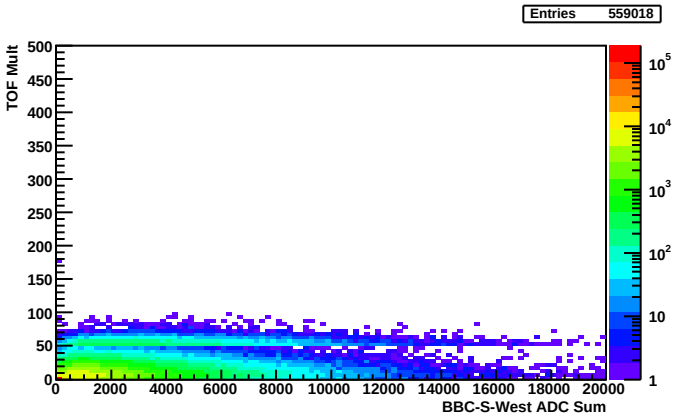

BBQ-VP004 (HI threshold)
Entries 8944288
ADC
QT Input Channel

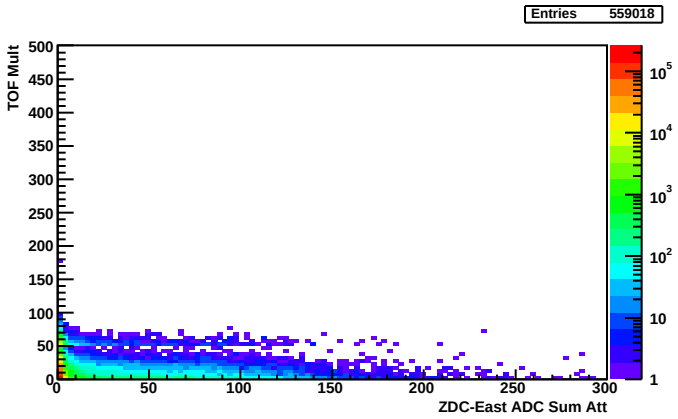

BBQ-VP004 (HI threshold)
Entries 8944288
TAC
QT Input Channel

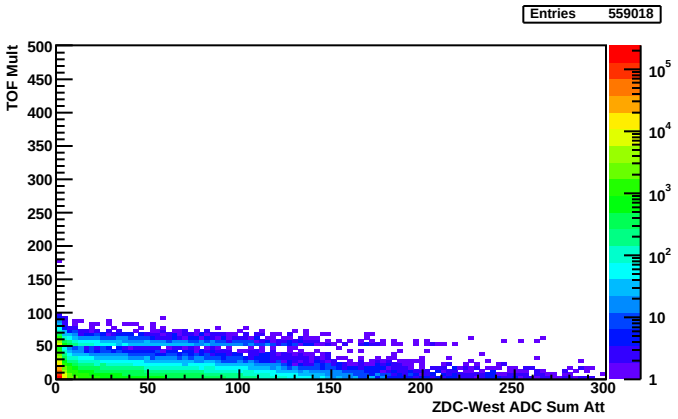

Entries 559018
TOF Mult
BBC-L-East ADC Sum

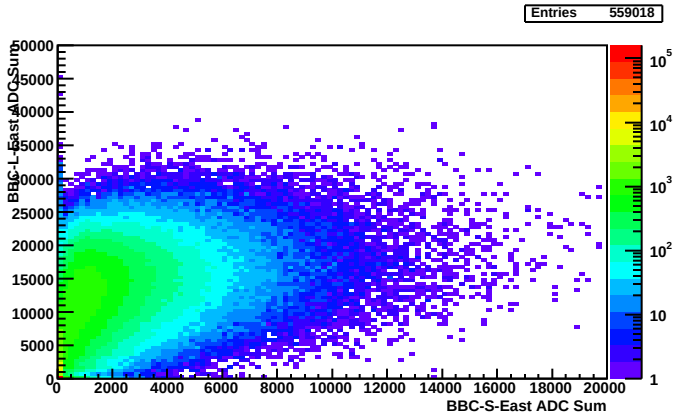

Entries 559018
TOF Mult
BBC-L-West ADC Sum

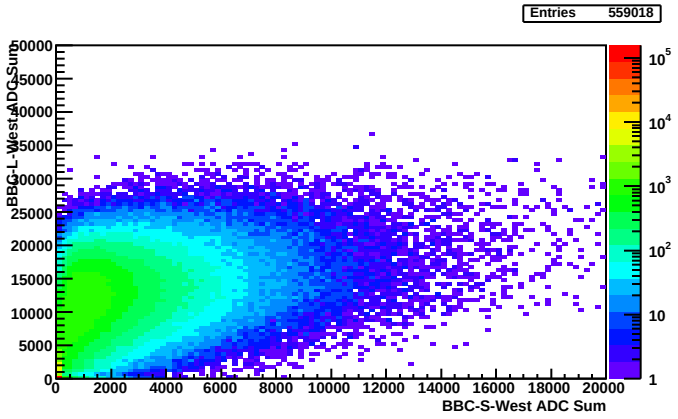

Entries 559018
TOF Mult
BBC-S-East ADC Sum

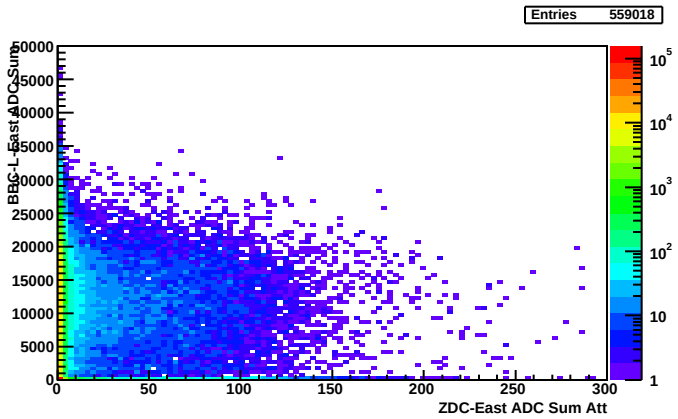

Entries 559018
TOF Mult
BBC-S-West ADC Sum

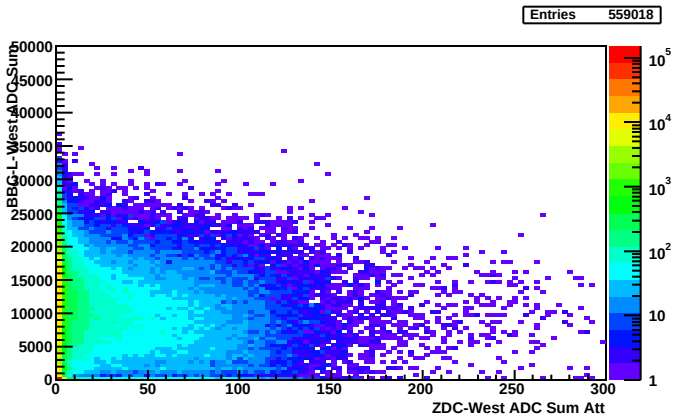

Entries 559018
TOF Mult
ZDC-East ADC Sum Att

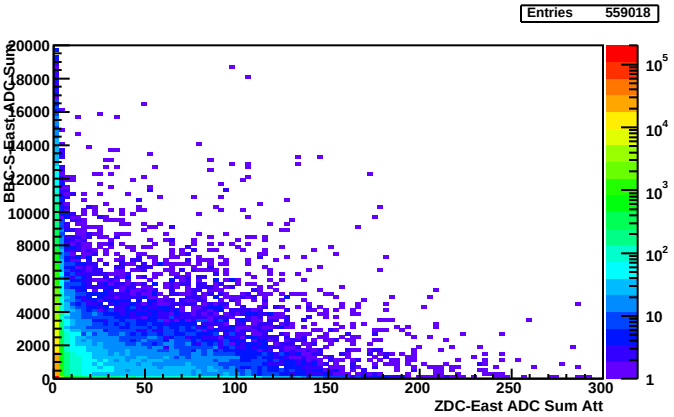

Entries 559018
TOF Mult
ZDC-West ADC Sum Att

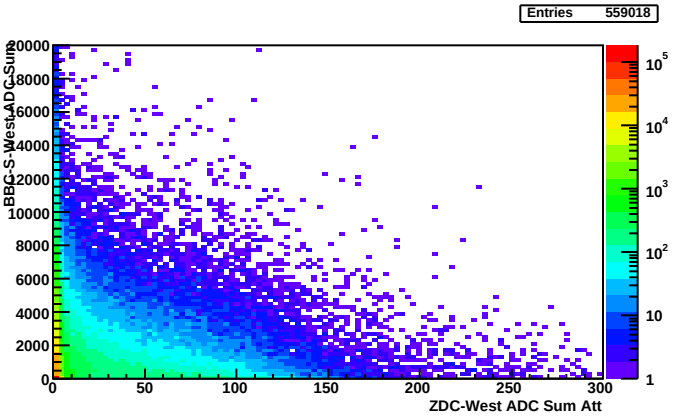

Entries 559018
BBC-L-East ADC Sum
BBC-S-East ADC Sum

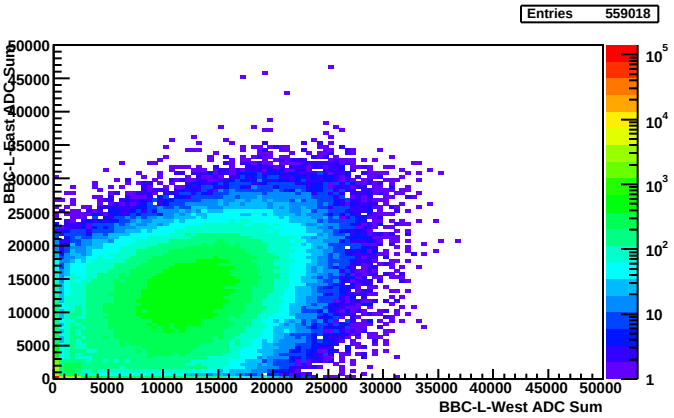

Entries 559018
BBC-L-West ADC Sum
BBC-S-West ADC Sum

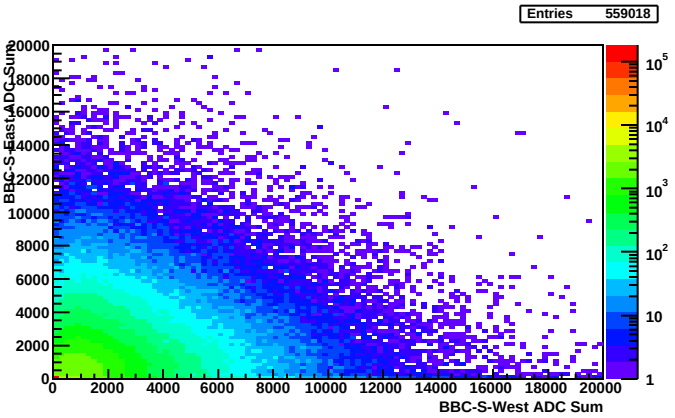

Entries 559018
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

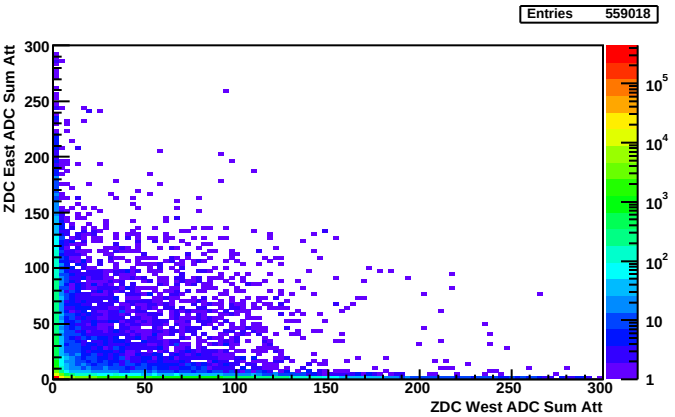

Entries 559018
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

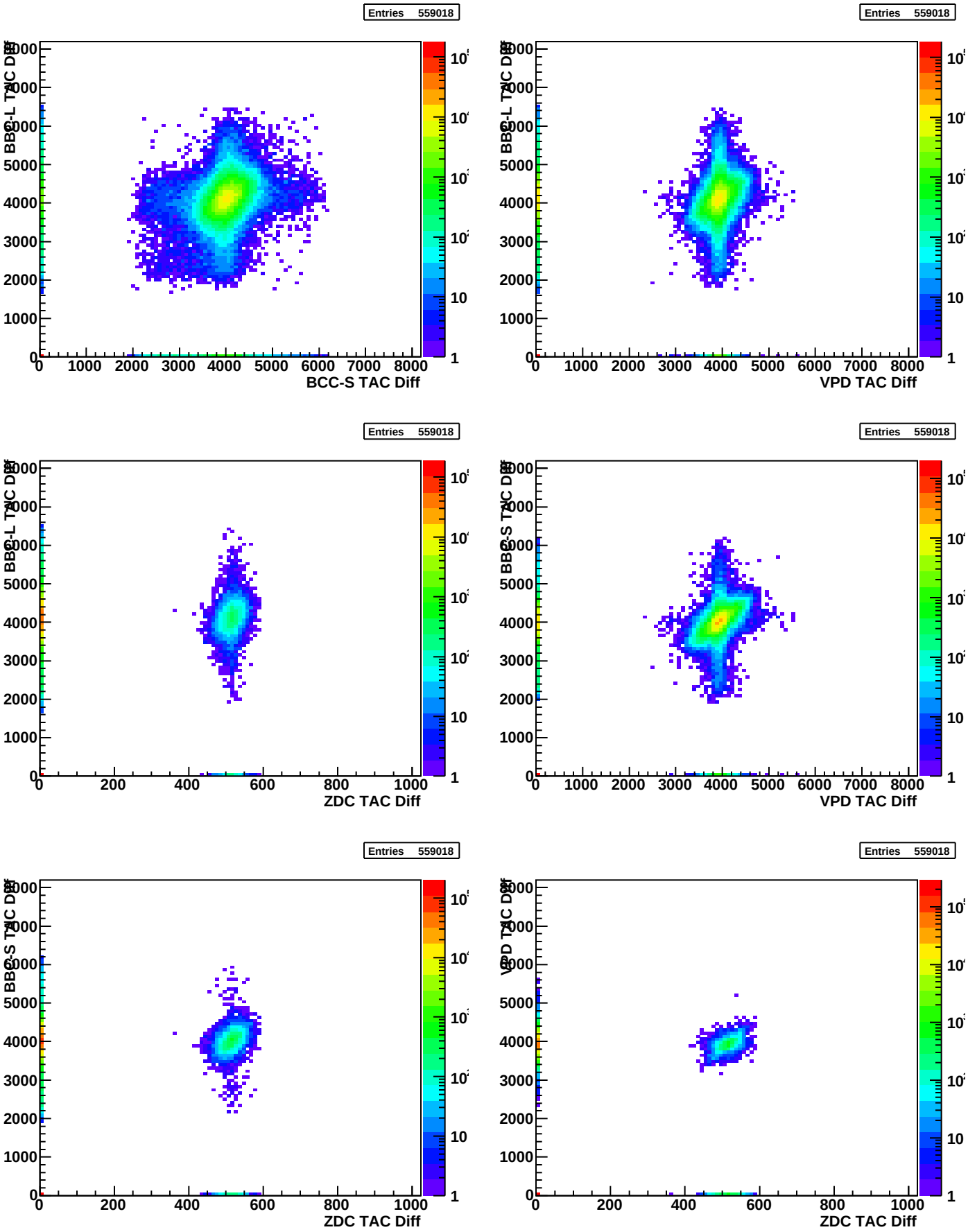

Entries 559018
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

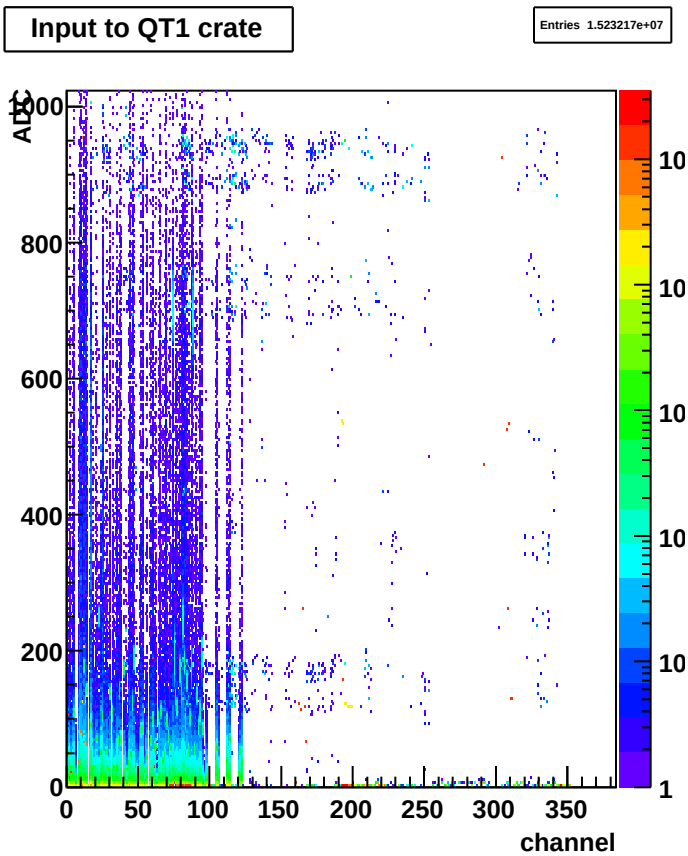

Entries 559018
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

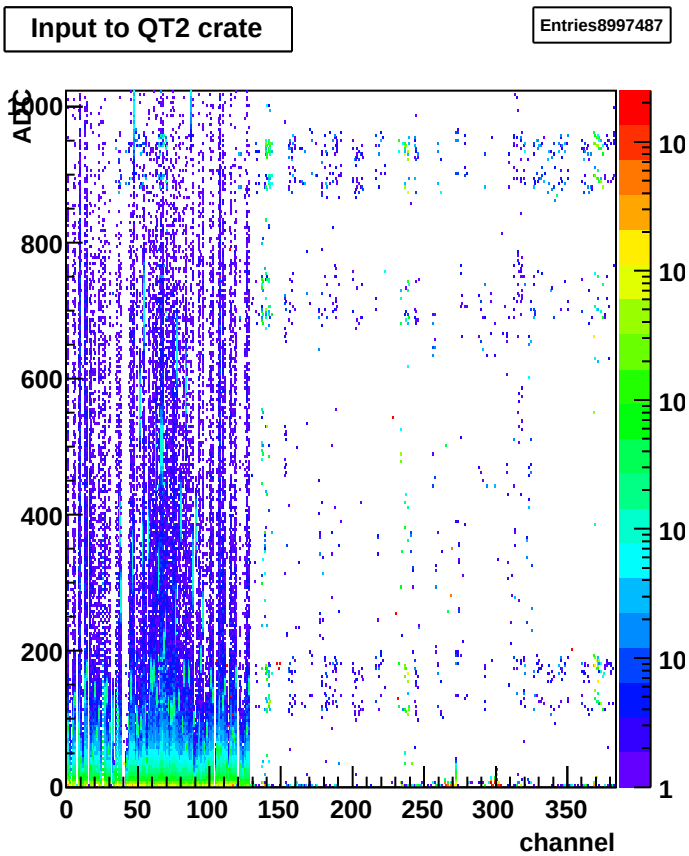

Entries 559018
BBC-L-East ADC Sum
BBC-L-West ADC Sum

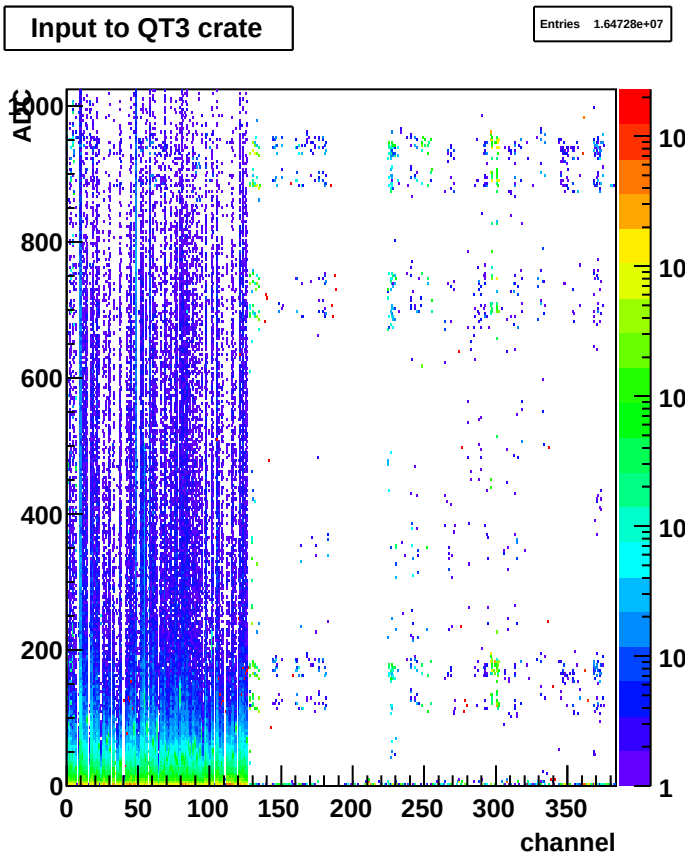

Entries 559018
BBC-S-East ADC Sum
BBC-S-West ADC Sum

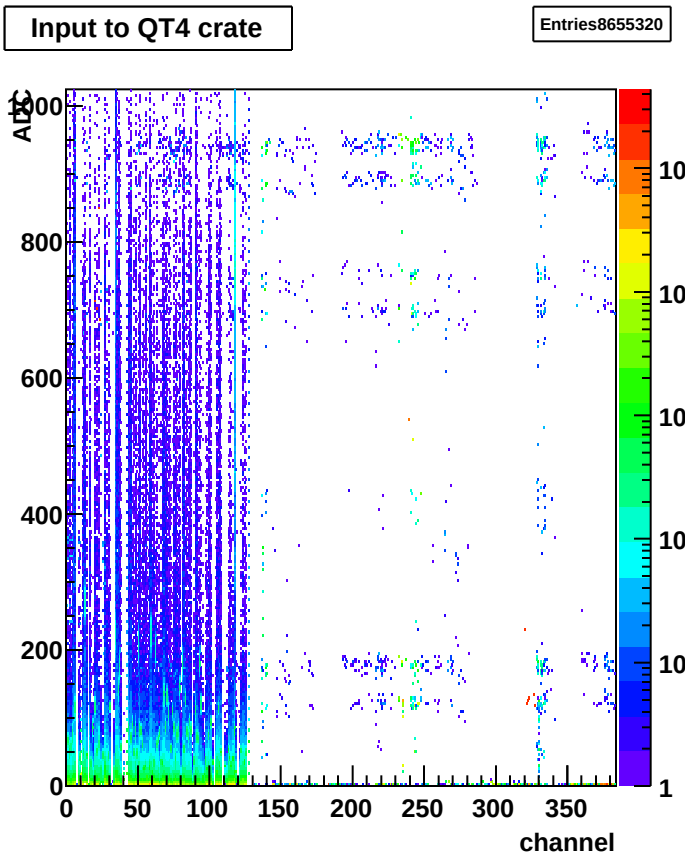

Entries 559018
ZDC East ADC Sum Att
ZDC West ADC Sum Att

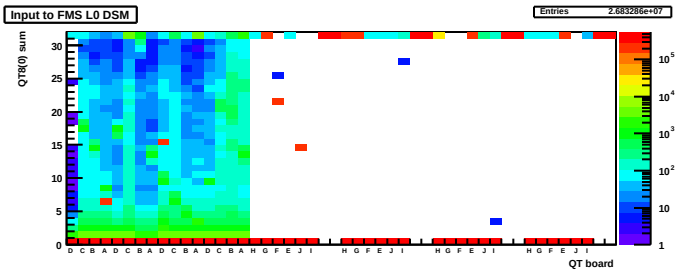

Entries 559018
BBC-L TAC Diff
BCC-S TAC Diff
Entries 559018
BBC-L TAC Diff
VPD TAC Diff
Entries 559018
BBC-L TAC Diff
ZDC TAC Diff
Entries 559018
BBC-S TAC Diff
VPD TAC Diff
Entries 559018
BBC-S TAC Diff
ZDC TAC Diff
Entries 559018
VPD TAC Diff
ZDC TAC Diff

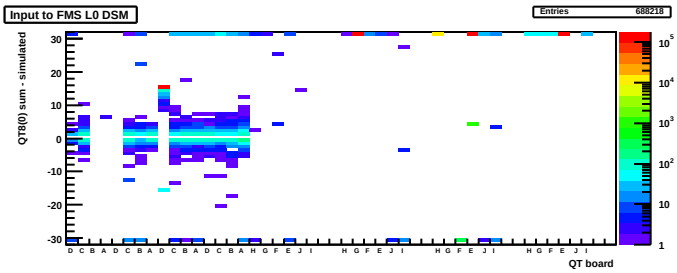

Input to QT1 crate
Entries 1.523217e+07
ADC
channel

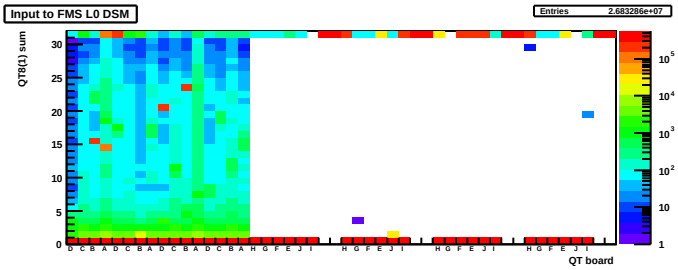

Input to QT2 crate
Entries8997487
ADC
channel

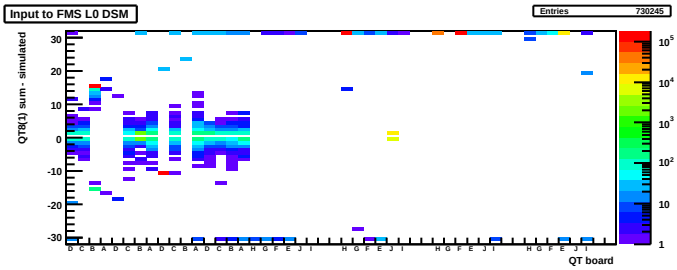

Input to QT3 crate
Entries 1.64728e+07
ADC
channel

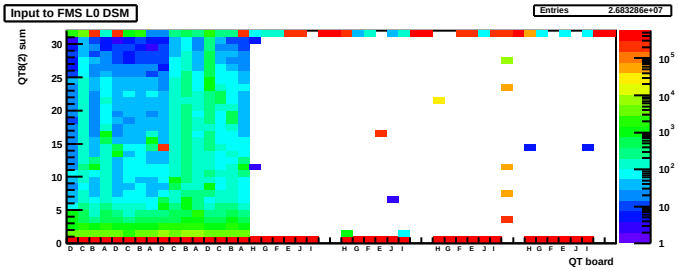

Input to QT4 crate
Entries8655320
ADC
channel

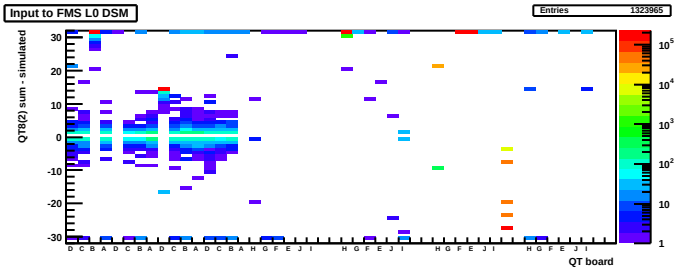

Input to FMS L0 DSM
Entries 2.683286e+07
QT8(0) sum
QT board

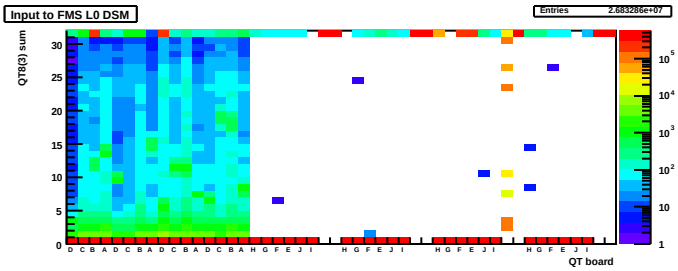

Input to FMS L0 DSM
Entries 688218
QT8(0) sum - simulated
QT board

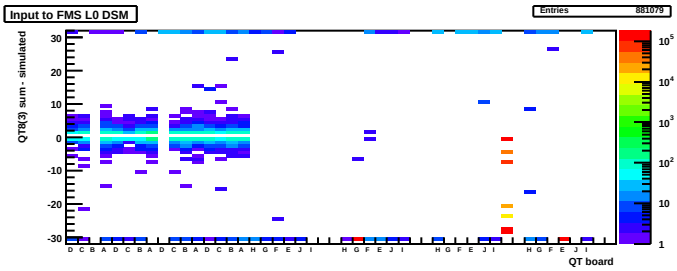

Input to FMS L0 DSM
Entries 2.683286e+07
QT8(1) sum
QT board

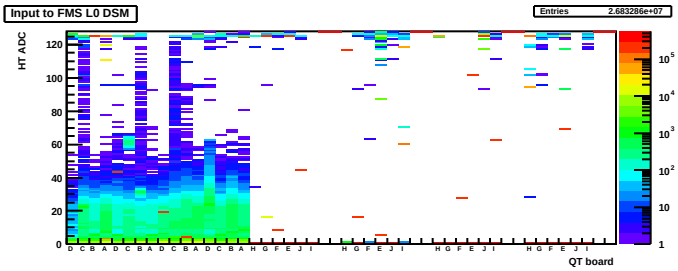

Input to FMS L0 DSM
Entries 730245
QT8(1) sum - simulated
QT board

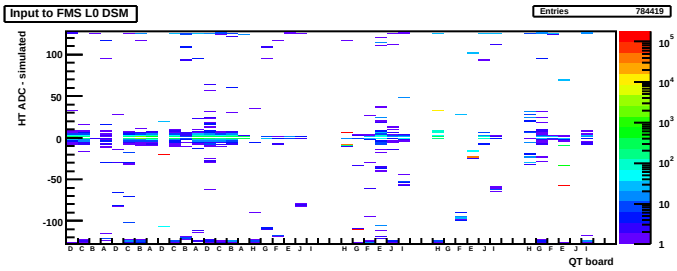

Input to FMS L0 DSM
Entries 2.683286e+07
QT8(2) sum
QT board

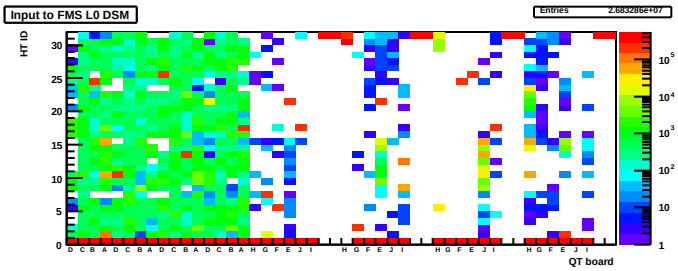

Input to FMS L0 DSM
Entries 1323965
QT8(2) sum - simulated
QT board

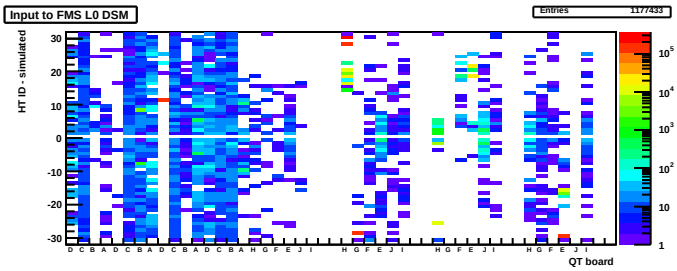

Input to FMS L0 DSM
Entries 2.683286e+07
QT8(3) sum
QT board

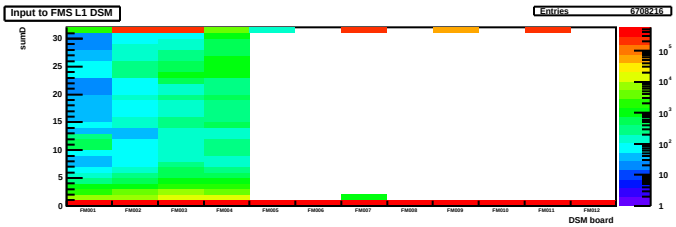

Input to FMS L0 DSM
Entries 881079
QT8(3) sum - simulated
QT board

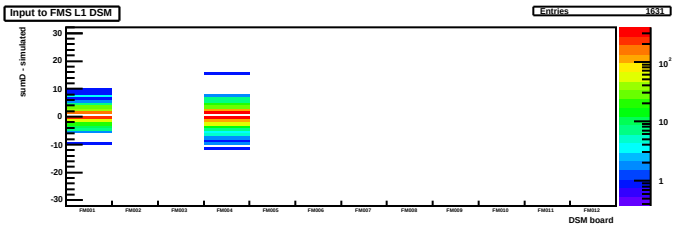

Input to FMS L0 DSM
Entries 2.683286e+07
HT ADC
QT board

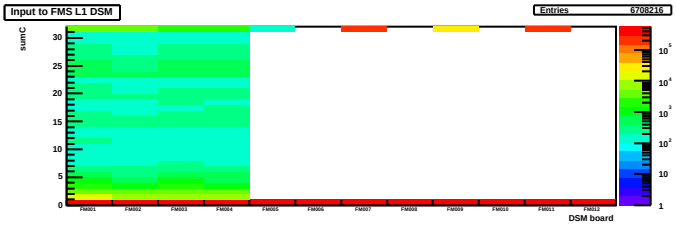

Input to FMS L0 DSM
Entries 784419
HT ADC - simulated
QT board

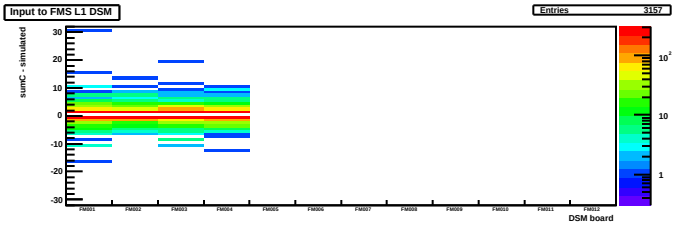

Input to FMS L0 DSM
Entries 2.683286e+07
HT ID
QT board

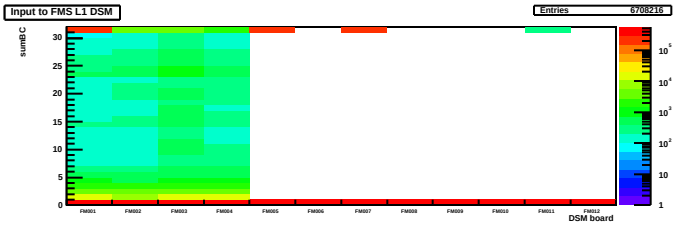

Input to FMS L0 DSM
Entries 1177433
HT ID - simulated
QT board

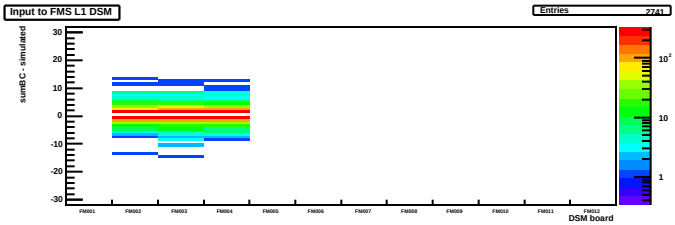

Input to FMS L1 DSM
Entries 6708216
sumD
DSM board

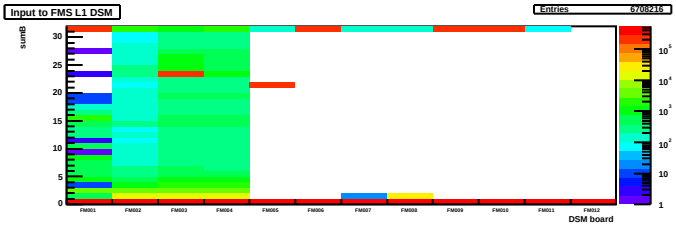

Input to FMS L1 DSM
Entries 1631
sumD - simulated
DSM board

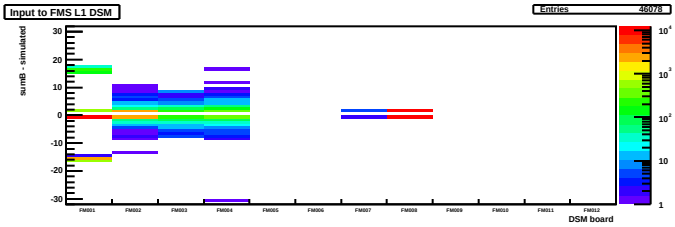

Input to FMS L1 DSM
Entries 6708216
sumC
DSM board

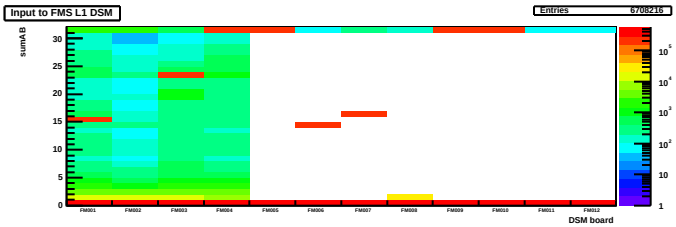

Input to FMS L1 DSM
Entries 3157
sumC - simulated
DSM board

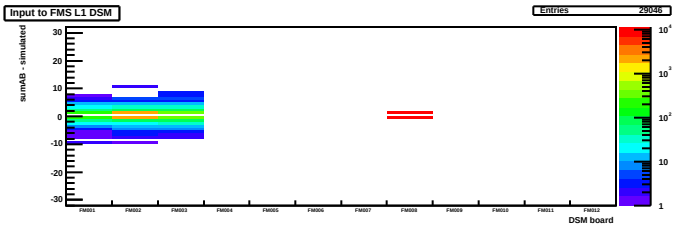

Input to FMS L1 DSM
Entries 6708216
sumBC
DSM board

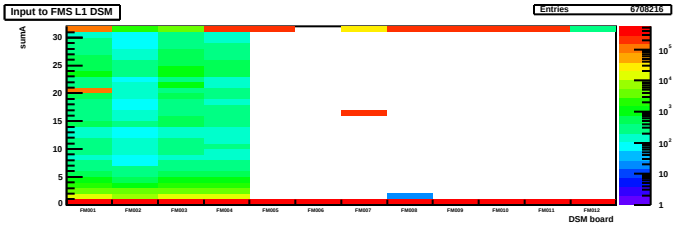

Input to FMS L1 DSM
Entries 2741
sumBC - simulated
DSM board

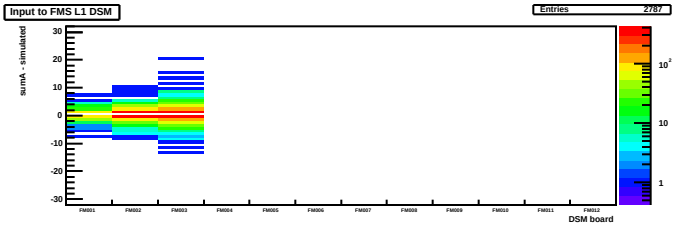

Input to FMS L1 DSM
Entries 6708216
sumB
DSM board

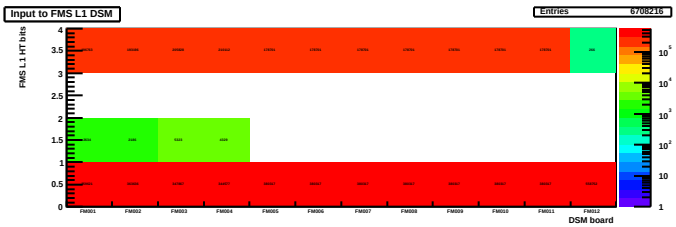

Input to FMS L1 DSM
Entries 46078
sumB - simulated
DSM board

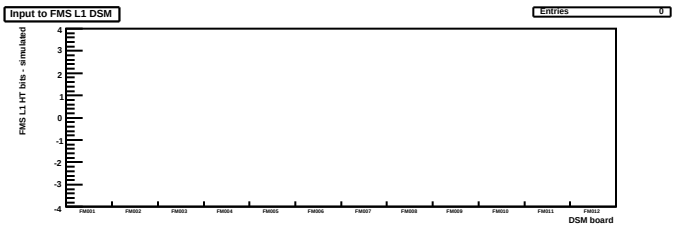

Input to FMS L1 DSM
Entries 6708216
sumAB
DSM board

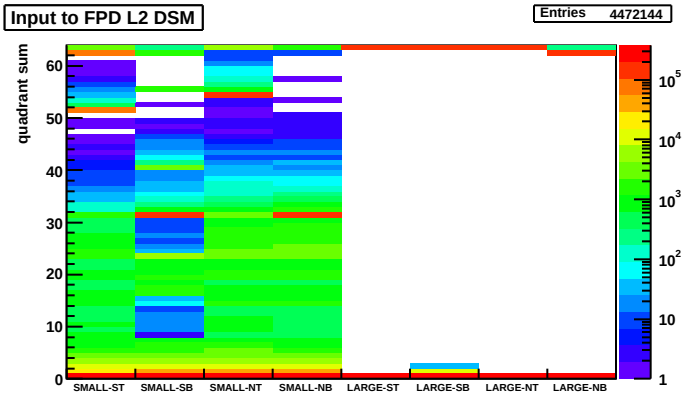

Input to FMS L1 DSM
Entries 29046
sumAB - simulated
DSM board

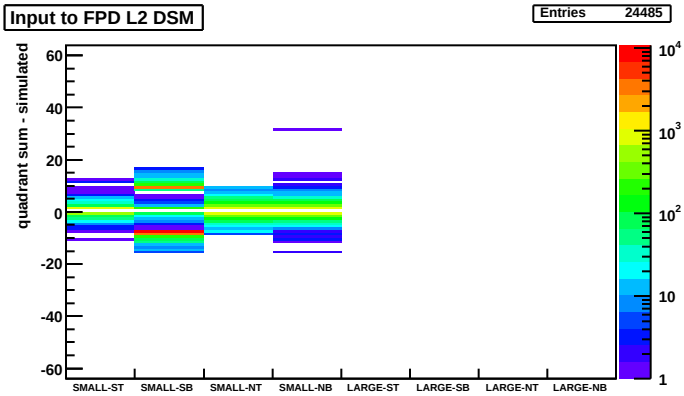

Input to FMS L1 DSM
Entries 6708216
sumA
DSM board

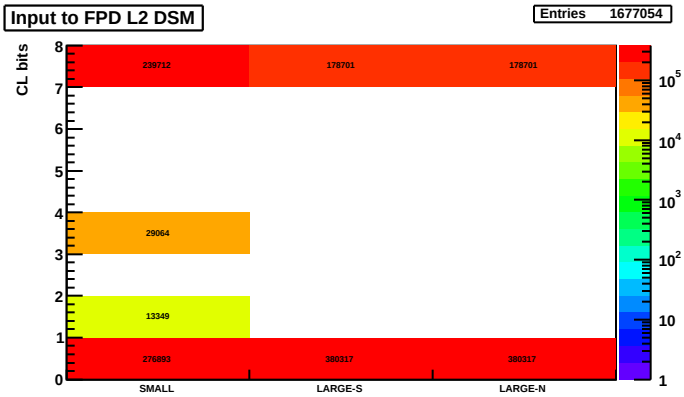

Input to FMS L1 DSM
Entries 2787
sumA - simulated
DSM board

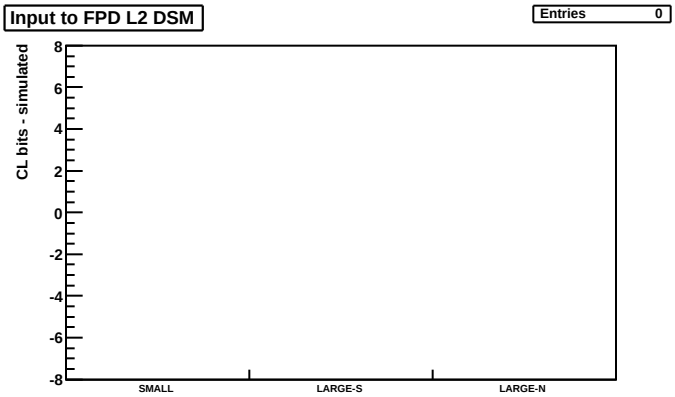

Input to FMS L1 DSM
Entries 6708216
FMS L1 HT bits
DSM board

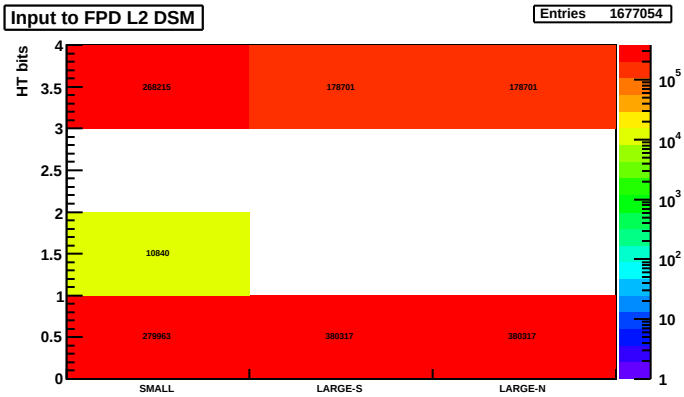

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

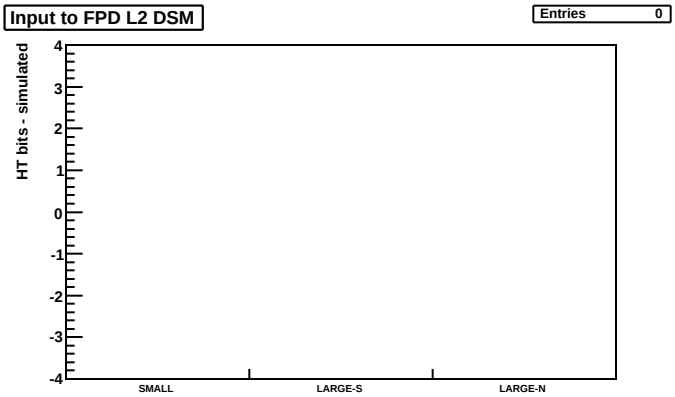

Input to FPD L2 DSM
Entries 4472144
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

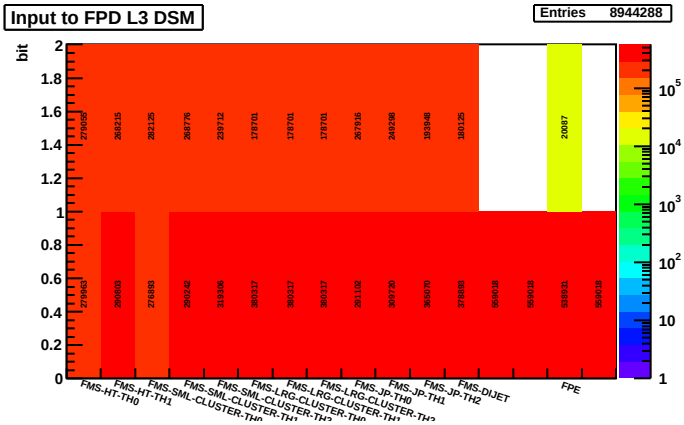

Input to FPD L2 DSM
Entries 24485
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

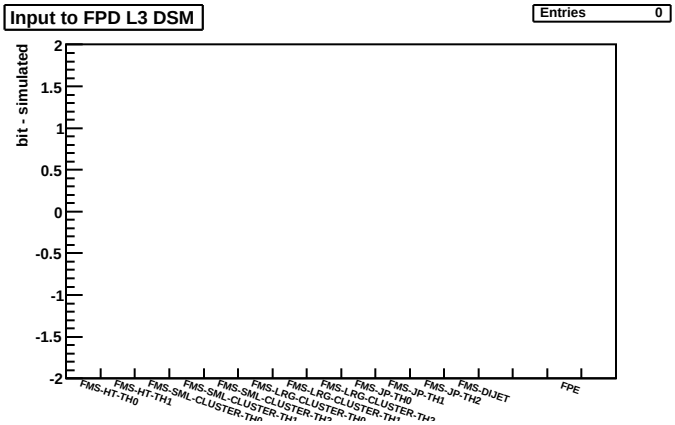

Input to FPD L2 DSM
Entries 1677054
CL bits
239712
178701
178701
29064
13349
276893
380317
380317
SMALL
LARGE-S
LARGE-N

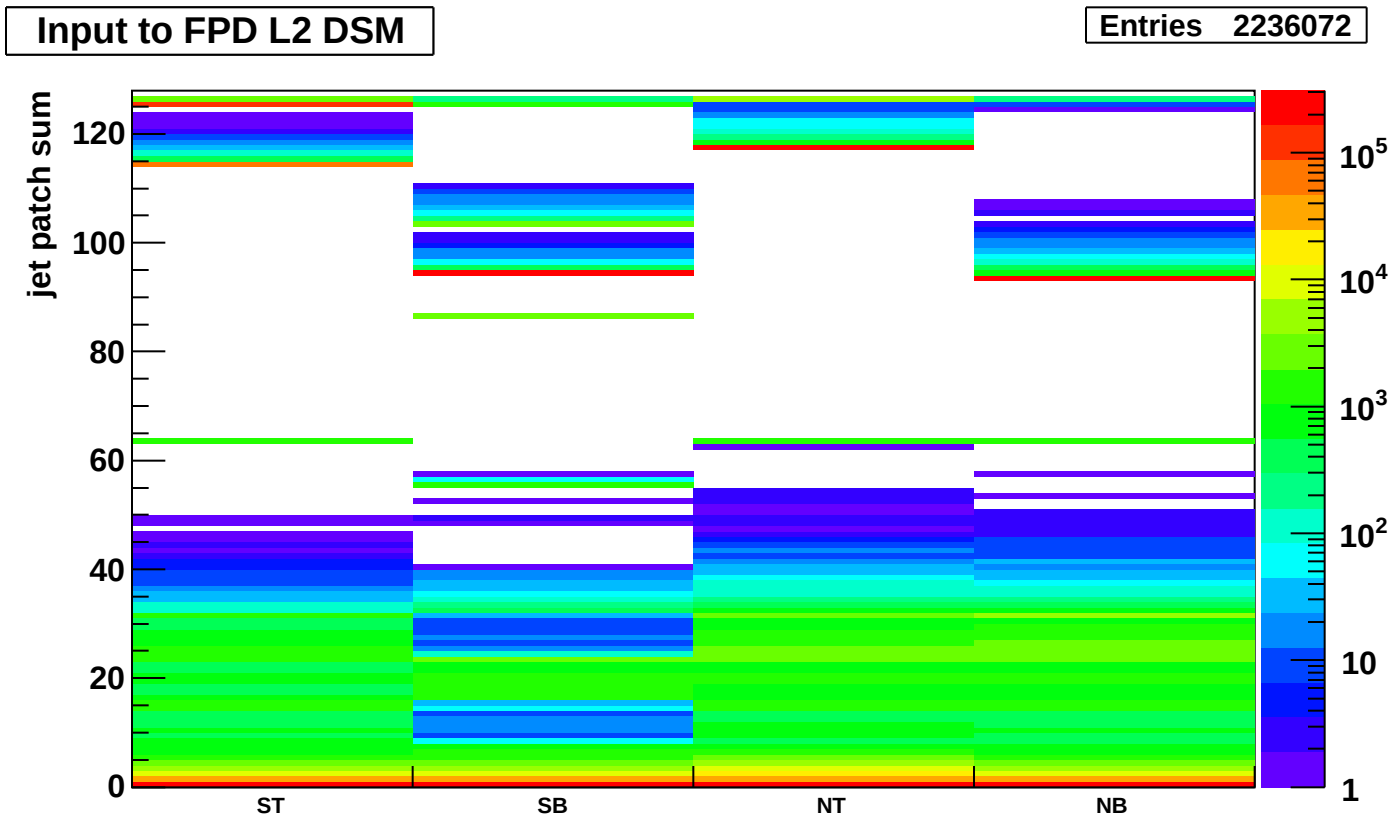

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

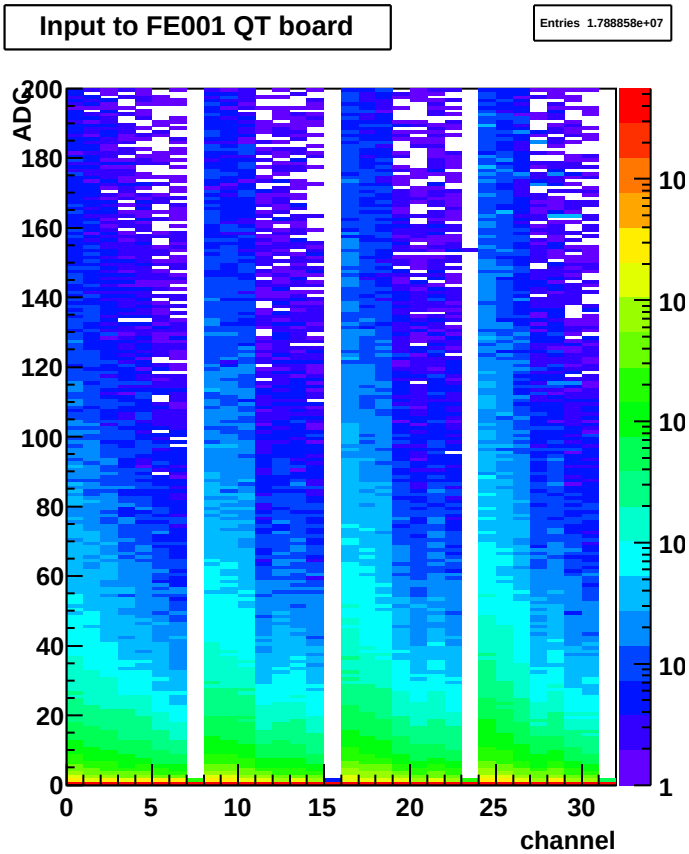

Input to FPD L2 DSM
Entries 1677054
HT bits
268215
178701
178701
10840
279963
380317
380317
SMALL
LARGE-S
LARGE-N

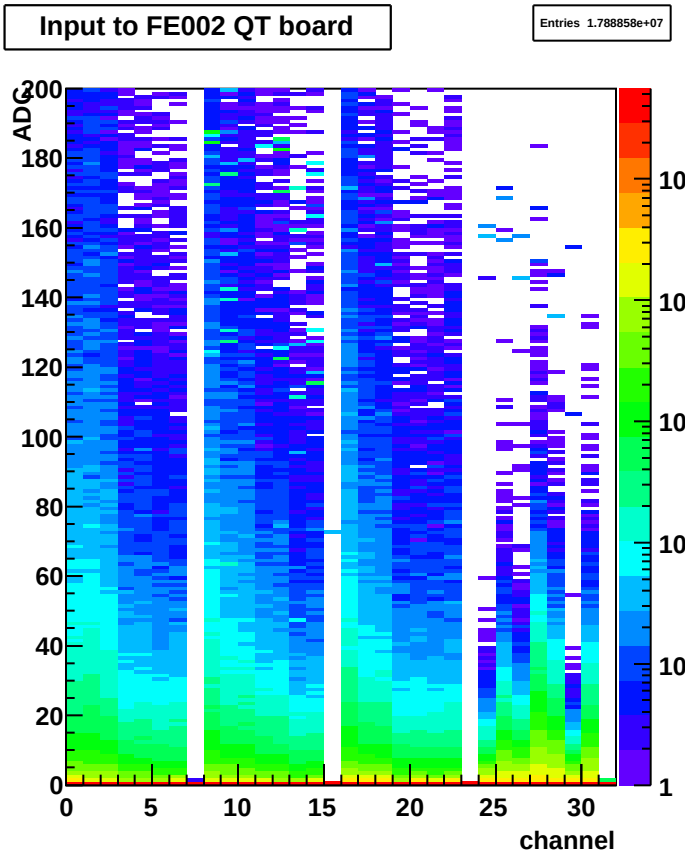

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

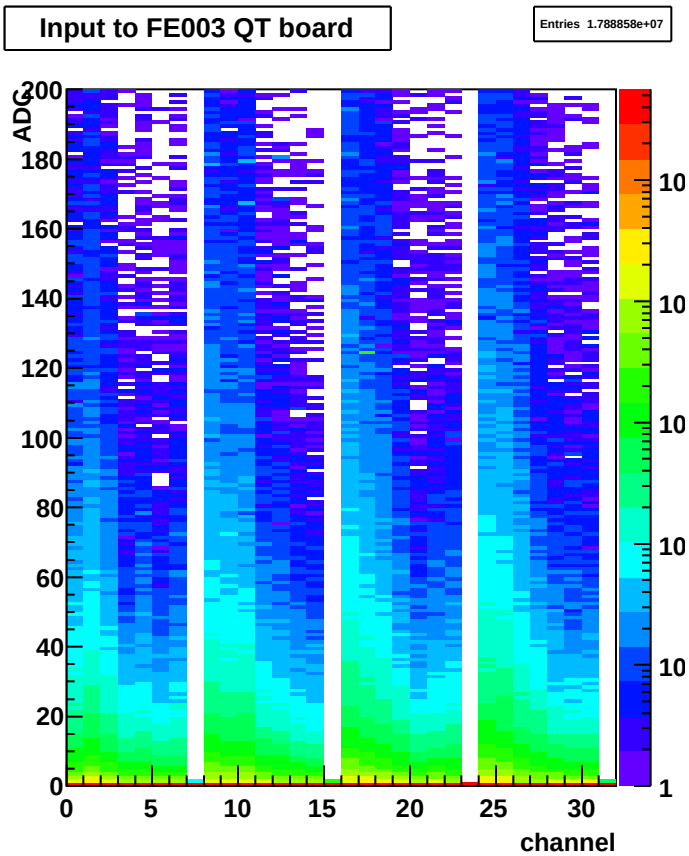

Input to FPD L3 DSM
Entries 8944288
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

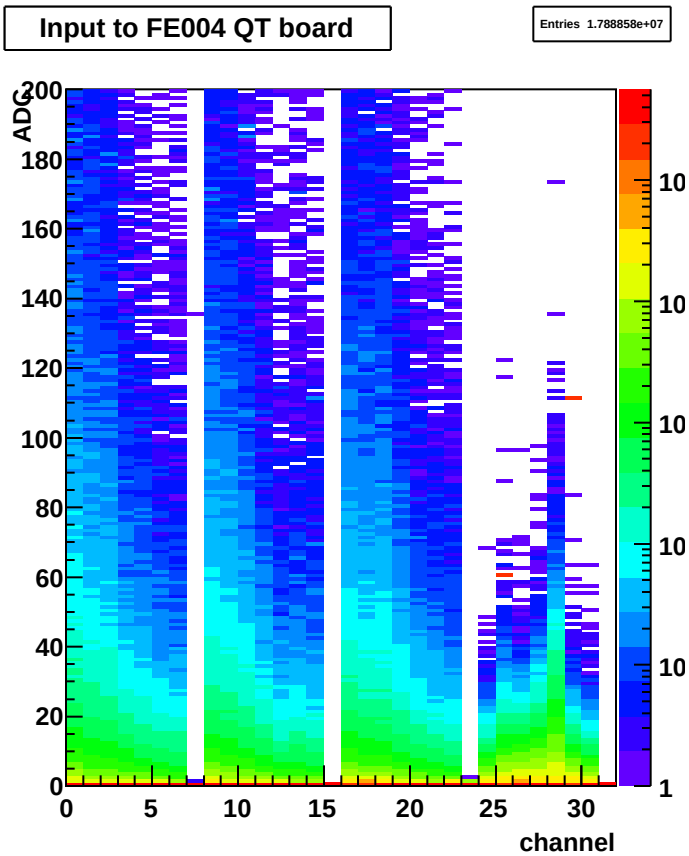

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

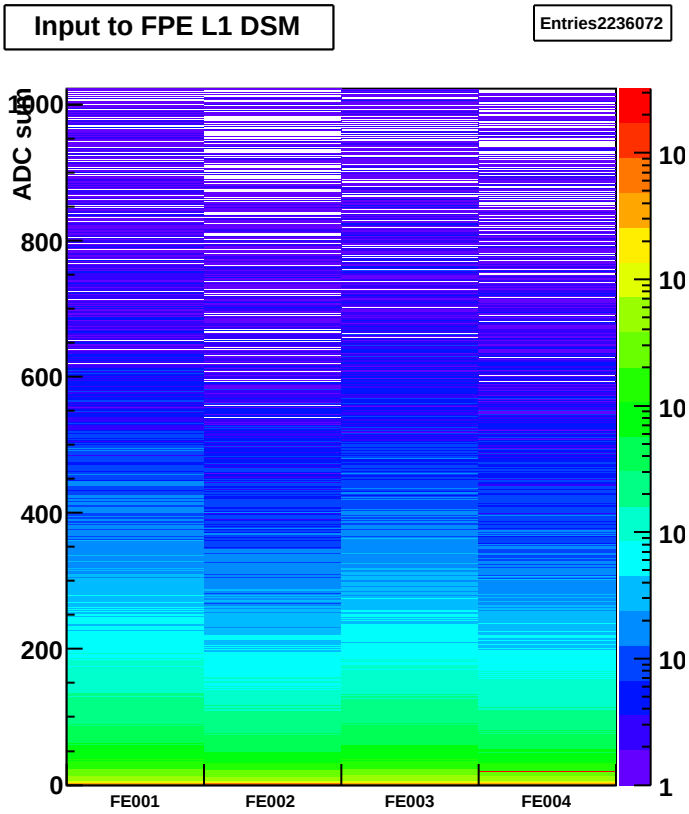

Input to FPD L2 DSM
Entries 2236072
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

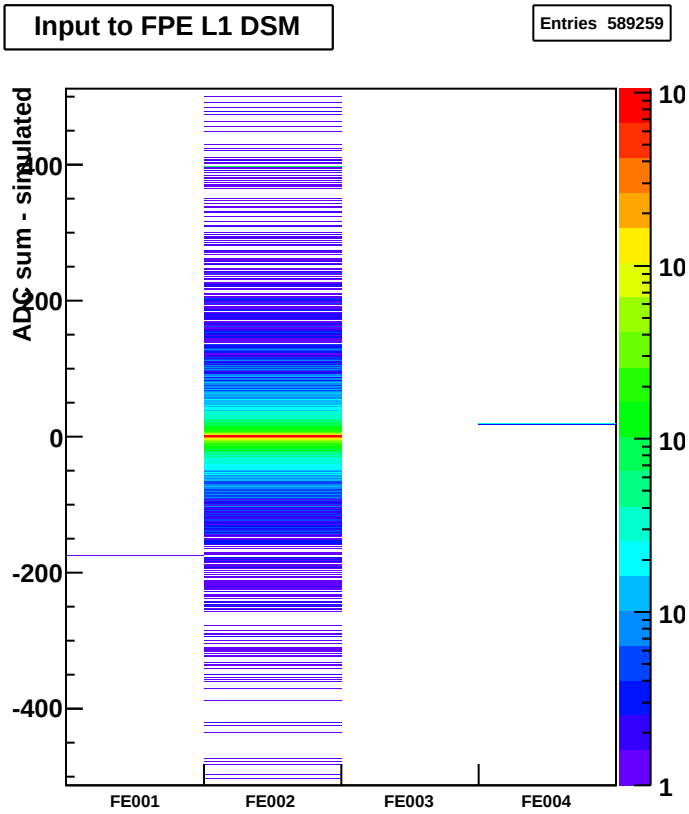

Input to FE001 QT board
Entries 1.788858e+07
ADC
channel

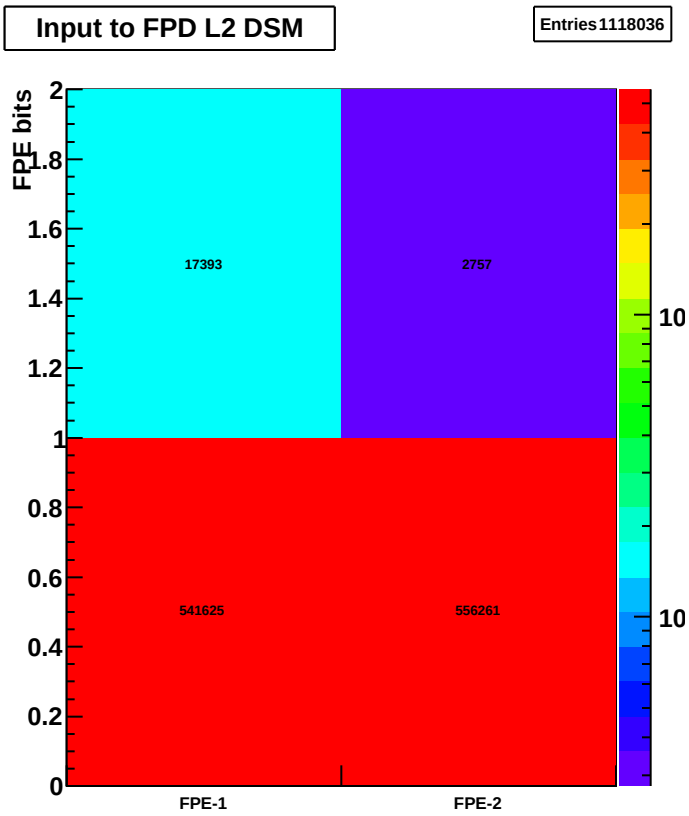

Input to FE002 QT board
Entries 1.788858e+07
ADC
channel

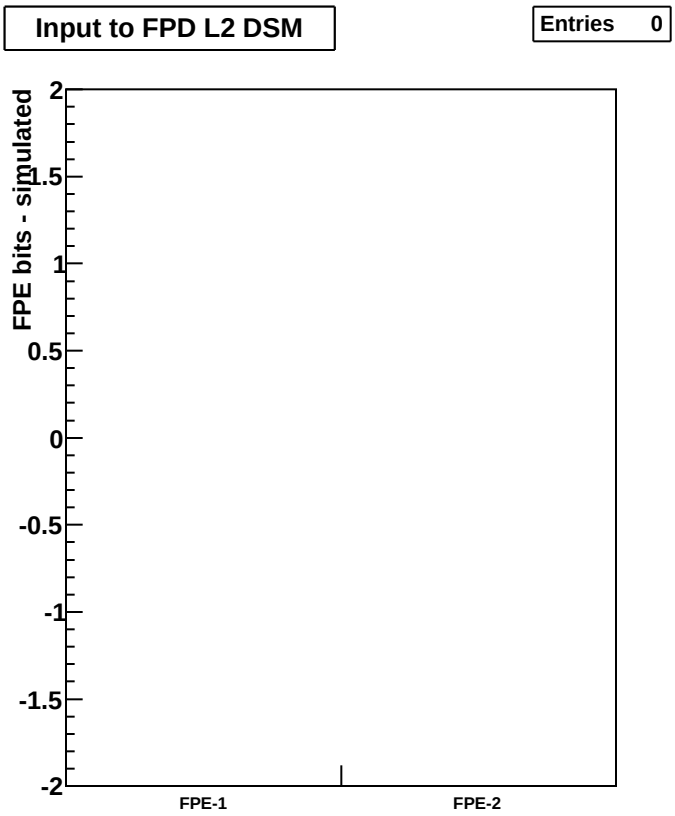

Input to FE003 QT board
Entries 1.788858e+07
ADC
channel


Input to FE004 QT board
Entries 1.788858e+07
ADC
channel


Input to FPE L1 DSM
Entries2236072
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 589259
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries1118036
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
17393
2757
541625
556261
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2