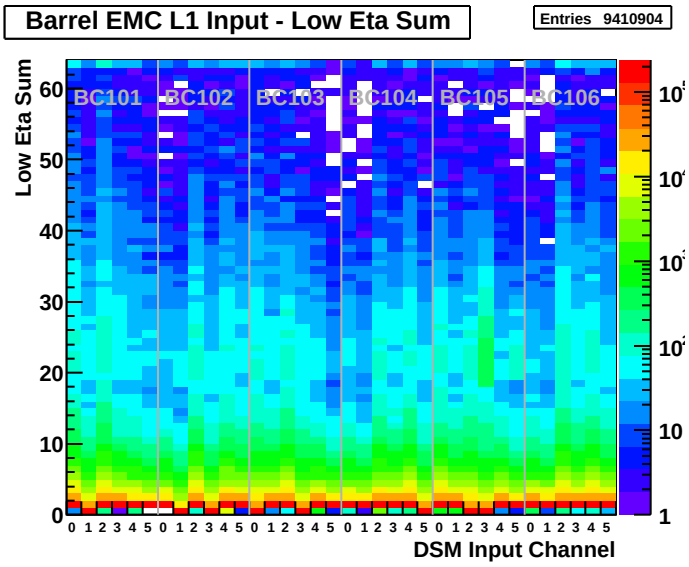
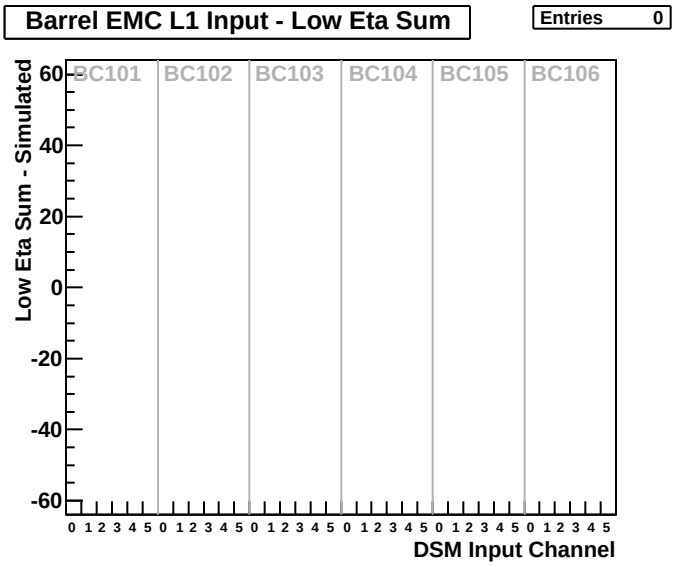
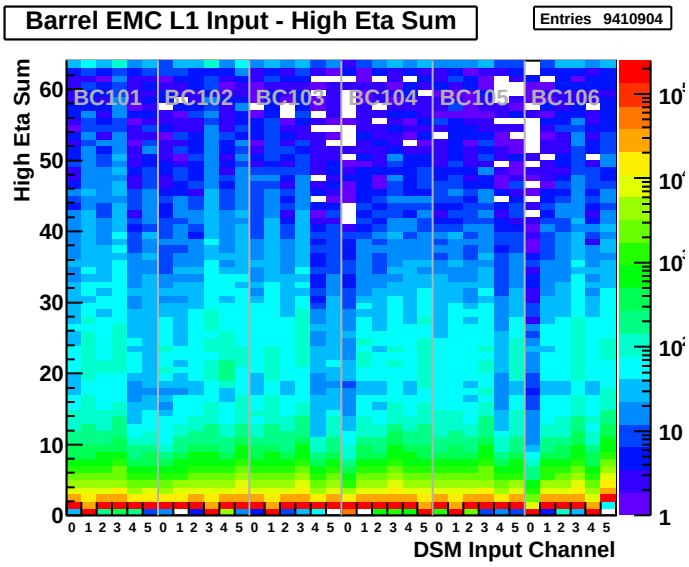
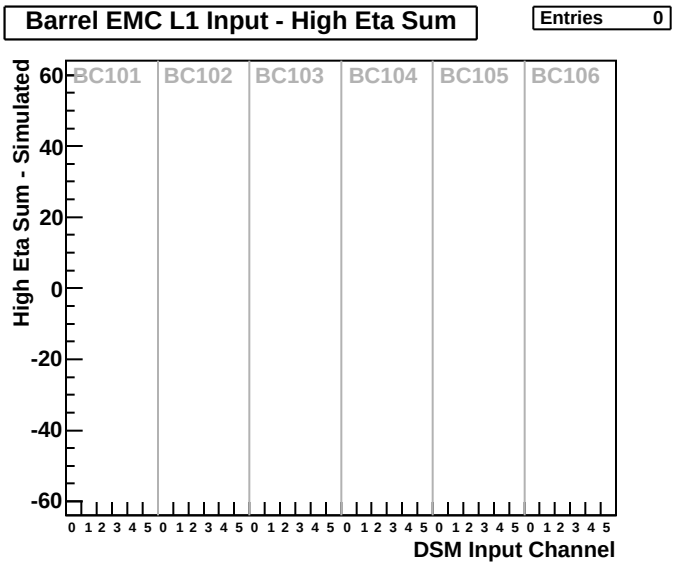
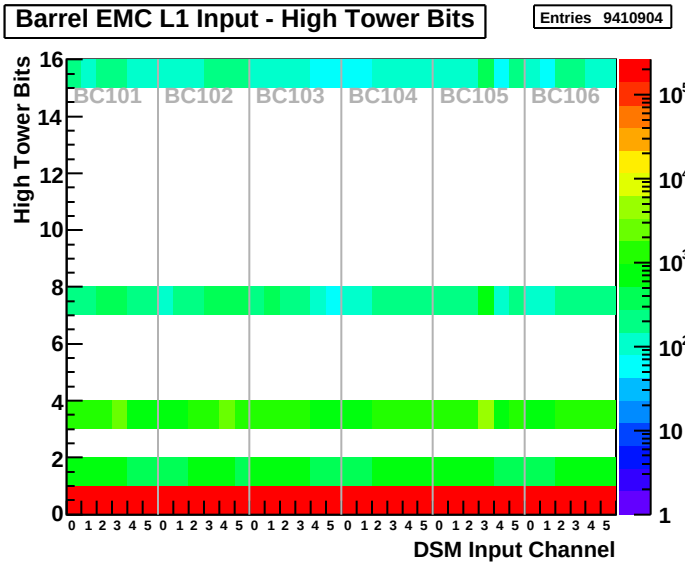
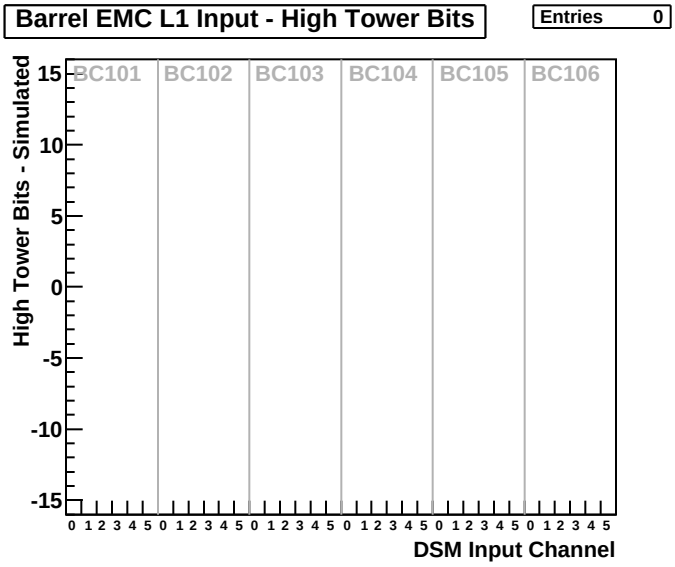

Barrel EMC L1 Input - Low Eta Sum
Entries 9410904
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

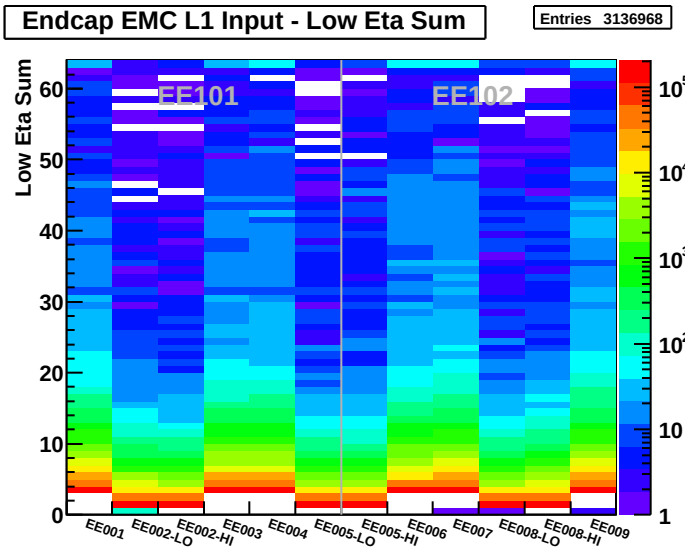

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

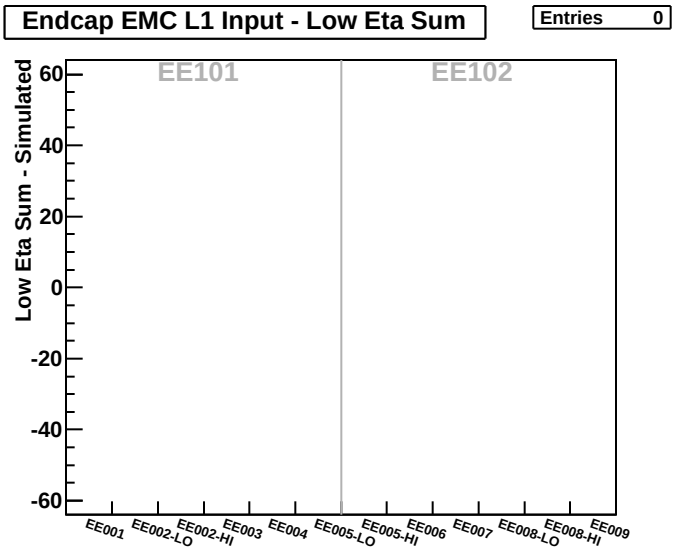

Barrel EMC L1 Input - High Eta Sum
Entries 9410904
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

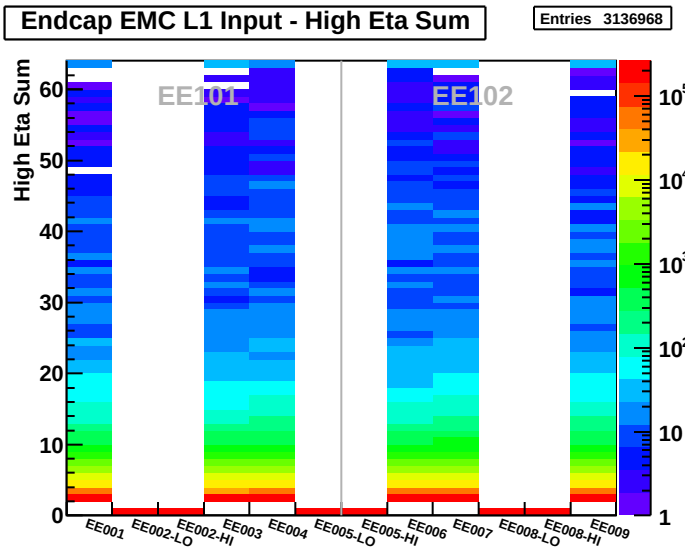

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

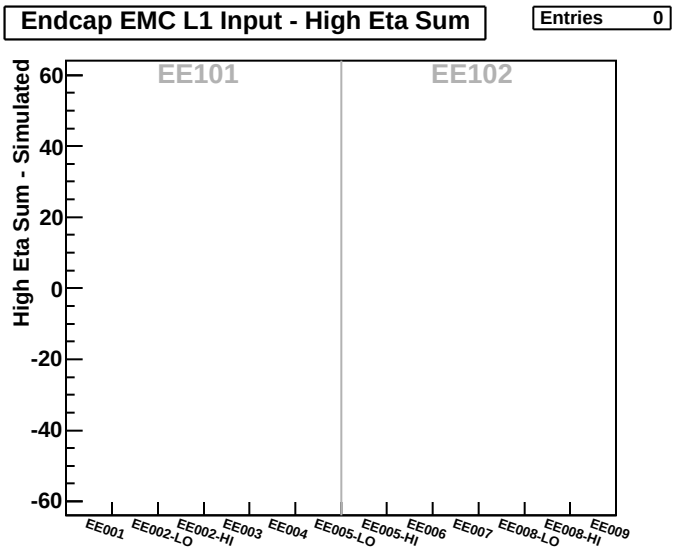

Barrel EMC L1 Input - High Tower Bits
Entries 9410904
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

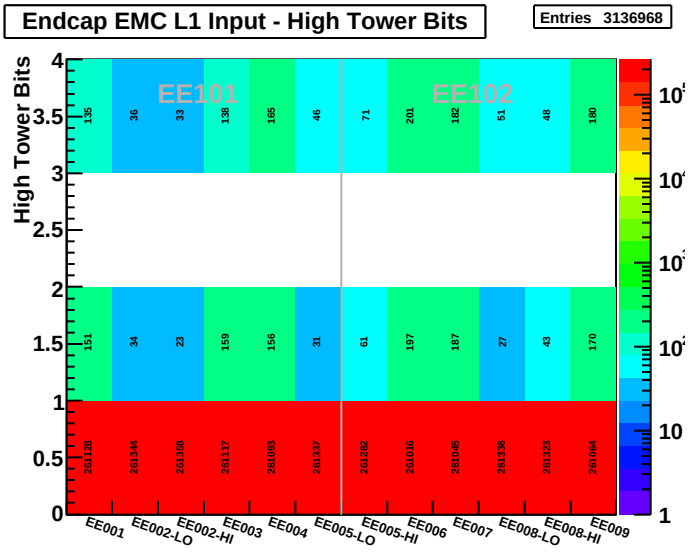

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

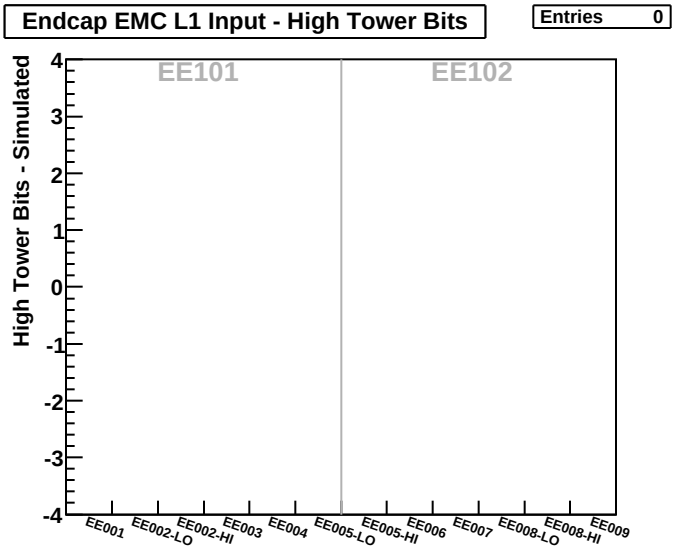

Endcap EMC L1 Input - Low Eta Sum
Entries 3136968
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

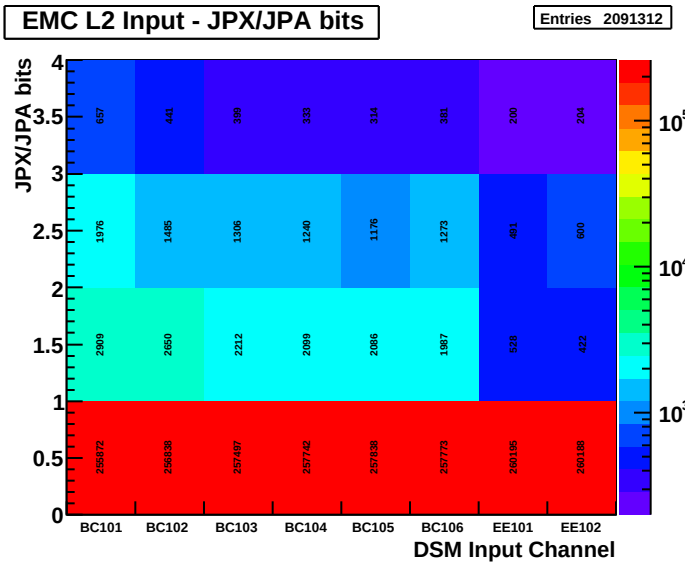

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

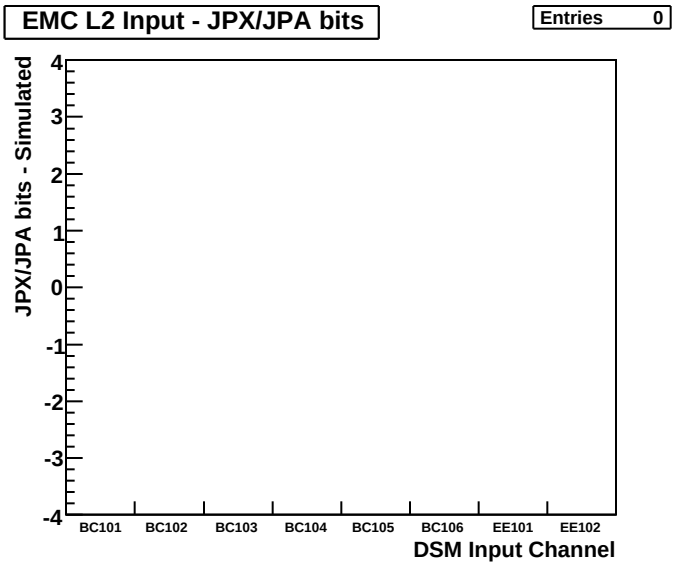

Endcap EMC L1 Input - High Eta Sum
Entries 3136968
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

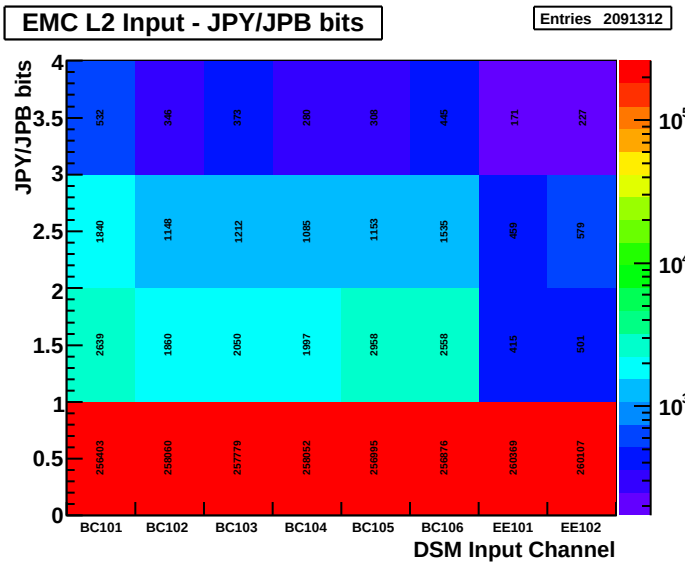

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

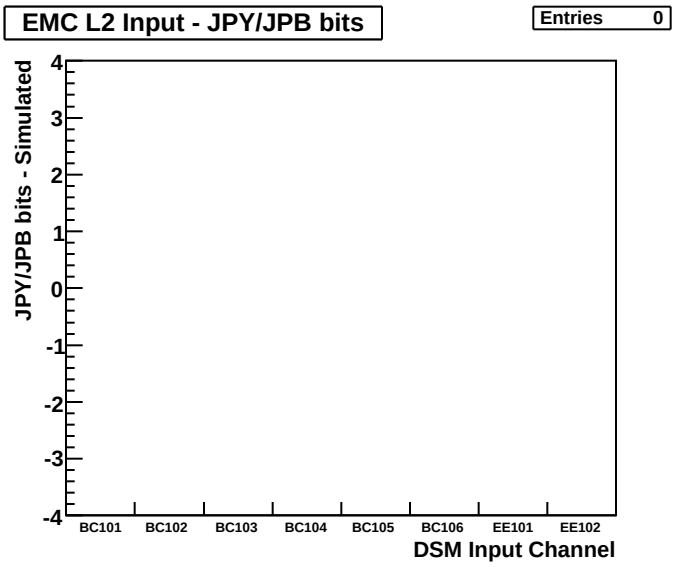

Endcap EMC L1 Input - High Tower Bits
Entries 3136968
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

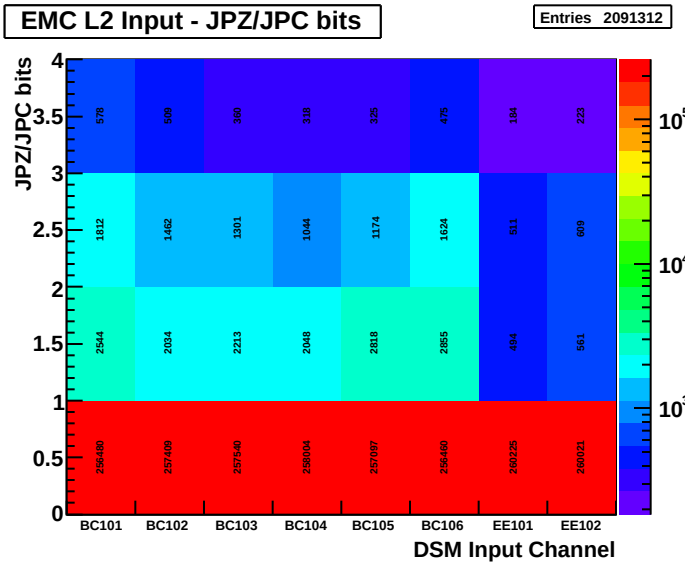

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

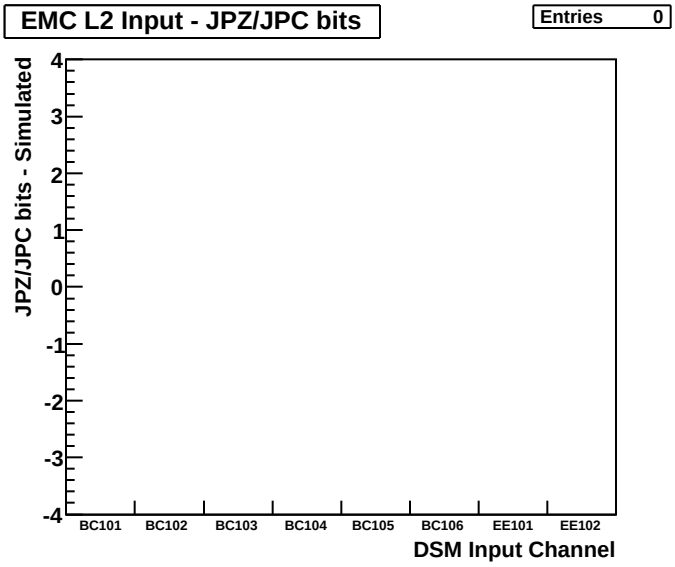

EMC L2 Input - JPX/JPA bits
Entries 2091312
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
657 441 399 333 314 381 200 204
1976 1485 1306 1240 1176 1273 491 600
2909 2650 2212 2099 2086 1987 528 422
255872 256838 257497 257742 257838 257773 260195 260188

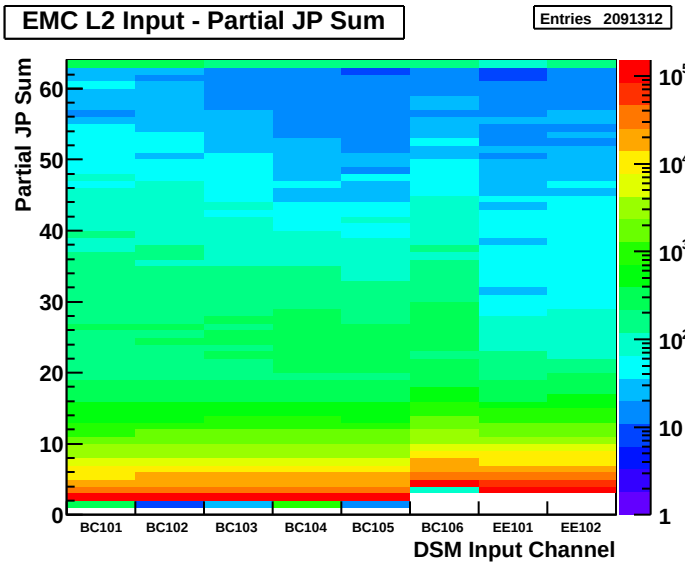

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

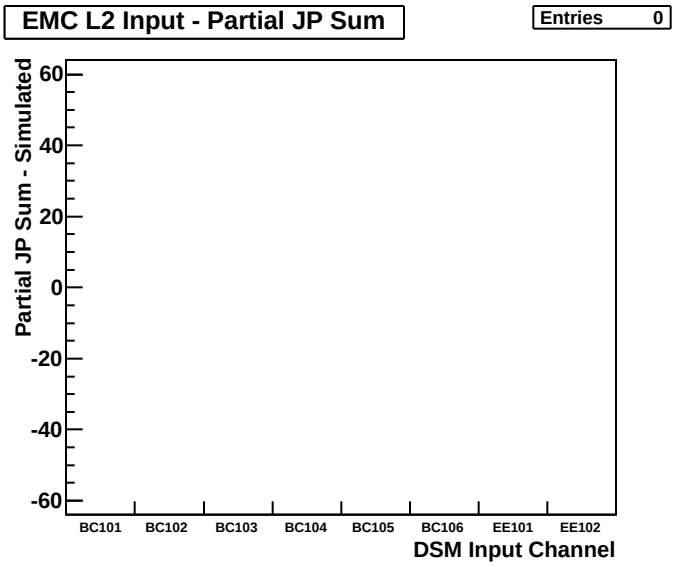

EMC L2 Input - JPY/JPB bits
Entries 2091312
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
532 346 378 280 308 445 171 227
1840 1148 1212 1085 1153 1535 459 579
2639 1860 2050 1997 2058 2558 415 501
256403 258060 257779 258052 258995 258876 260389 260107

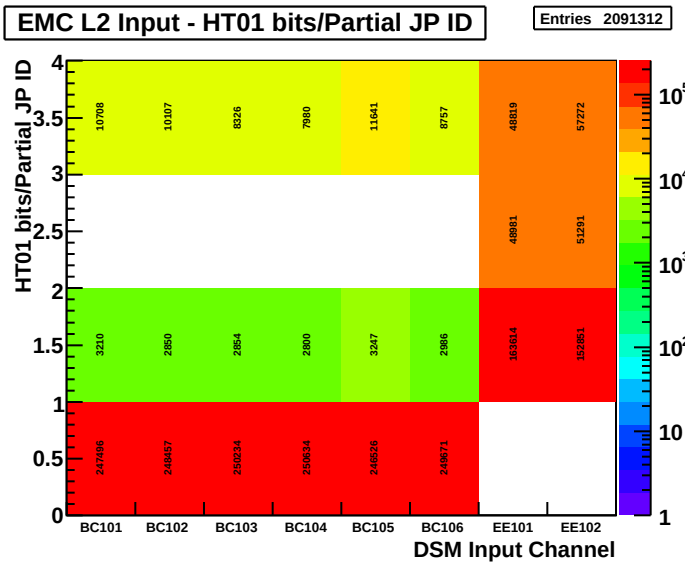

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

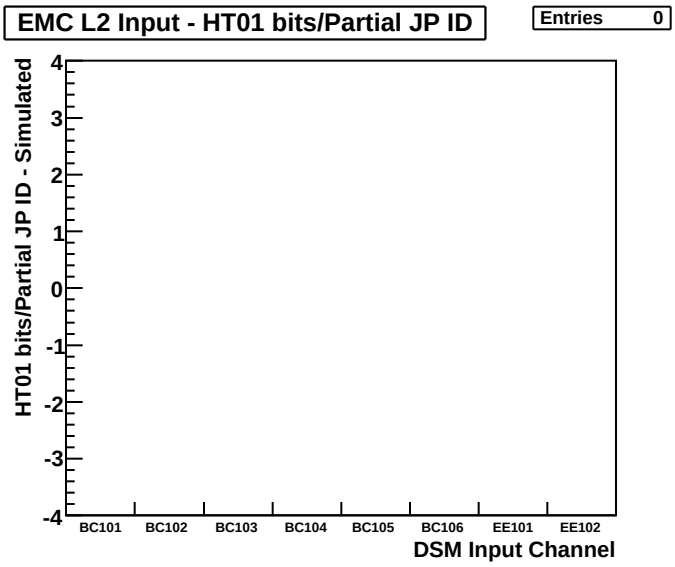

EMC L2 Input - JPZ/JPC bits
Entries 2091312
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
578 509 360 318 325 475 184 223
1812 1462 1301 1044 1174 1624 511 609
2544 2034 2213 2048 2818 2865 494 561
256480 257409 257540 258004 257097 256460 260225 260021

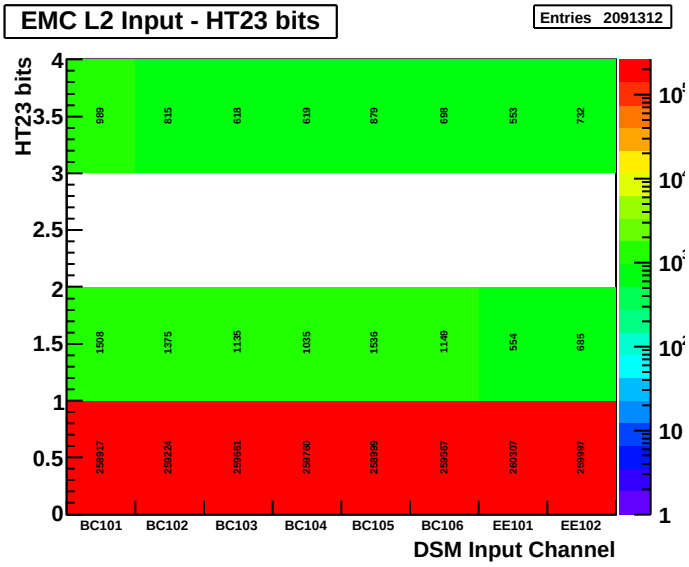

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

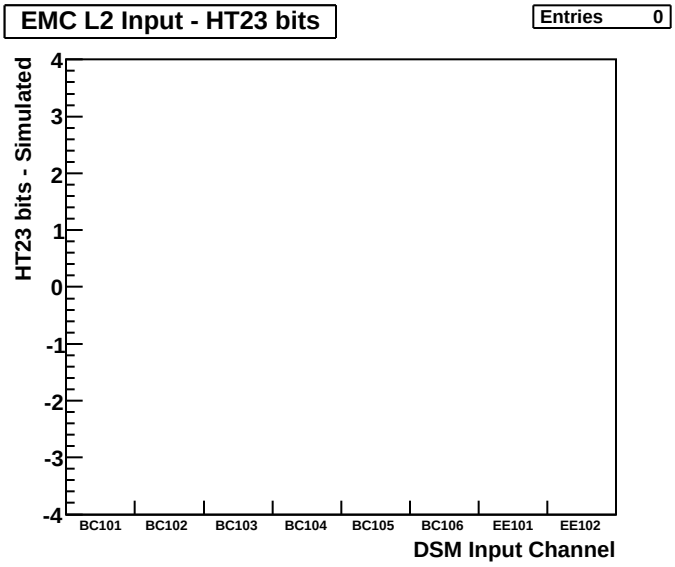

EMC L2 Input - Partial JP Sum
Entries 2091312
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

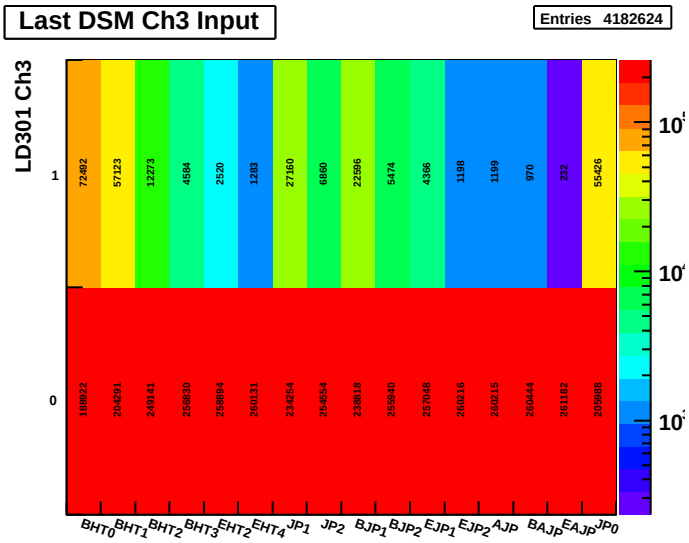

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

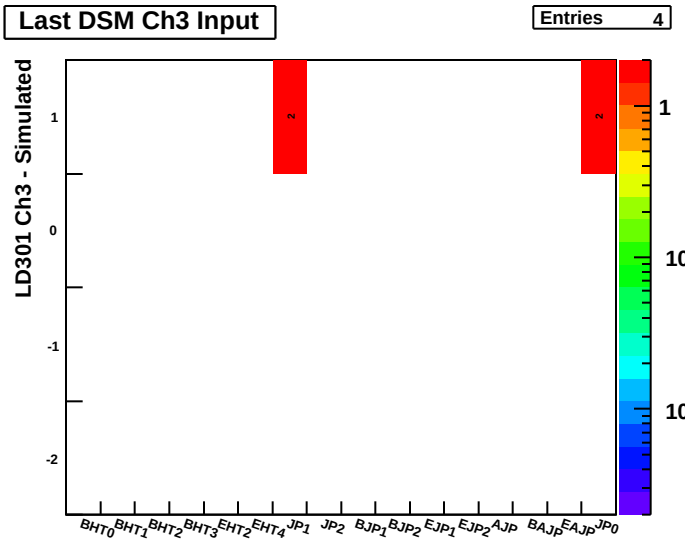

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2091312
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

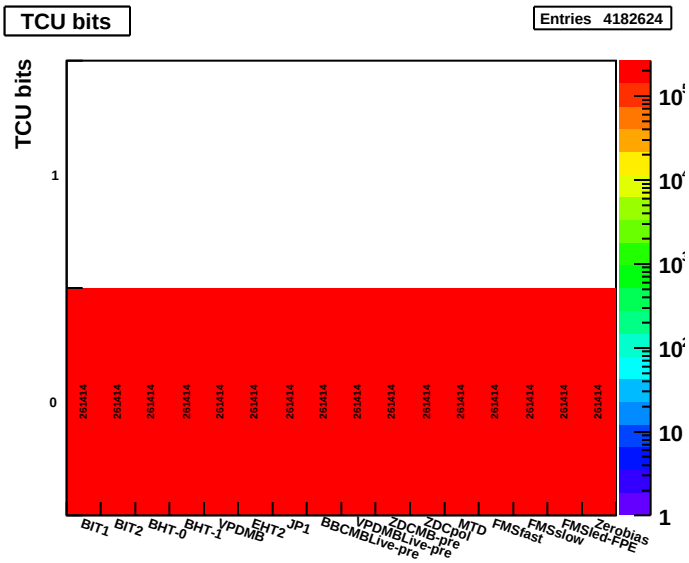

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

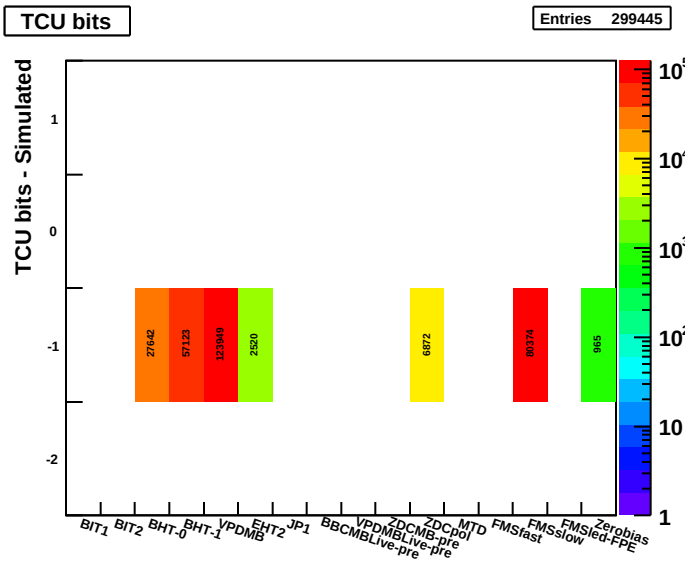

EMC L2 Input - HT23 bits
Entries 2091312
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

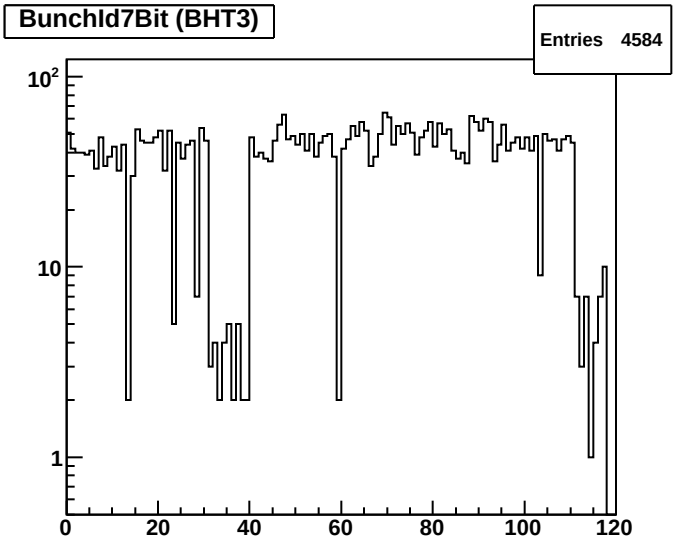

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

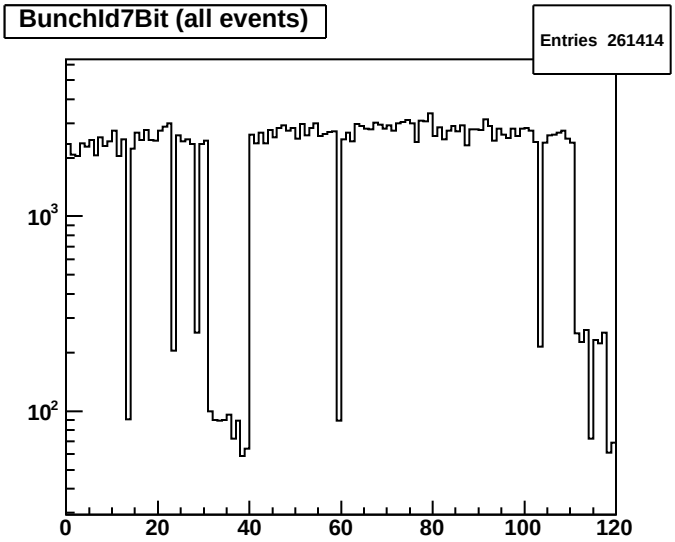

Last DSM Ch3 Input
Entries 4182624
LD301 Ch3
72492 57123 12273 4584 2520 1283 27160 6860 22596 5474 4366 1198 1199 970 232 55426
188922 204291 240141 256830 258894 260131 234254 254554 238818 259940 257048 260216 260215 260444 261182 205988
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

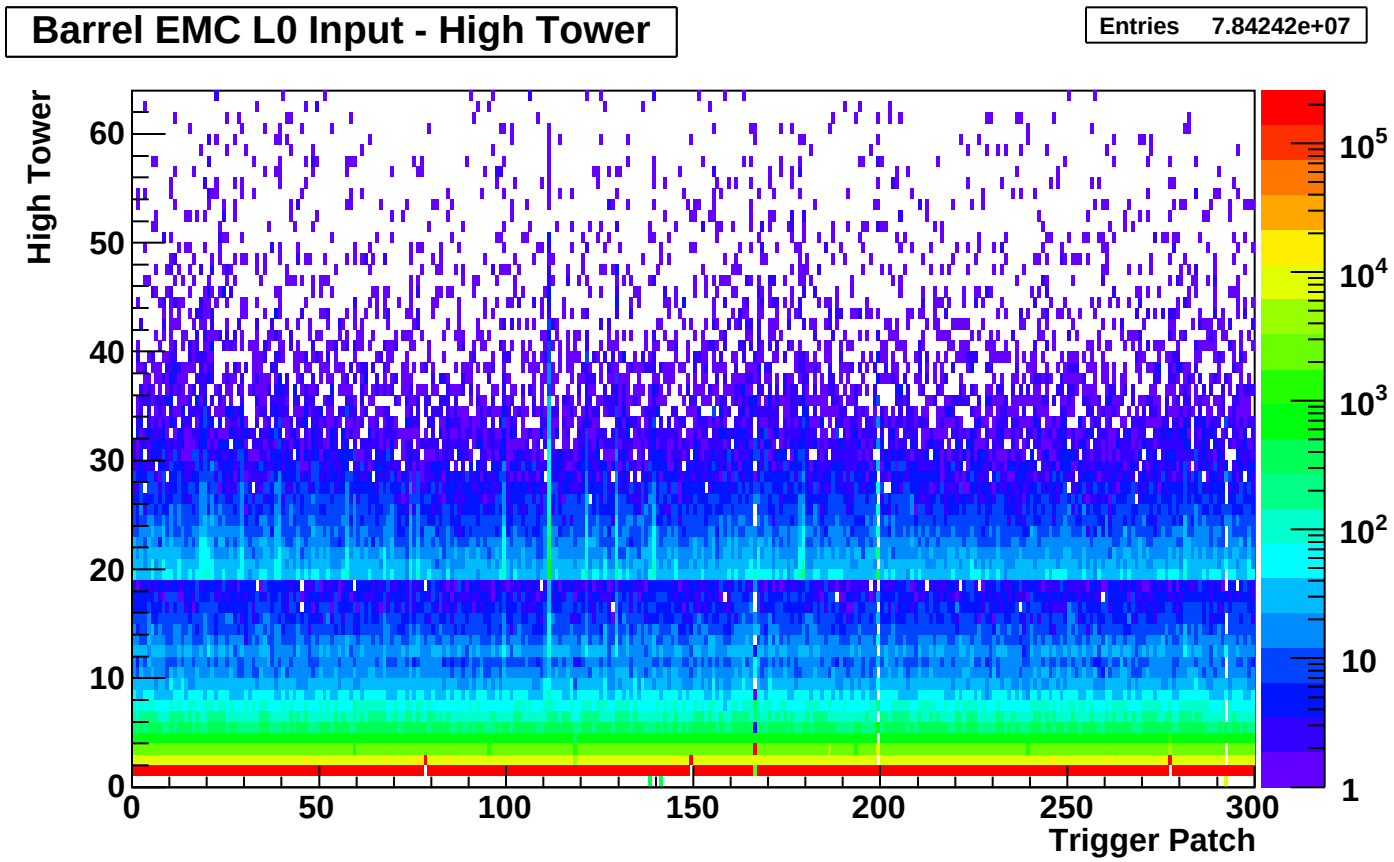

Last DSM Ch3 Input
Entries 4
LD301 Ch3 - Simulated
2 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

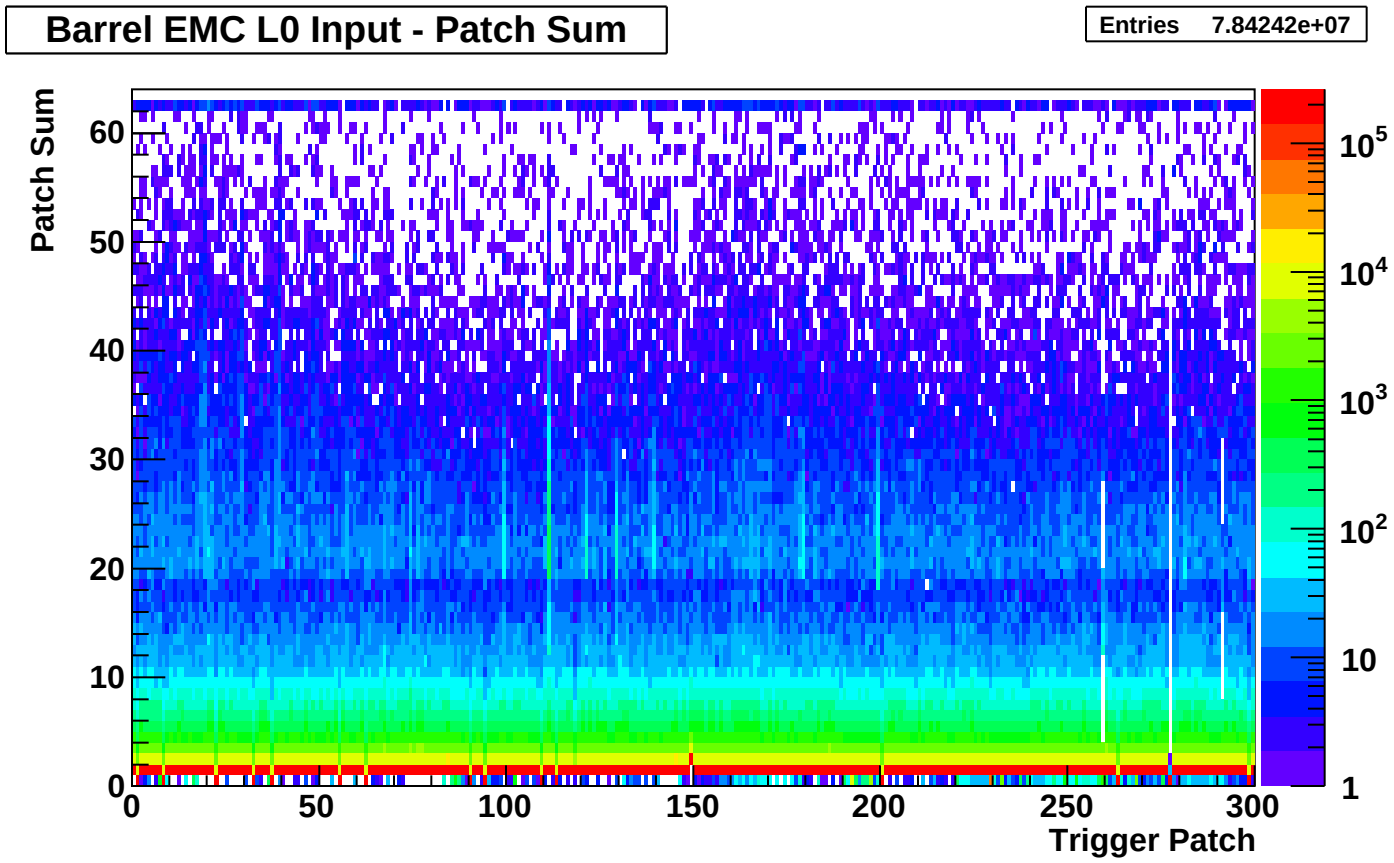

TCU bits
Entries 4182624
TCU bits
261414 261414 261414 261414 261414 261414 261414 261414 261414 261414 261414 261414 261414 261414 261414 261414
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

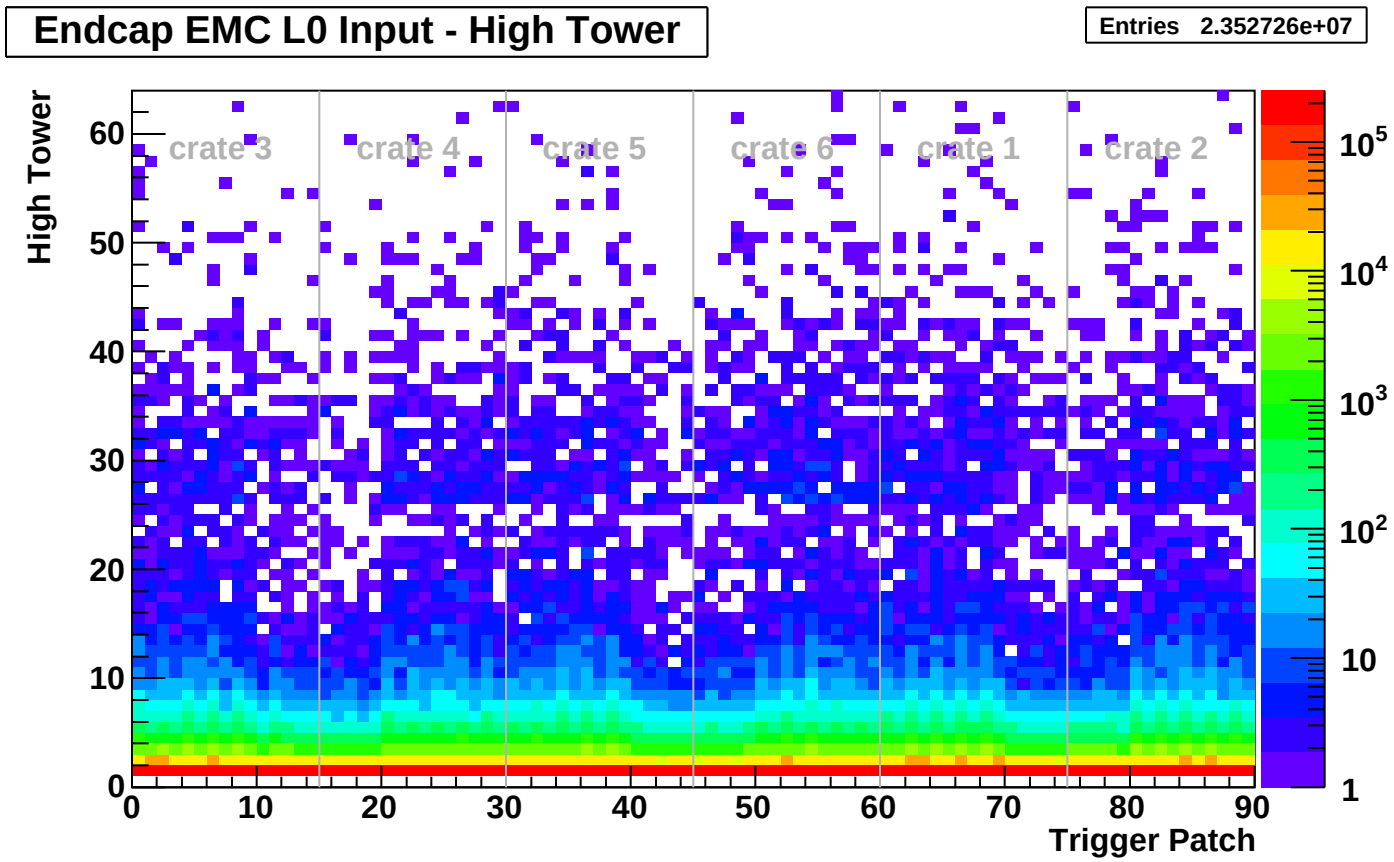

TCU bits
Entries 299445
TCU bits - Simulated
27642 57123 123949 2520 6872 80374 965
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

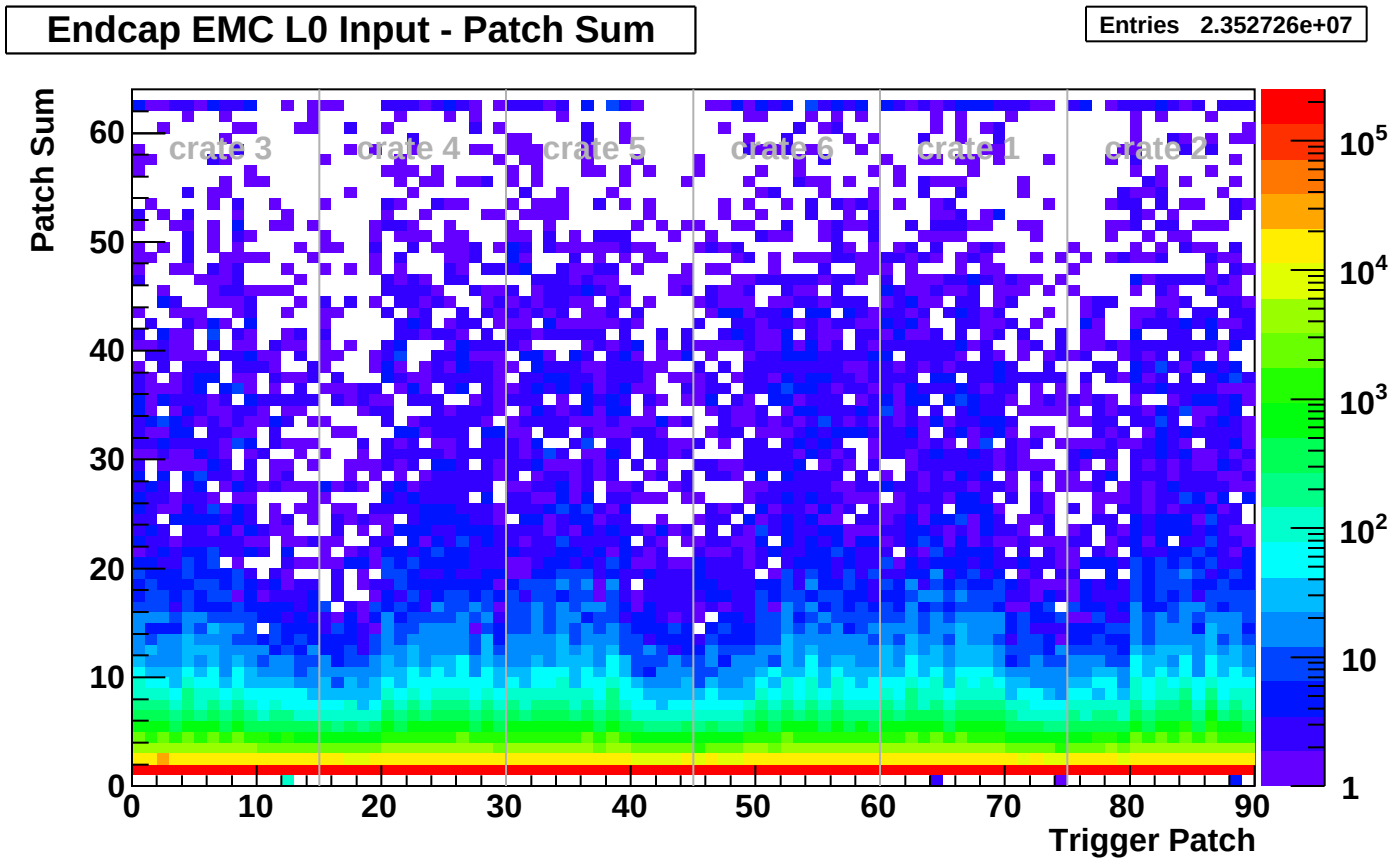

BunchId7Bit (BHT3)
Entries 4584

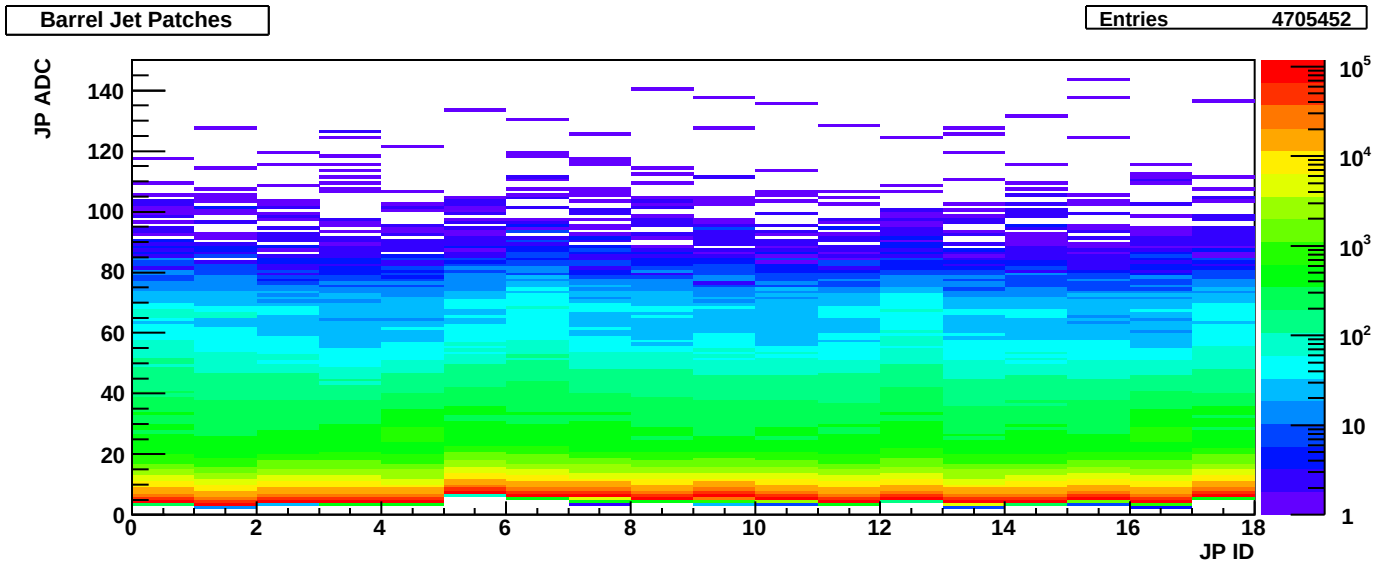

BunchId7Bit (all events)
Entries 261414

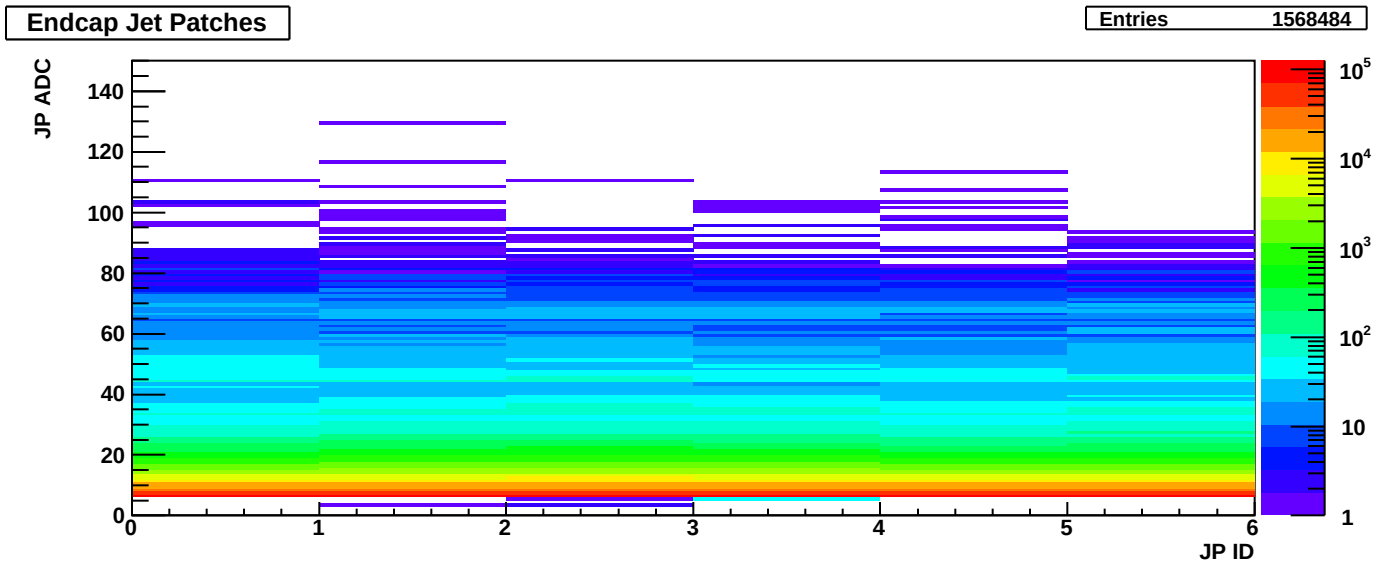

Barrel EMC L0 Input - High Tower
Entries 7.84242e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

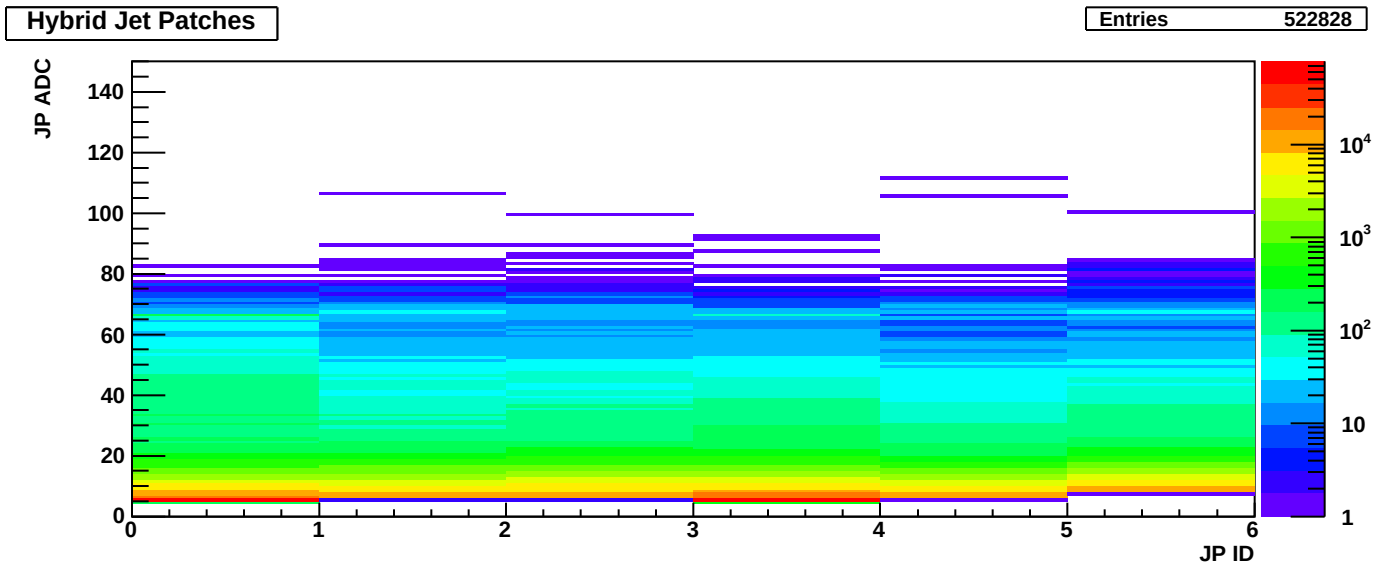

Barrel EMC L0 Input - Patch Sum
Entries 7.84242e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

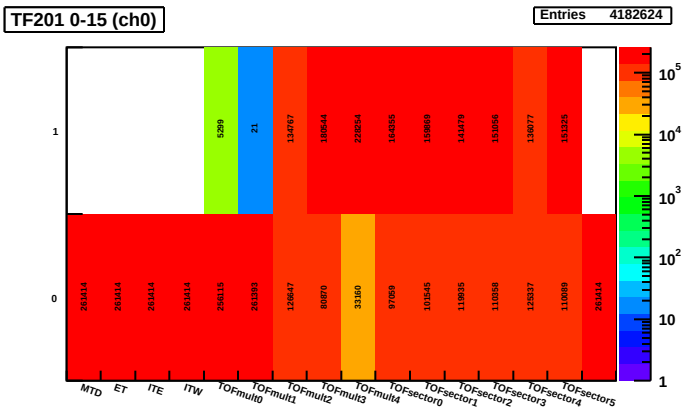

Endcap EMC L0 Input - High Tower
Entries 2.352726e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

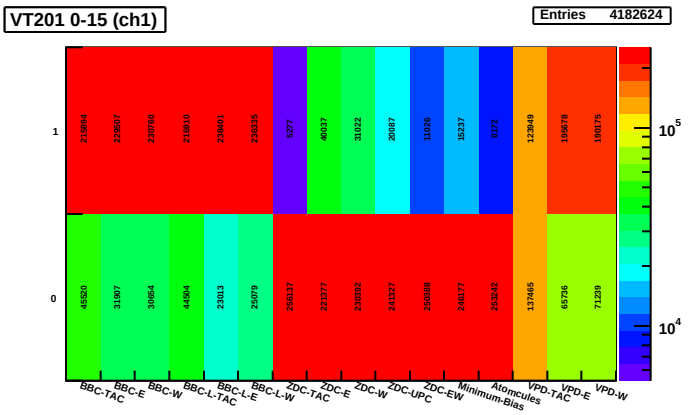

Endcap EMC L0 Input - Patch Sum
Entries 2.352726e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

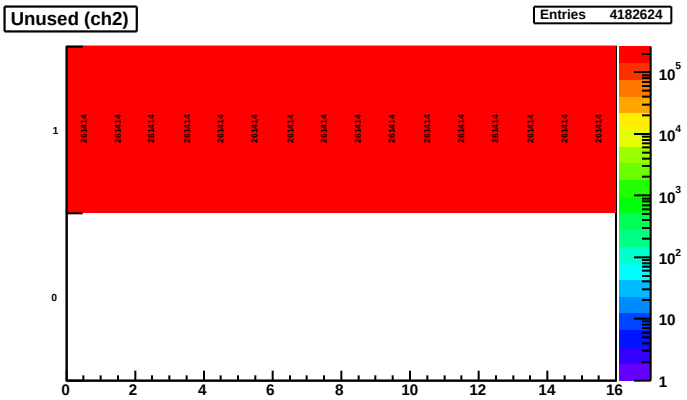

Barrel Jet Patches
Entries 4705452
JP ADC
JP ID

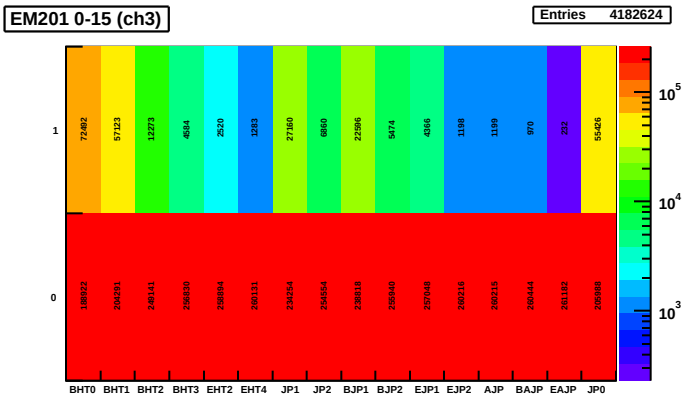

Endcap Jet Patches
Entries 1568484
JP ADC
JP ID

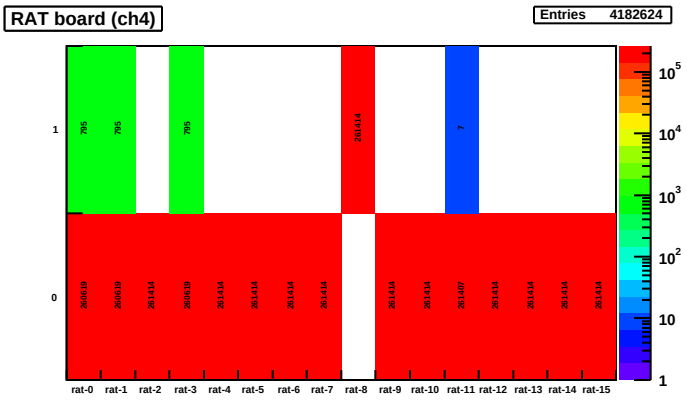

Hybrid Jet Patches
Entries 522828
JP ADC
JP ID

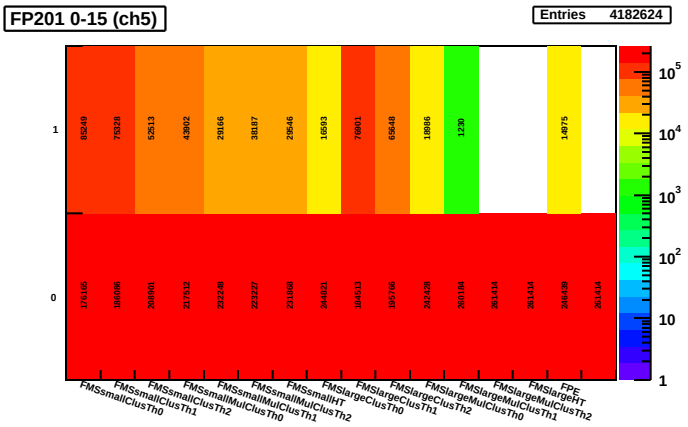

TF201 0-15 (ch0)
Entries 4182624
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

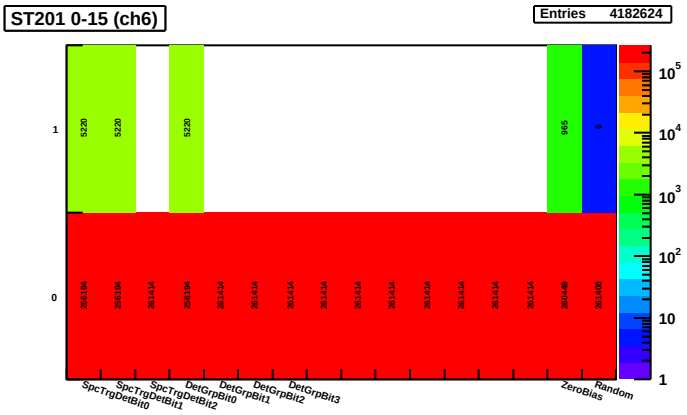

VT201 0-15 (ch1)
Entries 4182624
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

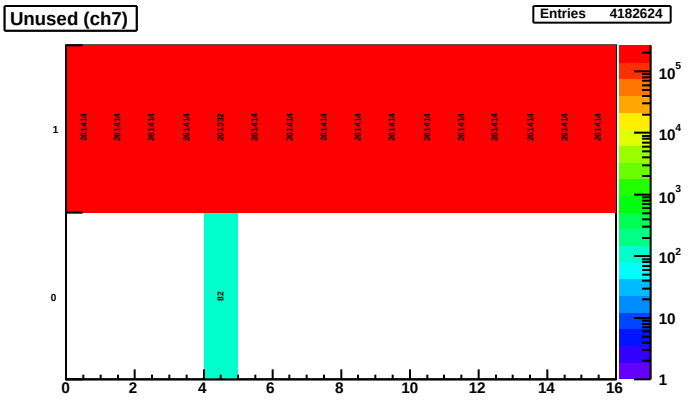

Unused (ch2)
Entries 4182624

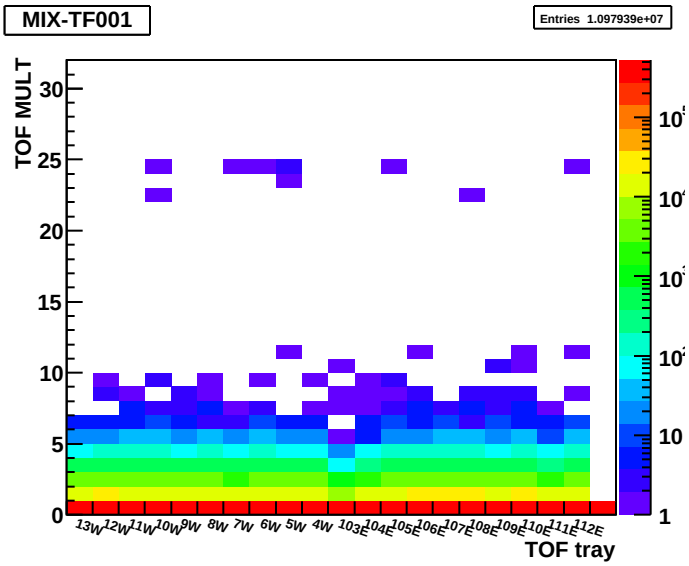

EM201 0-15 (ch3)
Entries 4182624
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

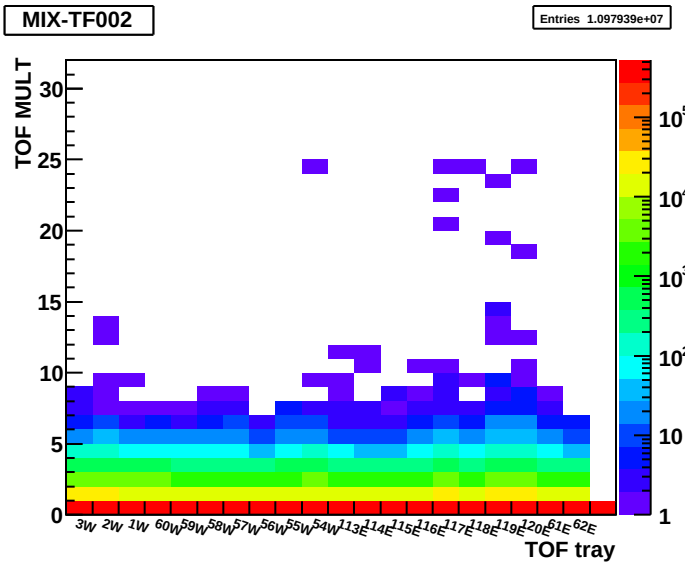

RAT board (ch4)
Entries 4182624
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

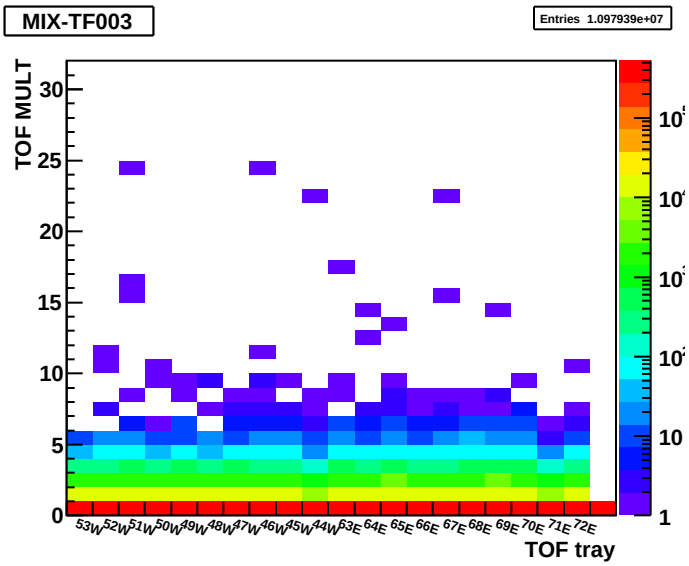

FP201 0-15 (ch5)
Entries 4182624
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

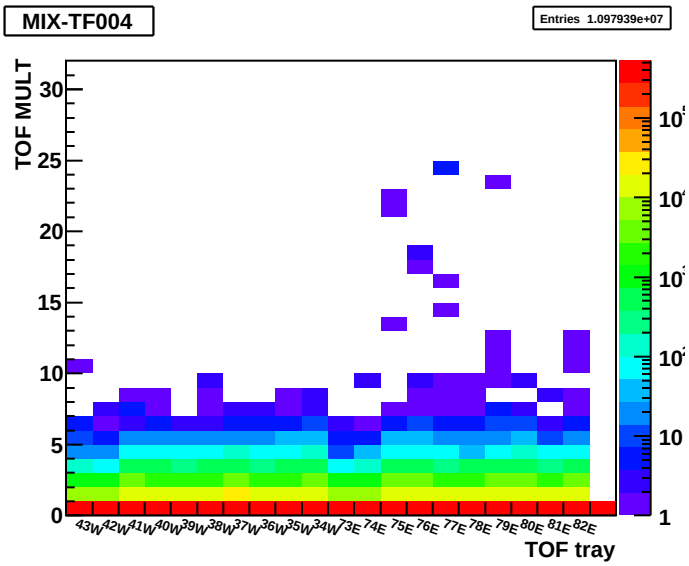

ST201 0-15 (ch6)
Entries 4182624
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

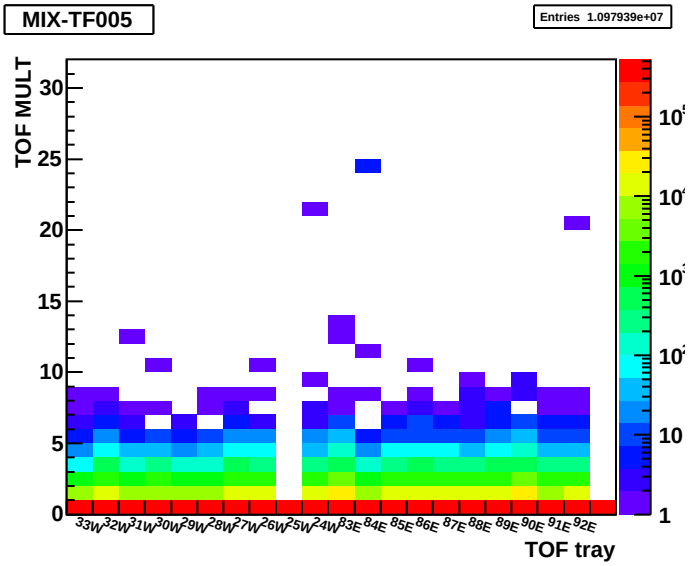

Unused (ch7)
Entries 4182624

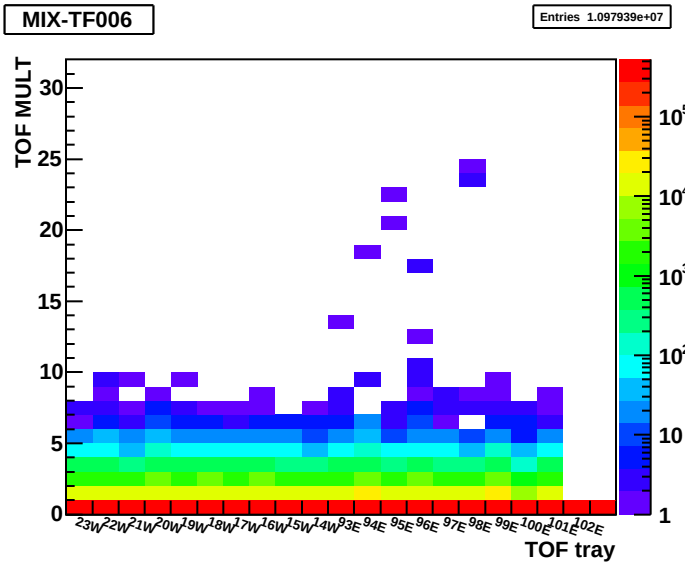

MIX-TF001
Entries 1.097939e+07
TOF MULT
TOF tray

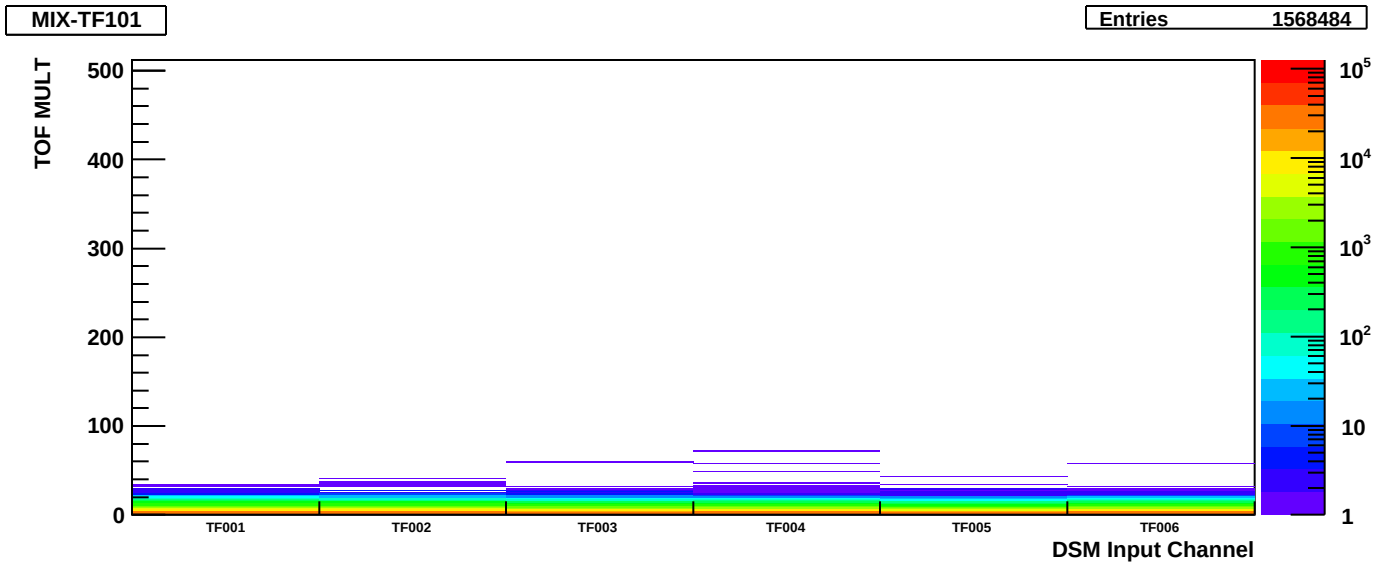

MIX-TF002
Entries 1.097939e+07
TOF MULT
TOF tray

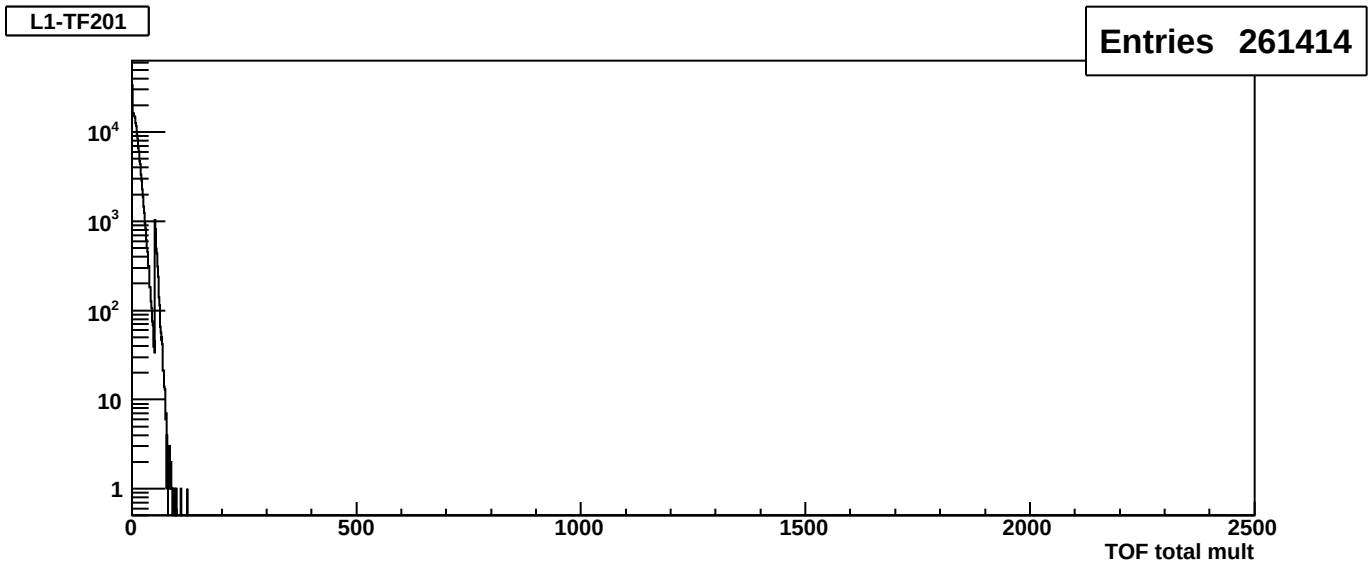

MIX-TF003
Entries 1.097939e+07
TOF MULT
TOF tray

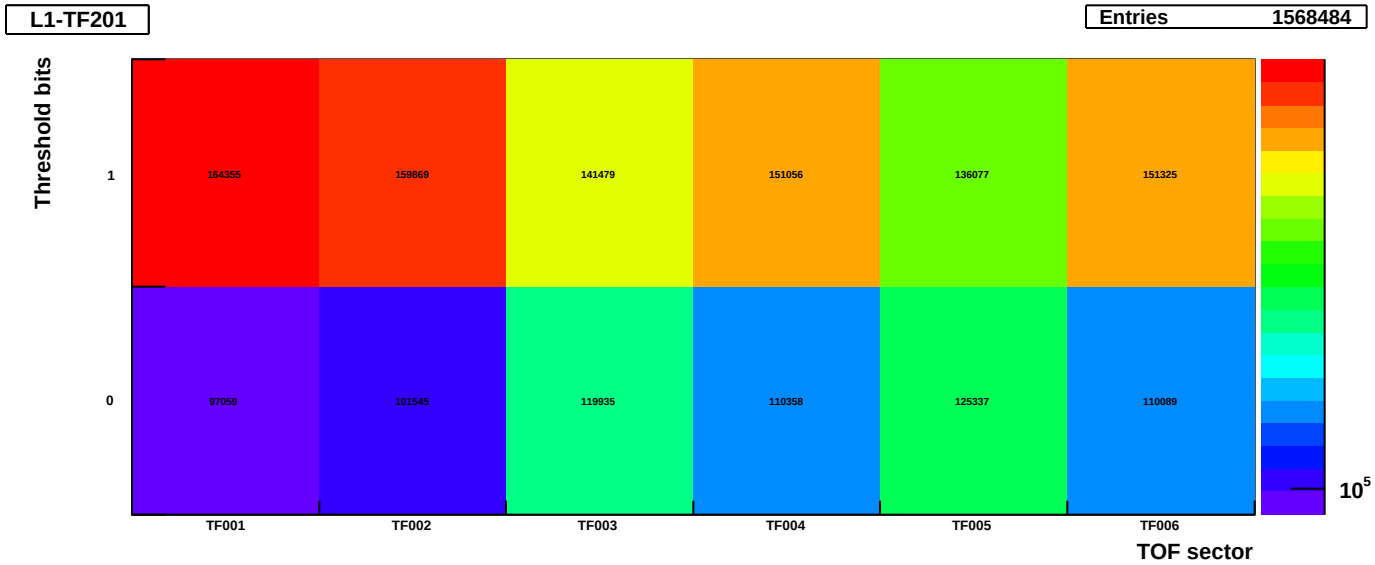

MIX-TF004
Entries 1.097939e+07
TOF MULT
TOF tray

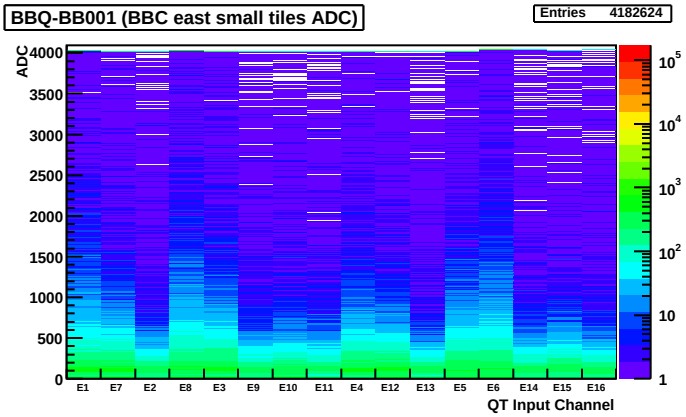

MIX-TF005
Entries 1.097939e+07
TOF MULT
TOF tray

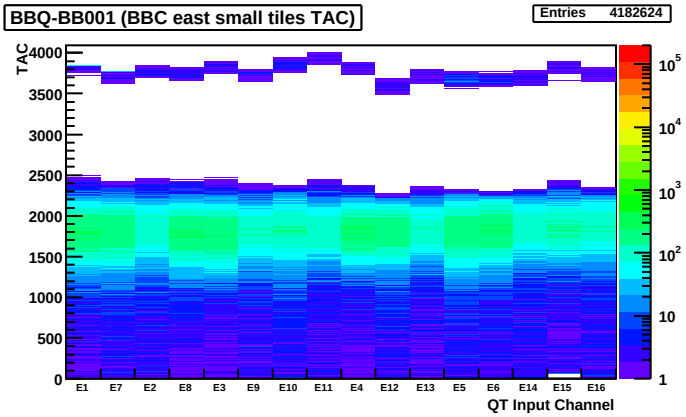

MIX-TF006
Entries 1.097939e+07
TOF MULT
TOF tray

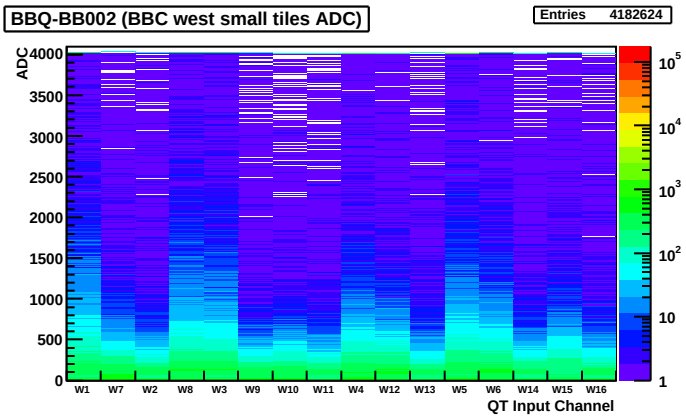

MIX-TF101
Entries 1568484
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

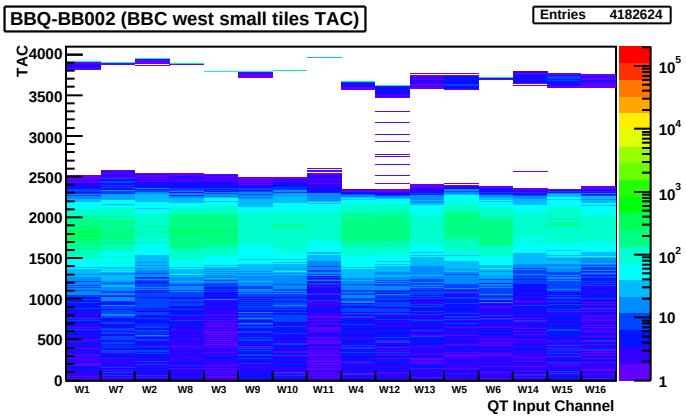

L1-TF201
Entries 261414
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

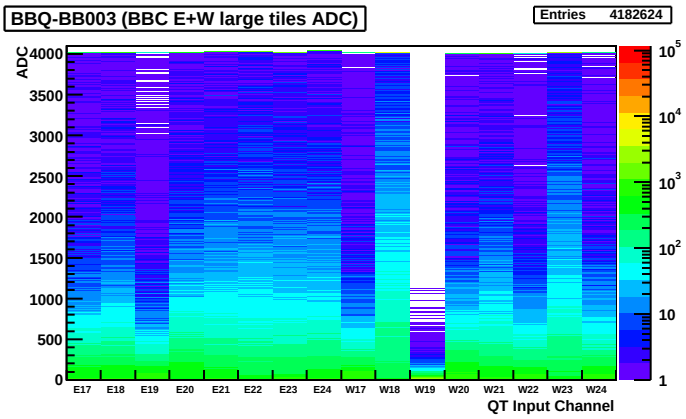

L1-TF201
Entries 1568484
Threshold bits
1
0
164355
159869
141479
151056
136077
151325
97059
101545
119935
110358
125337
110089
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

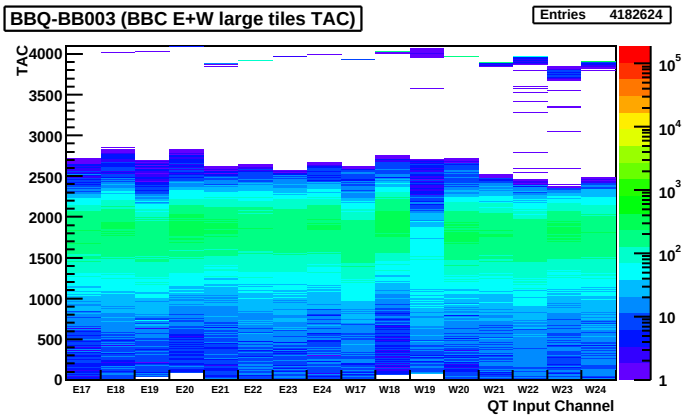

BBQ-BB001 (BBC east small tiles ADC)
Entries 4182624
ADC
QT Input Channel

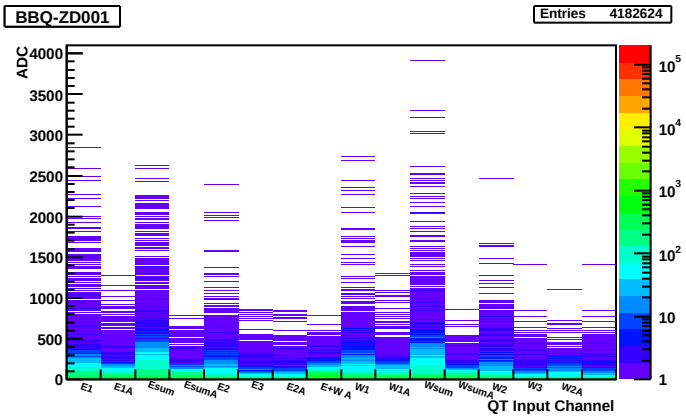

BBQ-BB001 (BBC east small tiles TAC)
Entries 4182624
TAC
QT Input Channel

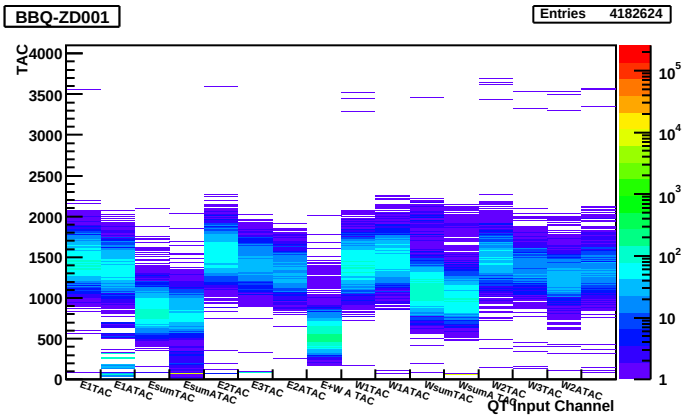

BBQ-BB002 (BBC west small tiles ADC)
Entries 4182624
ADC
QT Input Channel

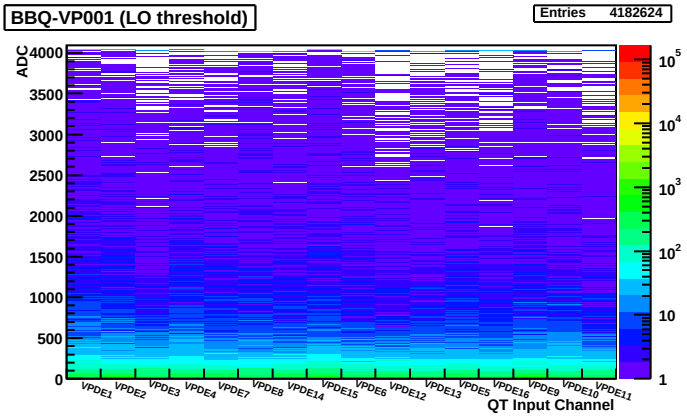

BBQ-BB002 (BBC west small tiles TAC)
Entries 4182624
TAC
QT Input Channel

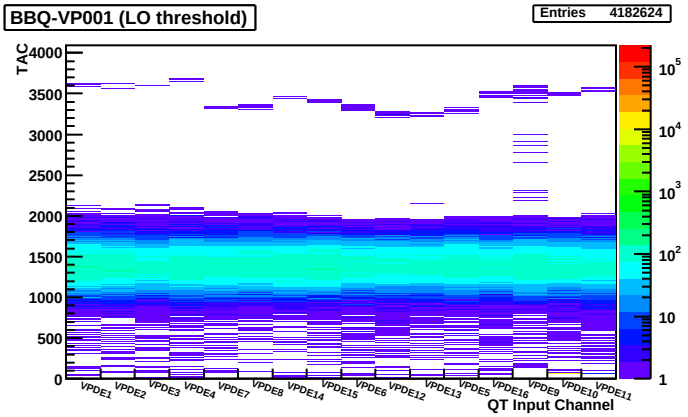

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4182624
ADC
QT Input Channel

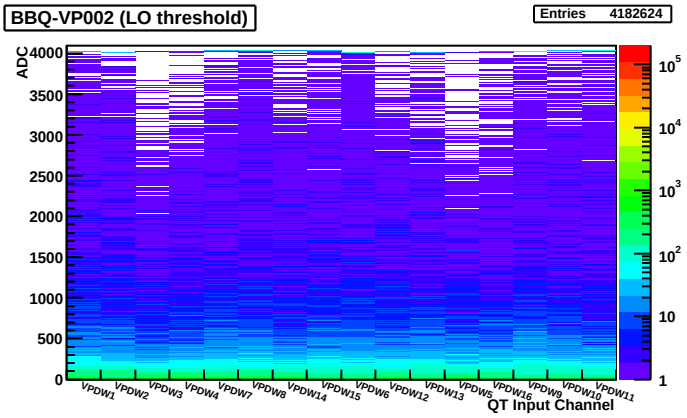

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4182624
TAC
QT Input Channel

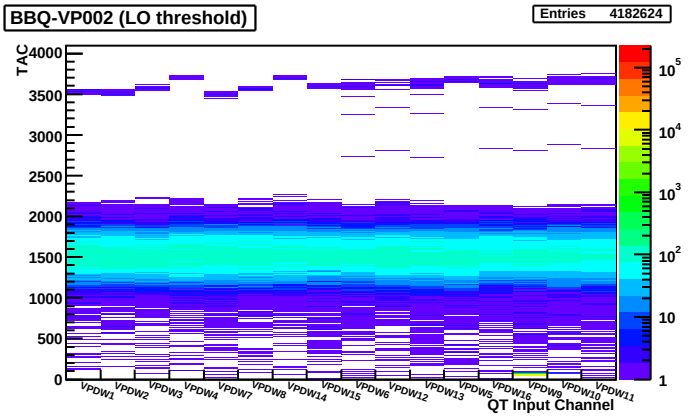

BBQ-ZD001
Entries 4182624
ADC
QT Input Channel

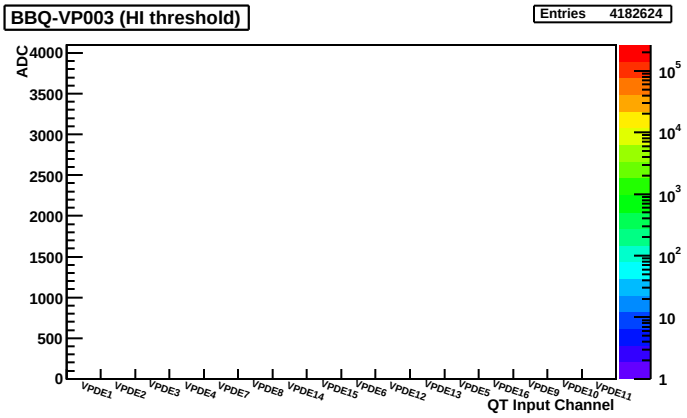

BBQ-ZD001
Entries 4182624
TAC
QT Input Channel

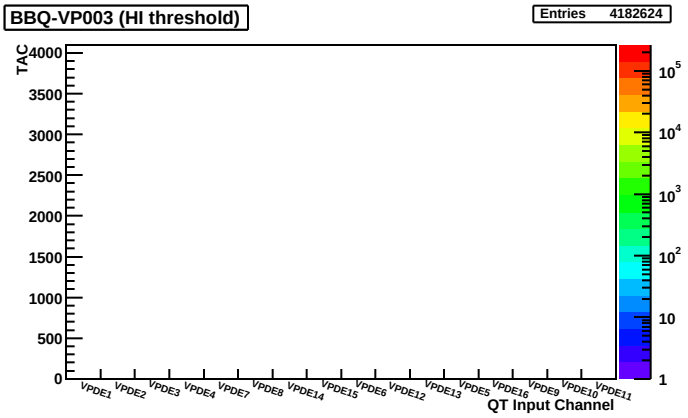

BBQ-VP001 (LO threshold)
Entries 4182624
ADC
QT Input Channel

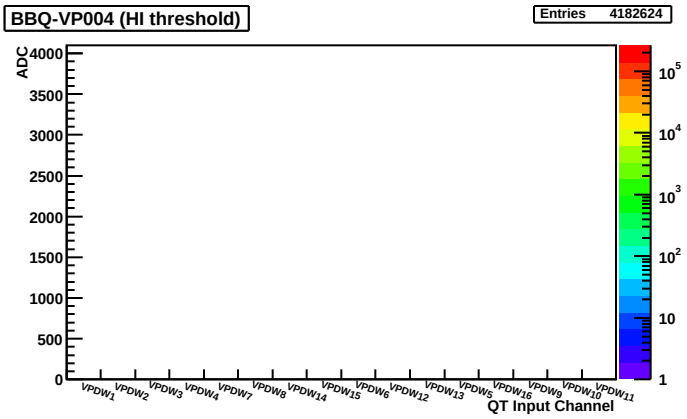

BBQ-VP001 (LO threshold)
Entries 4182624
TAC
QT Input Channel

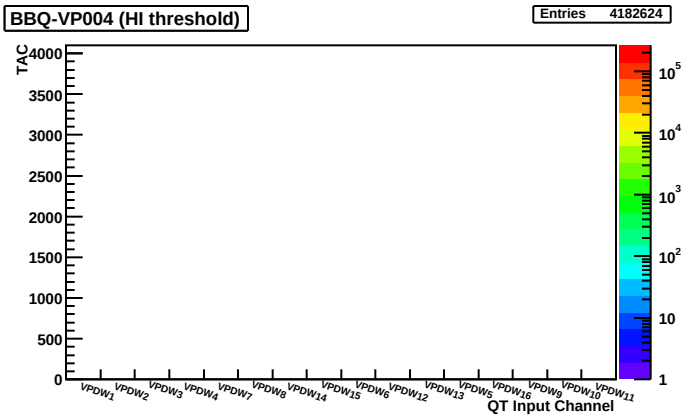

BBQ-VP002 (LO threshold)
Entries 4182624
ADC
QT Input Channel

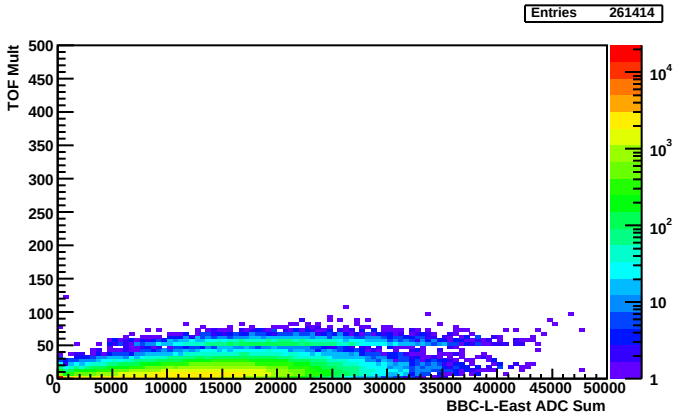

BBQ-VP002 (LO threshold)
Entries 4182624
TAC
QT Input Channel

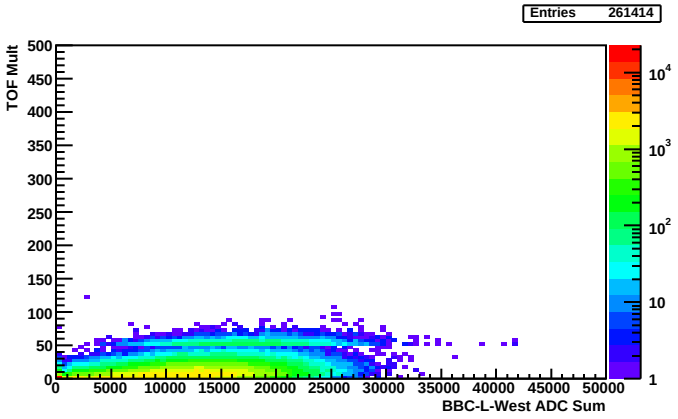

BBQ-VP003 (HI threshold)
Entries 4182624
ADC
QT Input Channel

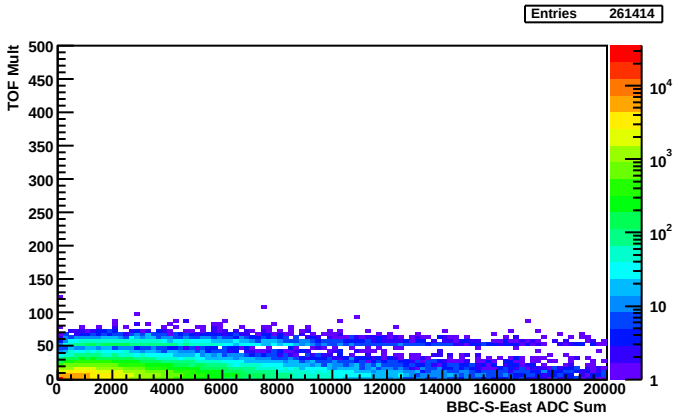

BBQ-VP003 (HI threshold)
Entries 4182624
TAC
QT Input Channel

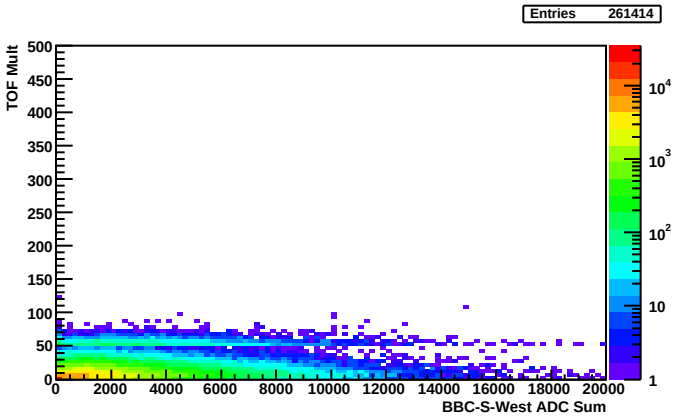

BBQ-VP004 (HI threshold)
Entries 4182624
ADC
QT Input Channel

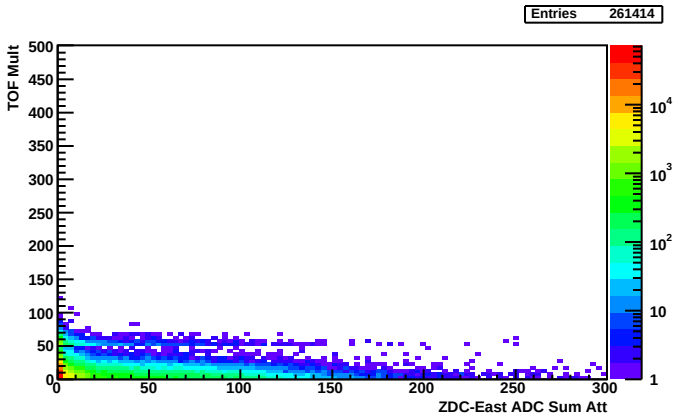

BBQ-VP004 (HI threshold)
Entries 4182624
TAC
QT Input Channel

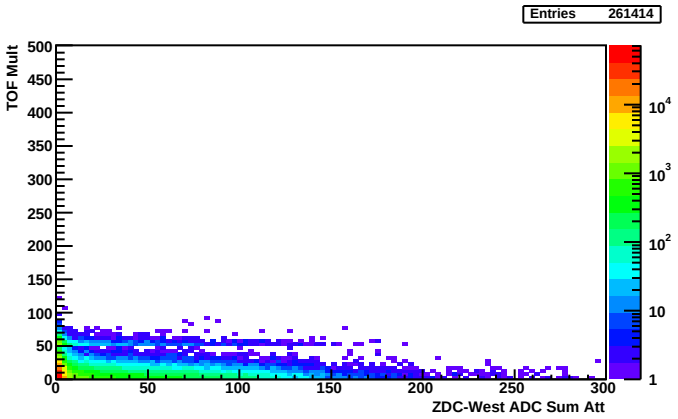

Entries 261414
TOF Mult
BBC-L-East ADC Sum

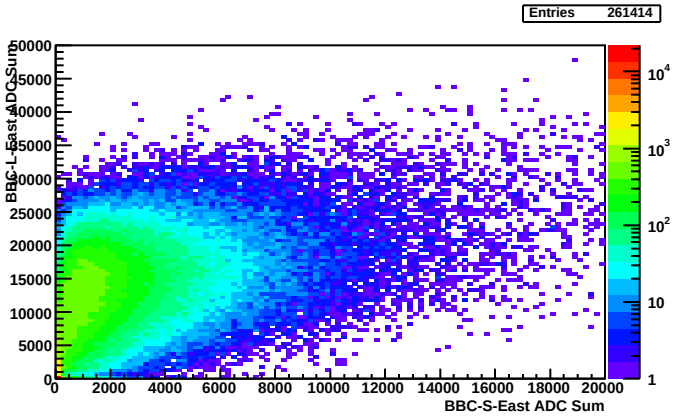

Entries 261414
TOF Mult
BBC-L-West ADC Sum

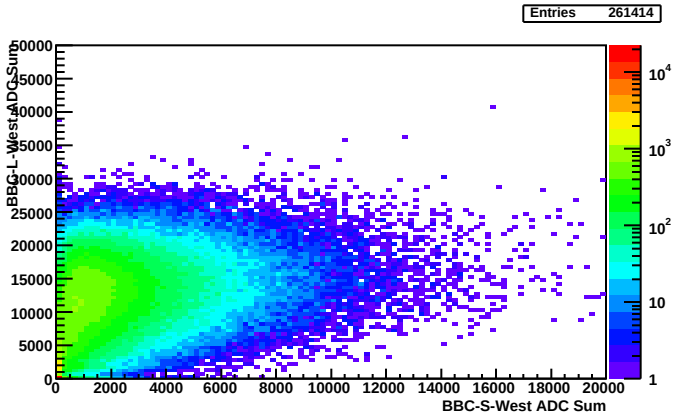

Entries 261414
TOF Mult
BBC-S-East ADC Sum

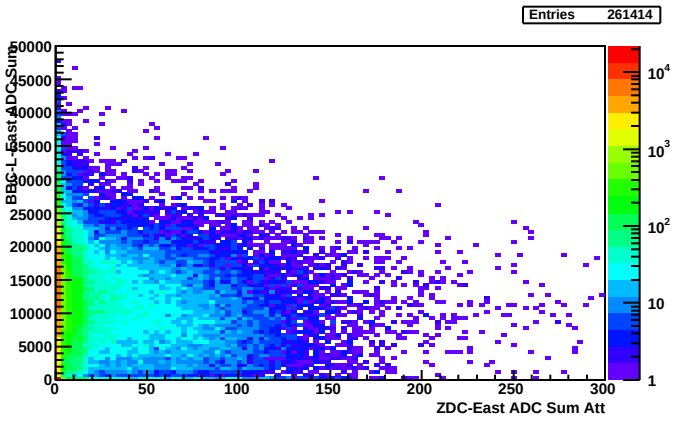

Entries 261414
TOF Mult
BBC-S-West ADC Sum

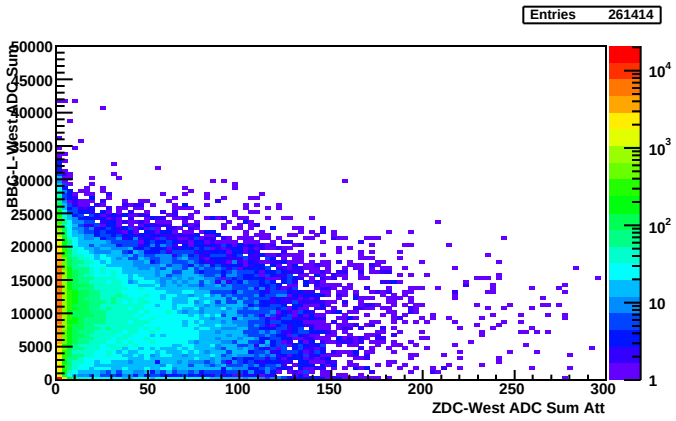

Entries 261414
TOF Mult
ZDC-East ADC Sum Att

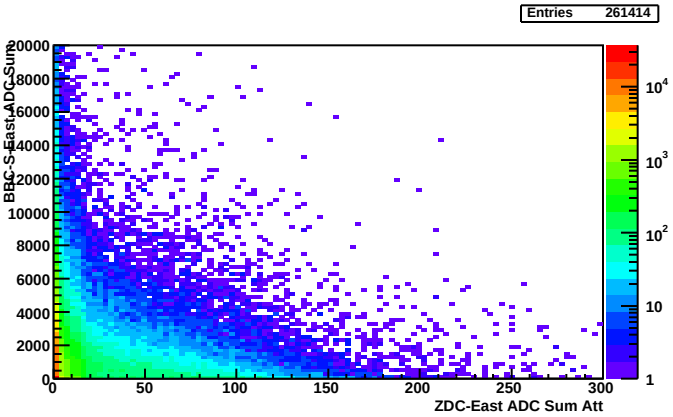

Entries 261414
TOF Mult
ZDC-West ADC Sum Att

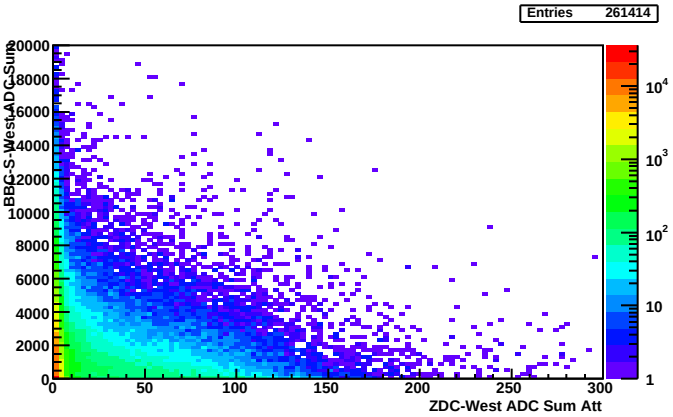

Entries 261414
BBC-L-East ADC Sum
BBC-S-East ADC Sum

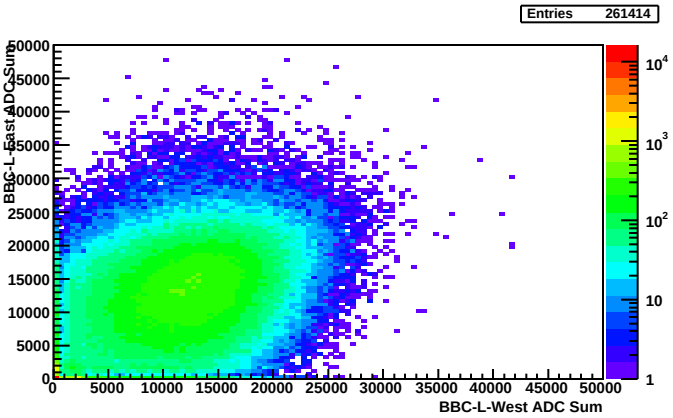

Entries 261414
BBC-L-West ADC Sum
BBC-S-West ADC Sum

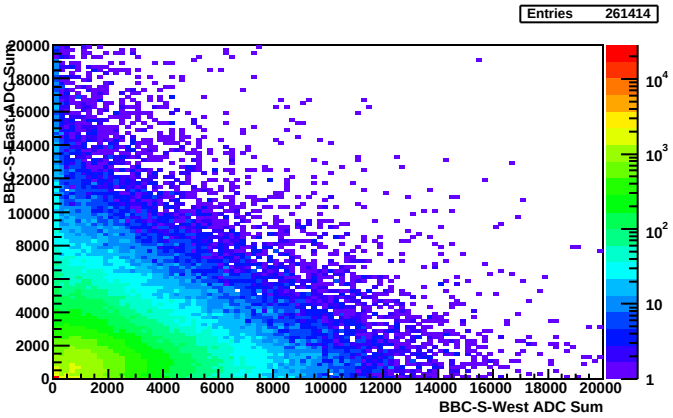

Entries 261414
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

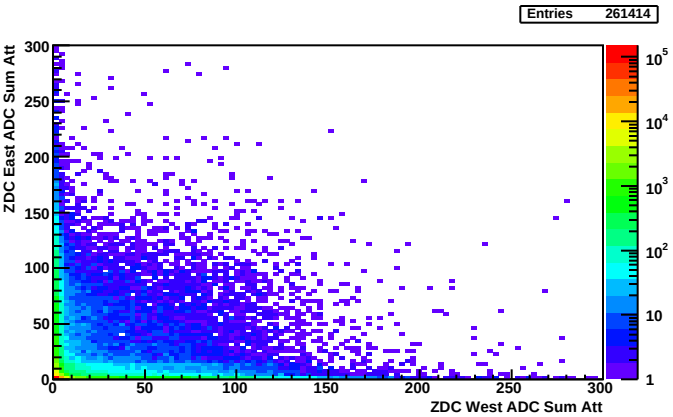

Entries 261414
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

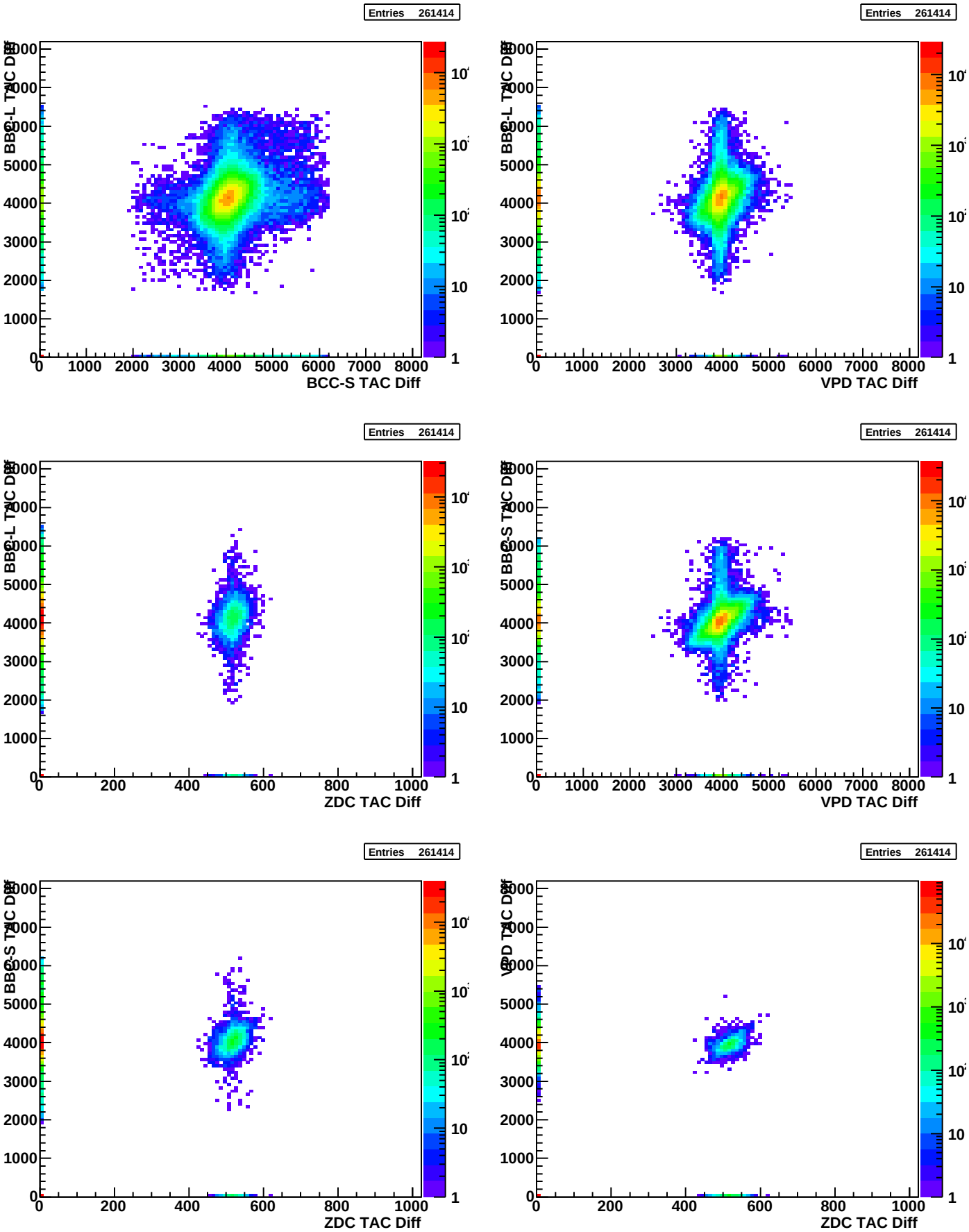

Entries 261414
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

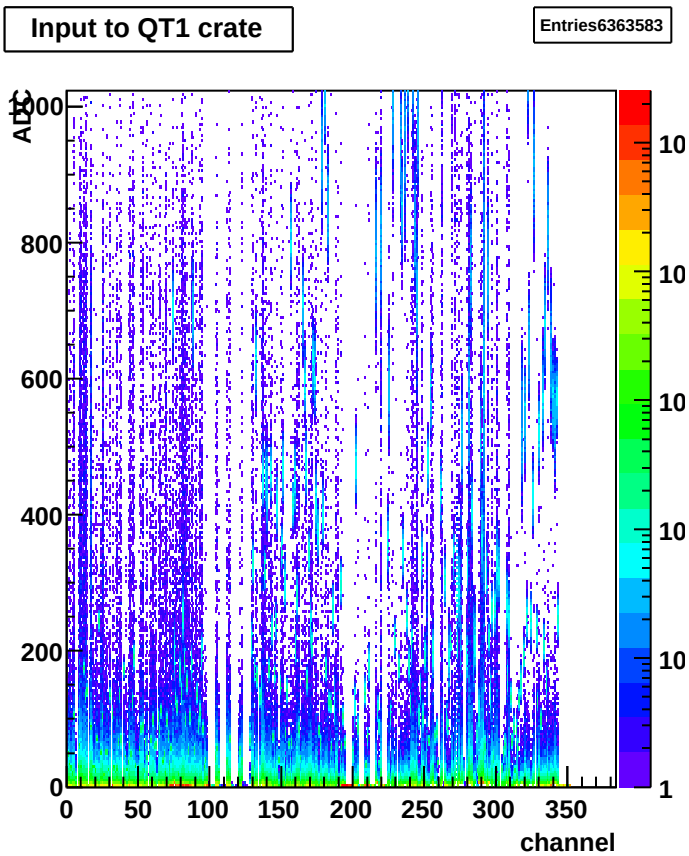

Entries 261414
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

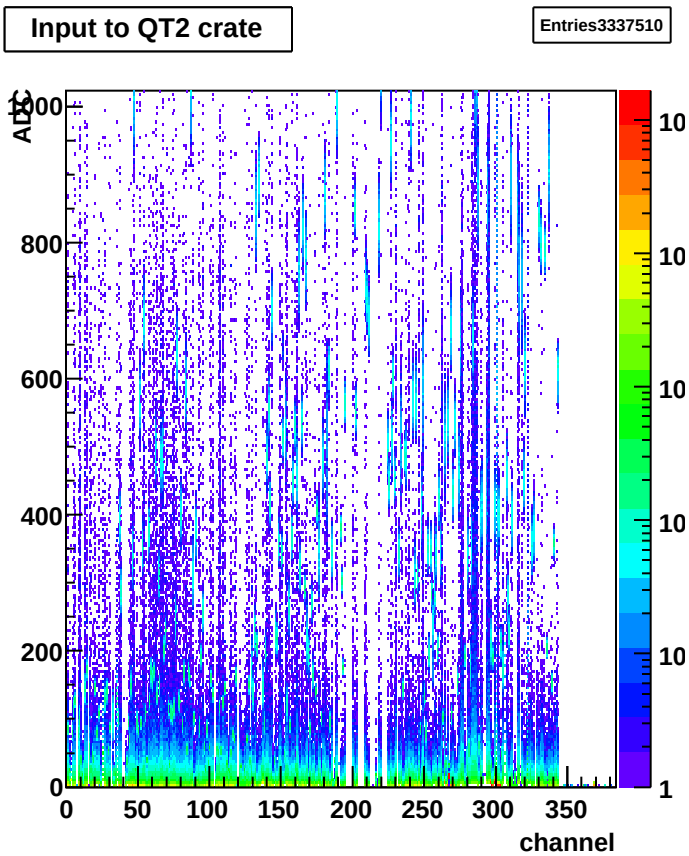

Entries 261414
BBC-L-East ADC Sum
BBC-L-West ADC Sum

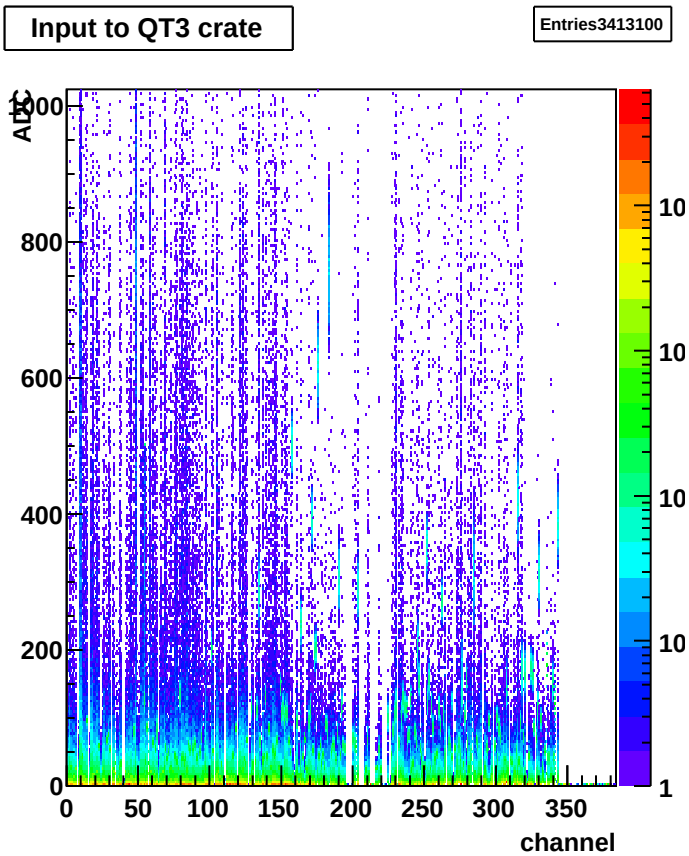

Entries 261414
BBC-S-East ADC Sum
BBC-S-West ADC Sum

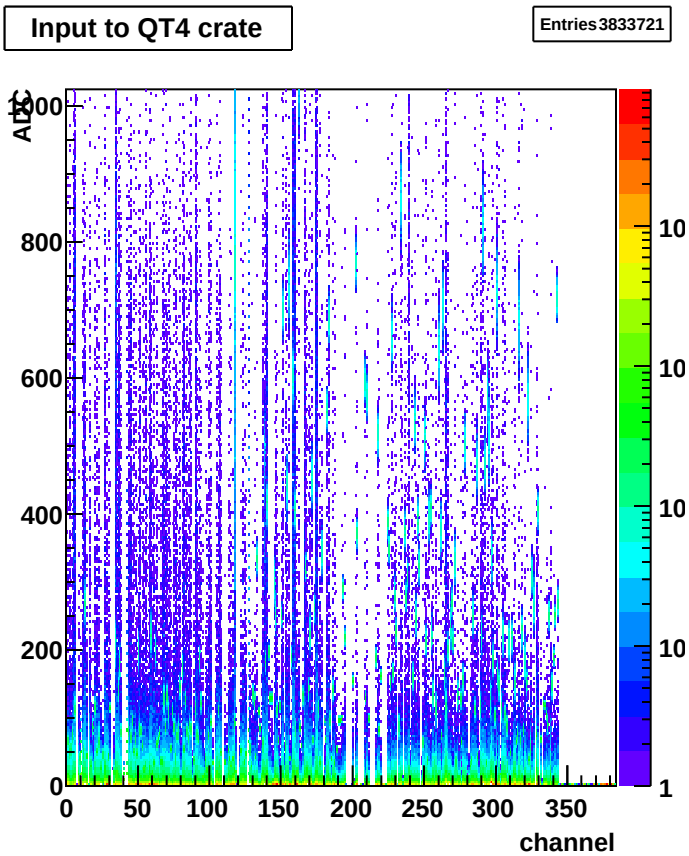

Entries 261414
ZDC East ADC Sum Att
ZDC West ADC Sum Att

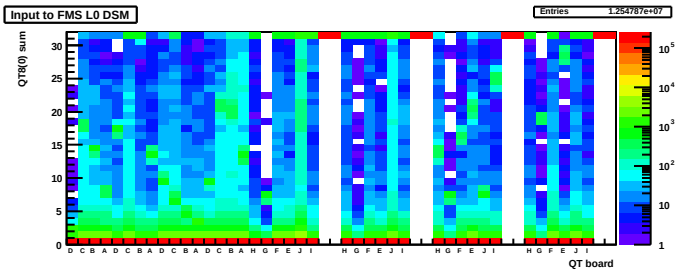

Entries 261414
BBC-L TAC Diff
BCC-S TAC Diff
Entries 261414
BBC-L TAC Diff
VPD TAC Diff
Entries 261414
BBC-L TAC Diff
ZDC TAC Diff
Entries 261414
BBC-S TAC Diff
VPD TAC Diff
Entries 261414
BBC-S TAC Diff
ZDC TAC Diff
Entries 261414
VPD TAC Diff
ZDC TAC Diff

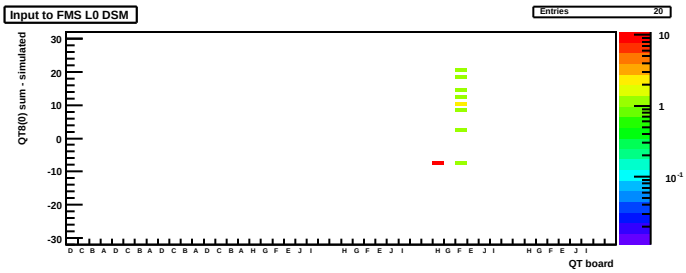

Input to QT1 crate
Entries6363583
ADC
channel

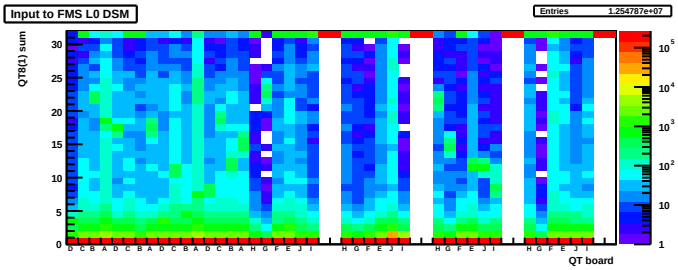

Input to QT2 crate
Entries3337510
ADC
channel

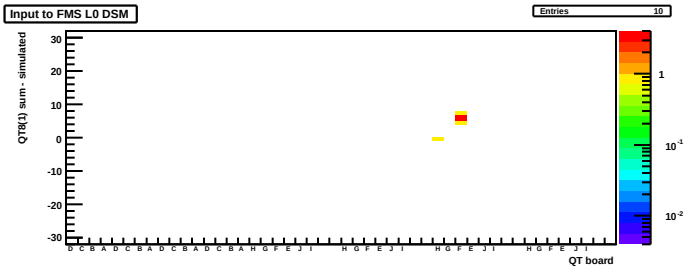

Input to QT3 crate
Entries3413100
ADC
channel

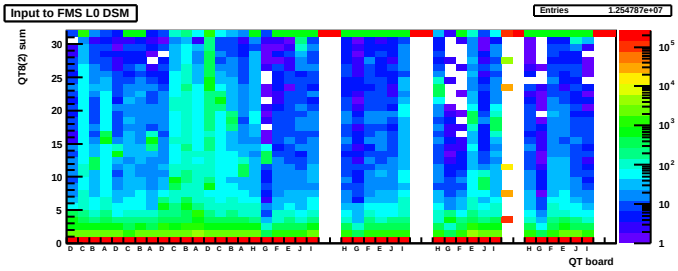

Input to QT4 crate
Entries3833721
ADC
channel

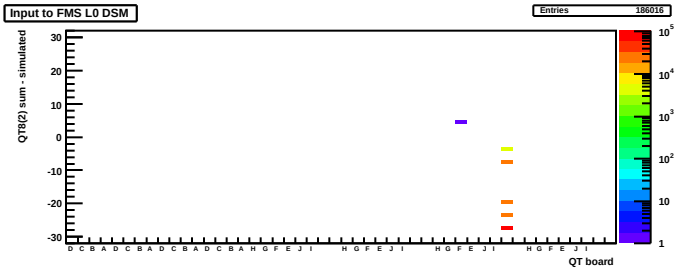

Input to FMS L0 DSM
Entries 1.254787e+07
QT8(0) sum
QT board

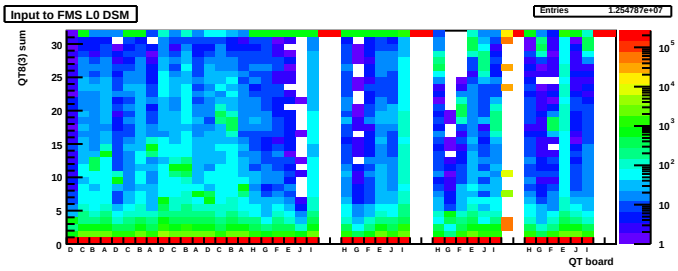

Input to FMS L0 DSM
Entries 20
QT8(0) sum - simulated
QT board

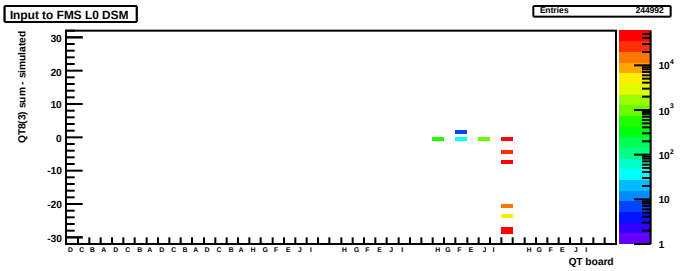

Input to FMS L0 DSM
Entries 1.254787e+07
QT8(1) sum
QT board

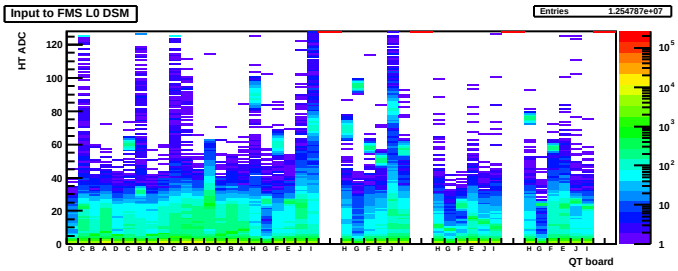

Input to FMS L0 DSM
Entries 10
QT8(1) sum - simulated
QT board

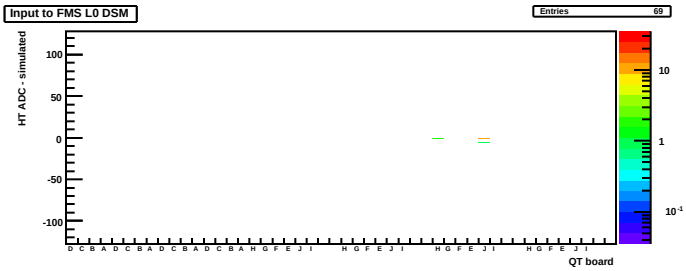

Input to FMS L0 DSM
Entries 1.254787e+07
QT8(2) sum
QT board

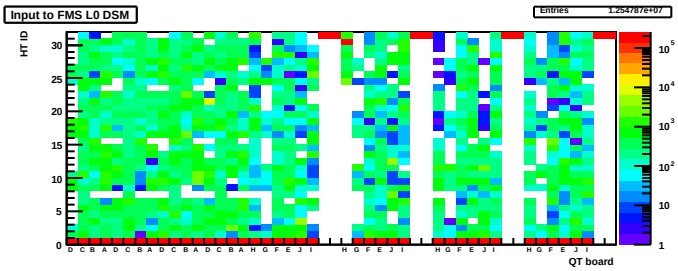

Input to FMS L0 DSM
Entries 186016
QT8(2) sum - simulated
QT board

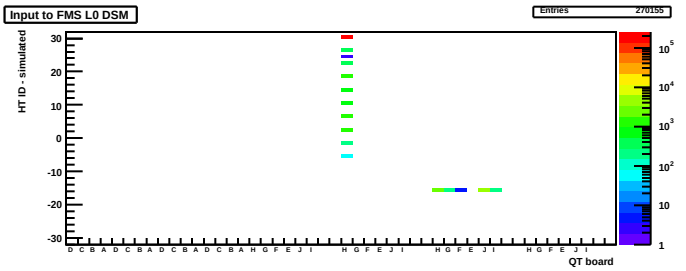

Input to FMS L0 DSM
Entries 1.254787e+07
QT8(3) sum
QT board

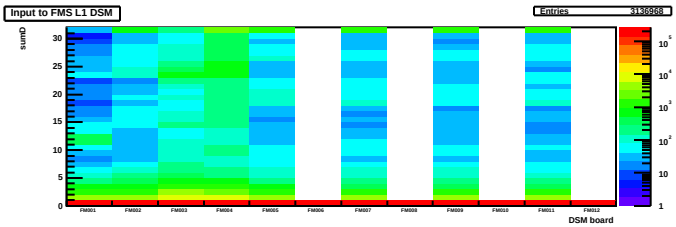

Input to FMS L0 DSM
Entries 244992
QT8(3) sum - simulated
QT board

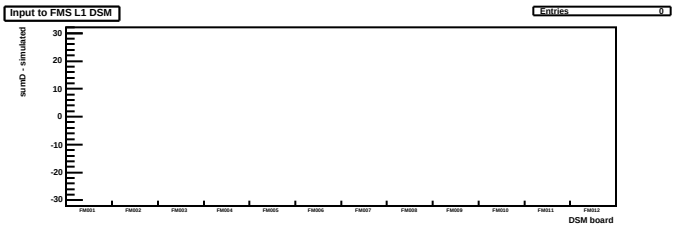

Input to FMS L0 DSM
Entries 1.254787e+07
HT ADC
QT board

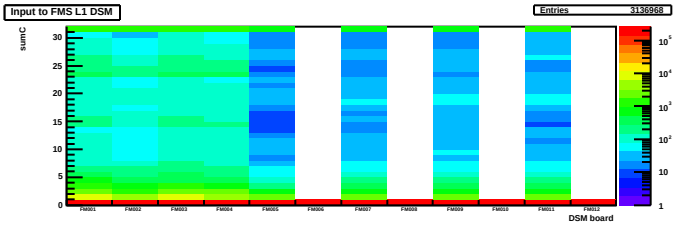

Input to FMS L0 DSM
Entries 69
HT ADC - simulated
QT board

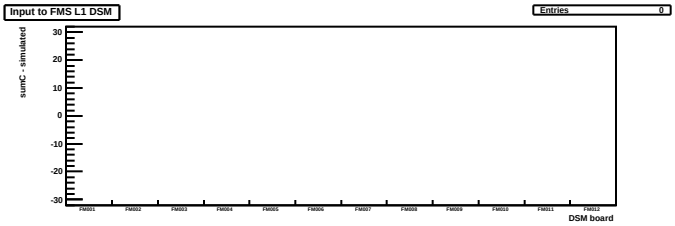

Input to FMS L0 DSM
Entries 1.254787e+07
HT ID
QT board

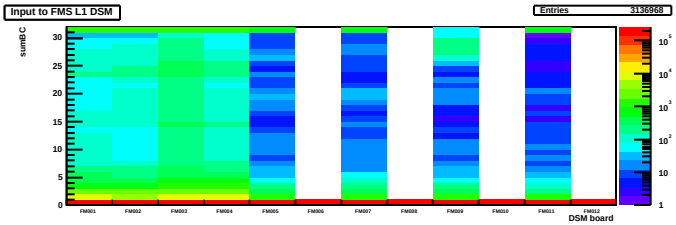

Input to FMS L0 DSM
Entries 270155
HT ID - simulated
QT board

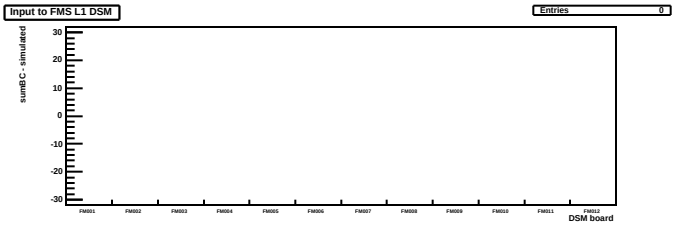

Input to FMS L1 DSM
Entries 3136968
sumD
DSM board

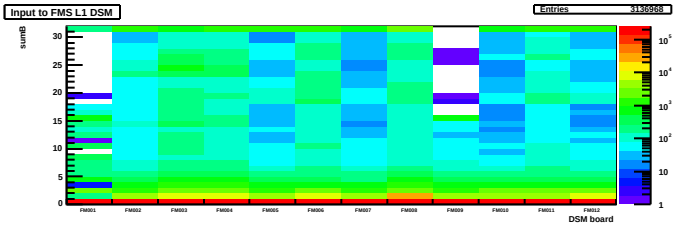

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

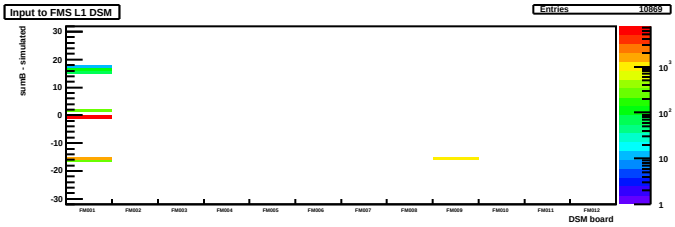

Input to FMS L1 DSM
Entries 3136968
sumC
DSM board

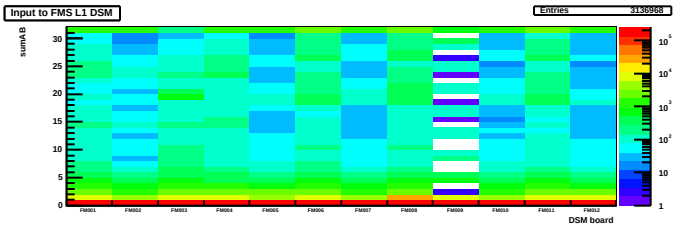

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

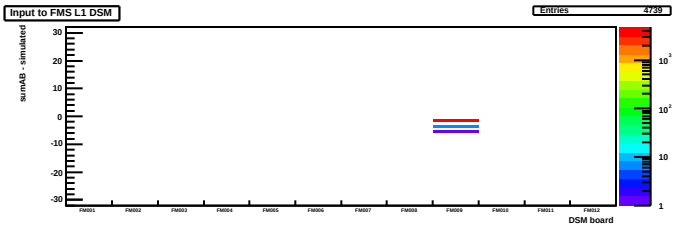

Input to FMS L1 DSM
Entries 3136968
sumBC
DSM board

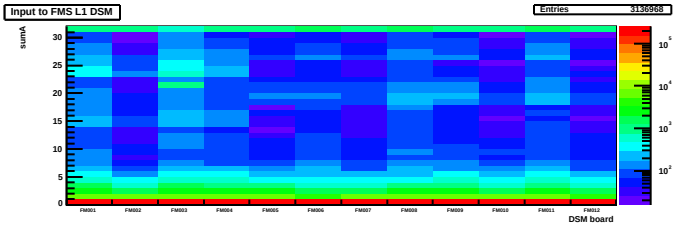

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

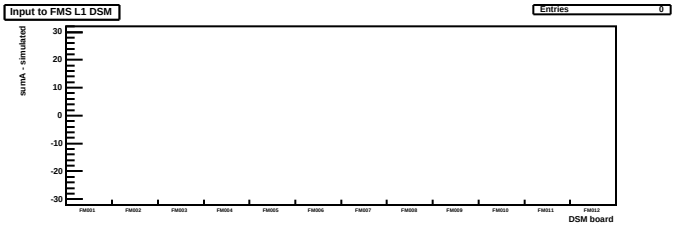

Input to FMS L1 DSM
Entries 3136968
sumB
DSM board

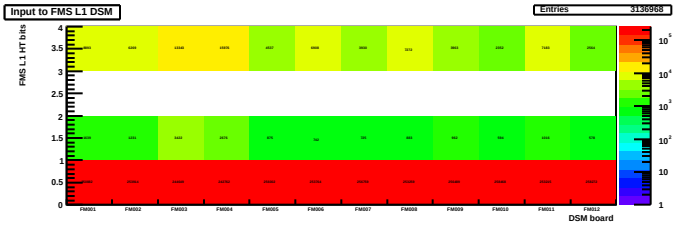

Input to FMS L1 DSM
Entries 10869
sumB - simulated
DSM board

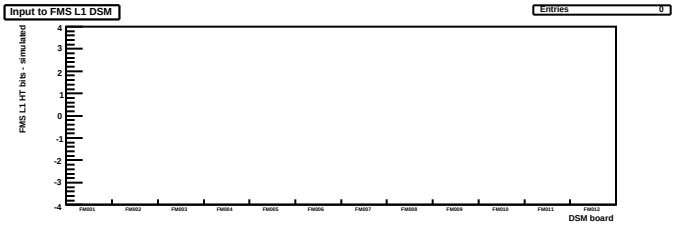

Input to FMS L1 DSM
Entries 3136968
sumAB
DSM board

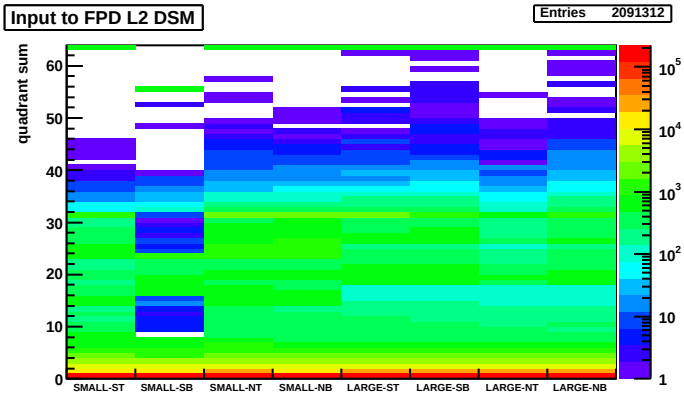

Input to FMS L1 DSM
Entries 4739
sumAB - simulated
DSM board

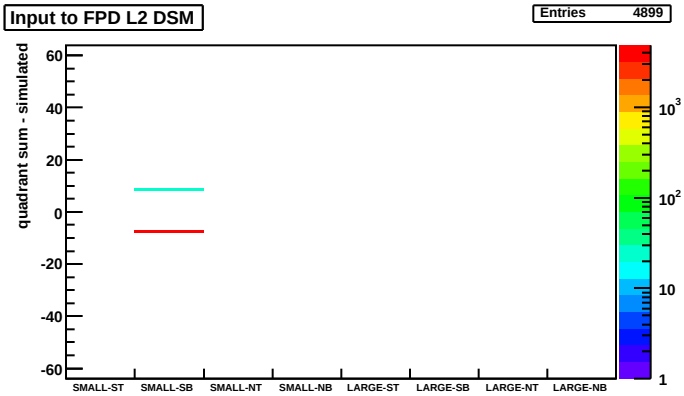

Input to FMS L1 DSM
Entries 3136968
sumA
DSM board

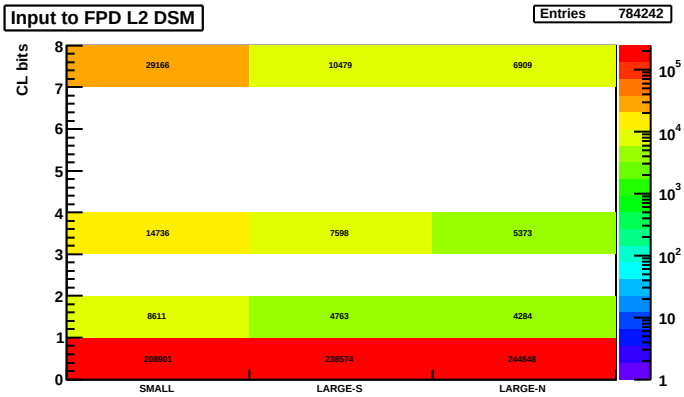

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

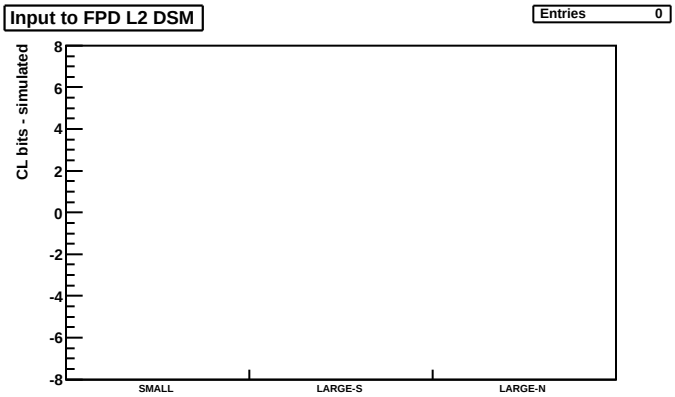

Input to FMS L1 DSM
Entries 3136968
FMS L1 HT bits
DSM board

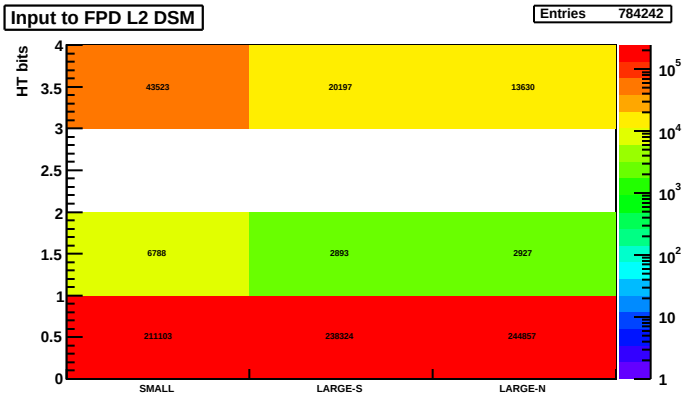

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

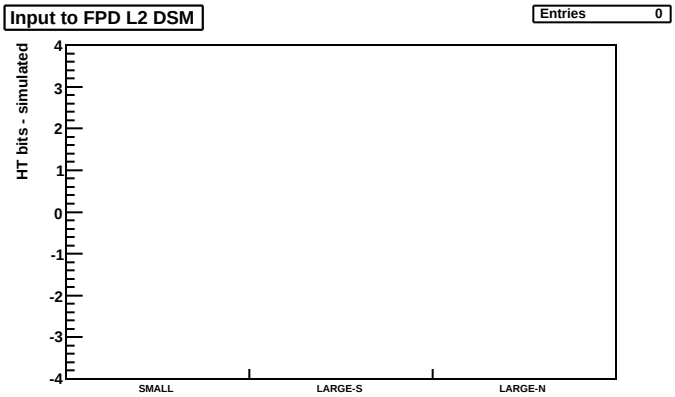

Input to FPD L2 DSM
Entries 2091312
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

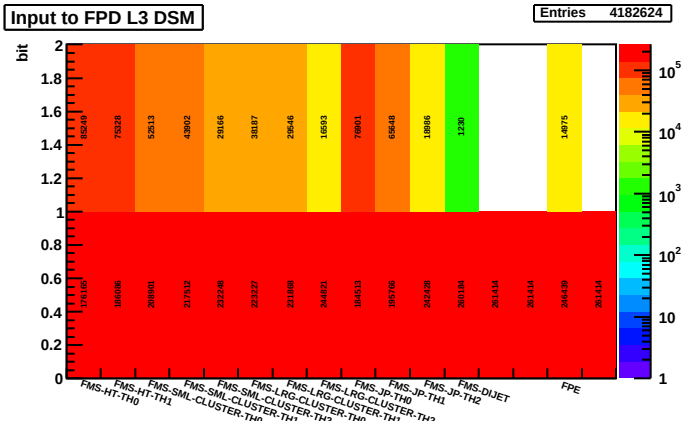

Input to FPD L2 DSM
Entries 4899
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

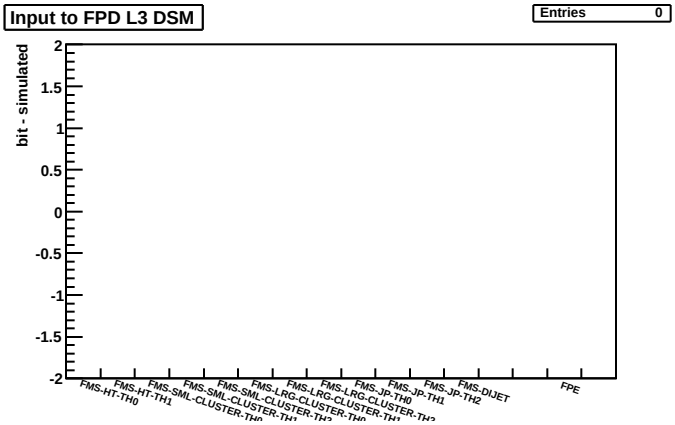

Input to FPD L2 DSM
Entries 784242
CL bits
29166
10479
6909
14736
7598
5373
8611
4763
4284
208901
238574
244848
SMALL
LARGE-S
LARGE-N

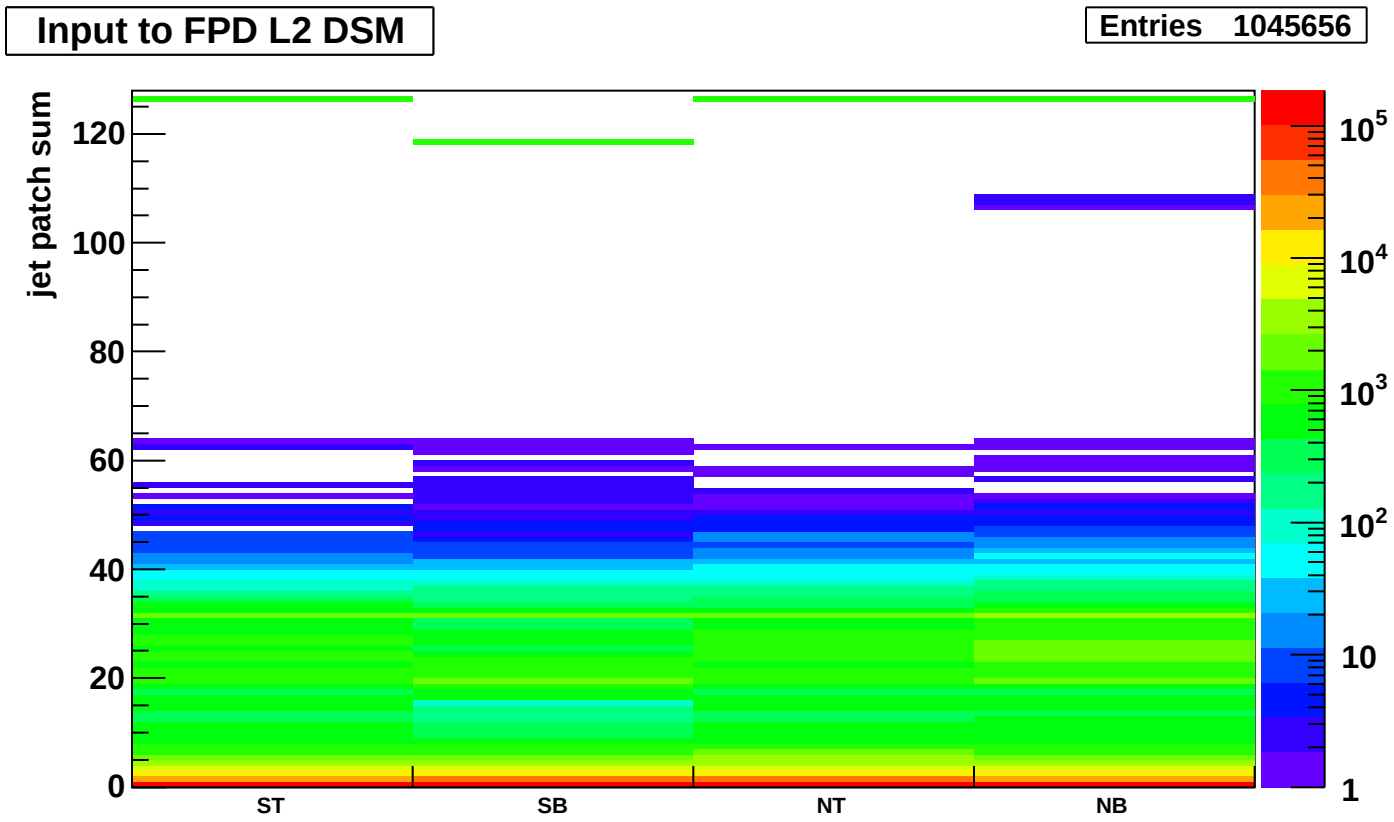

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

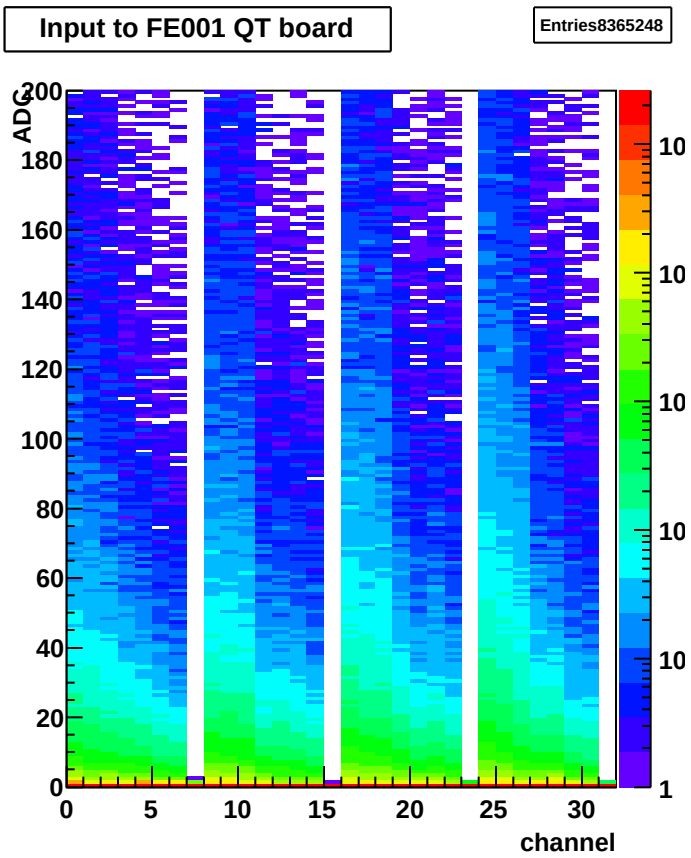

Input to FPD L2 DSM
Entries 784242
HT bits
43523
20197
13630
6788
2893
2927
211103
238324
244857
SMALL
LARGE-S
LARGE-N

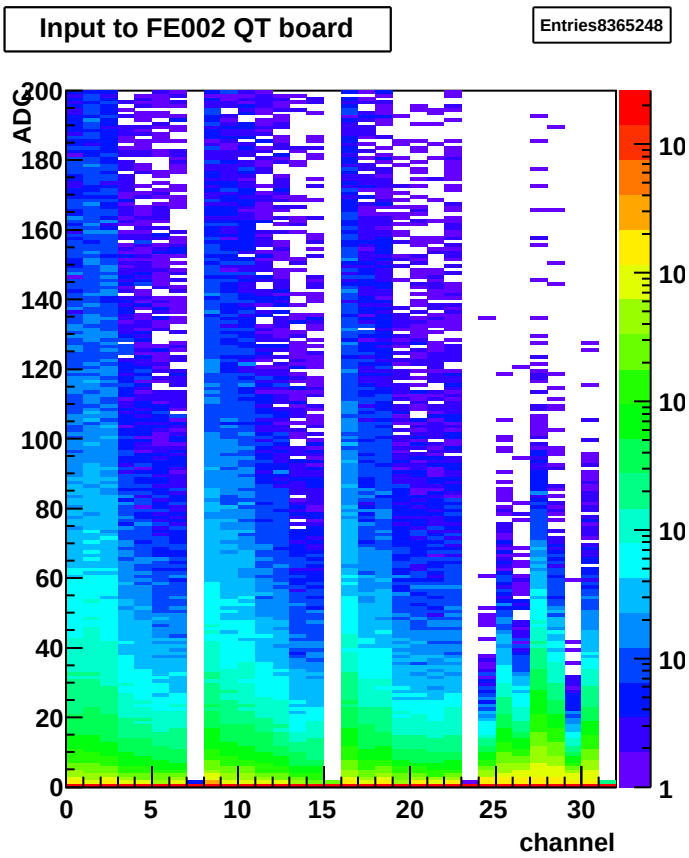

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

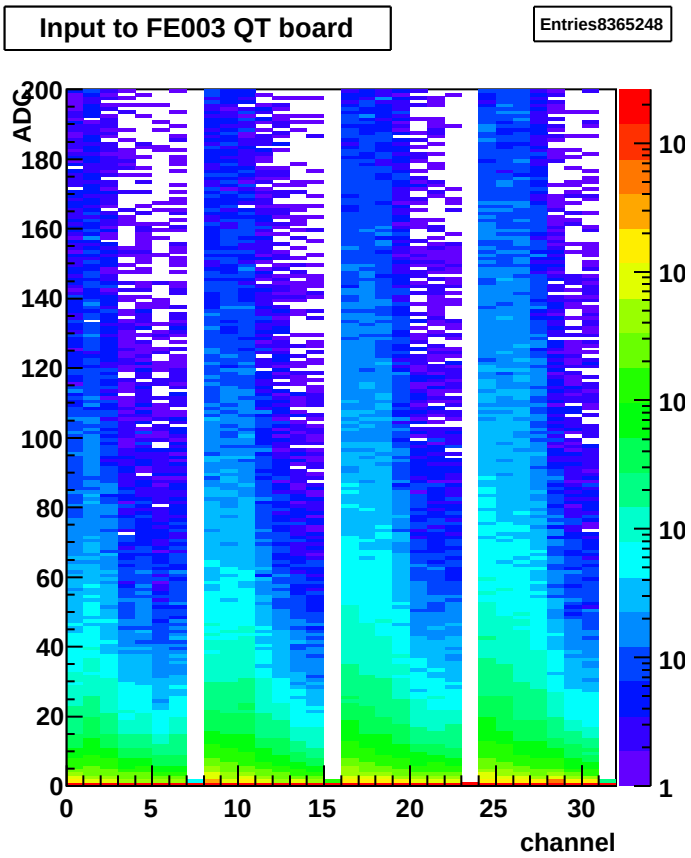

Input to FPD L3 DSM
Entries 4182624
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

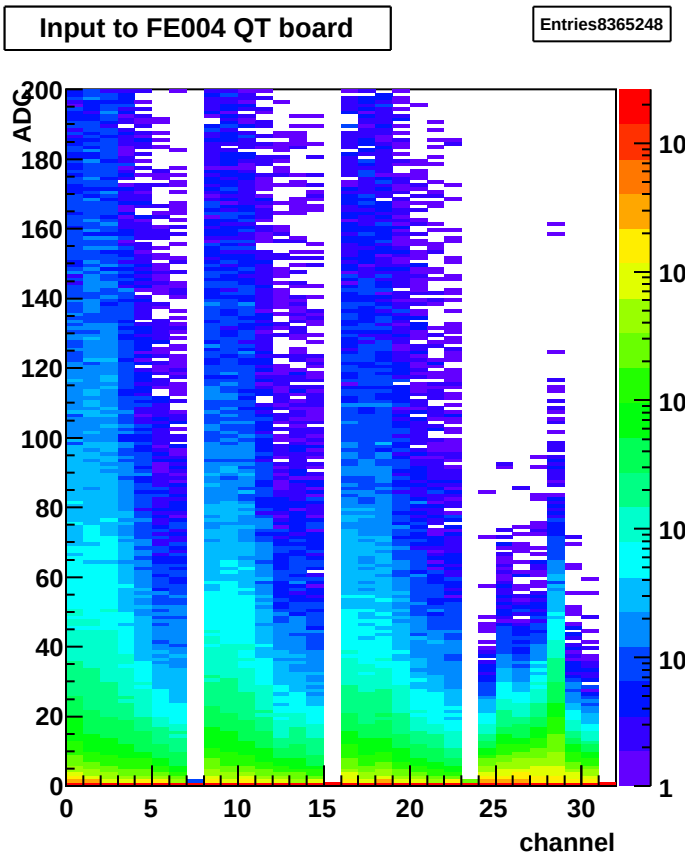

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

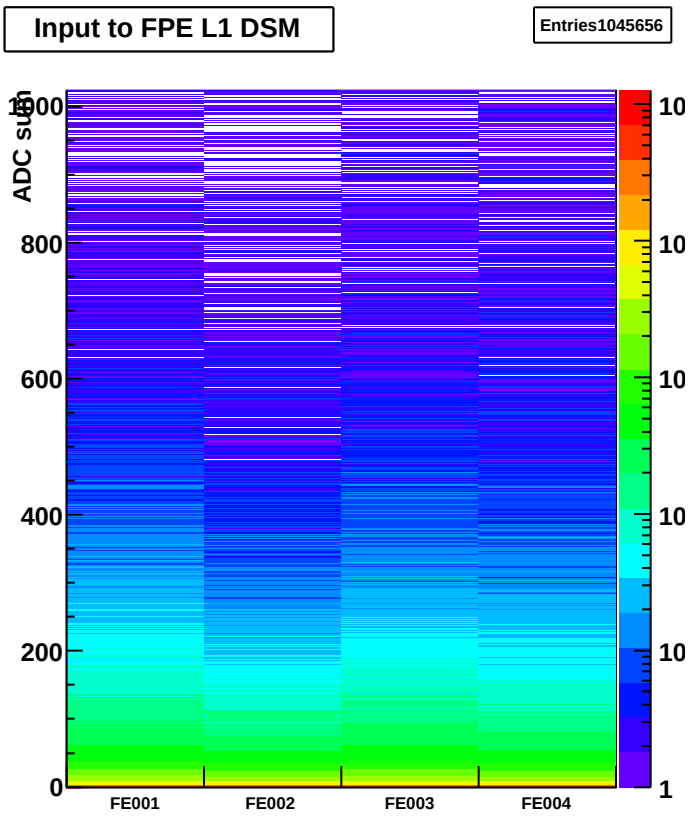

Input to FPD L2 DSM
Entries 1045656
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

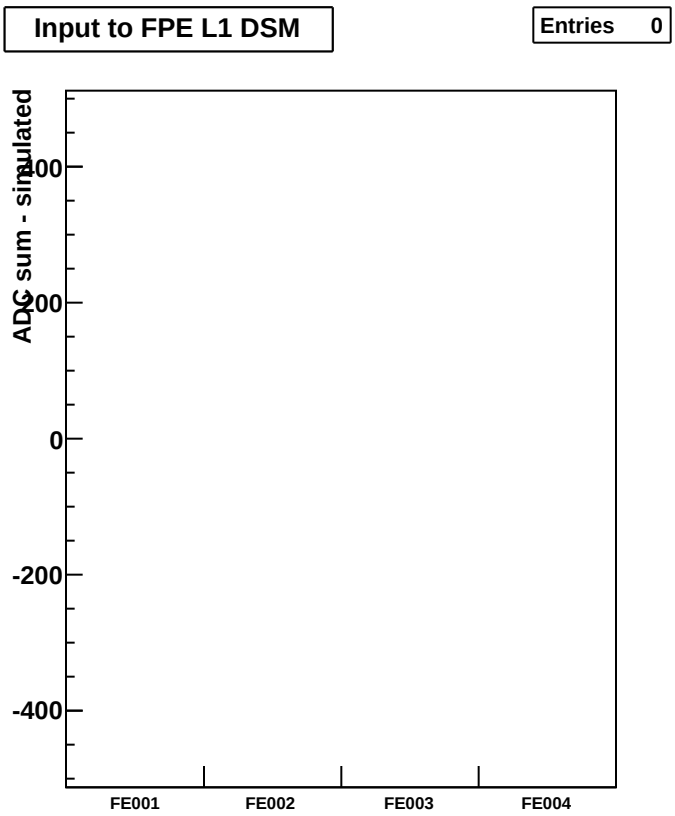

Input to FE001 QT board
Entries8365248
ADC
channel

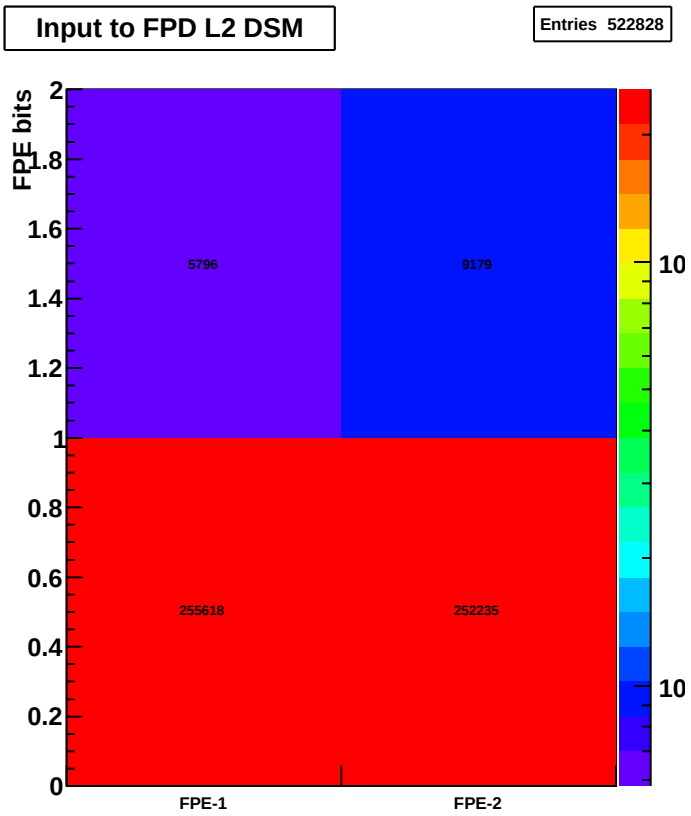

Input to FE002 QT board
Entries8365248
ADC
channel

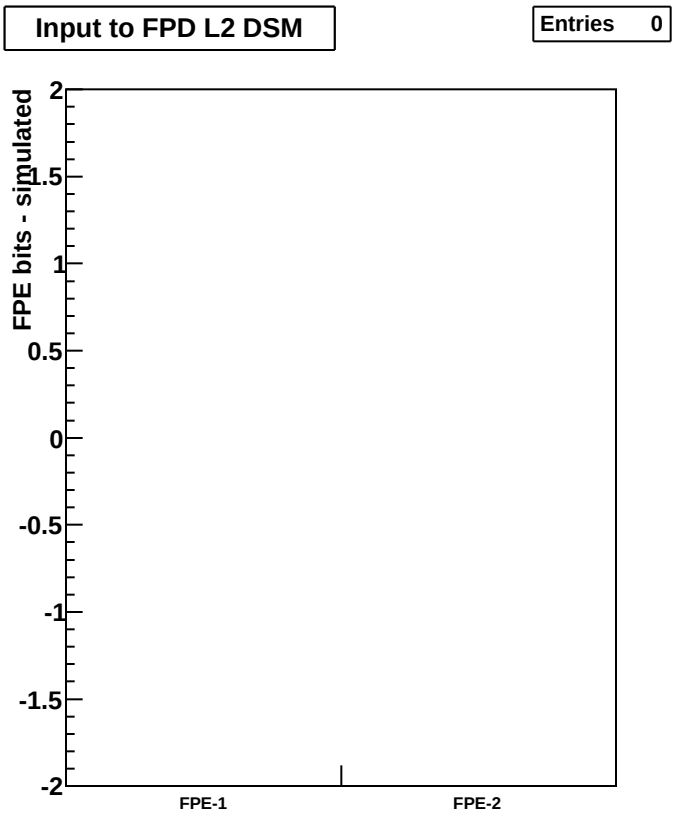

Input to FE003 QT board
Entries8365248
ADC
channel


Input to FE004 QT board
Entries8365248
ADC
channel


Input to FPE L1 DSM
Entries1045656
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 522828
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
5796
9179
255618
252235
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2