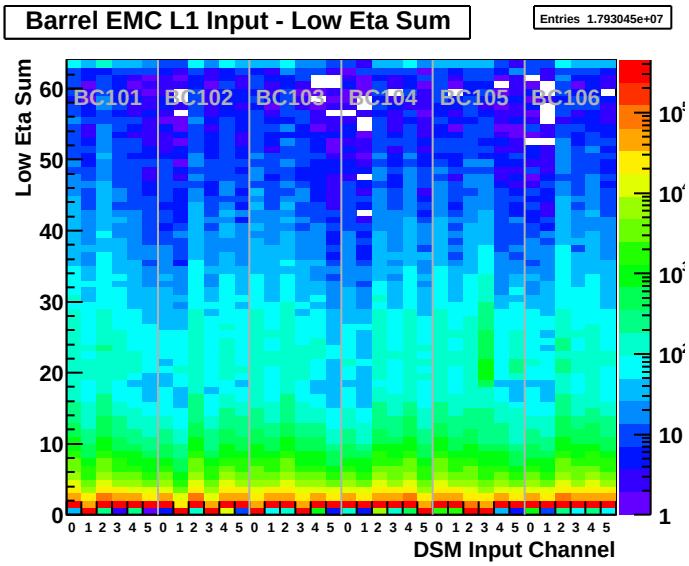
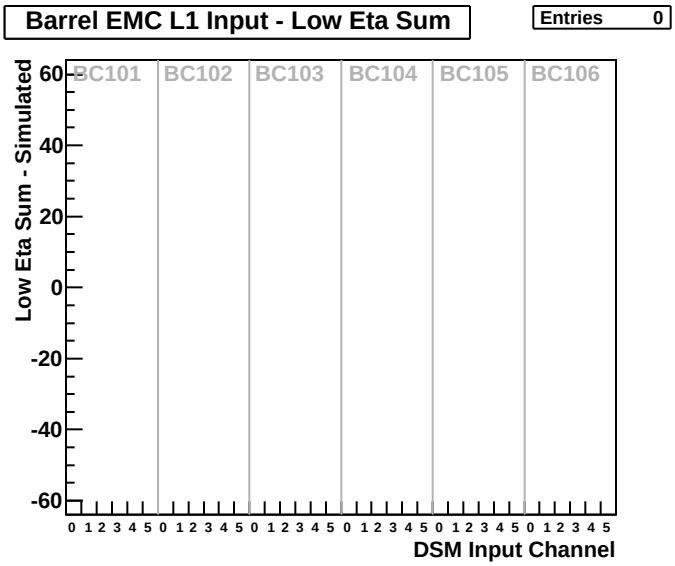
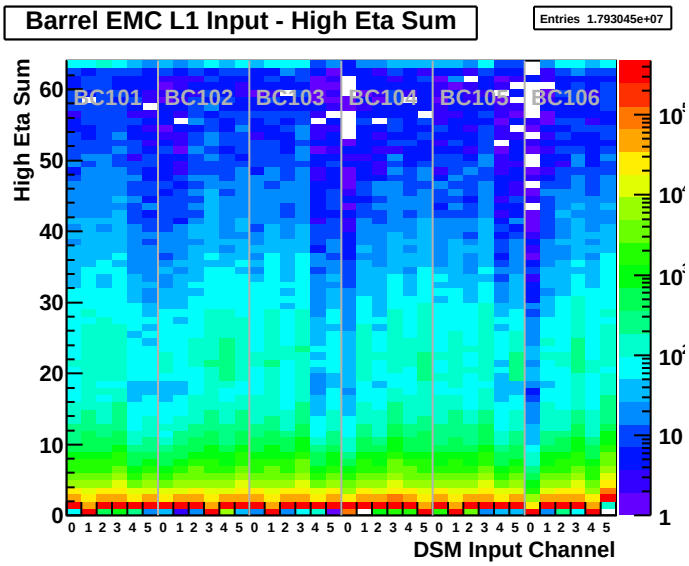
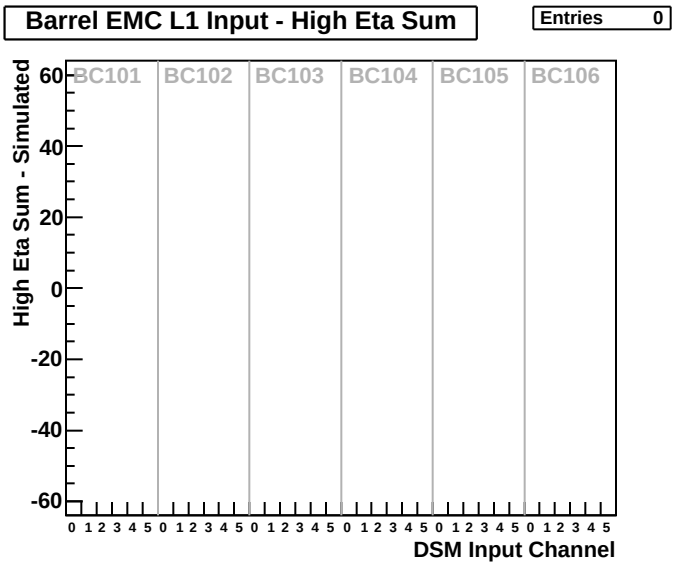
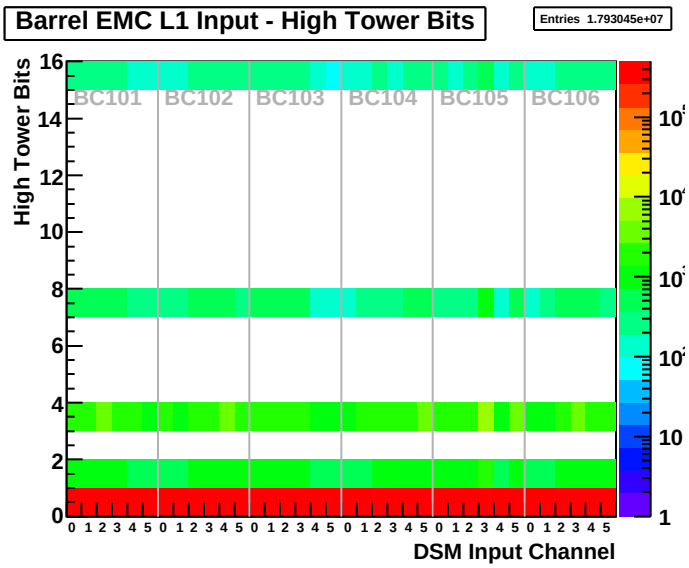
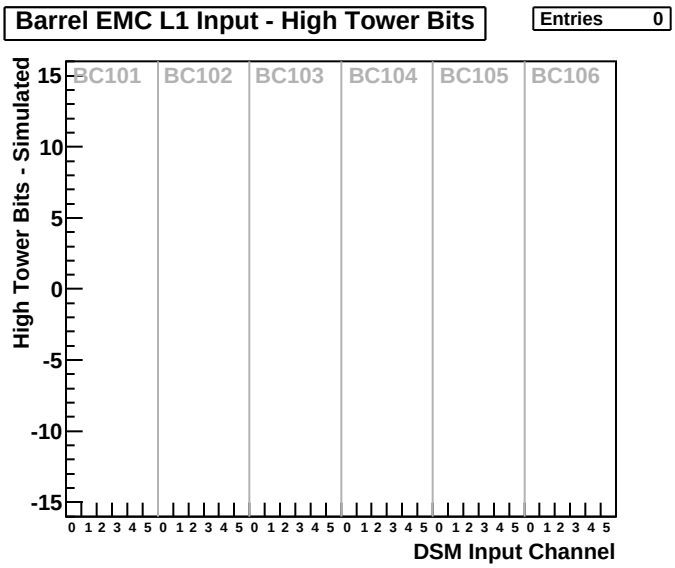

Barrel EMC L1 Input - Low Eta Sum
Entries 1.793045e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

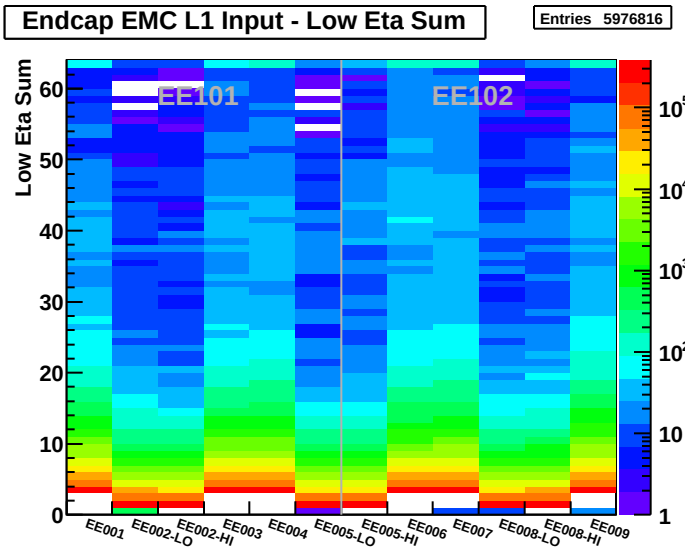

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

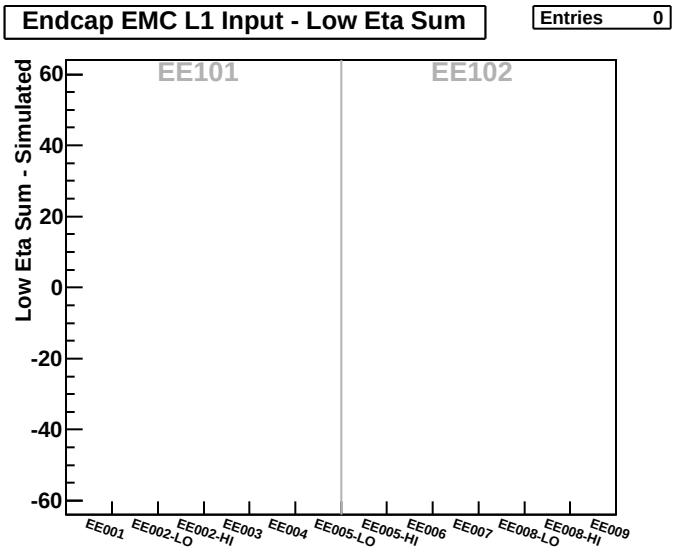

Barrel EMC L1 Input - High Eta Sum
Entries 1.793045e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

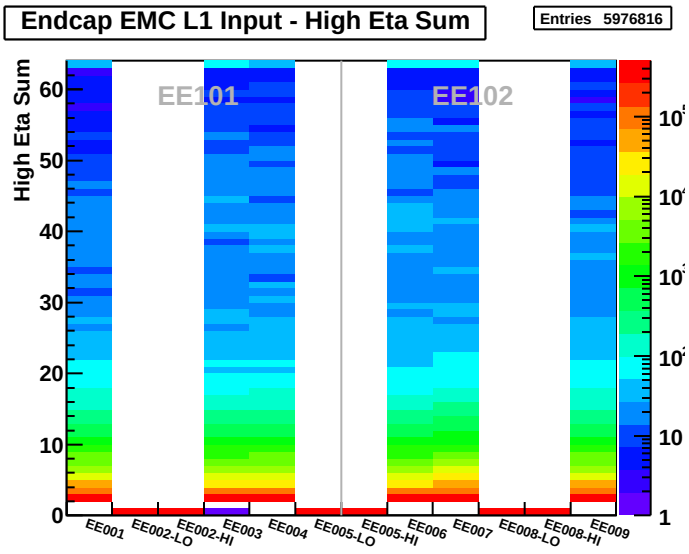

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

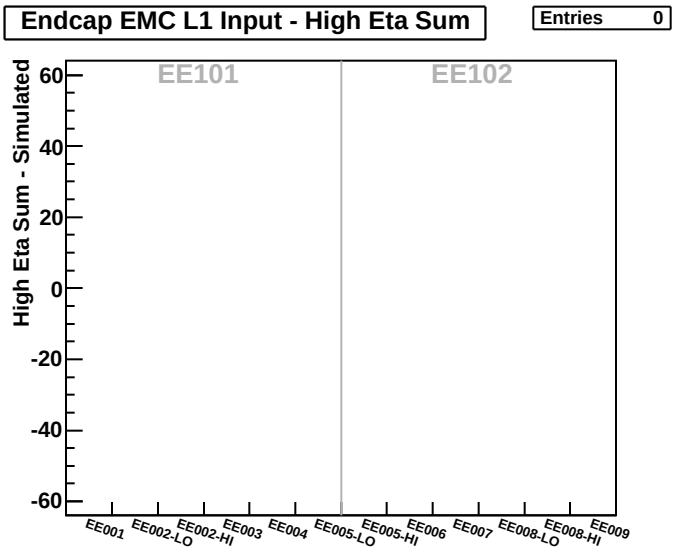

Barrel EMC L1 Input - High Tower Bits
Entries 1.793045e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

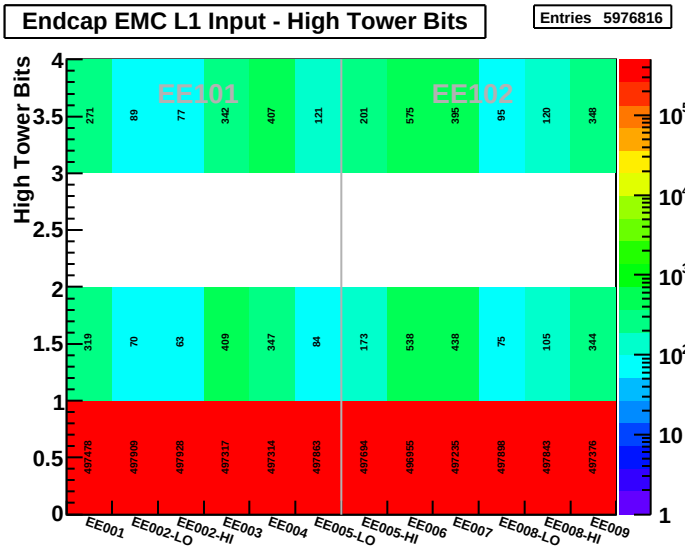

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

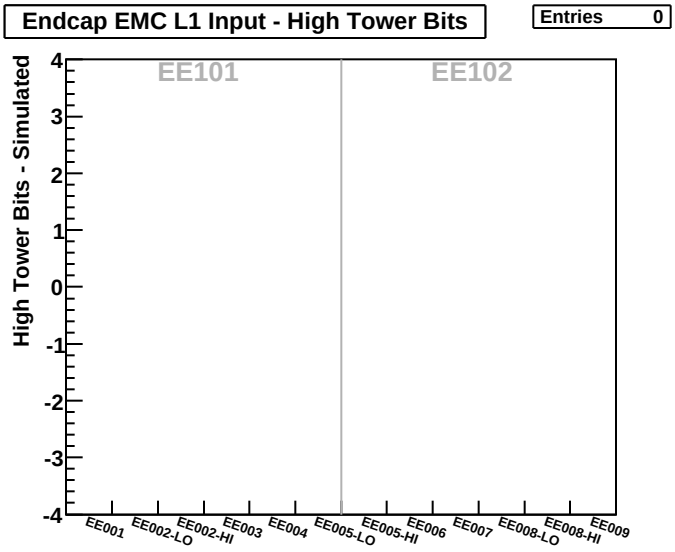

Endcap EMC L1 Input - Low Eta Sum
Entries 5976816
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

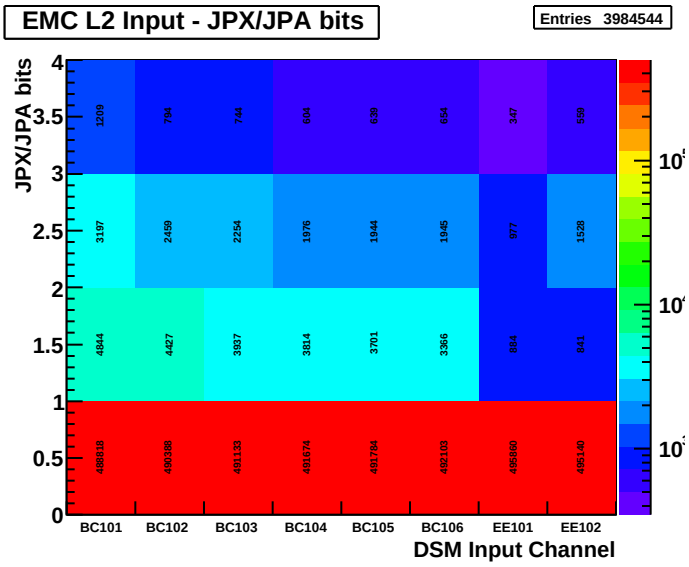

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

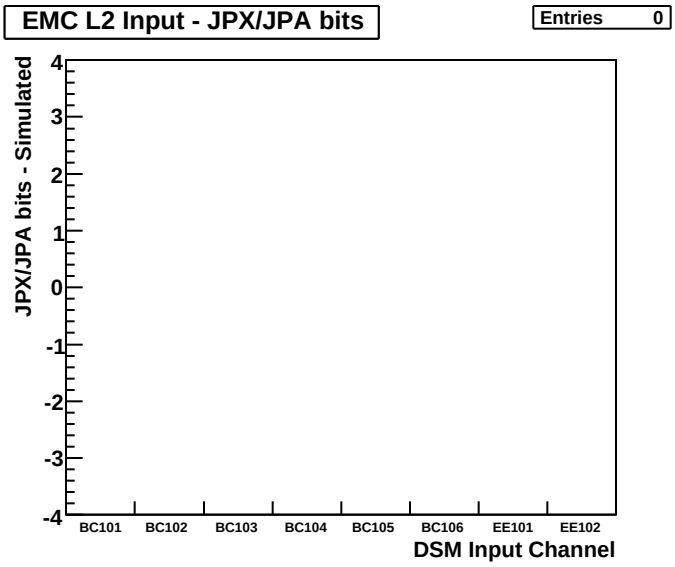

Endcap EMC L1 Input - High Eta Sum
Entries 5976816
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

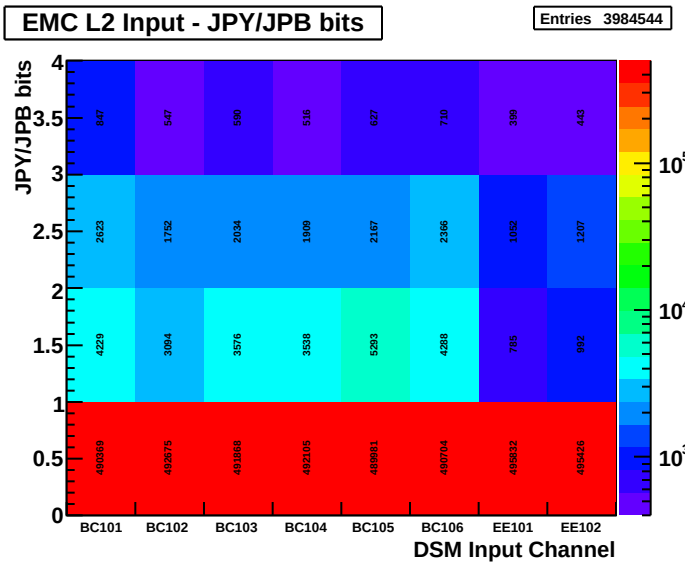

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

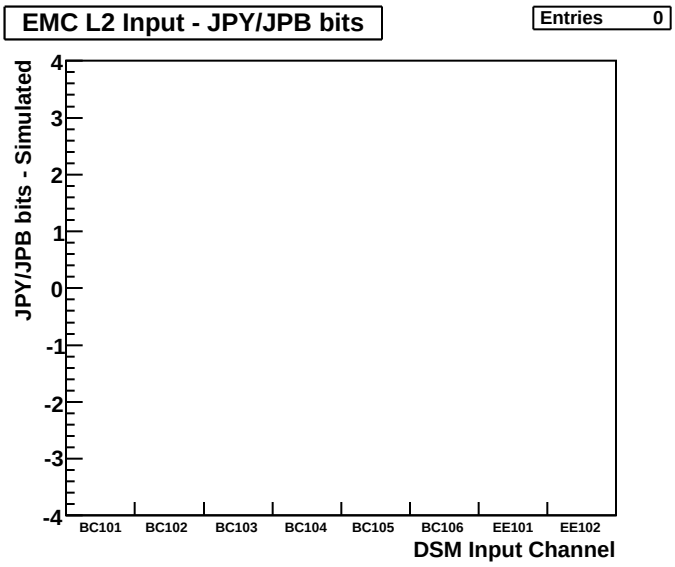

Endcap EMC L1 Input - High Tower Bits
Entries 5976816
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

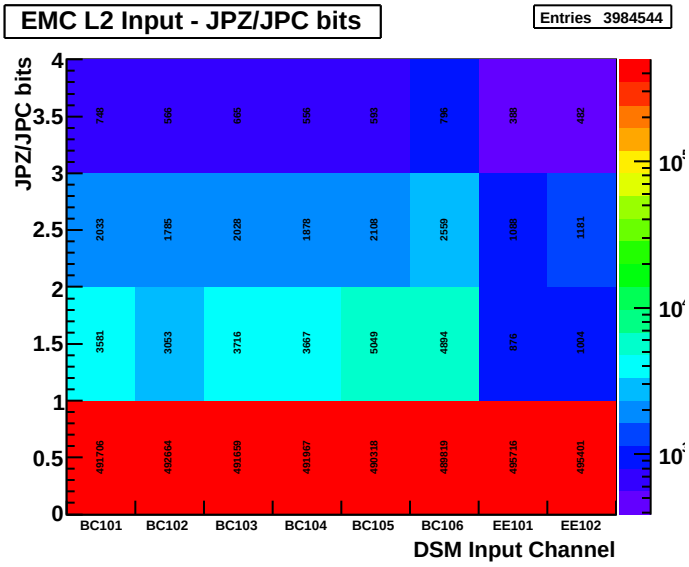

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

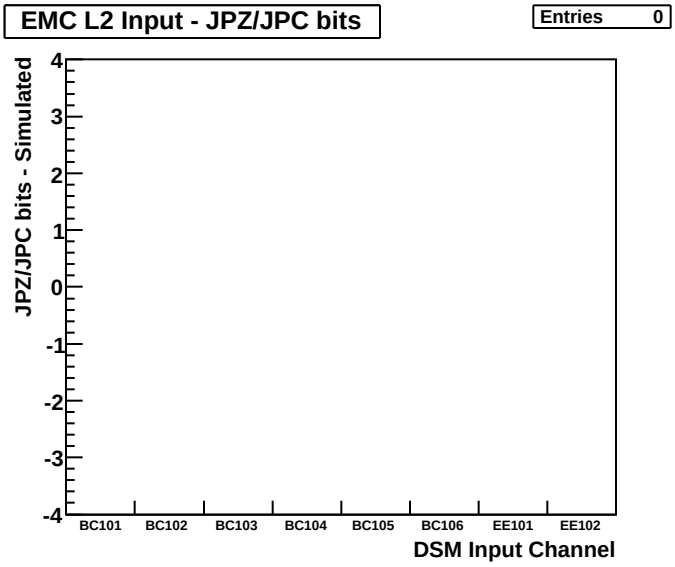

EMC L2 Input - JPX/JPA bits
Entries 3984544
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1209 794 744 604 639 654 347 559
3197 2459 2254 1976 1944 1945 977 1528
4844 4427 3937 3814 3701 3366 884 841
488818 490388 491123 491674 491784 492103 495860 495140

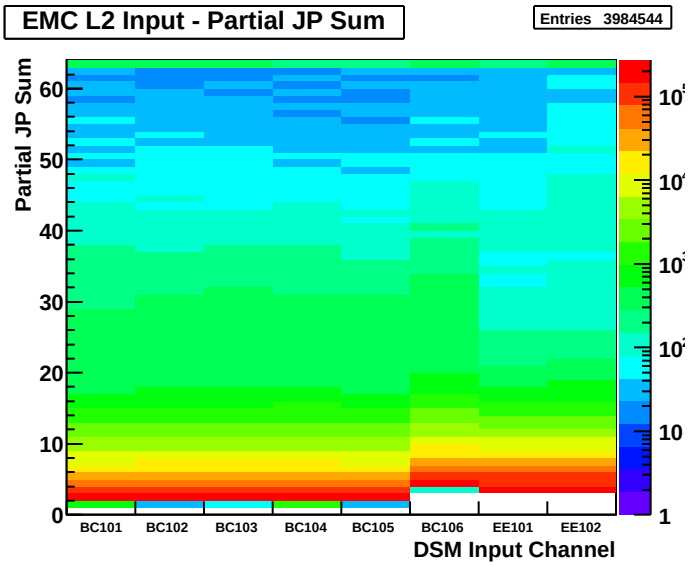

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

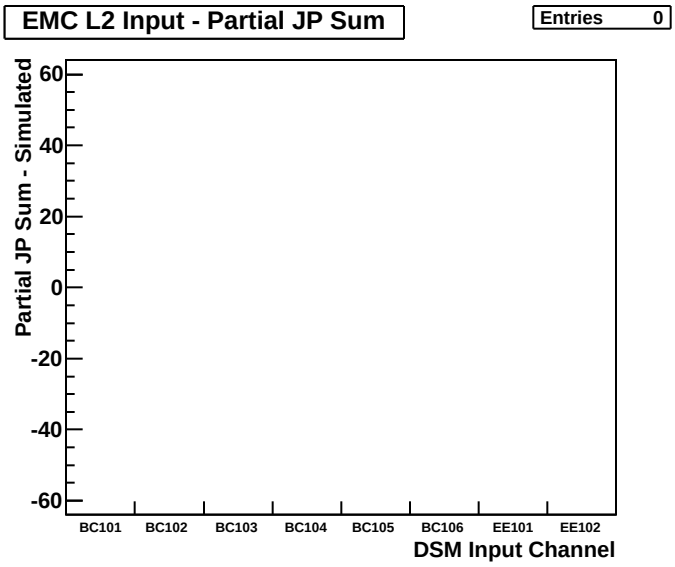

EMC L2 Input - JPY/JPB bits
Entries 3984544
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
847 547 590 516 627 710 399 443
2623 1752 2034 1909 2167 2366 1052 1207
4229 3094 3576 3538 5293 4288 785 992
490369 492375 491868 492105 489981 490704 495832 495426

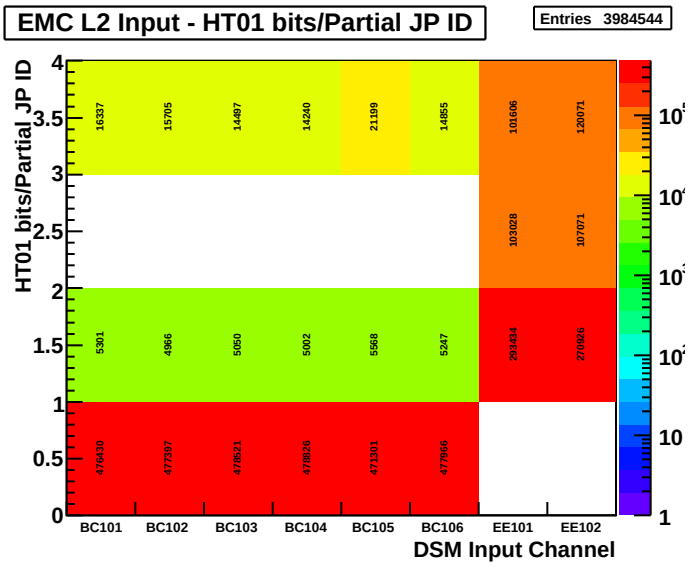

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

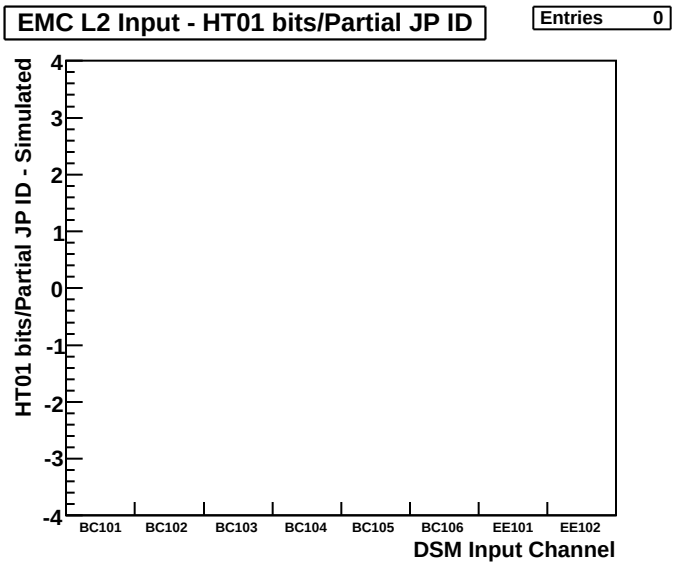

EMC L2 Input - JPZ/JPC bits
Entries 3984544
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
748 566 665 556 593 796 388 482
2033 1785 2028 1878 2108 2559 1088 1181
3581 3053 3716 3667 5049 4894 876 1004
491706 492564 491659 491967 490318 489819 495716 495401

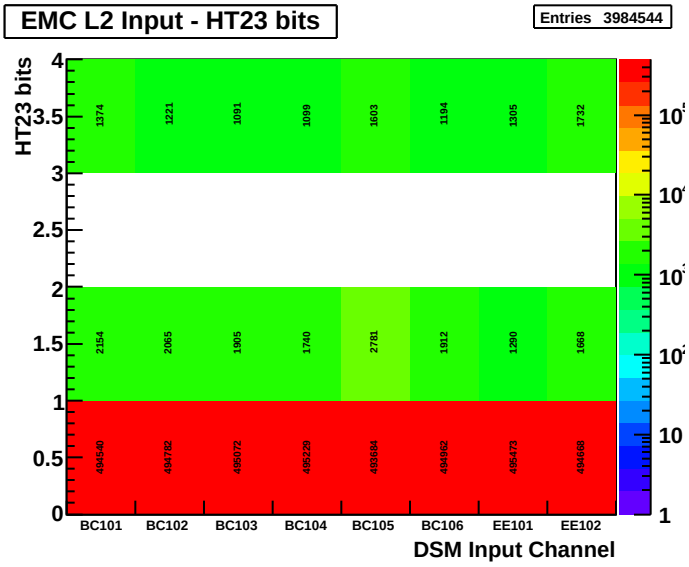

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

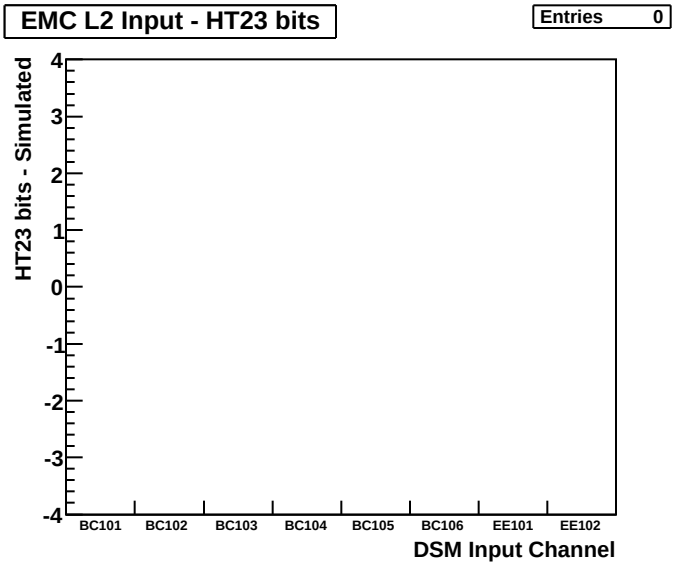

EMC L2 Input - Partial JP Sum
Entries 3984544
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

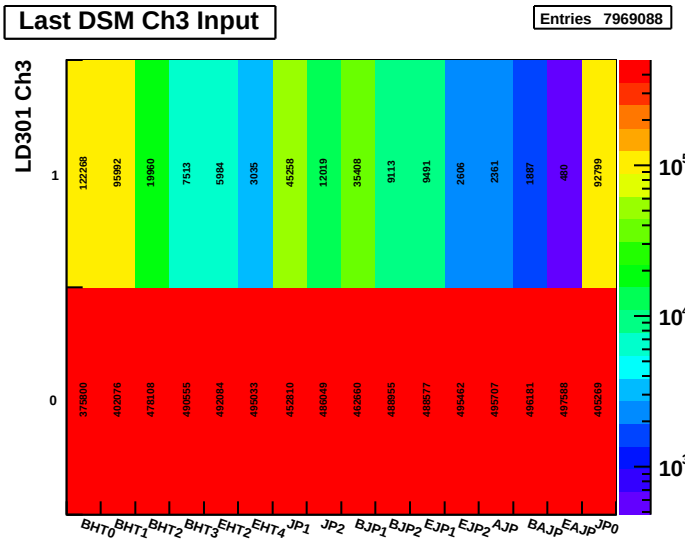

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

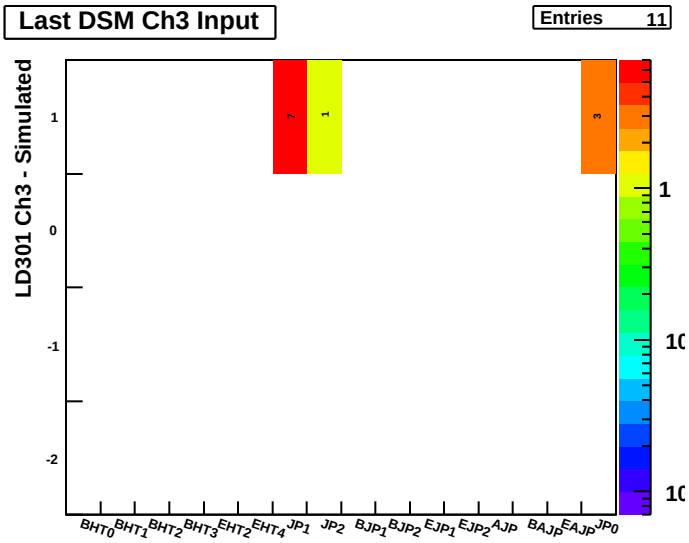

EMC L2 Input - HT01 bits/Partial JP ID
Entries 3984544
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

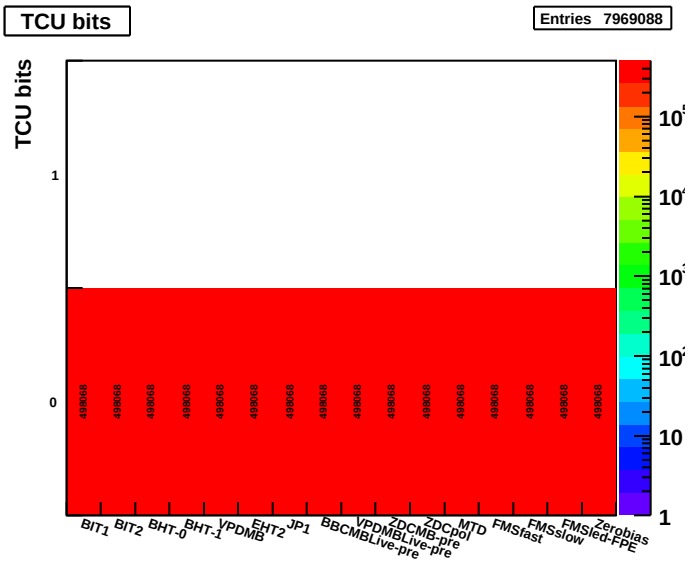

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

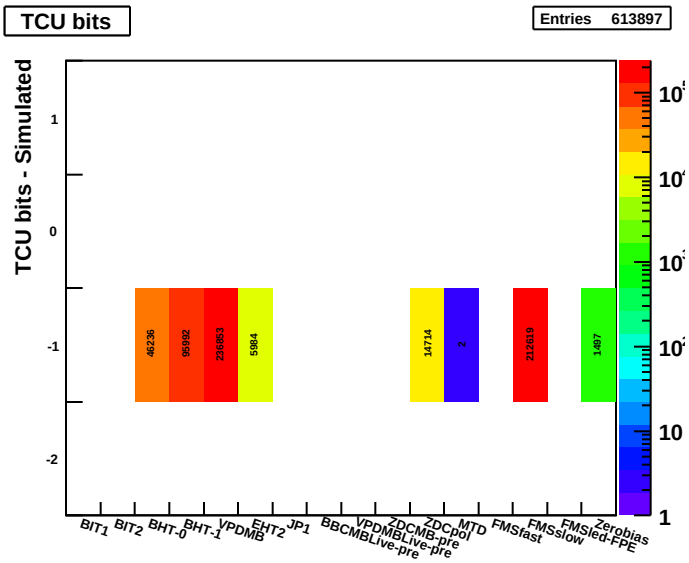

EMC L2 Input - HT23 bits
Entries 3984544
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

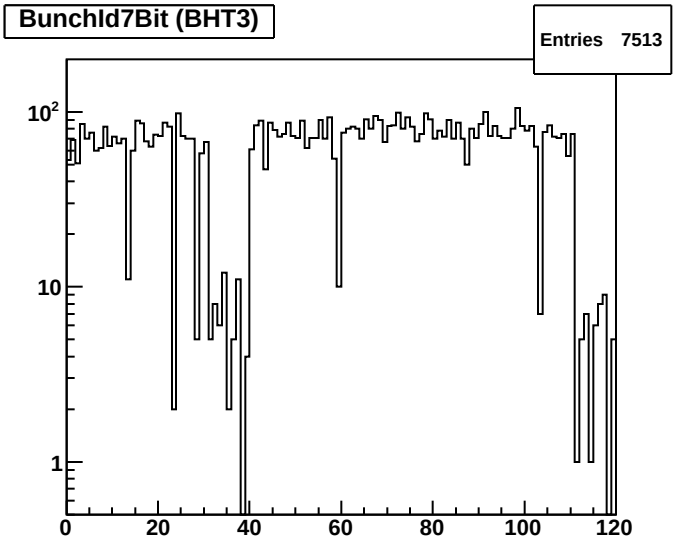

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

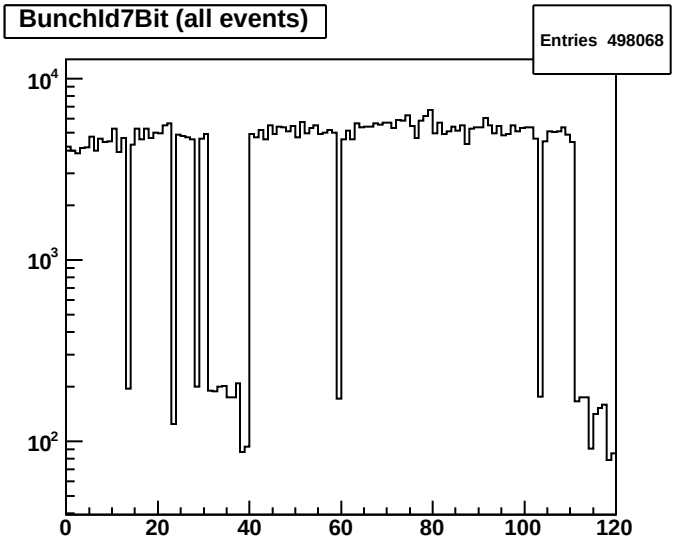

Last DSM Ch3 Input
Entries 7969088
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

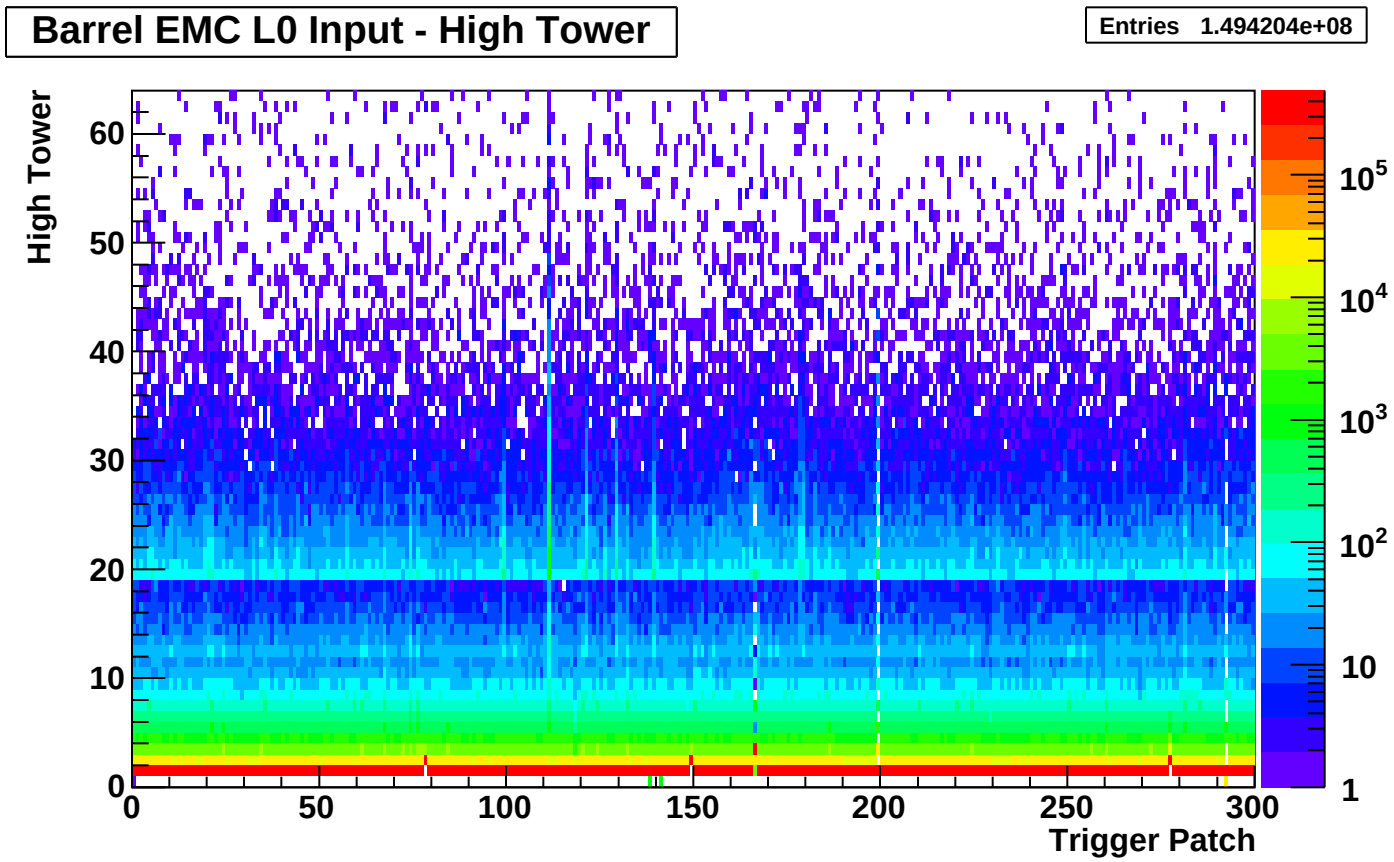

Last DSM Ch3 Input
Entries 11
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

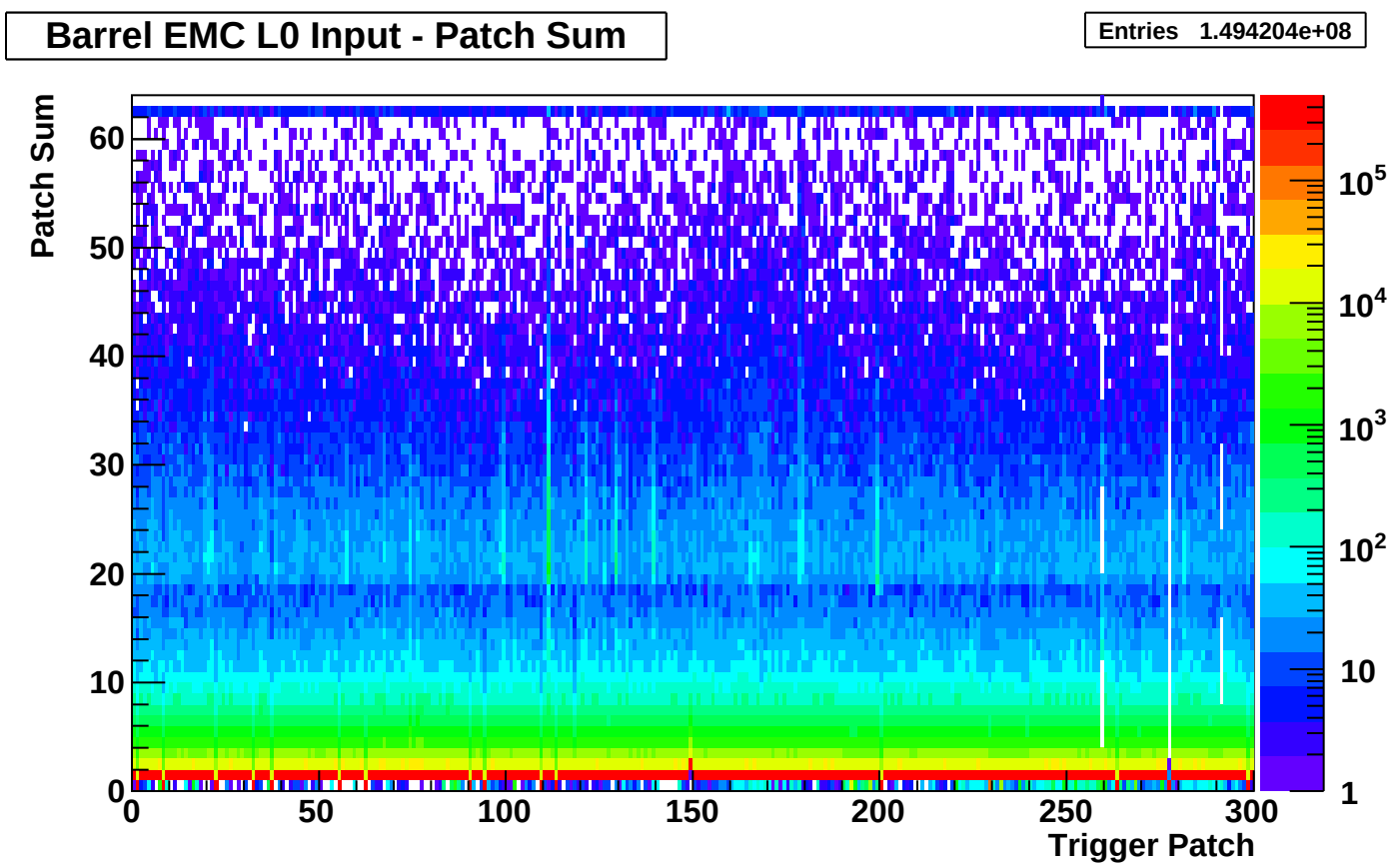

TCU bits
Entries 7969088
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

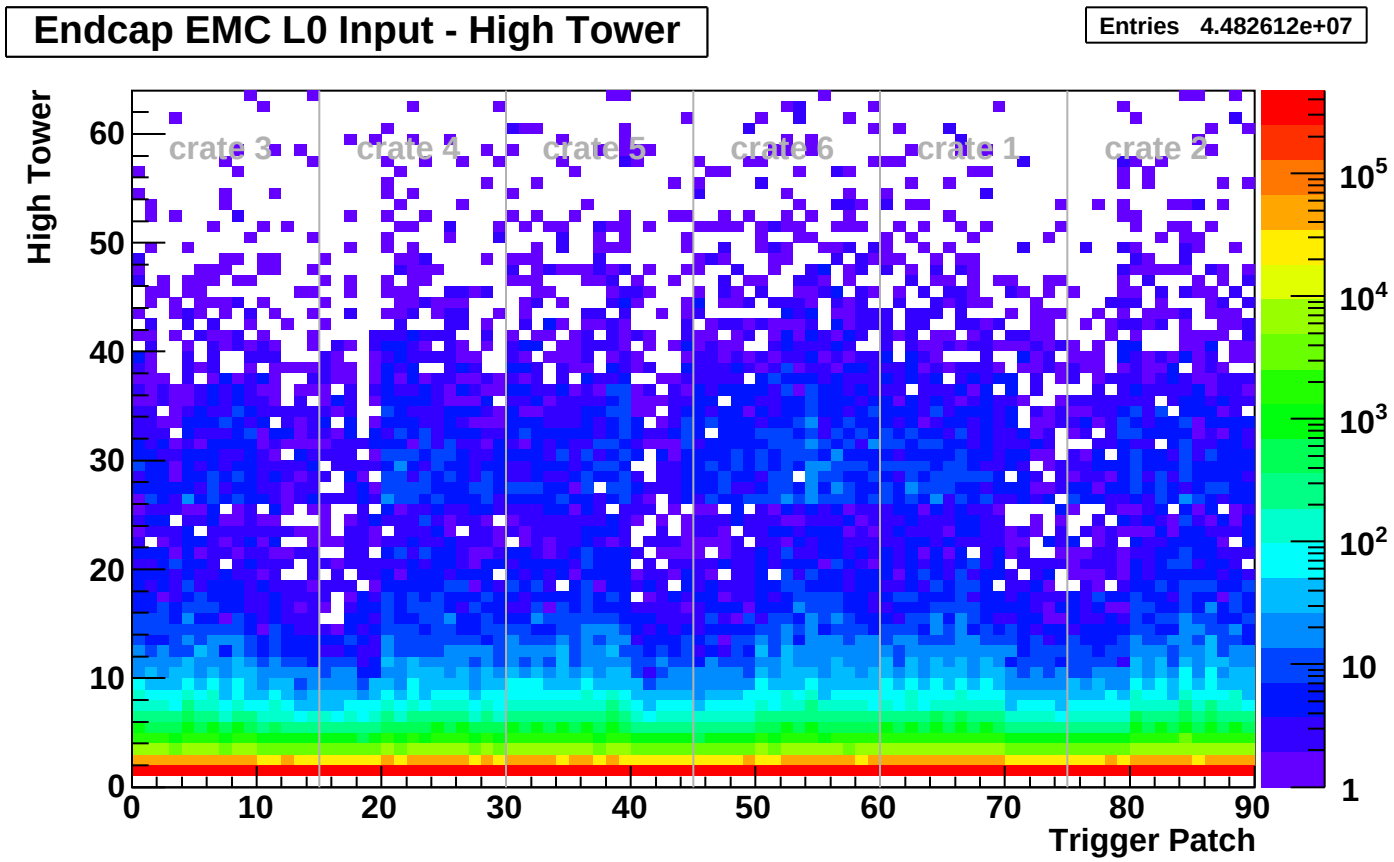

TCU bits
Entries 613897
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

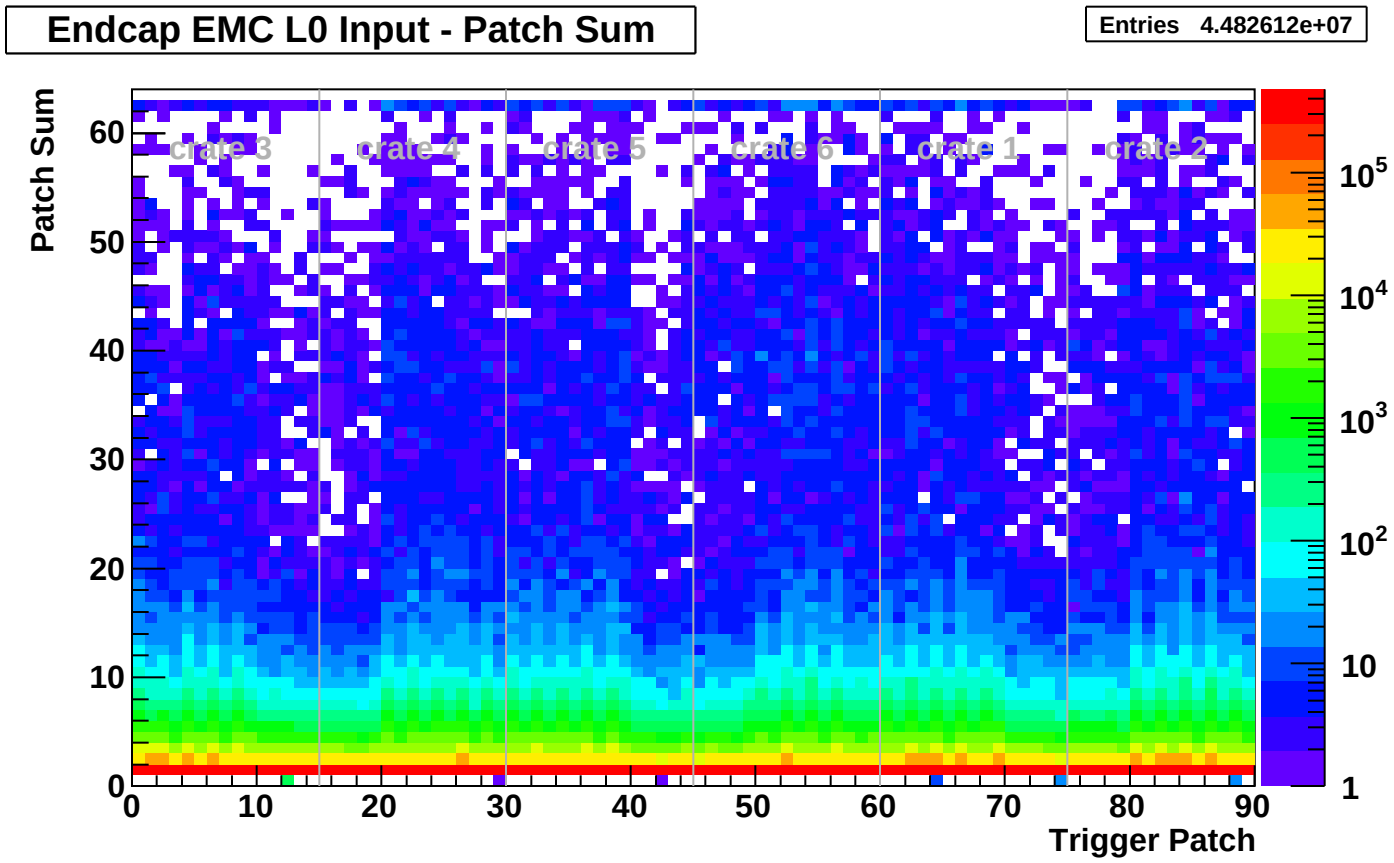

BunchId7Bit (BHT3)
Entries 7513

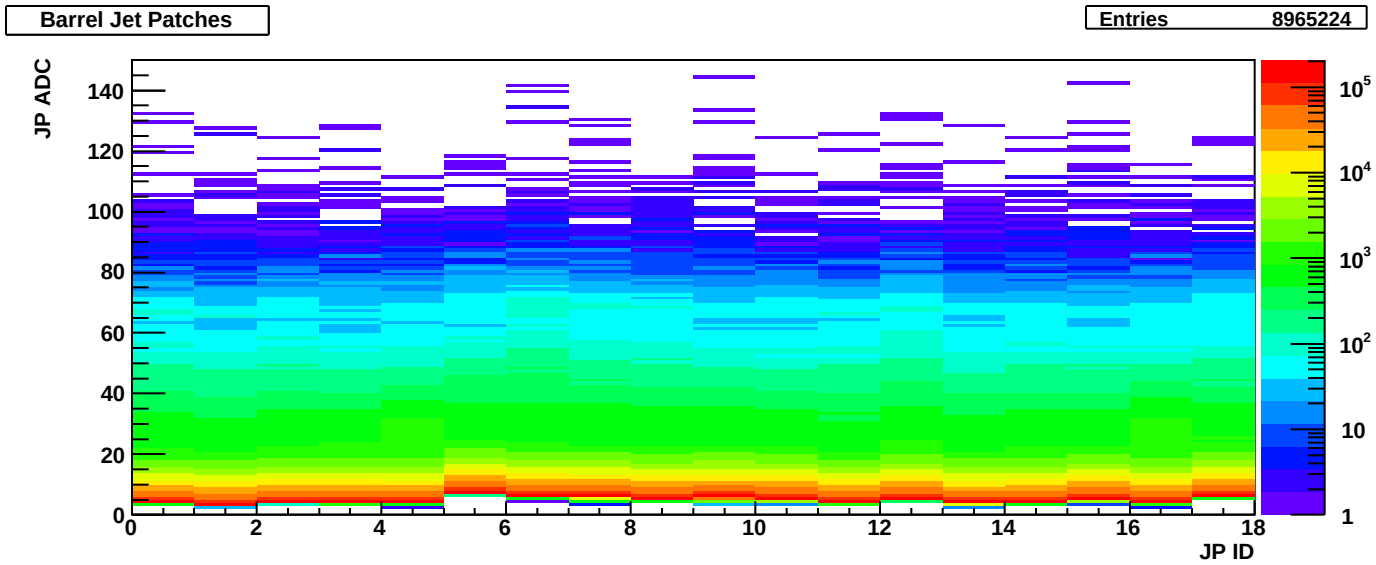

BunchId7Bit (all events)
Entries 498068

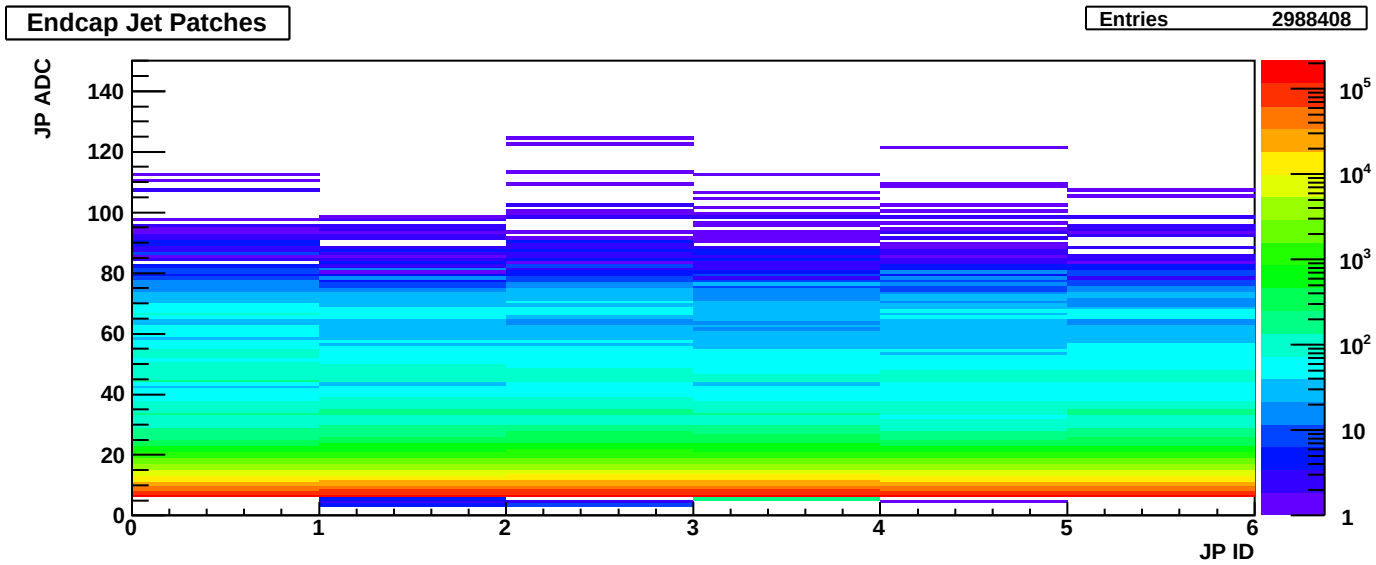

Barrel EMC L0 Input - High Tower
Entries 1.494204e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

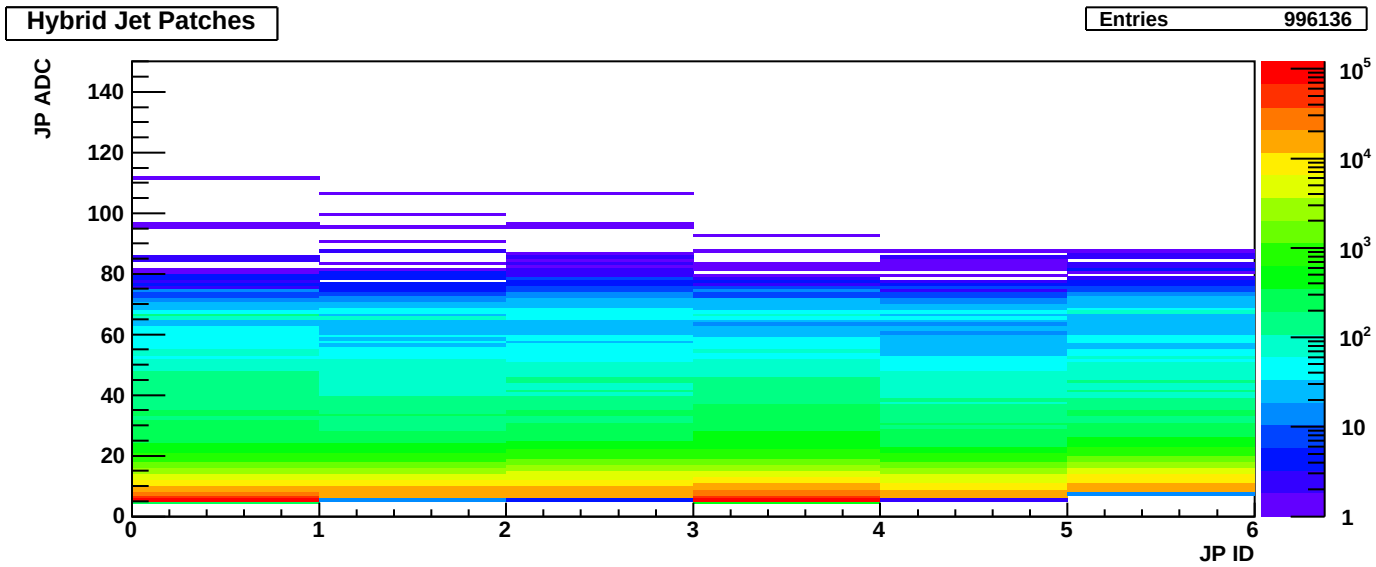

Barrel EMC L0 Input - Patch Sum
Entries 1.494204e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

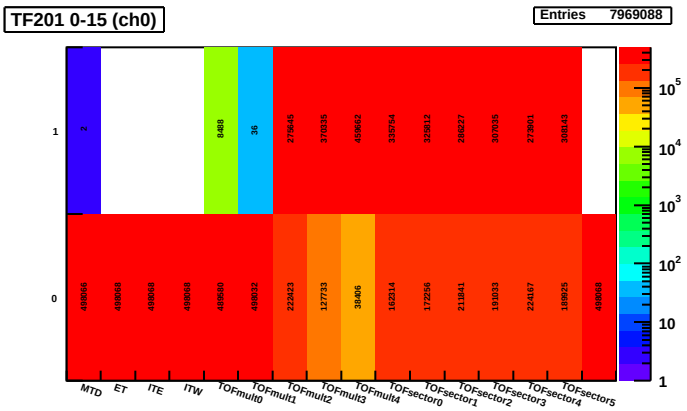

Endcap EMC L0 Input - High Tower
Entries 4.482612e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

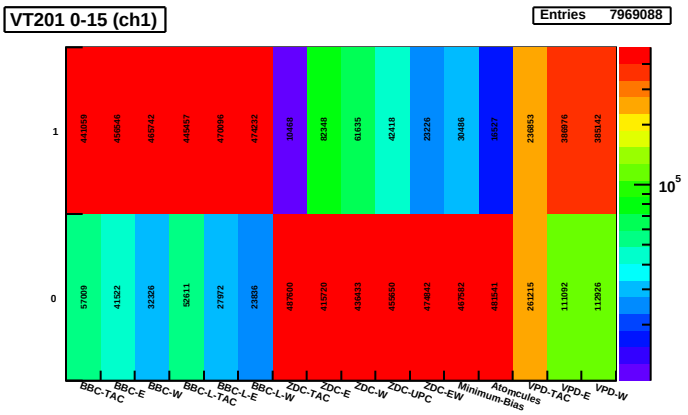

Endcap EMC L0 Input - Patch Sum
Entries 4.482612e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

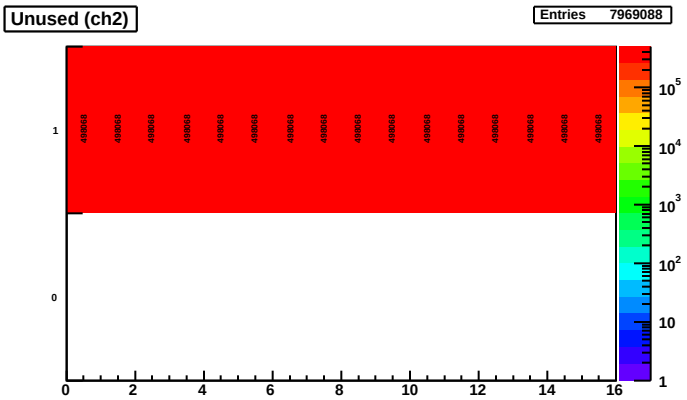

Barrel Jet Patches
Entries 8965224
JP ADC
JP ID

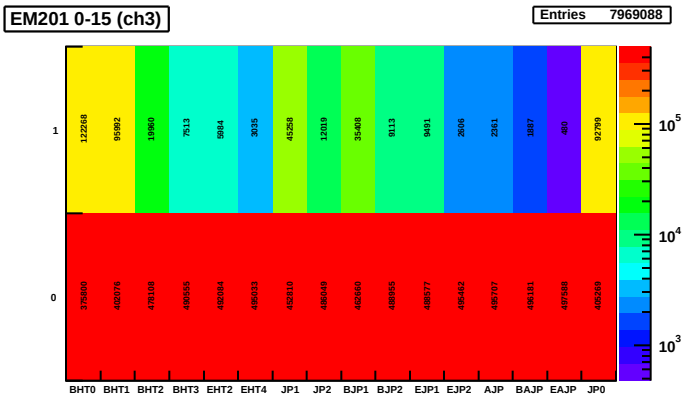

Endcap Jet Patches
Entries 2988408
JP ADC
JP ID

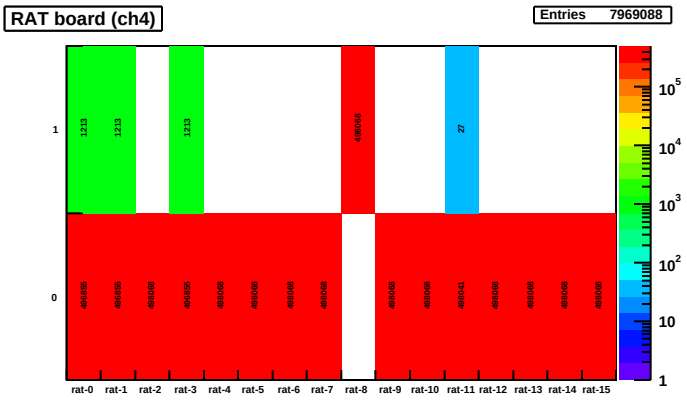

Hybrid Jet Patches
Entries 996136
JP ADC
JP ID

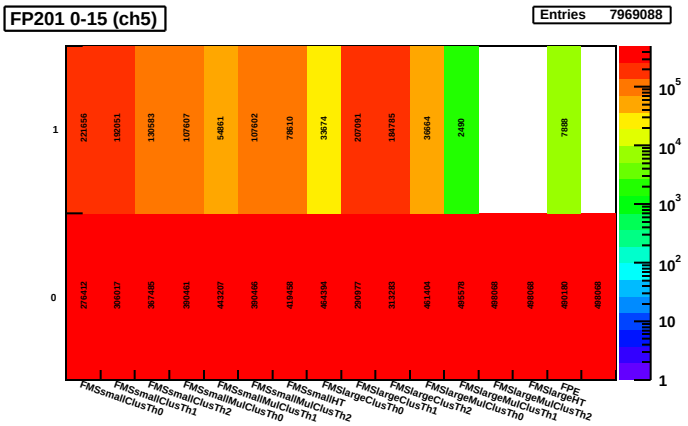

TF201 0-15 (ch0)
Entries 7969088
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

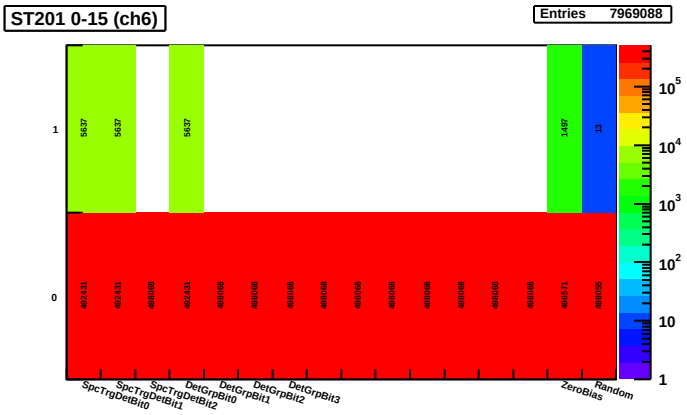

VT201 0-15 (ch1)
Entries 7969088
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

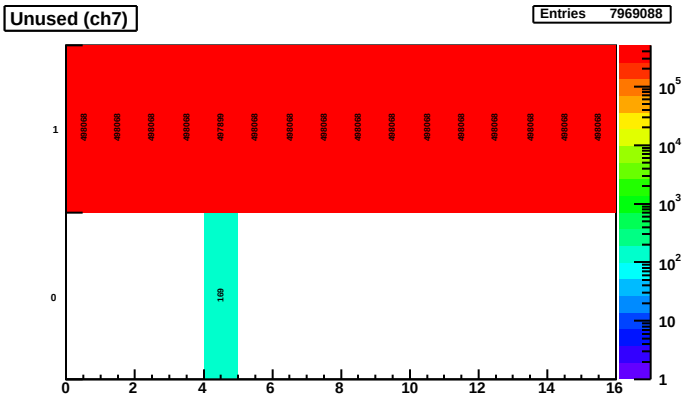

Unused (ch2)
Entries 7969088

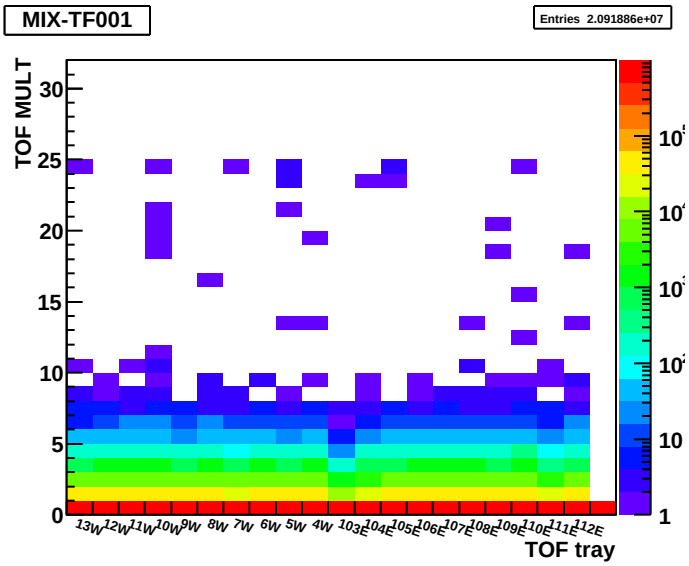

EM201 0-15 (ch3)
Entries 7969088
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

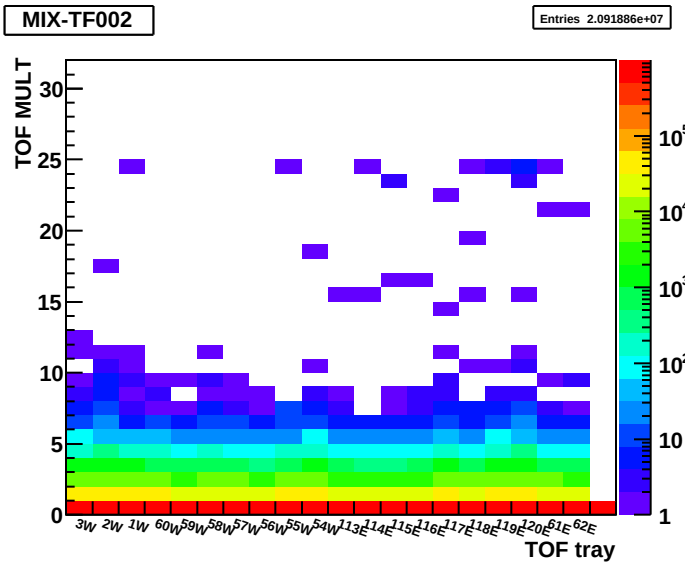

RAT board (ch4)
Entries 7969088
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

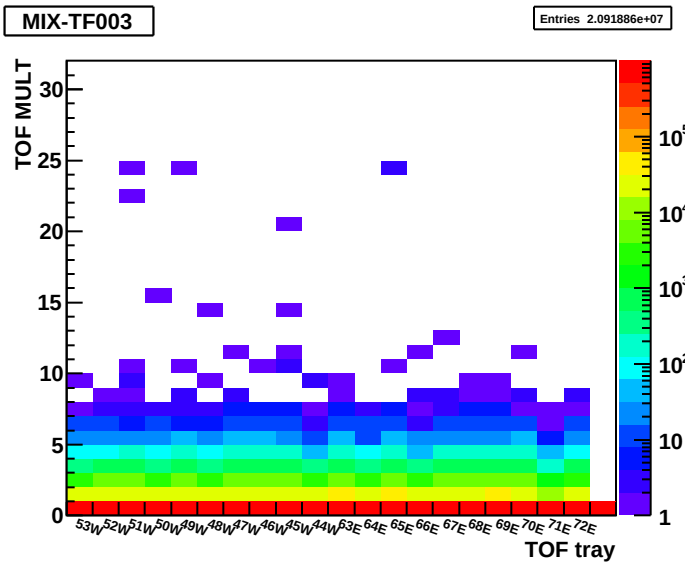

FP201 0-15 (ch5)
Entries 7969088
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

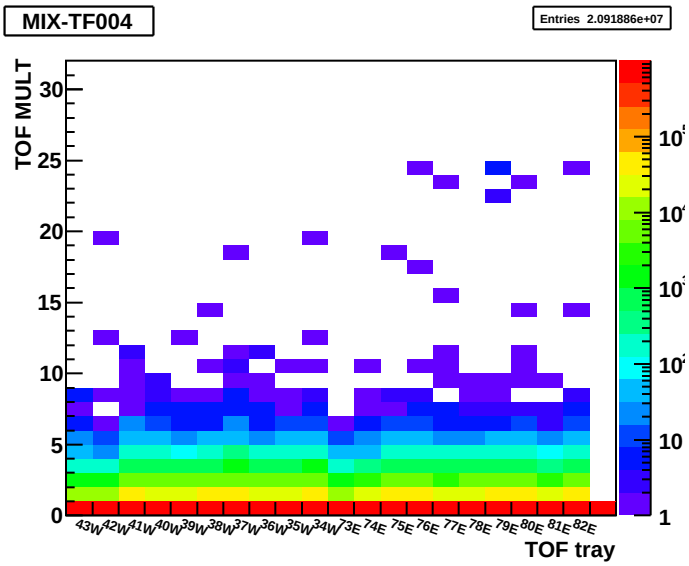

ST201 0-15 (ch6)
Entries 7969088
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

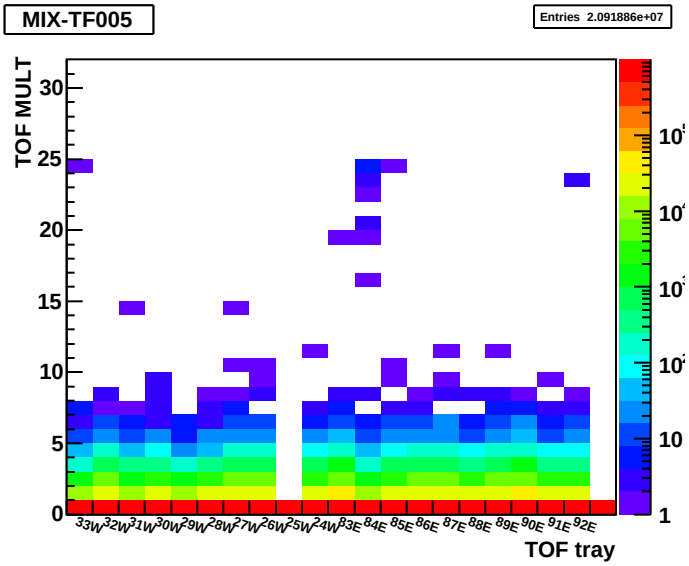

Unused (ch7)
Entries 7969088

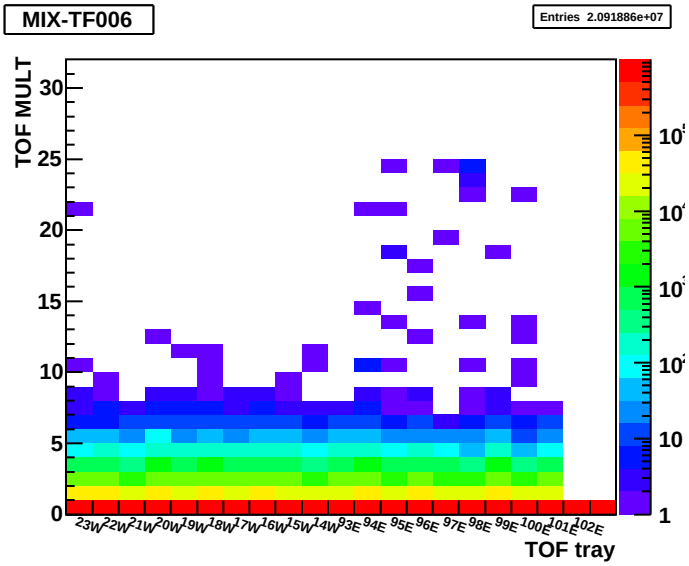

MIX-TF001
Entries 2.091886e+07
TOF MULT
TOF tray

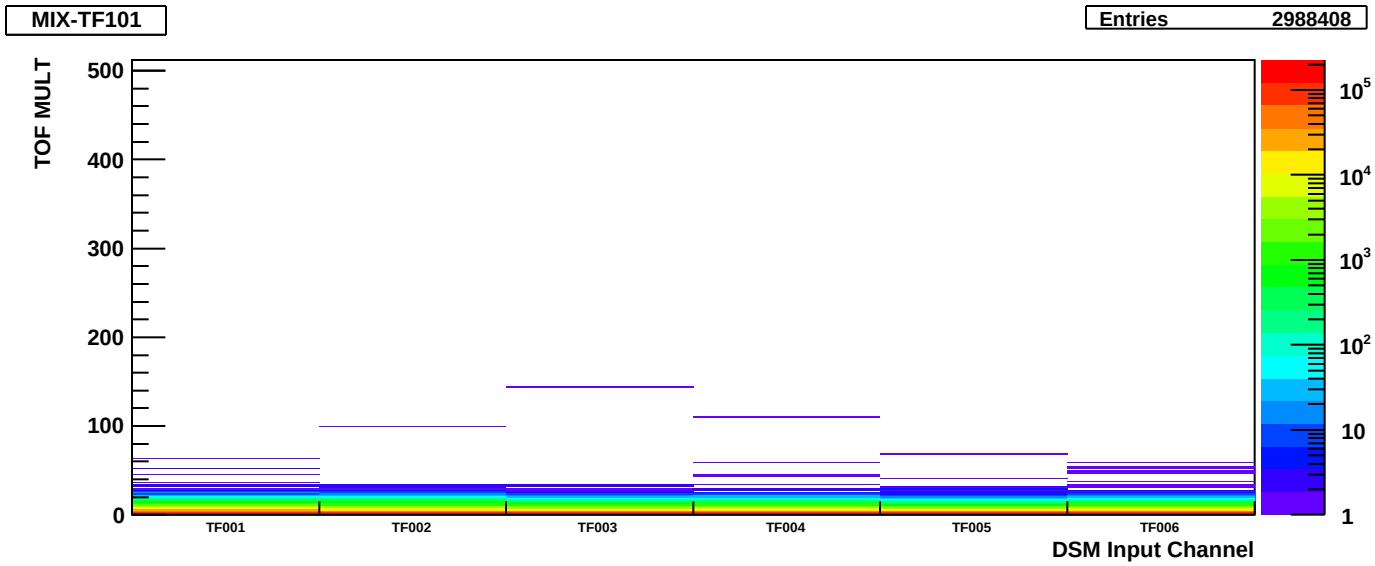

MIX-TF002
Entries 2.091886e+07
TOF MULT
TOF tray

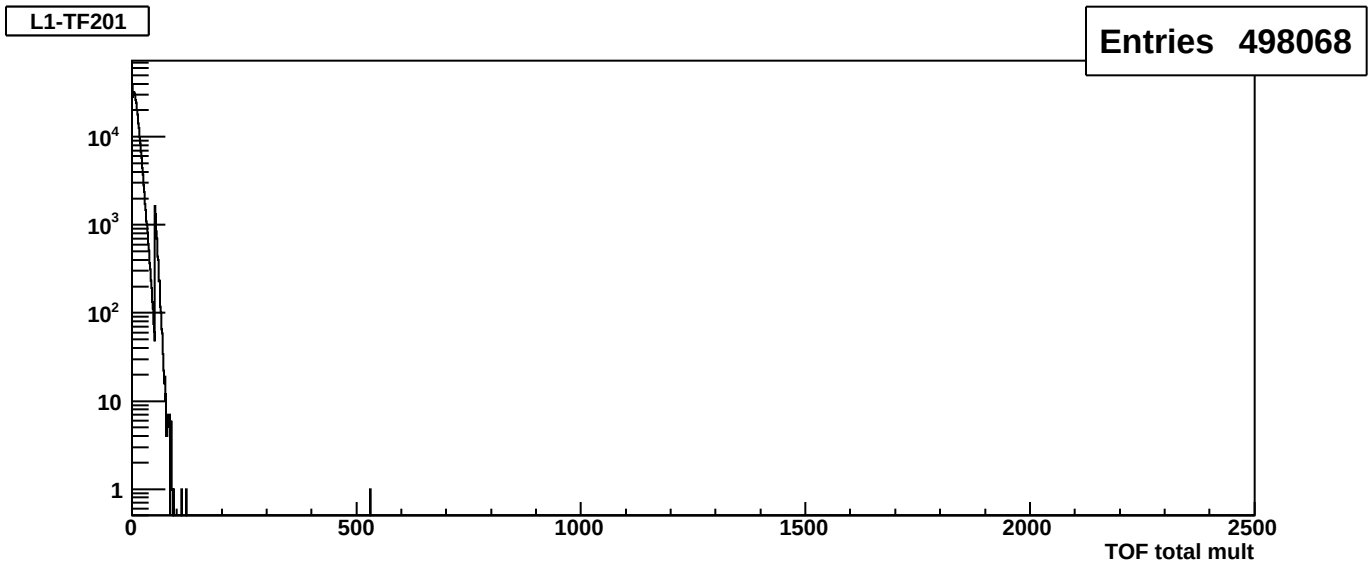

MIX-TF003
Entries 2.091886e+07
TOF MULT
TOF tray

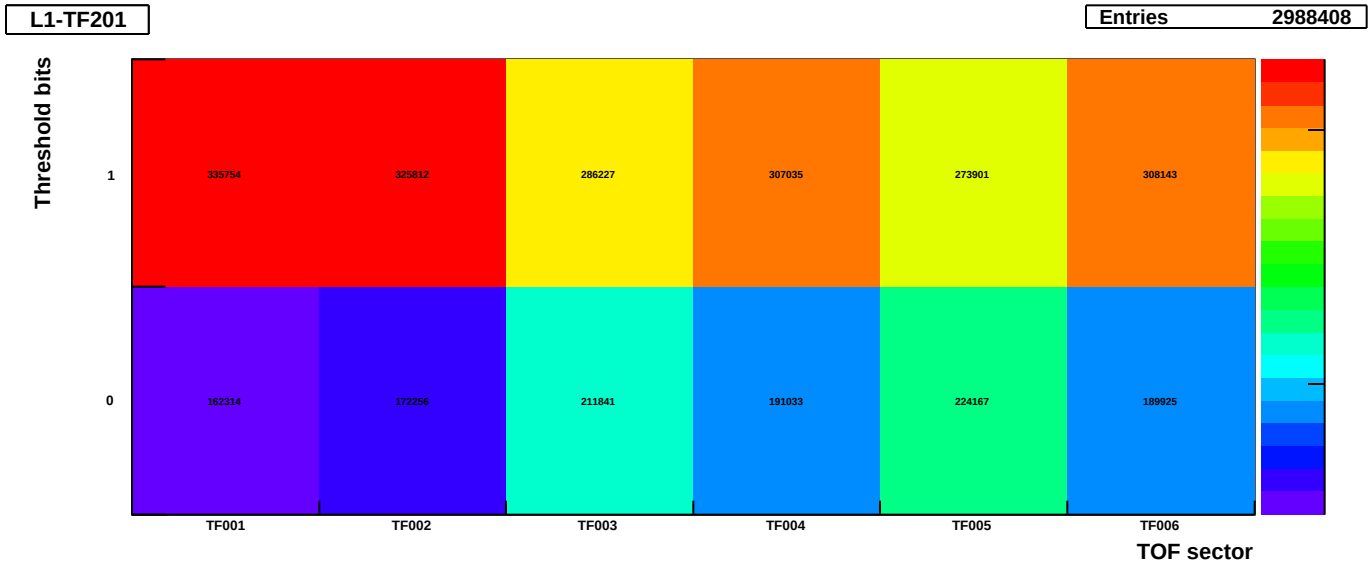

MIX-TF004
Entries 2.091886e+07
TOF MULT
TOF tray

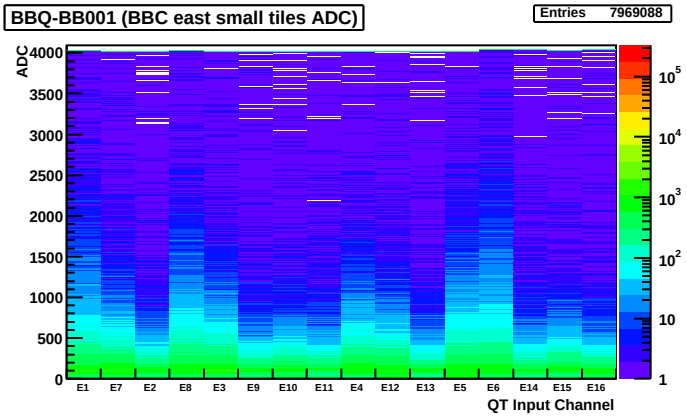

MIX-TF005
Entries 2.091886e+07
TOF MULT
TOF tray

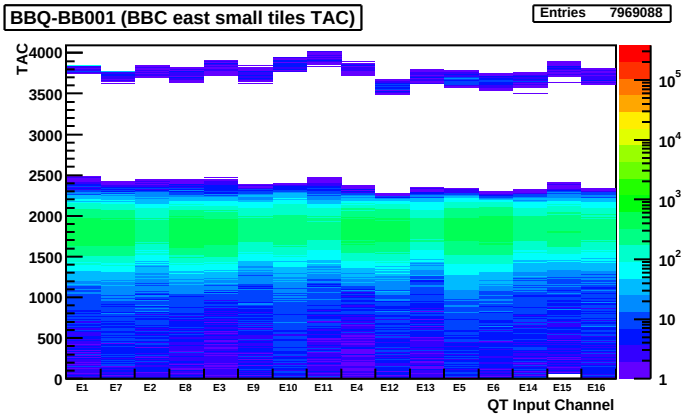

MIX-TF006
Entries 2.091886e+07
TOF MULT
TOF tray

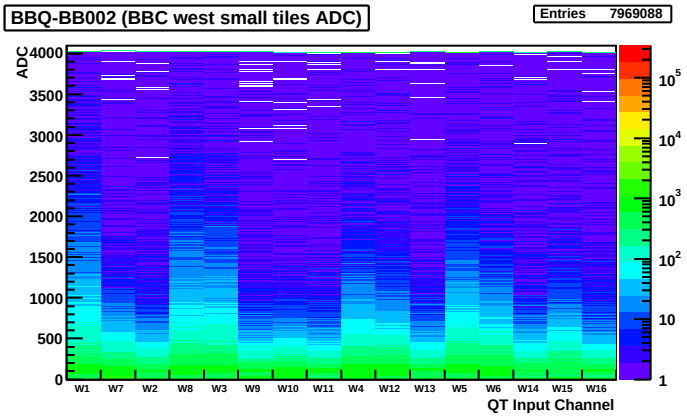

MIX-TF101
Entries 2988408
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

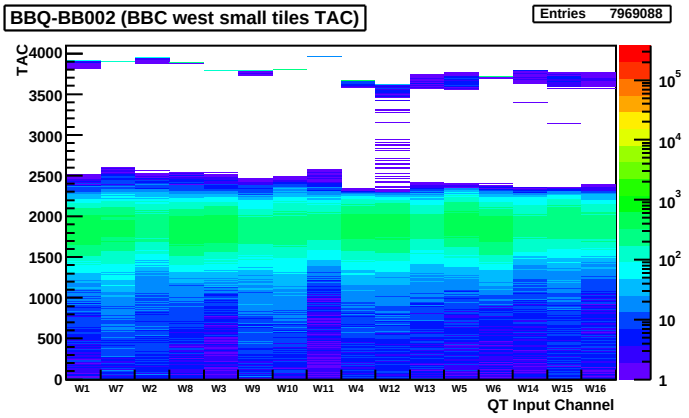

L1-TF201
Entries 498068
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

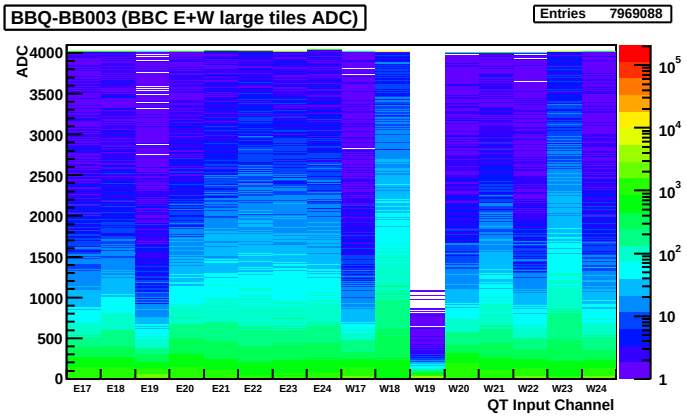

L1-TF201
Entries 2988408
Threshold bits
1
0
335754
325812
286227
307035
273901
308143
162314
172256
211841
191033
224167
189925
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

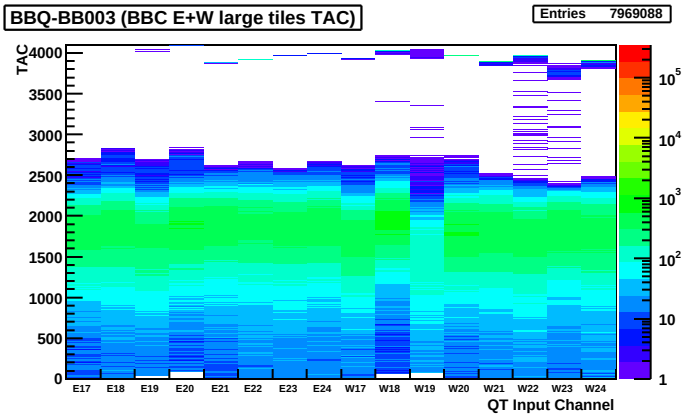

BBQ-BB001 (BBC east small tiles ADC)
Entries 7969088
ADC
QT Input Channel

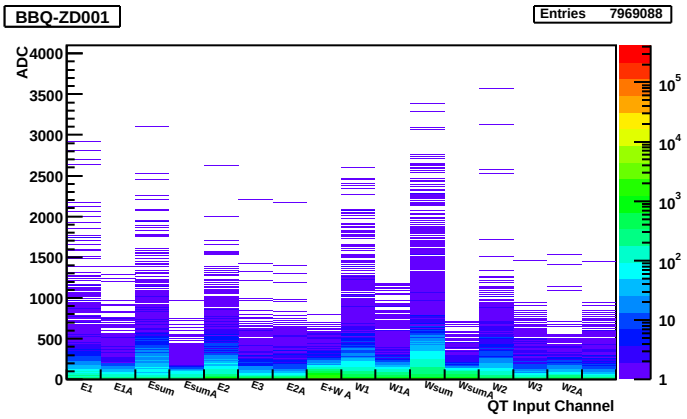

BBQ-BB001 (BBC east small tiles TAC)
Entries 7969088
TAC
QT Input Channel

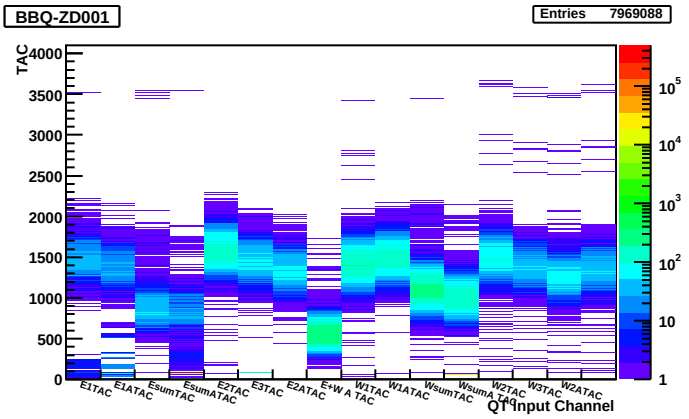

BBQ-BB002 (BBC west small tiles ADC)
Entries 7969088
ADC
QT Input Channel

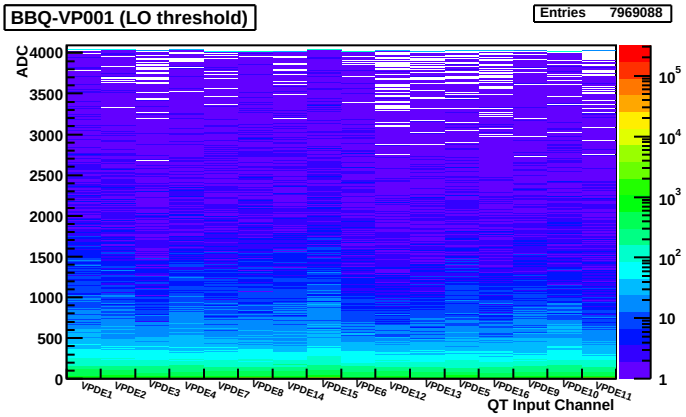

BBQ-BB002 (BBC west small tiles TAC)
Entries 7969088
TAC
QT Input Channel

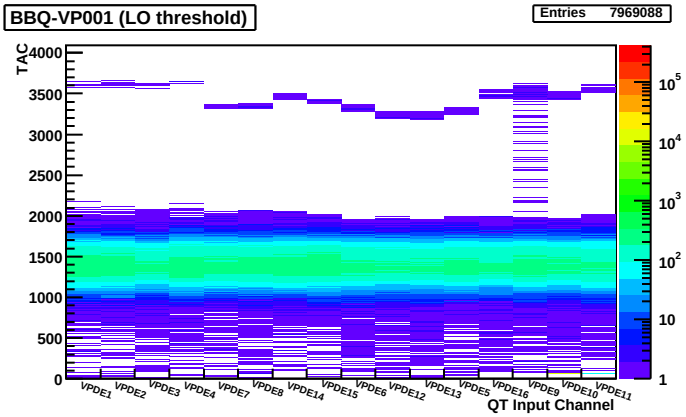

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 7969088
ADC
QT Input Channel

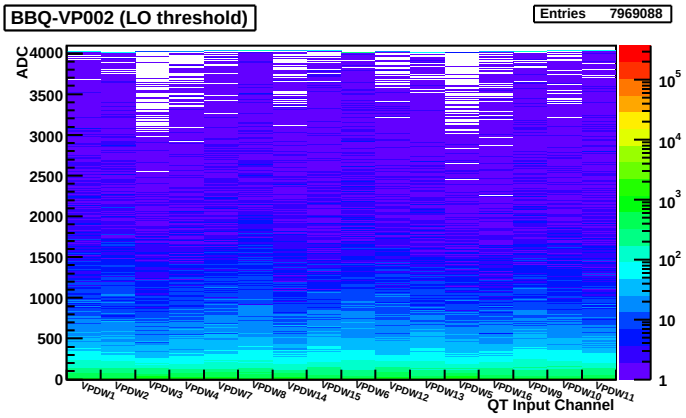

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 7969088
TAC
QT Input Channel

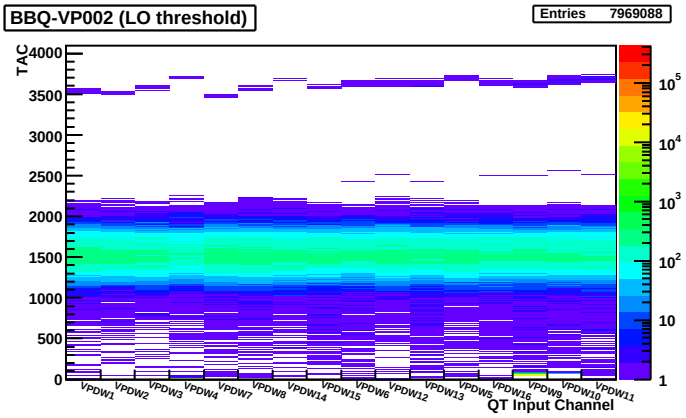

BBQ-ZD001
Entries 7969088
ADC
QT Input Channel

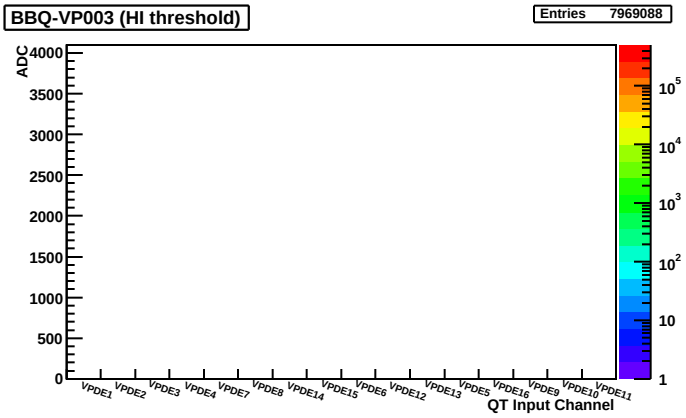

BBQ-ZD001
Entries 7969088
TAC
QT Input Channel

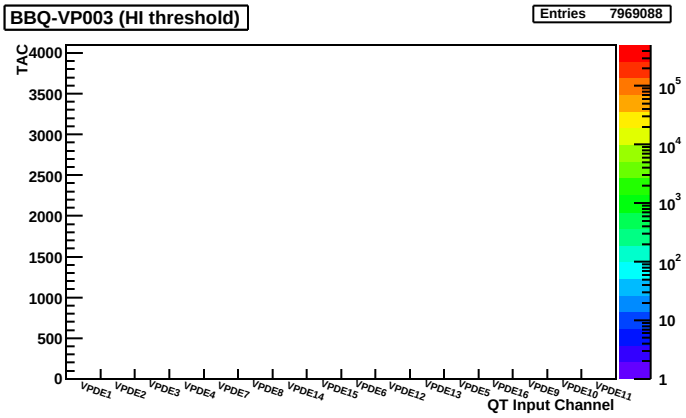

BBQ-VP001 (LO threshold)
Entries 7969088
ADC
QT Input Channel

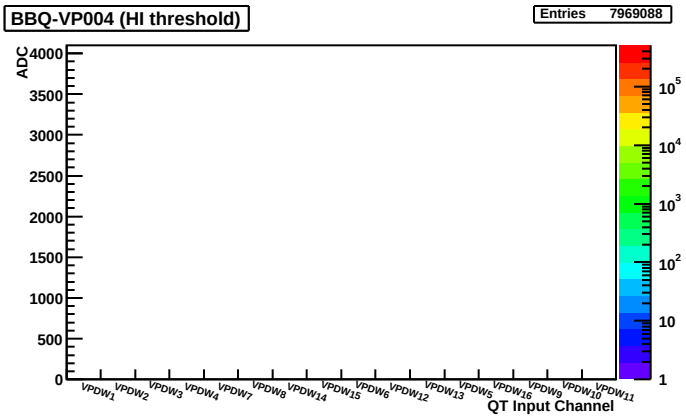

BBQ-VP001 (LO threshold)
Entries 7969088
TAC
QT Input Channel

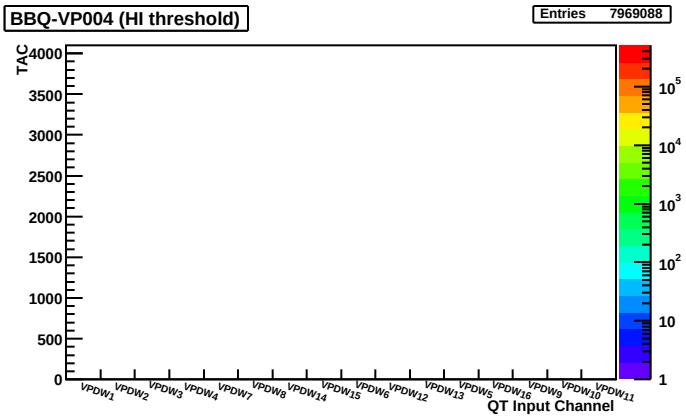

BBQ-VP002 (LO threshold)
Entries 7969088
ADC
QT Input Channel

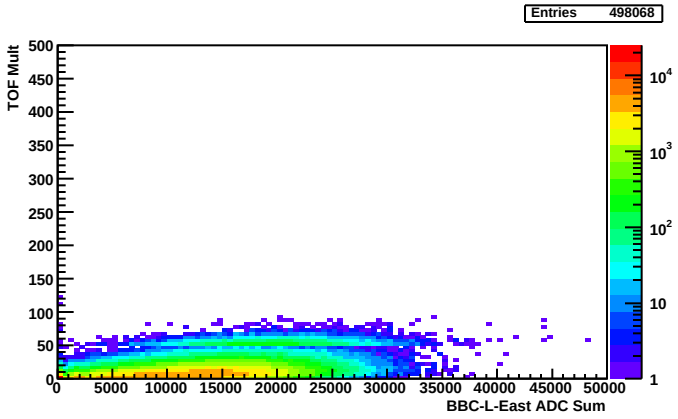

BBQ-VP002 (LO threshold)
Entries 7969088
TAC
QT Input Channel

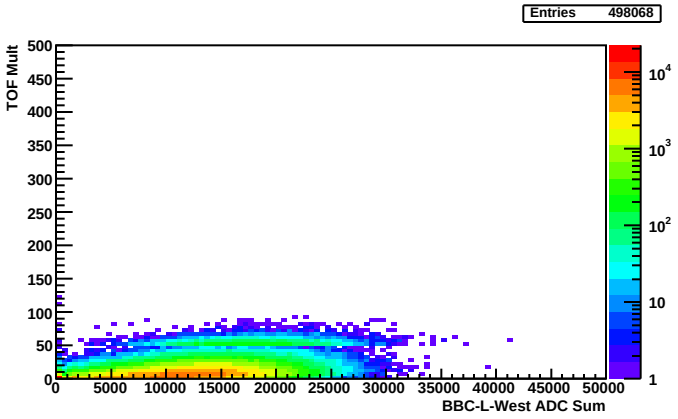

BBQ-VP003 (HI threshold)
Entries 7969088
ADC
QT Input Channel

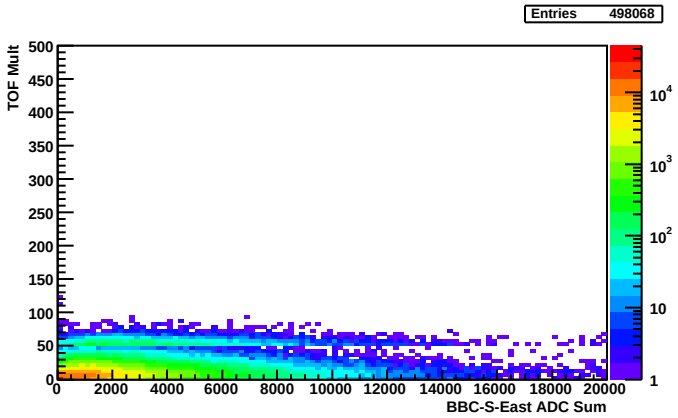

BBQ-VP003 (HI threshold)
Entries 7969088
TAC
QT Input Channel

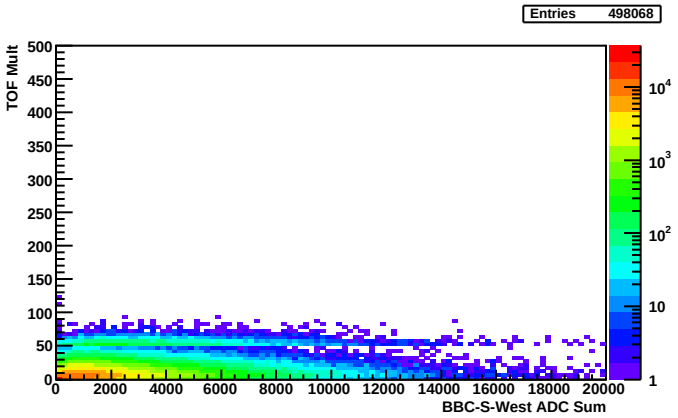

BBQ-VP004 (HI threshold)
Entries 7969088
ADC
QT Input Channel

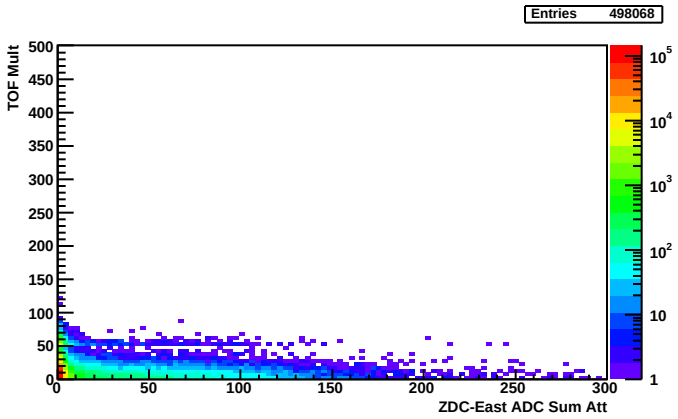

BBQ-VP004 (HI threshold)
Entries 7969088
TAC
QT Input Channel

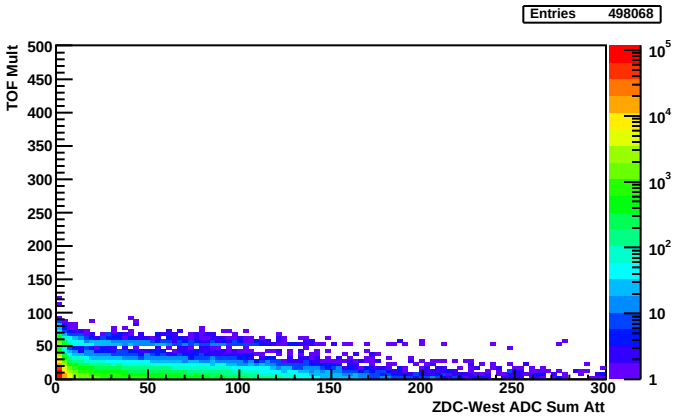

Entries 498068
TOF Mult
BBC-L-East ADC Sum

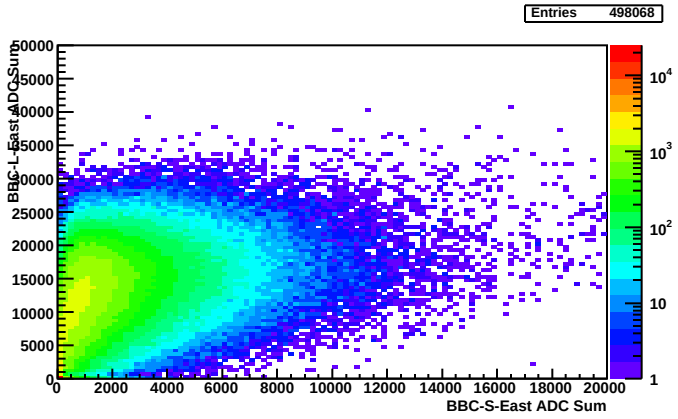

Entries 498068
TOF Mult
BBC-L-West ADC Sum

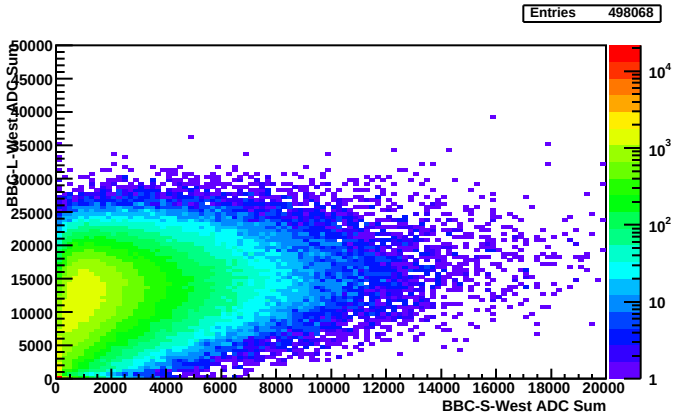

Entries 498068
TOF Mult
BBC-S-East ADC Sum

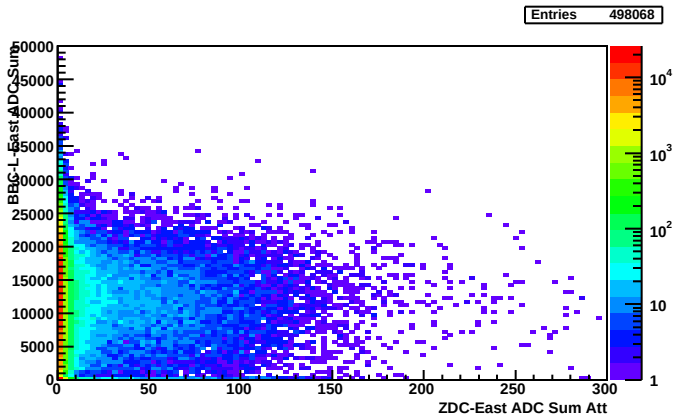

Entries 498068
TOF Mult
BBC-S-West ADC Sum

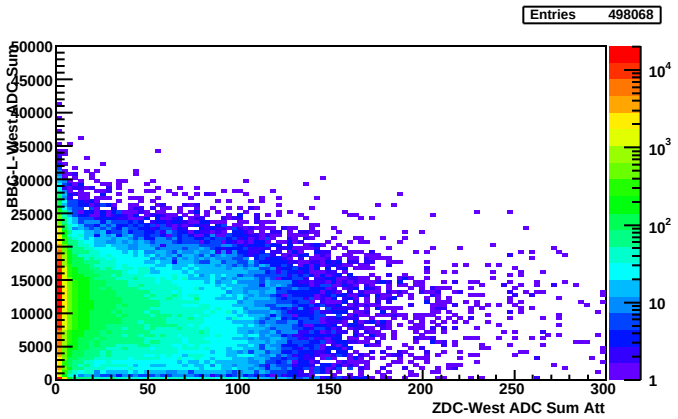

Entries 498068
TOF Mult
ZDC-East ADC Sum Att

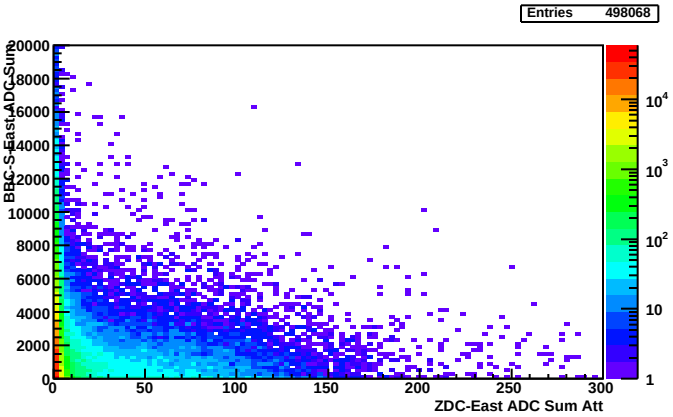

Entries 498068
TOF Mult
ZDC-West ADC Sum Att

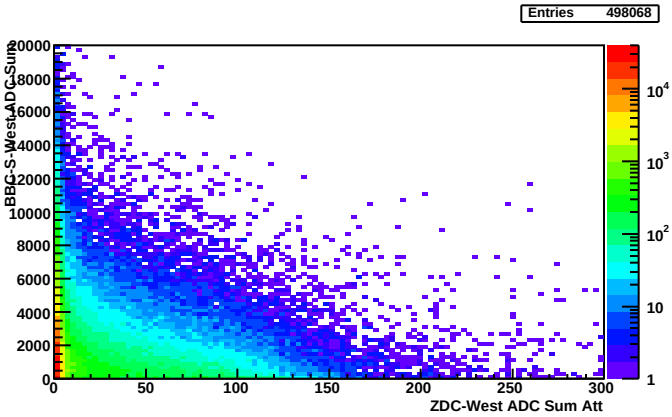

Entries 498068
BBC-L-East ADC Sum
BBC-S-East ADC Sum

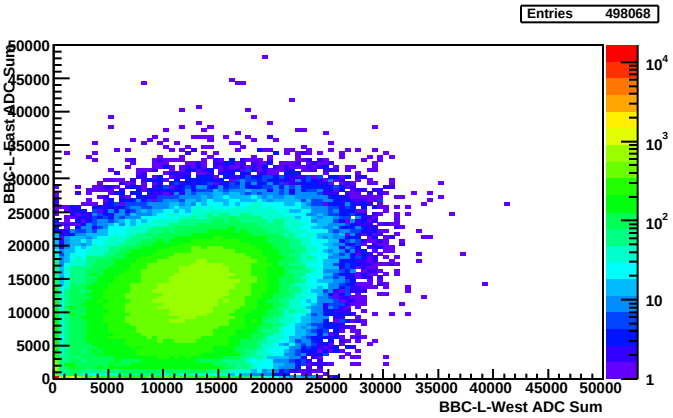

Entries 498068
BBC-L-West ADC Sum
BBC-S-West ADC Sum

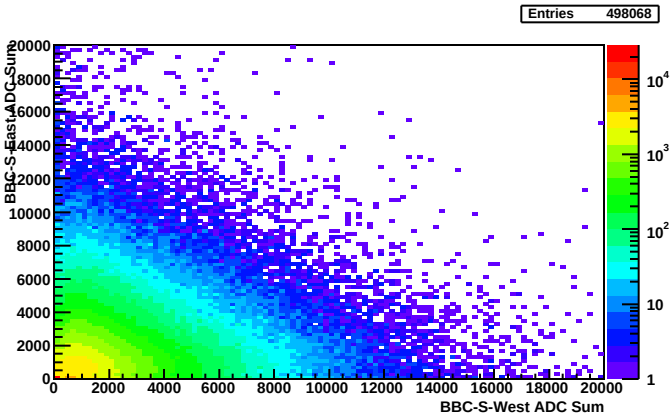

Entries 498068
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

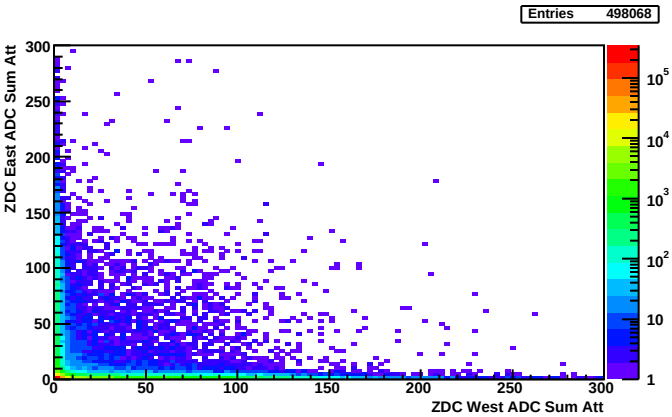

Entries 498068
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

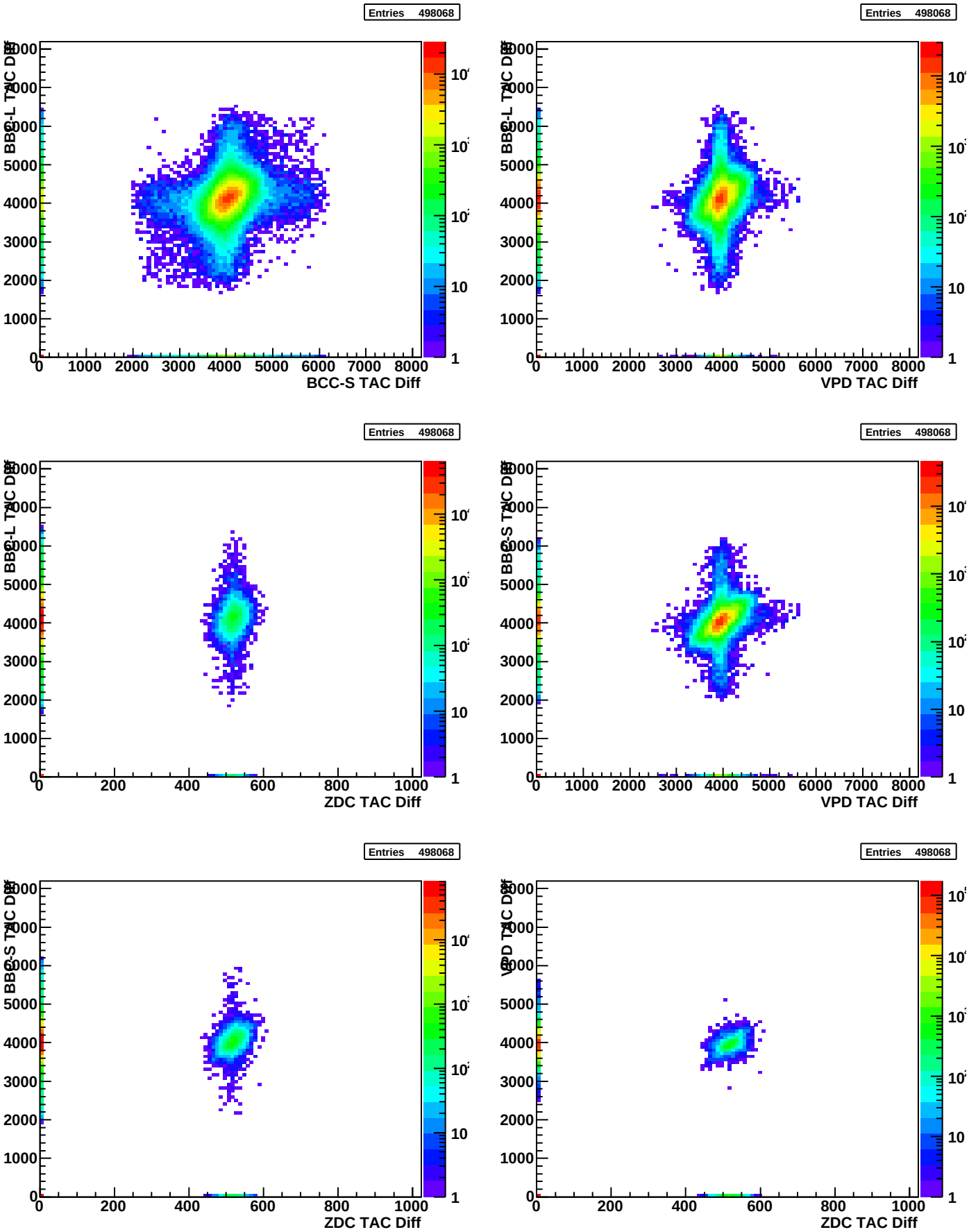

Entries 498068
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

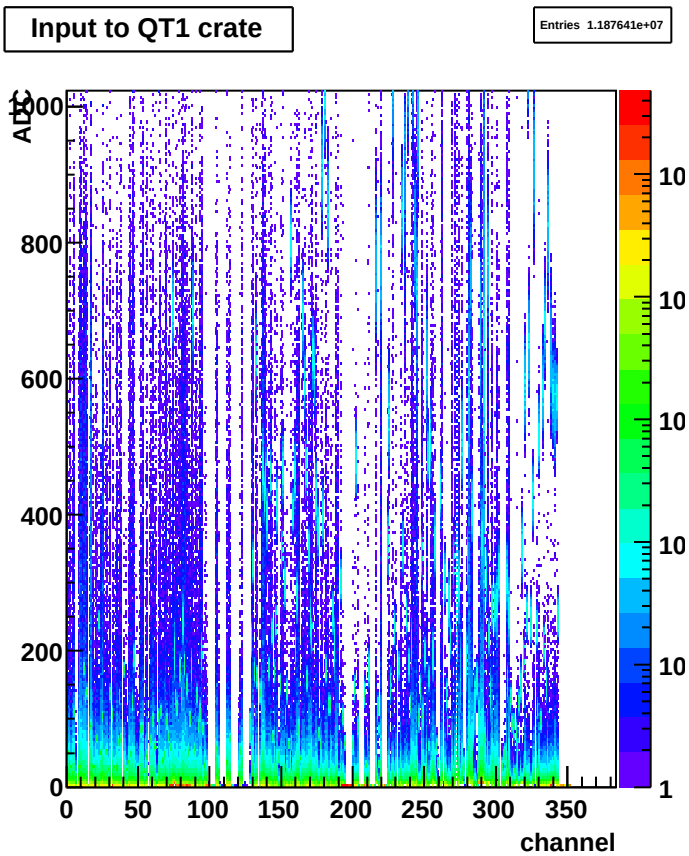

Entries 498068
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

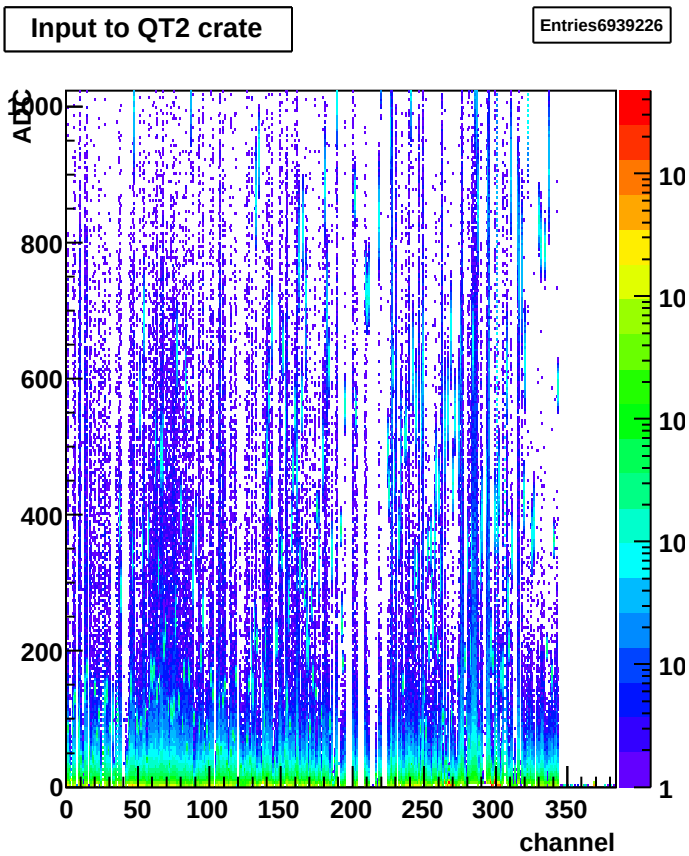

Entries 498068
BBC-L-East ADC Sum
BBC-L-West ADC Sum

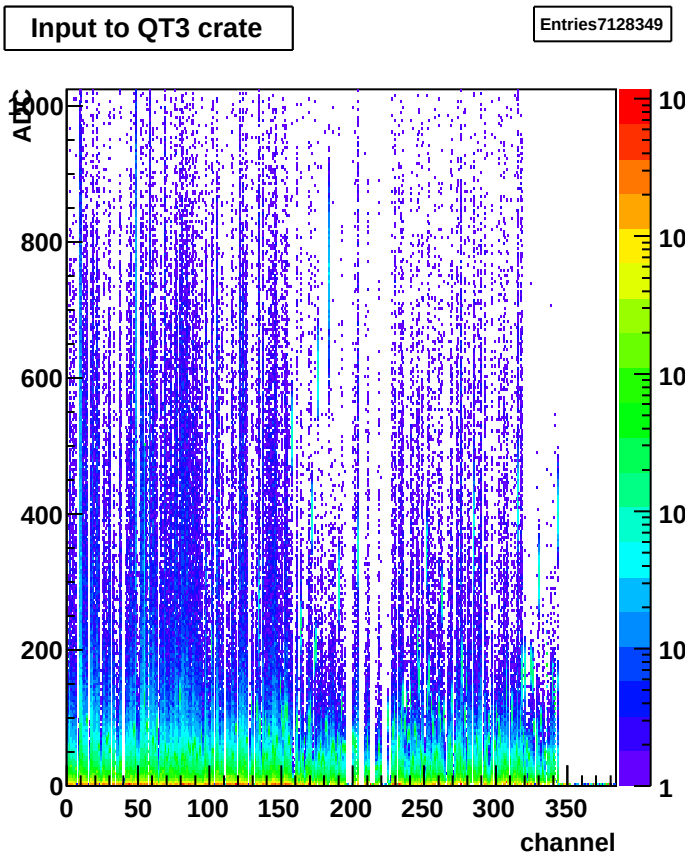

Entries 498068
BBC-S-East ADC Sum
BBC-S-West ADC Sum

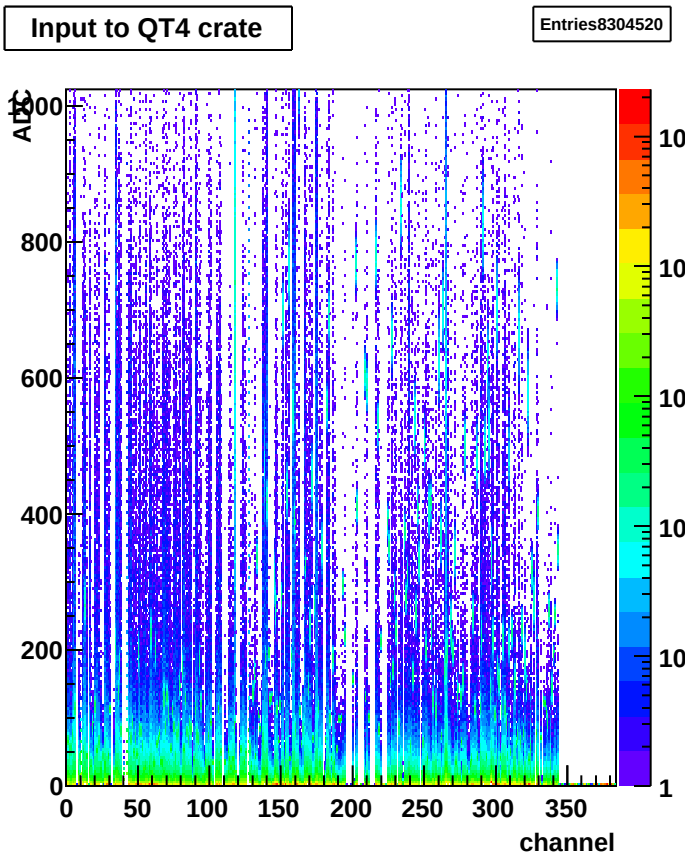

Entries 498068
ZDC East ADC Sum Att
ZDC West ADC Sum Att

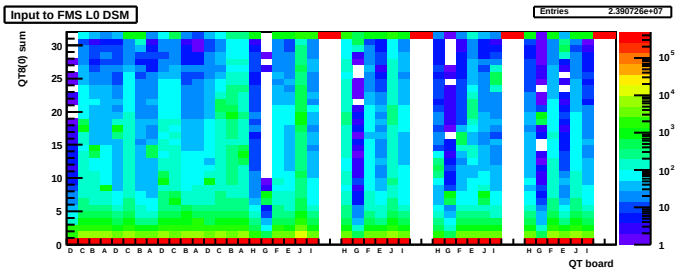

Entries 498068
BBC-L TAC Diff
BCC-S TAC Diff
Entries 498068
BBC-L TAC Diff
VPD TAC Diff
Entries 498068
BBC-L TAC Diff
ZDC TAC Diff
Entries 498068
BBC-S TAC Diff
VPD TAC Diff
Entries 498068
BBC-S TAC Diff
ZDC TAC Diff
Entries 498068
VPD TAC Diff
ZDC TAC Diff

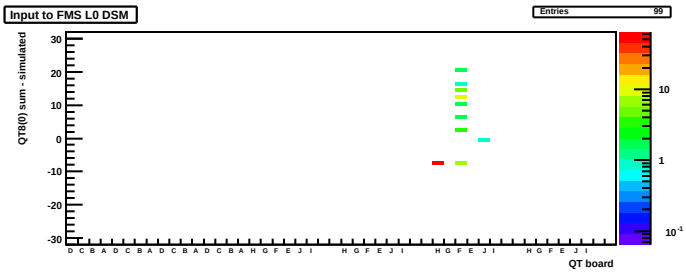

Input to QT1 crate
Entries 1.187641e+07
ADC
channel

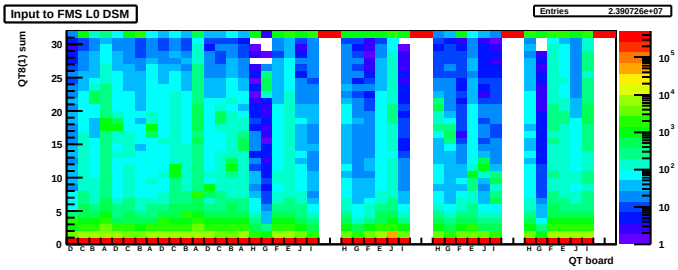

Input to QT2 crate
Entries6939226
ADC
channel

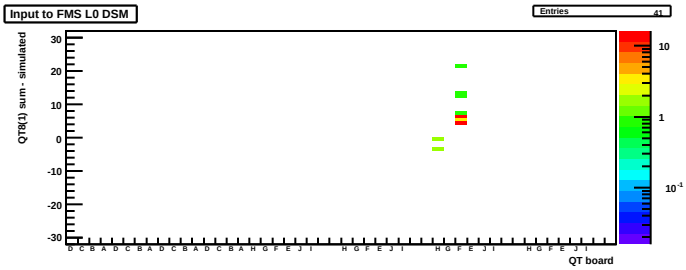

Input to QT3 crate
Entries7128349
ADC
channel

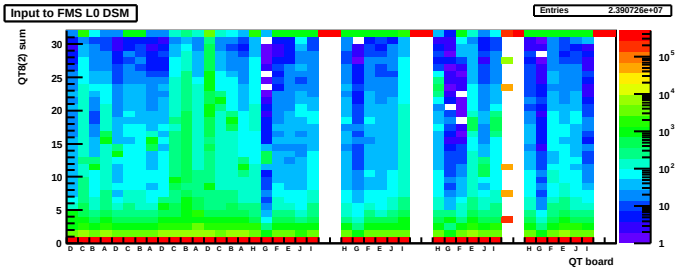

Input to QT4 crate
Entries8304520
ADC
channel

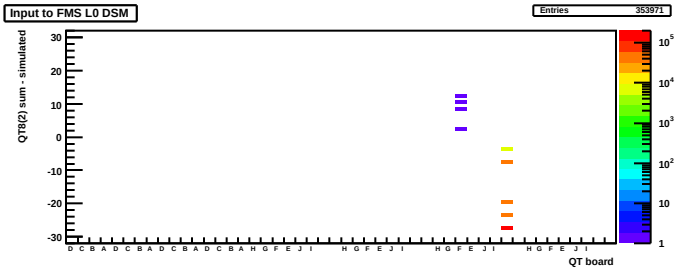

Input to FMS L0 DSM
Entries 2.390726e+07
QT8(0) sum
QT board

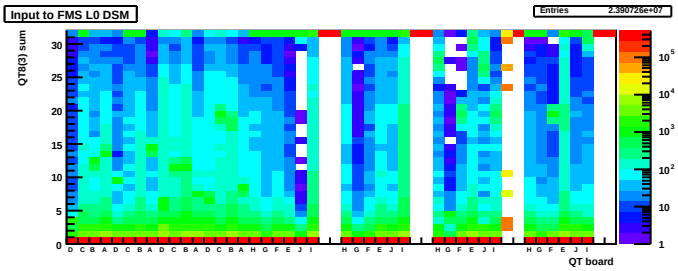

Input to FMS L0 DSM
Entries 99
QT8(0) sum - simulated
QT board

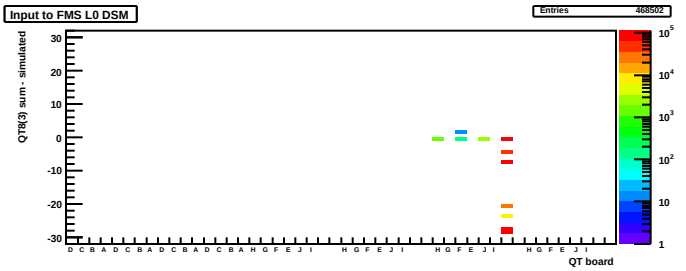

Input to FMS L0 DSM
Entries 2.390726e+07
QT8(1) sum
QT board

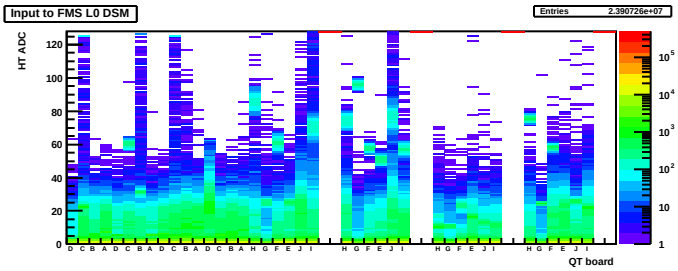

Input to FMS L0 DSM
Entries 41
QT8(1) sum - simulated
QT board

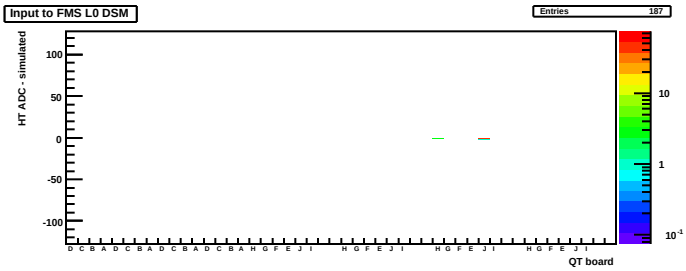

Input to FMS L0 DSM
Entries 2.390726e+07
QT8(2) sum
QT board

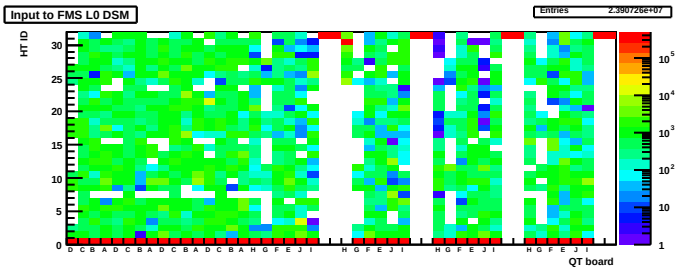

Input to FMS L0 DSM
Entries 353971
QT8(2) sum - simulated
QT board

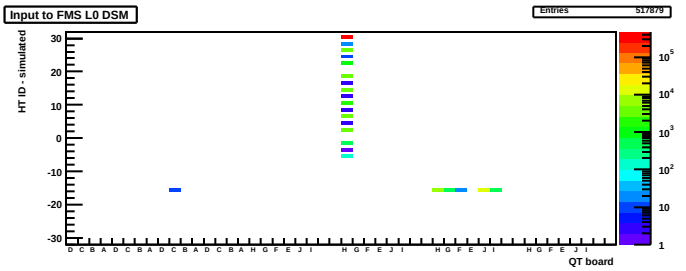

Input to FMS L0 DSM
Entries 2.390726e+07
QT8(3) sum
QT board

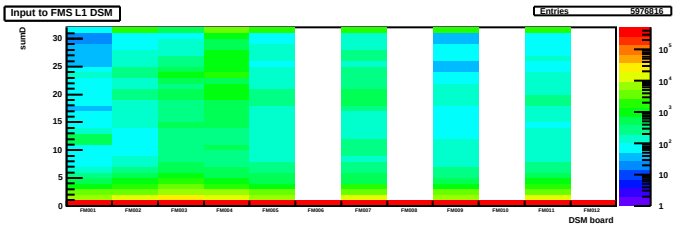

Input to FMS L0 DSM
Entries 468502
QT8(3) sum - simulated
QT board

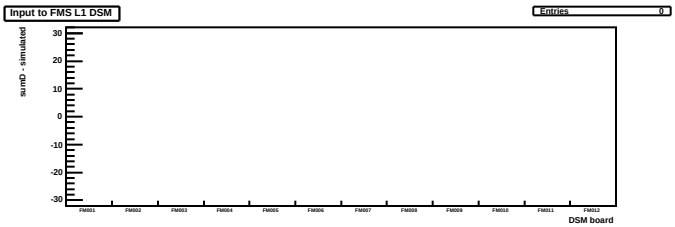

Input to FMS L0 DSM
Entries 2.390726e+07
HT ADC
QT board

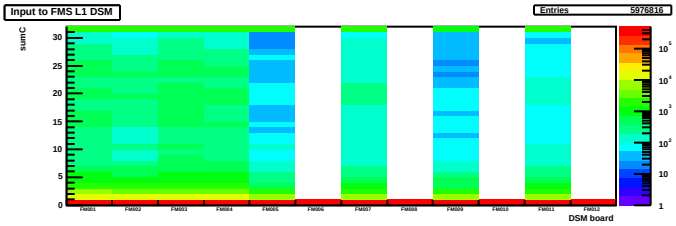

Input to FMS L0 DSM
Entries 187
HT ADC - simulated
QT board

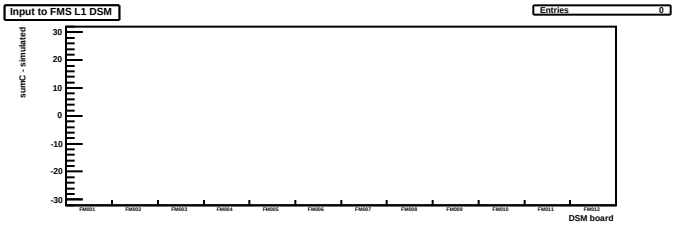

Input to FMS L0 DSM
Entries 2.390726e+07
HT ID
QT board

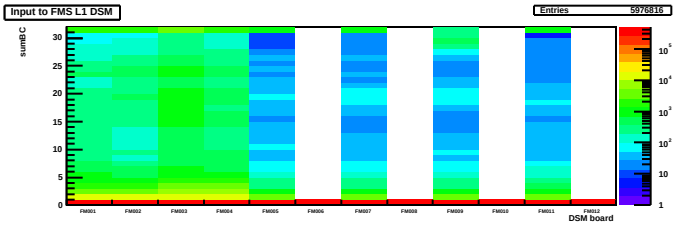

Input to FMS L0 DSM
Entries 517879
HT ID - simulated
QT board

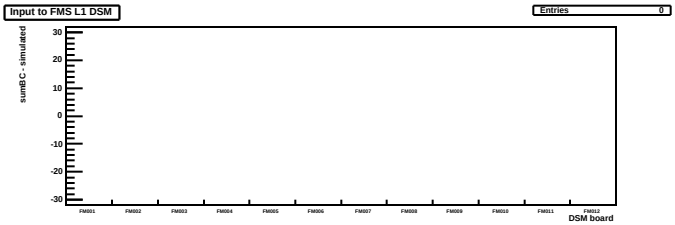

Input to FMS L1 DSM
Entries 5976816
sumD
DSM board

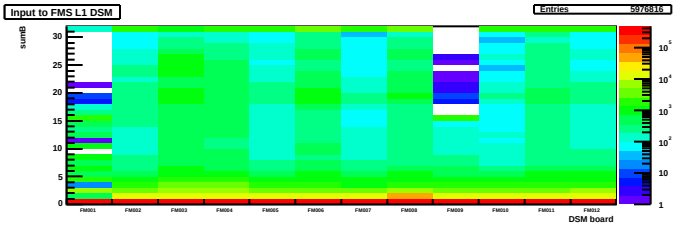

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

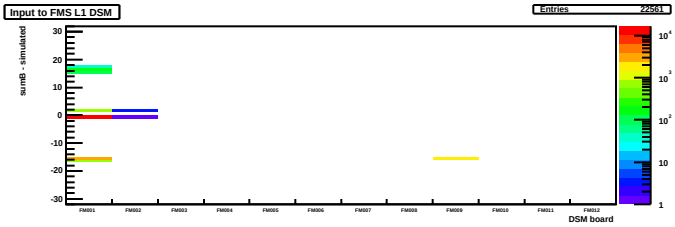

Input to FMS L1 DSM
Entries 5976816
sumC
DSM board

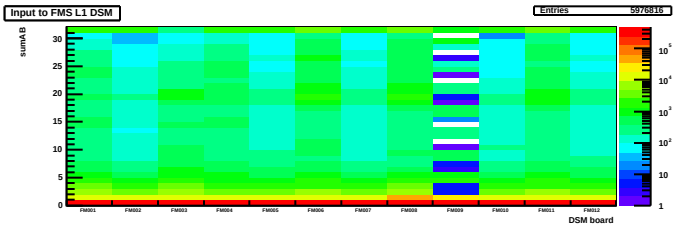

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

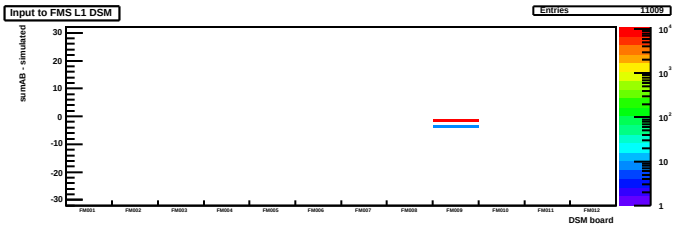

Input to FMS L1 DSM
Entries 5976816
sumBC
DSM board

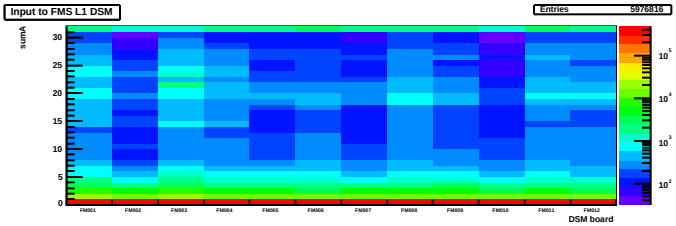

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

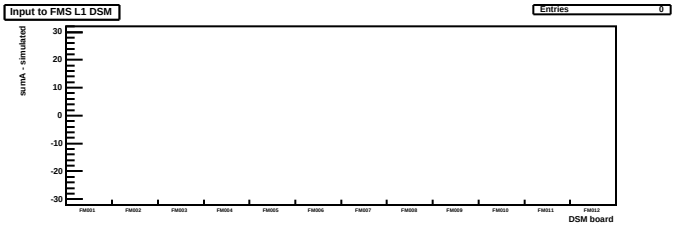

Input to FMS L1 DSM
Entries 5976816
sumB
DSM board

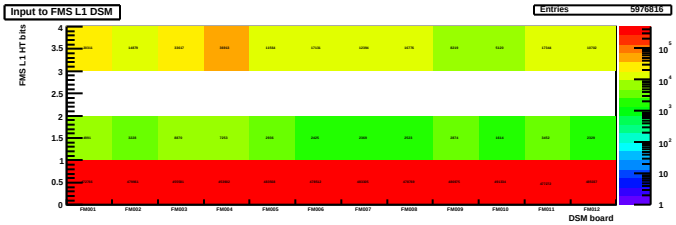

Input to FMS L1 DSM
Entries 22561
sumB - simulated
DSM board

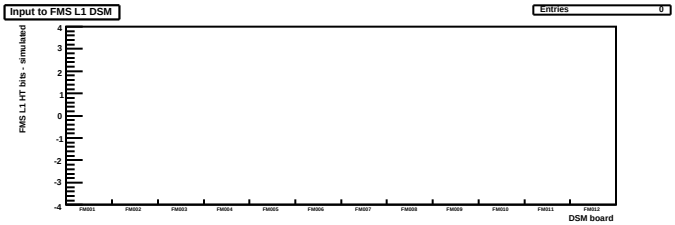

Input to FMS L1 DSM
Entries 5976816
sumAB
DSM board

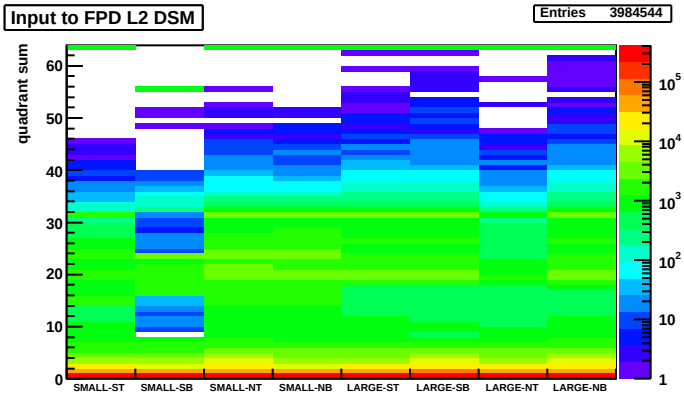

Input to FMS L1 DSM
Entries 11009
sumAB - simulated
DSM board

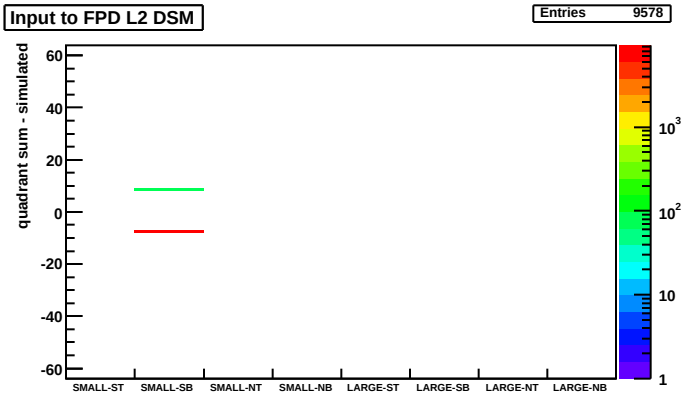

Input to FMS L1 DSM
Entries 5976816
sumA
DSM board

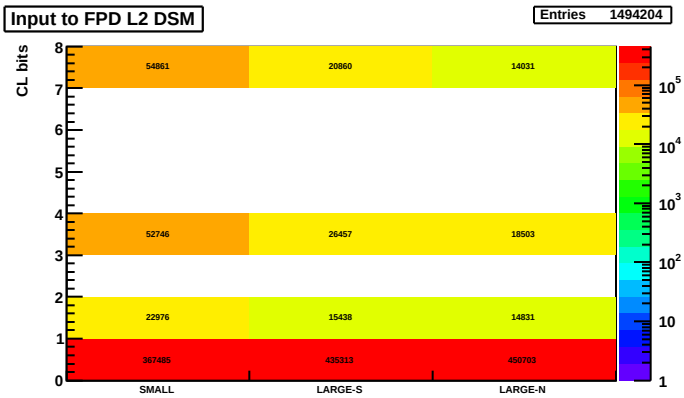

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

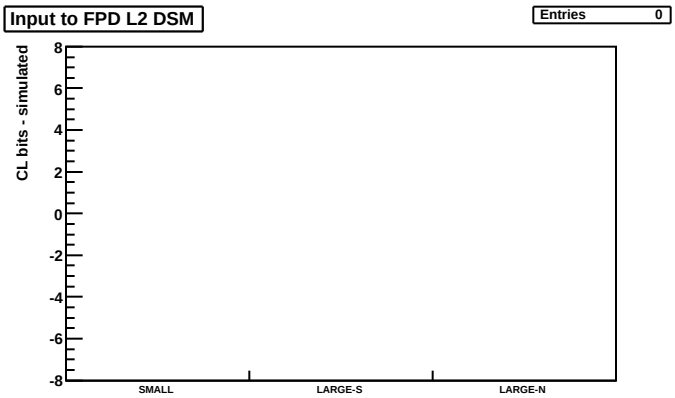

Input to FMS L1 DSM
Entries 5976816
FMS L1 HT bits
DSM board

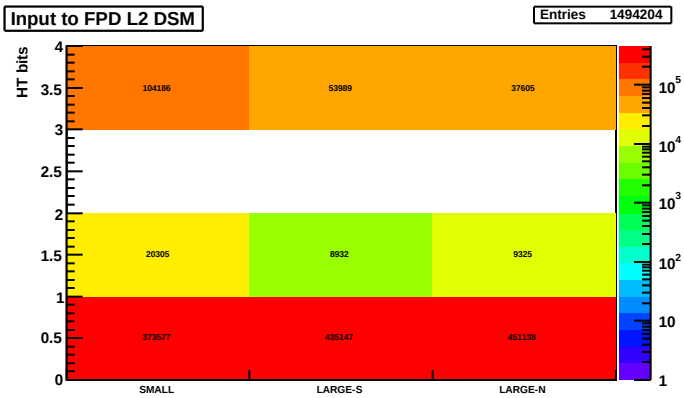

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

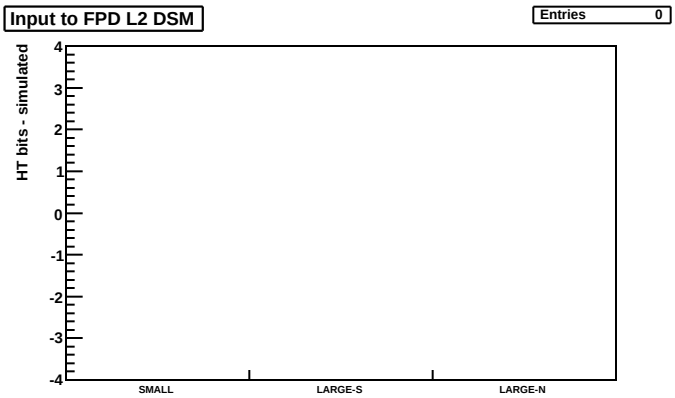

Input to FPD L2 DSM
Entries 3984544
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

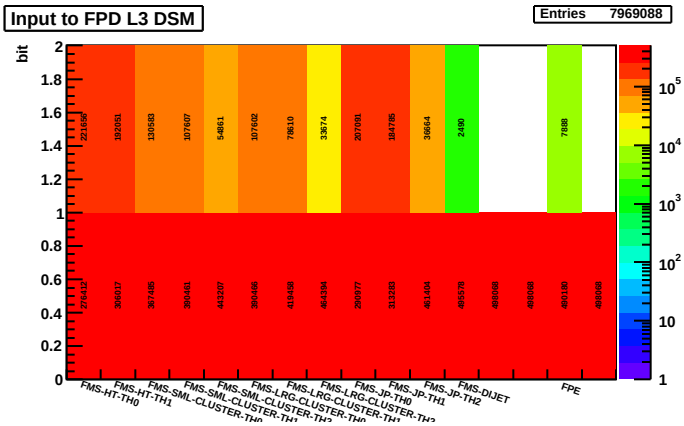

Input to FPD L2 DSM
Entries 9578
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

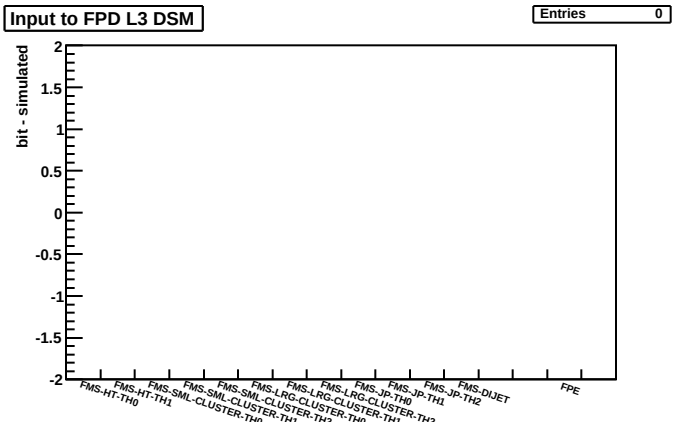

Input to FPD L2 DSM
Entries 1494204
CL bits
54861
20860
14031
52746
26457
18503
22976
15438
14831
367485
435313
450703
SMALL
LARGE-S
LARGE-N

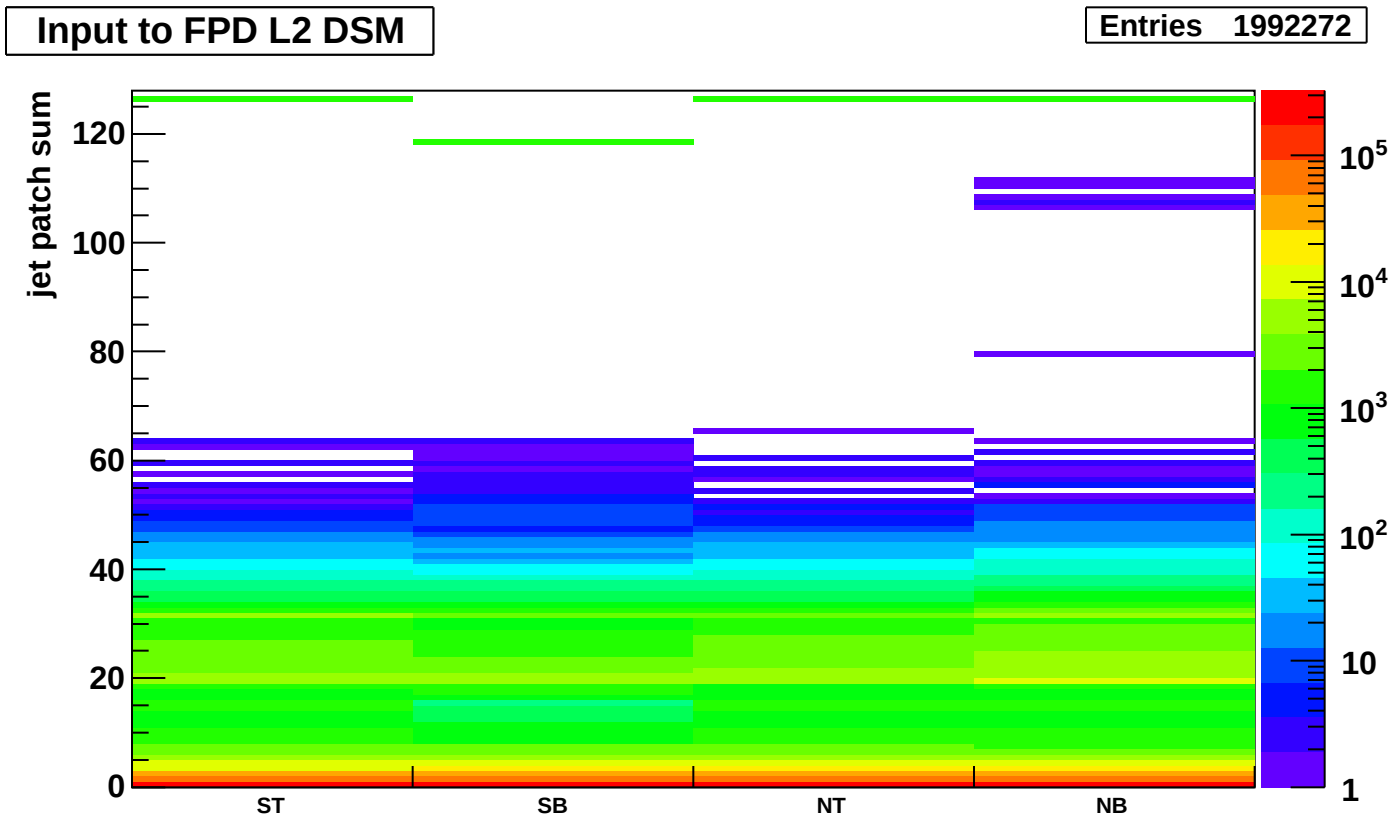

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

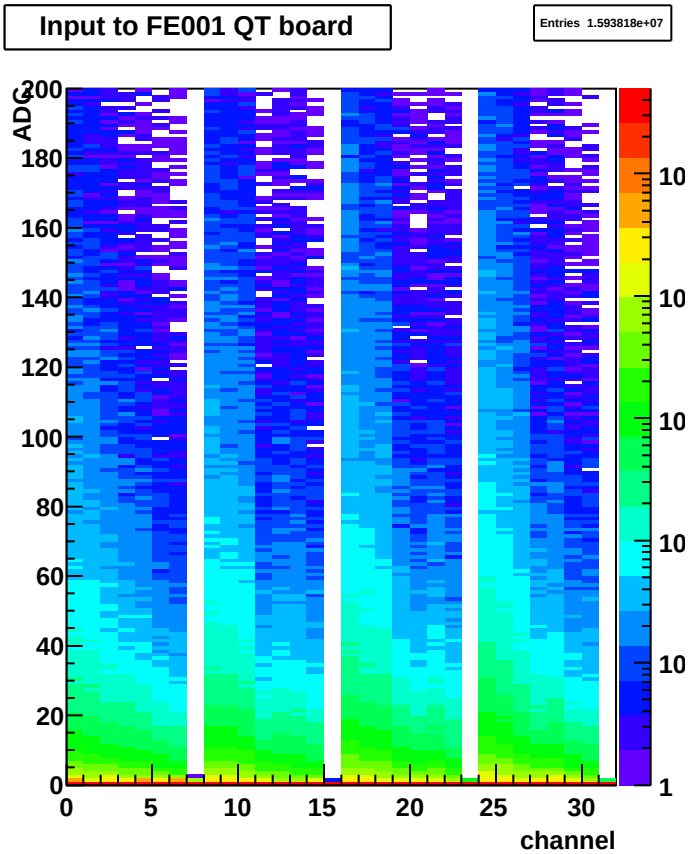

Input to FPD L2 DSM
Entries 1494204
HT bits
104186
53989
37605
20305
8932
9325
373577
435147
451138
SMALL
LARGE-S
LARGE-N

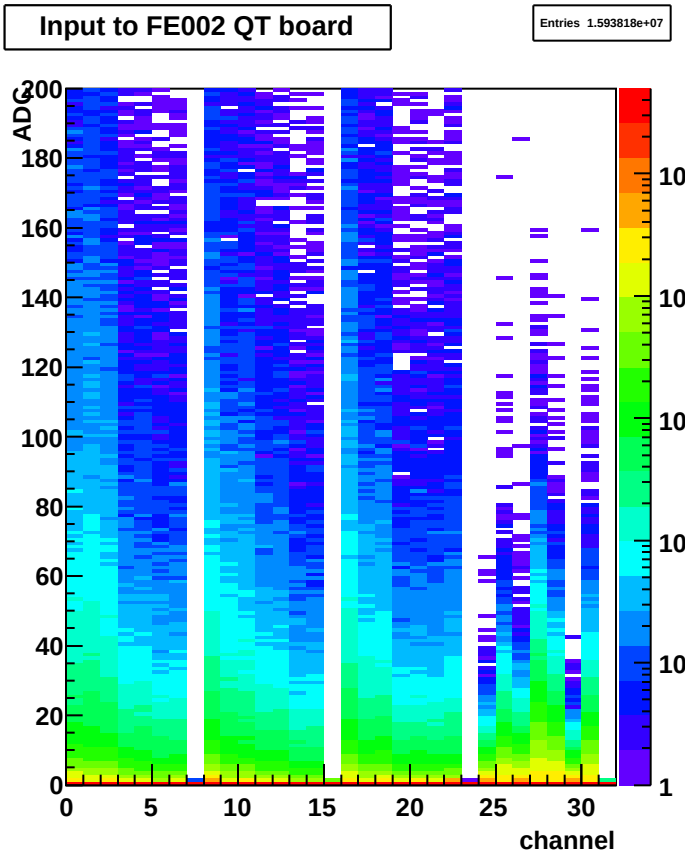

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

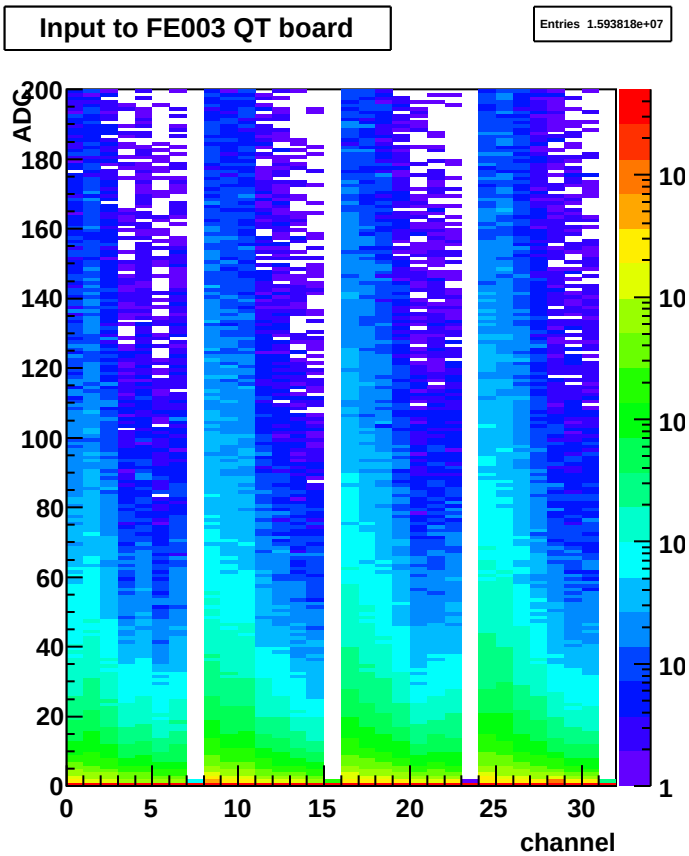

Input to FPD L3 DSM
Entries 7969088
bit
221058
192051
130583
107607
54861
107602
79810
33874
207691
184785
36664
2490
7898
276412
306617
397485
390461
443207
390466
419459
464394
200977
313283
461404
495578
498068
498068
490190
498068
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

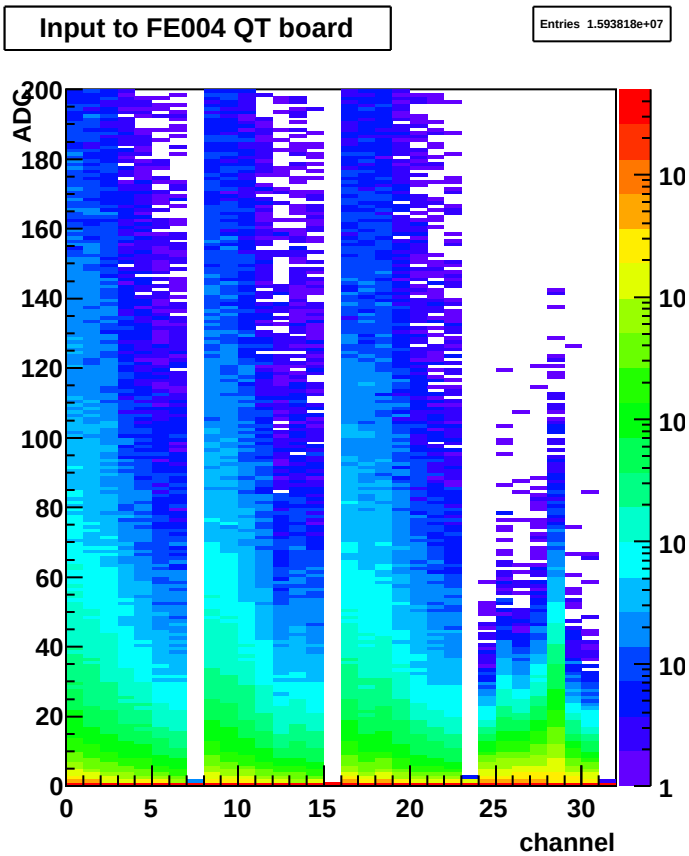

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

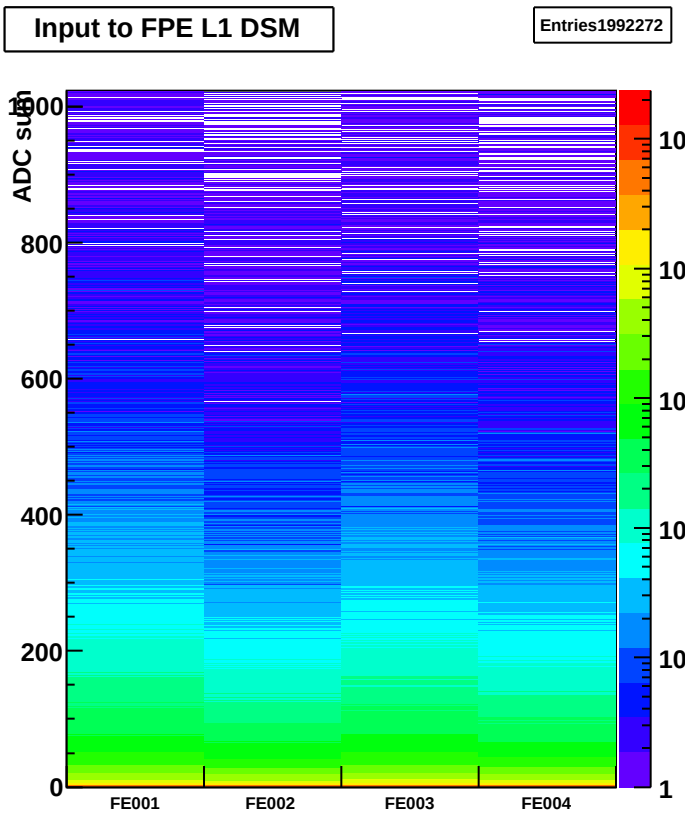

Input to FPD L2 DSM
Entries 1992272
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

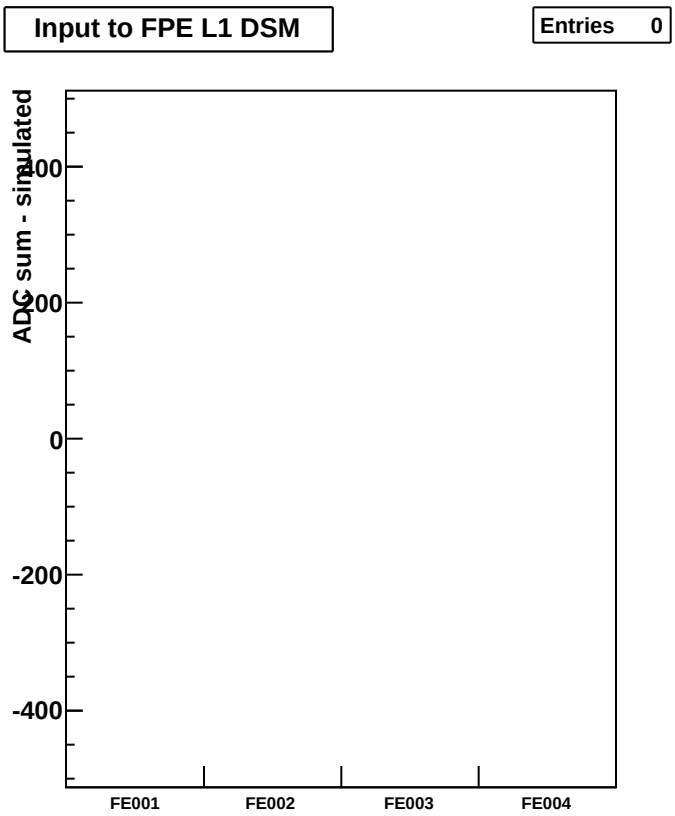

Input to FE001 QT board
Entries 1.593818e+07
ADC
channel

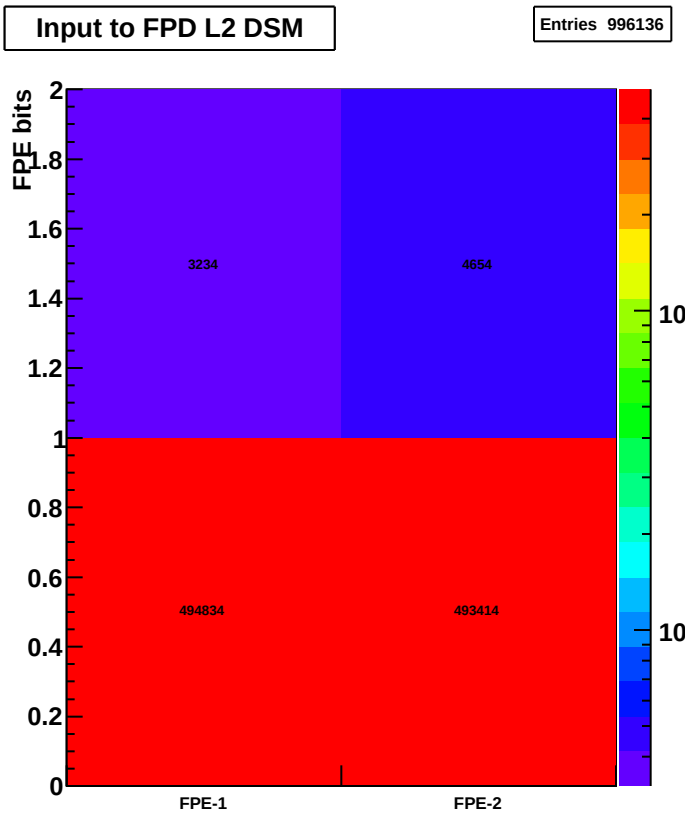

Input to FE002 QT board
Entries 1.593818e+07
ADC
channel

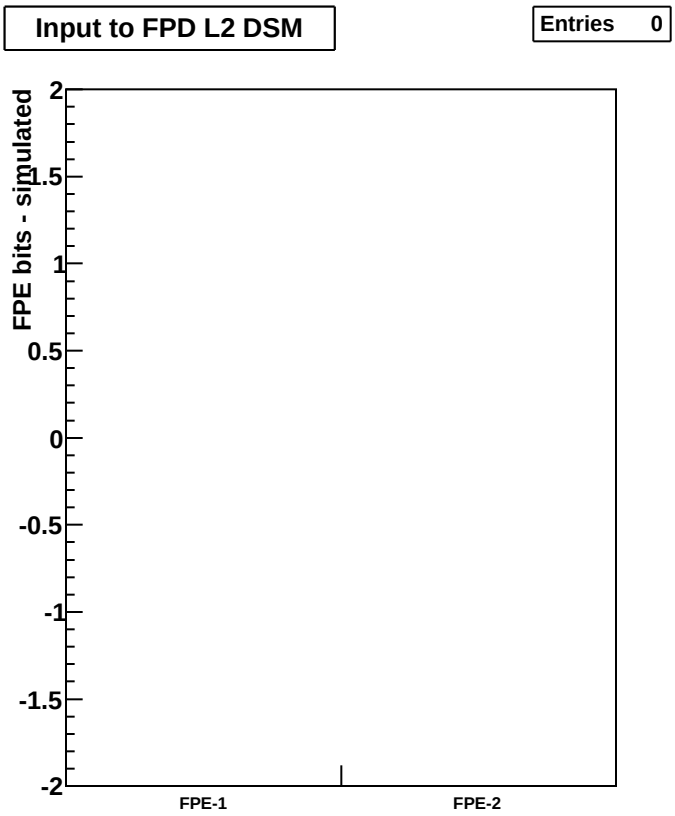

Input to FE003 QT board
Entries 1.593818e+07
ADC
channel


Input to FE004 QT board
Entries 1.593818e+07
ADC
channel


Input to FPE L1 DSM
Entries1992272
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 996136
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
3234
4654
494834
493414
FPE-1
FPE-2
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2