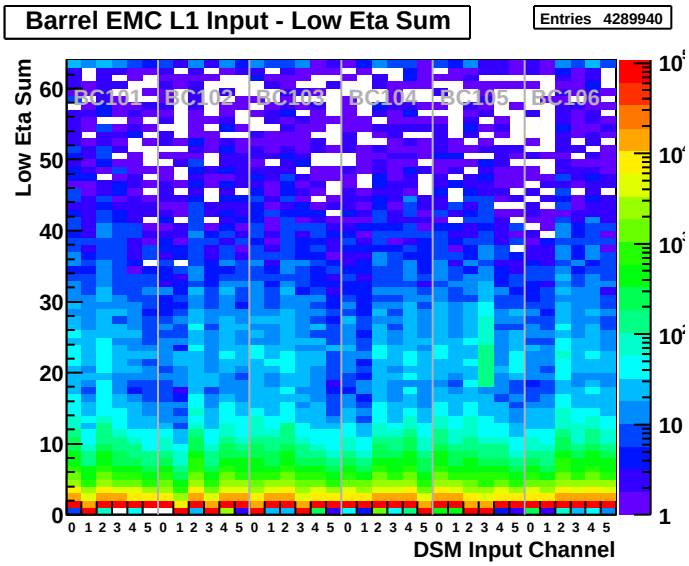
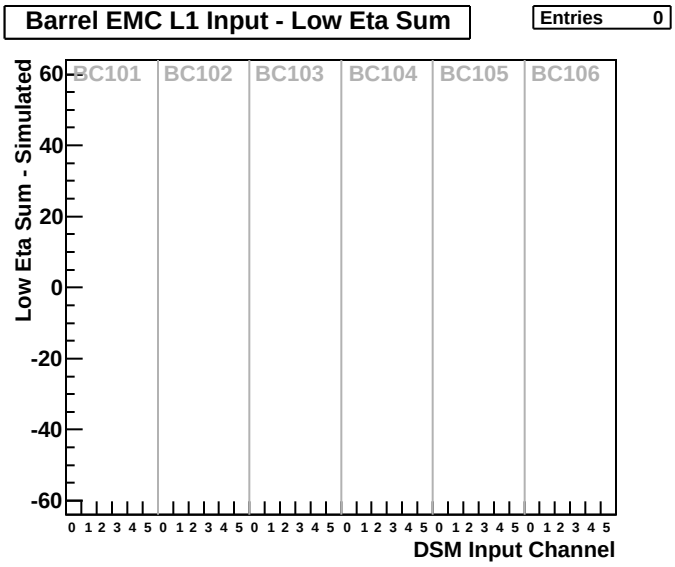
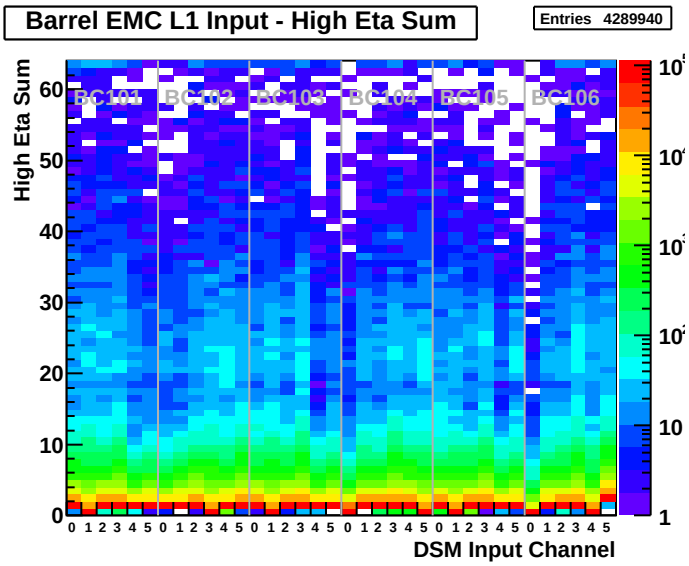
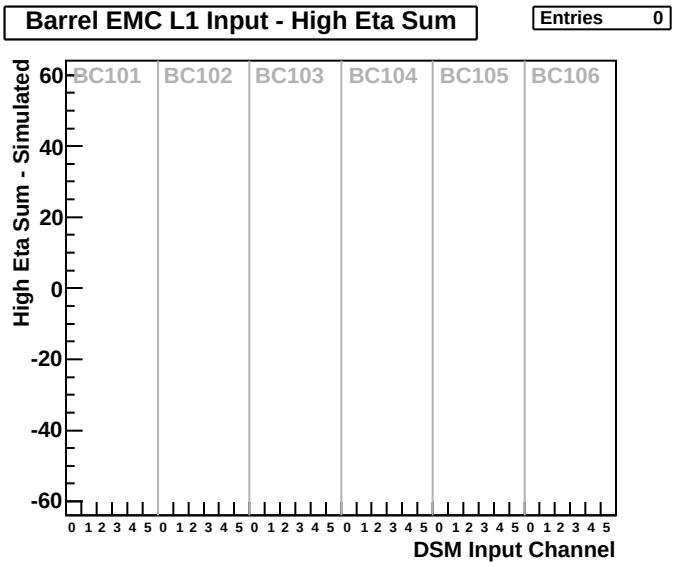
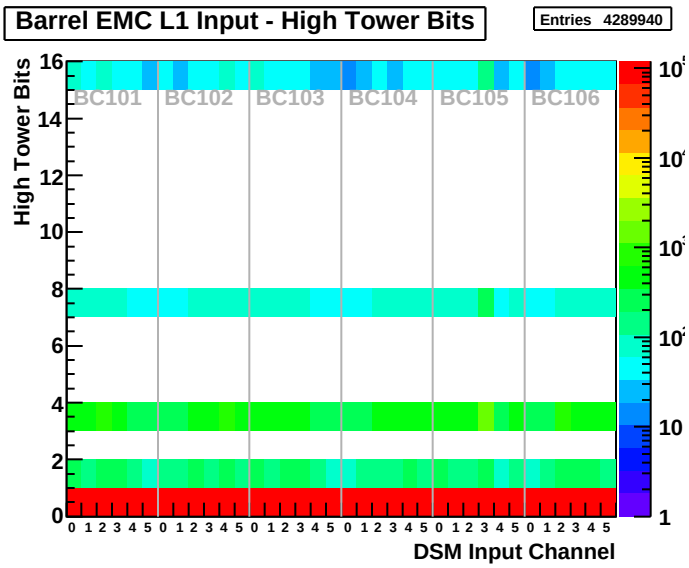
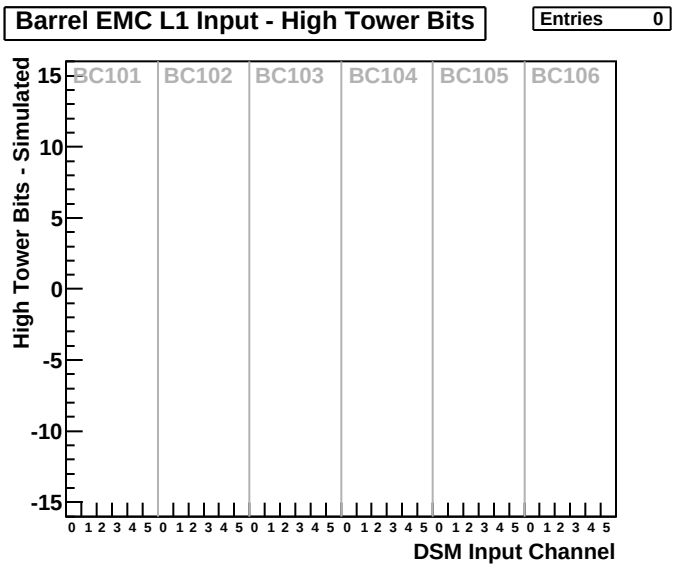

Barrel EMC L1 Input - Low Eta Sum
Entries 4289940
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

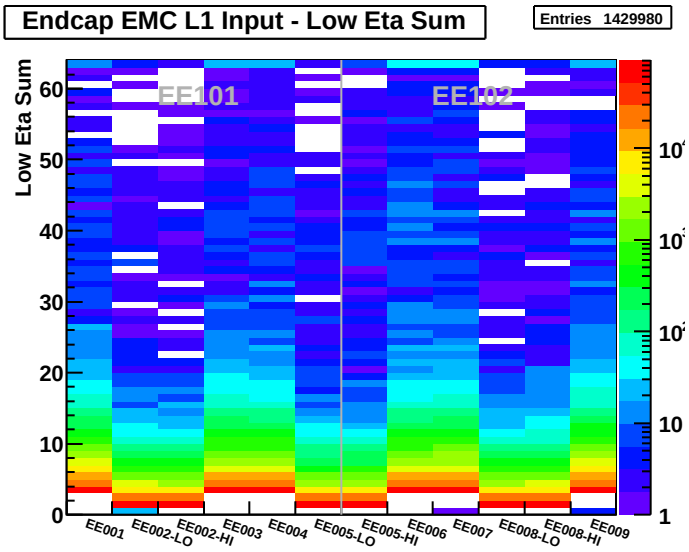

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

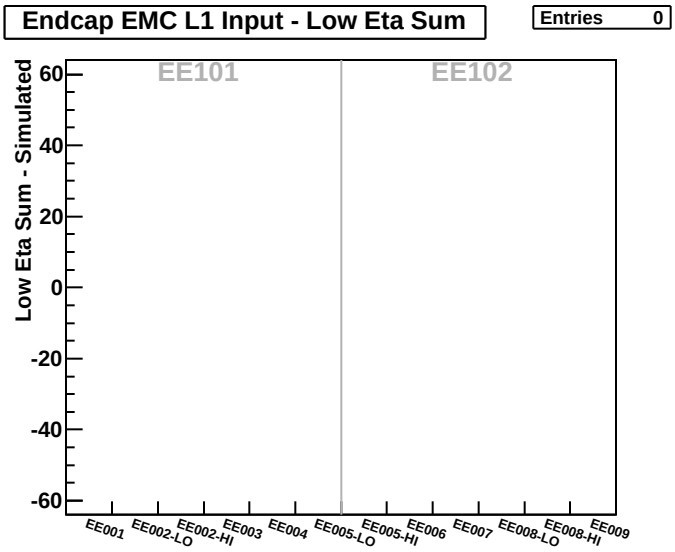

Barrel EMC L1 Input - High Eta Sum
Entries 4289940
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

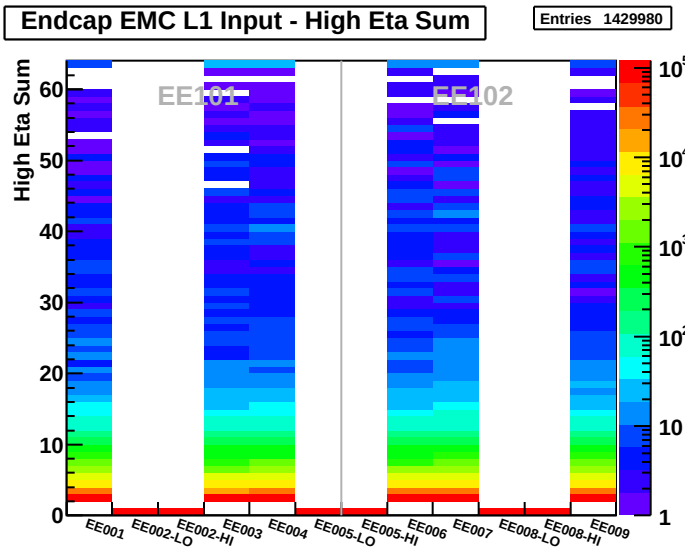

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

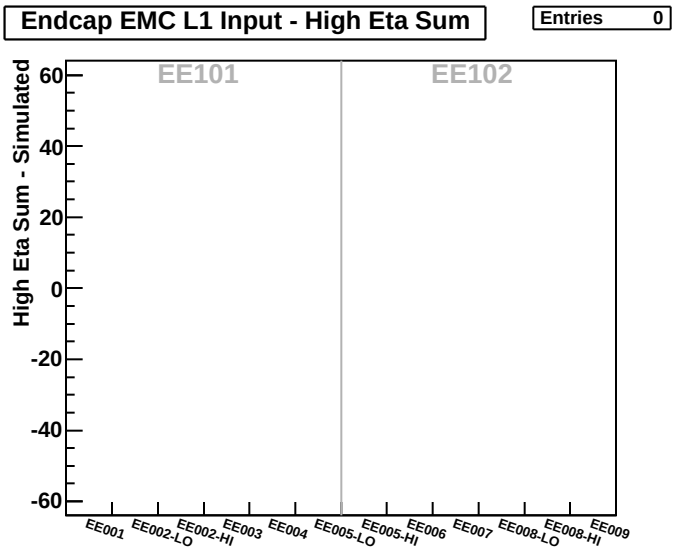

Barrel EMC L1 Input - High Tower Bits
Entries 4289940
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

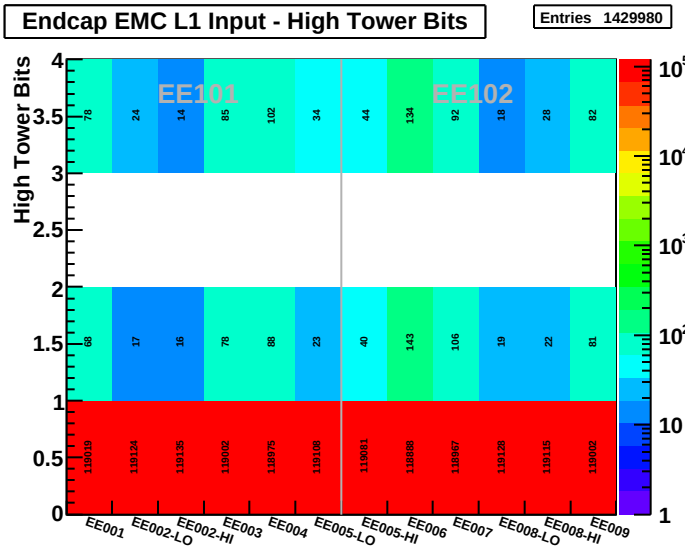

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

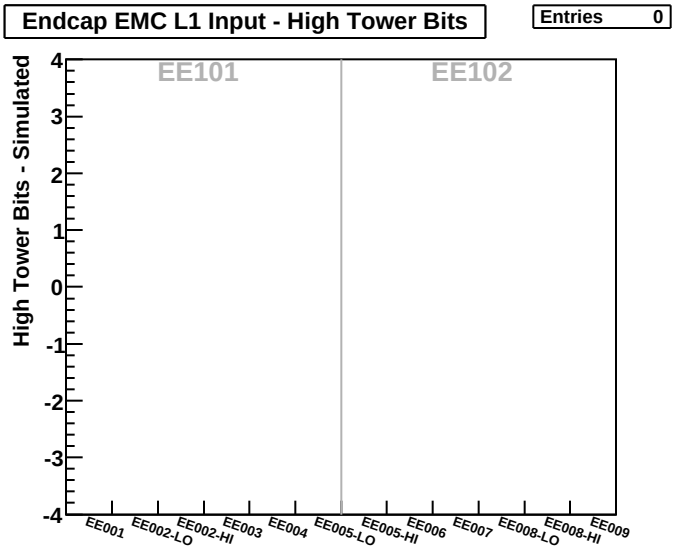

Endcap EMC L1 Input - Low Eta Sum
Entries 1429980
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

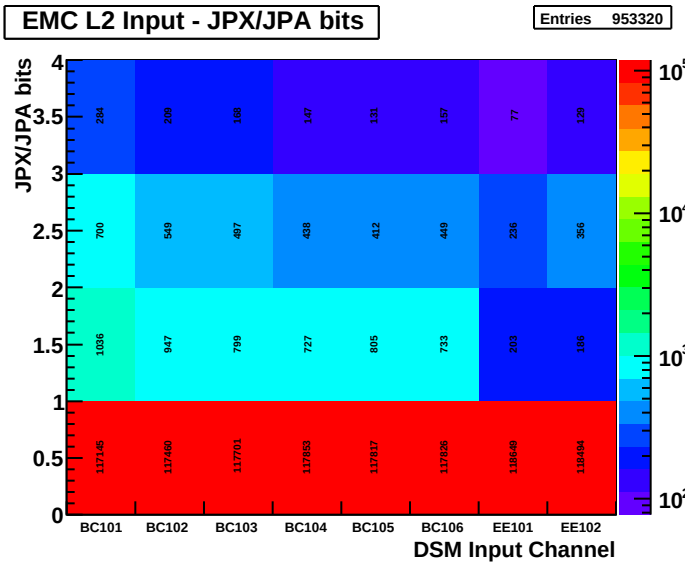

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

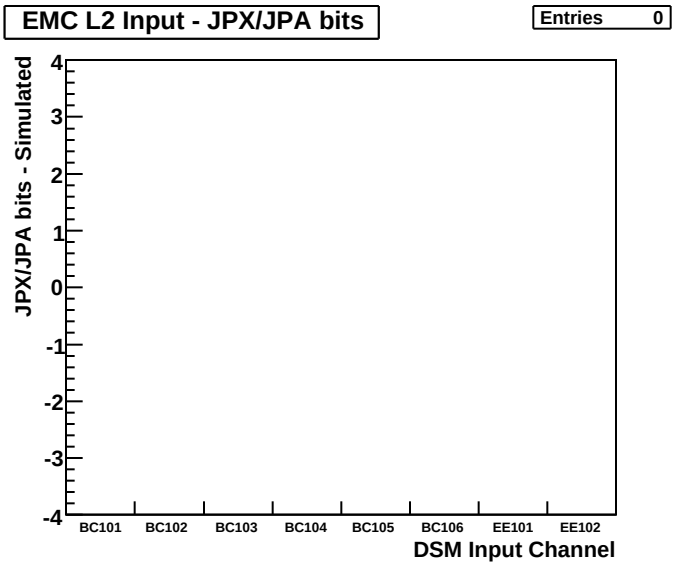

Endcap EMC L1 Input - High Eta Sum
Entries 1429980
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

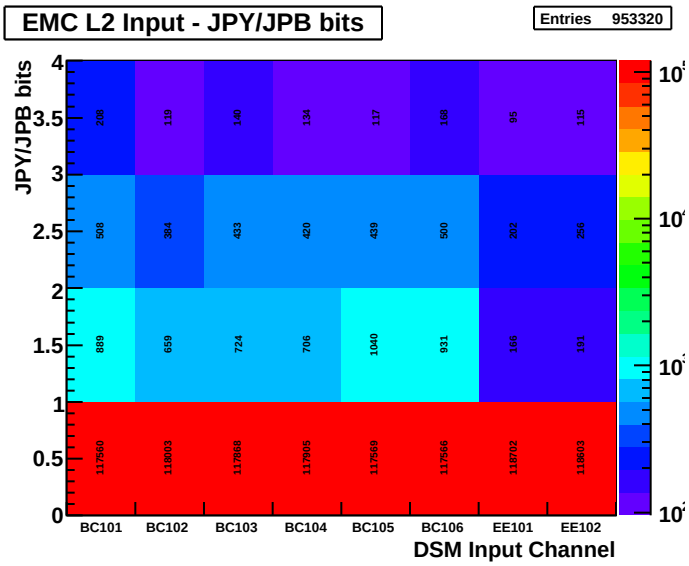

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

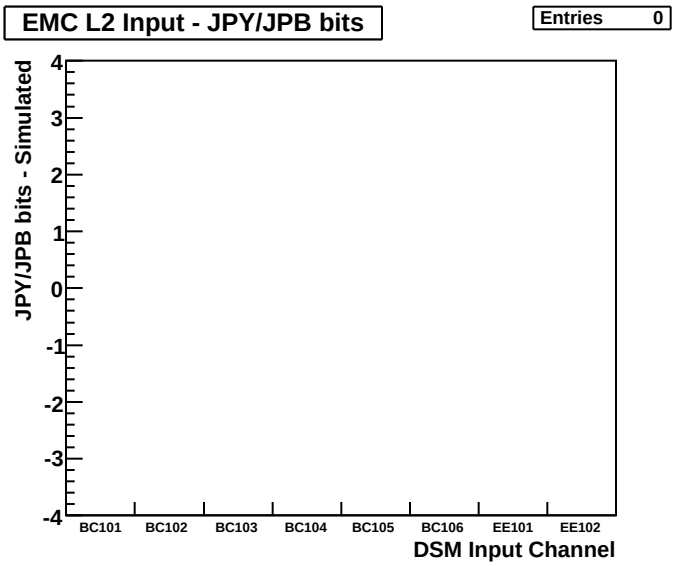

Endcap EMC L1 Input - High Tower Bits
Entries 1429980
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

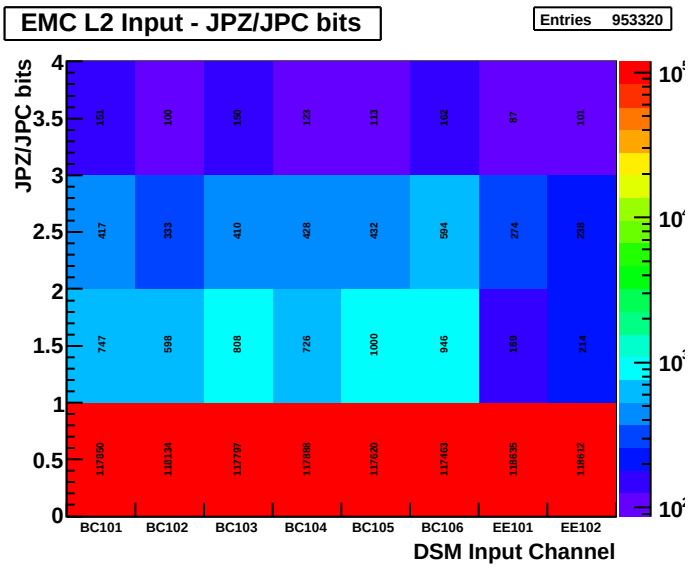

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

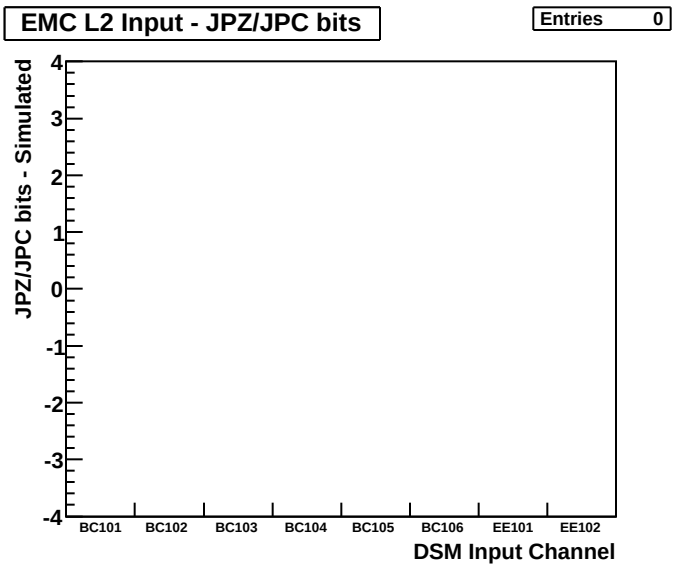

EMC L2 Input - JPX/JPA bits
Entries 953320
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
284 209 168 147 131 157 77 129
700 549 497 438 412 449 236 356
1036 947 799 727 805 733 203 186
117145 117460 117701 117853 117817 117826 118649 118494

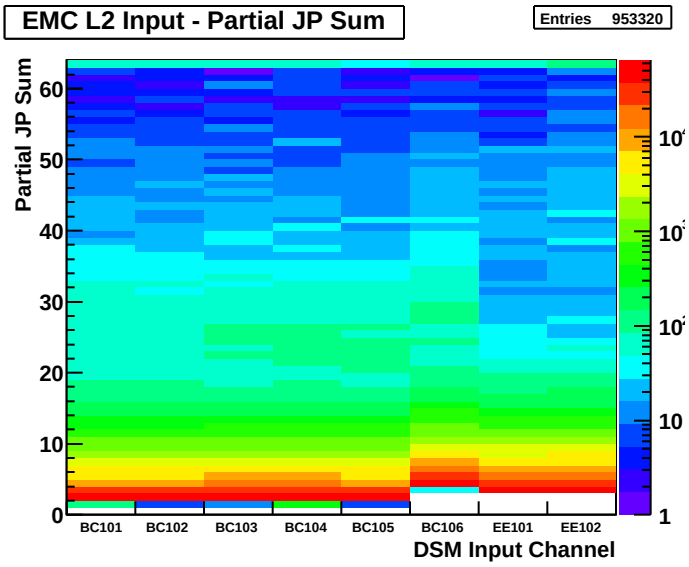

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

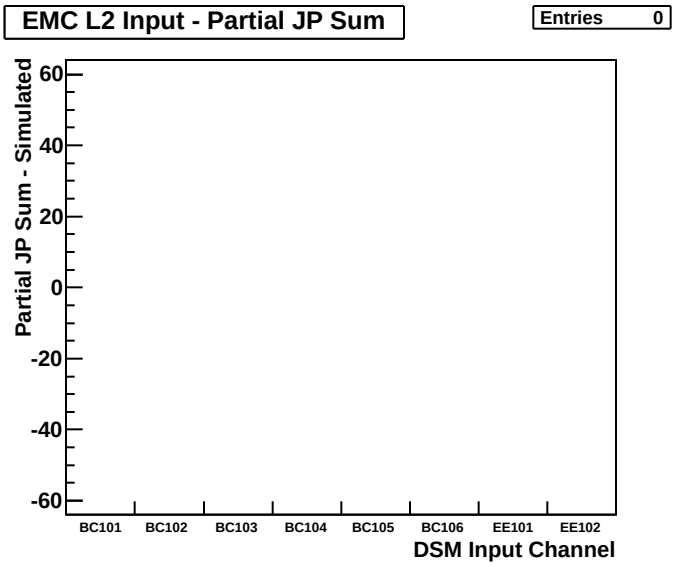

EMC L2 Input - JPY/JPB bits
Entries 953320
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
208 119 140 134 117 168 95 115
508 384 433 420 439 500 202 256
889 659 724 706 1040 931 166 191
117560 118003 117868 117905 117569 117566 118702 118603

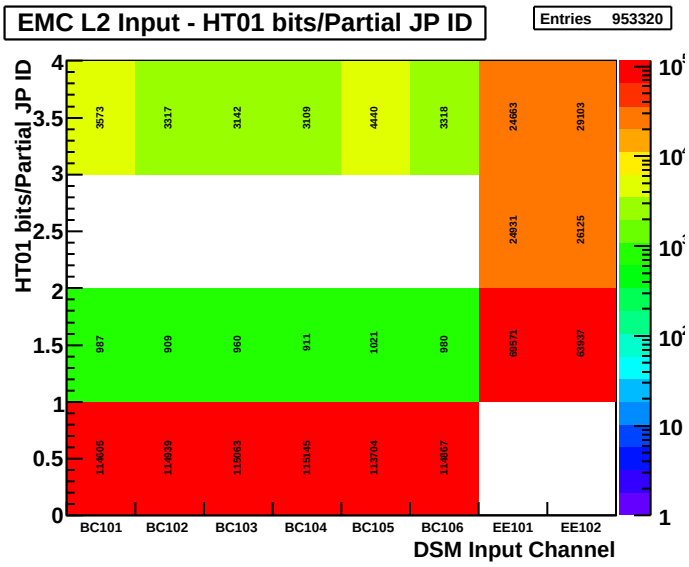

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

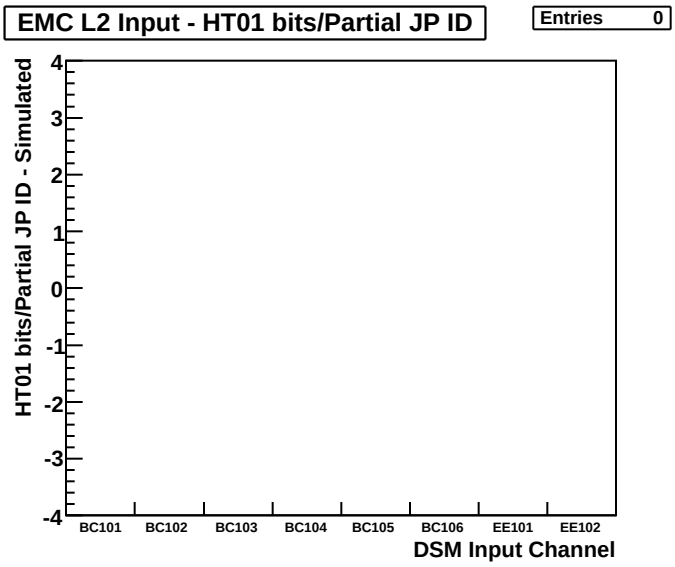

EMC L2 Input - JPZ/JPC bits
Entries 953320
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
151 100 150 123 113 162 87 101
417 333 410 428 432 594 274 238
747 598 808 726 1000 946 169 214
117850 118134 117797 117888 117620 117463 118535 118612

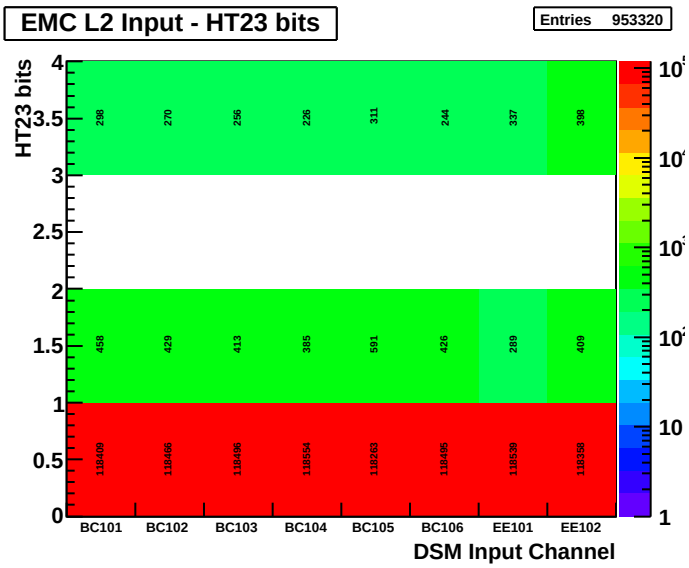

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

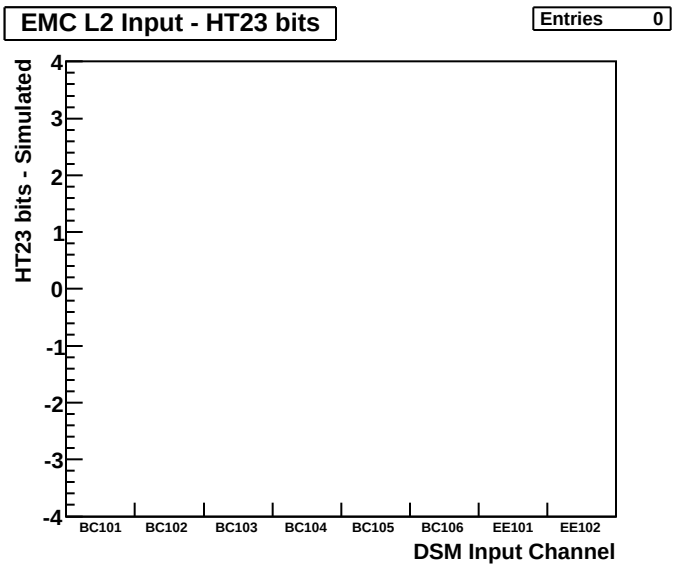

EMC L2 Input - Partial JP Sum
Entries 953320
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

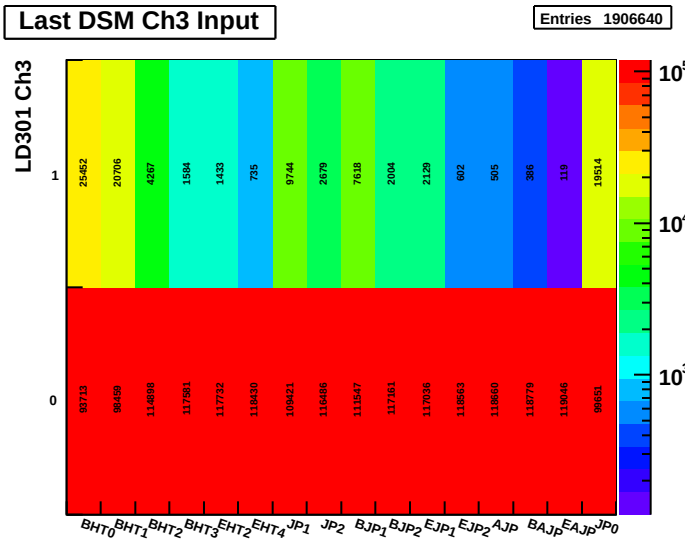

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

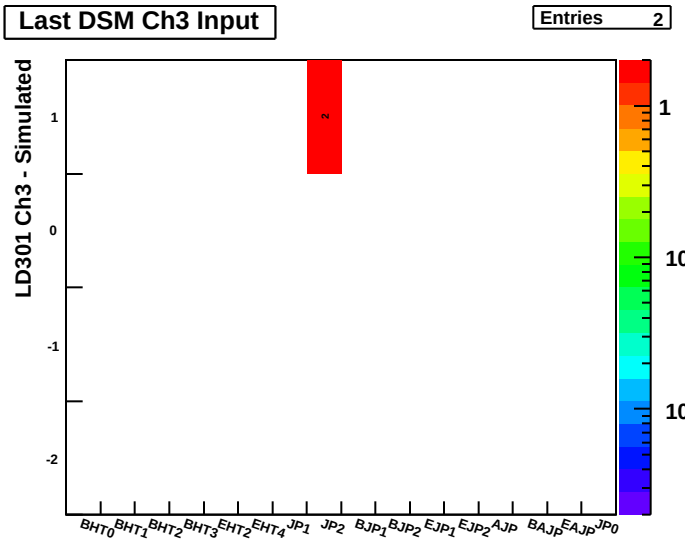

EMC L2 Input - HT01 bits/Partial JP ID
Entries 953320
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

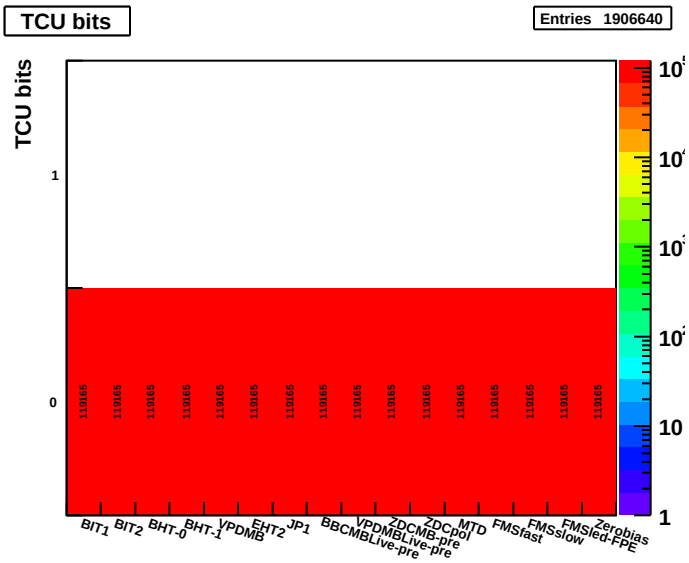

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

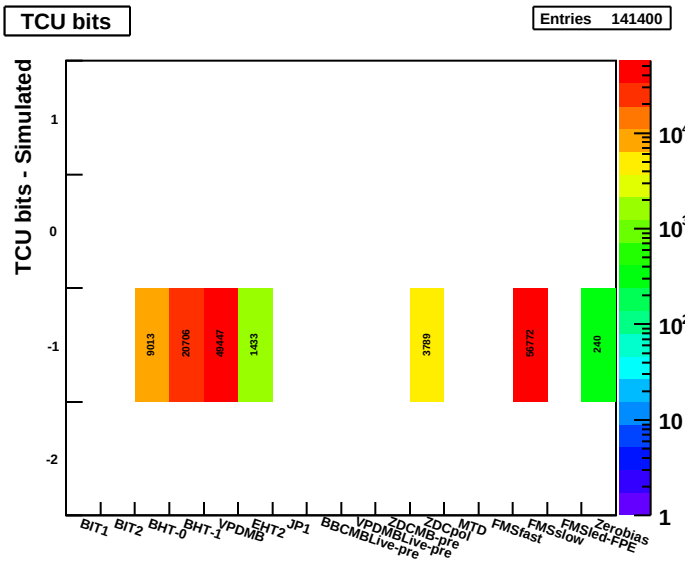

EMC L2 Input - HT23 bits
Entries 953320
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

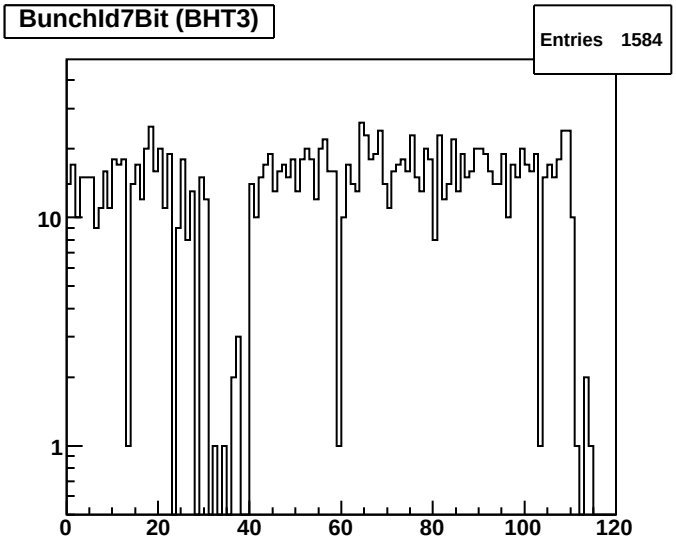

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

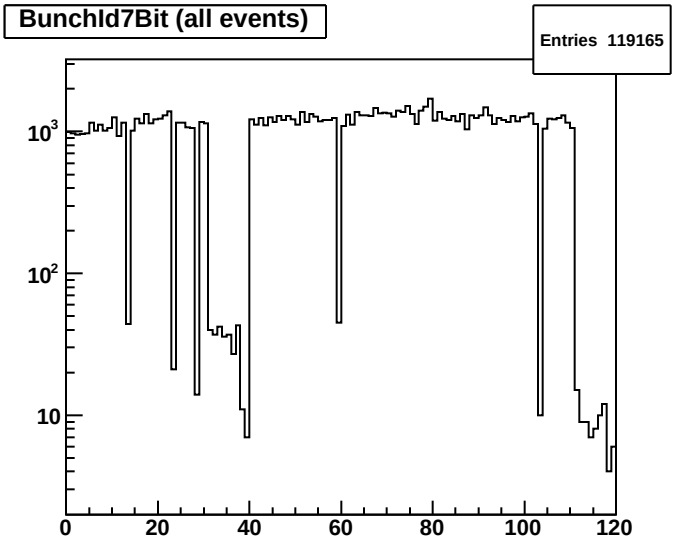

Last DSM Ch3 Input
Entries 1906640
LD301 Ch3
25452 20706 4267 1584 1433 735 9744 2679 7618 2004 2129 602 505 386 119 19514
93713 99499 114898 117581 117732 118430 109421 116486 111547 117161 117036 118563 118660 118779 119046 99651
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

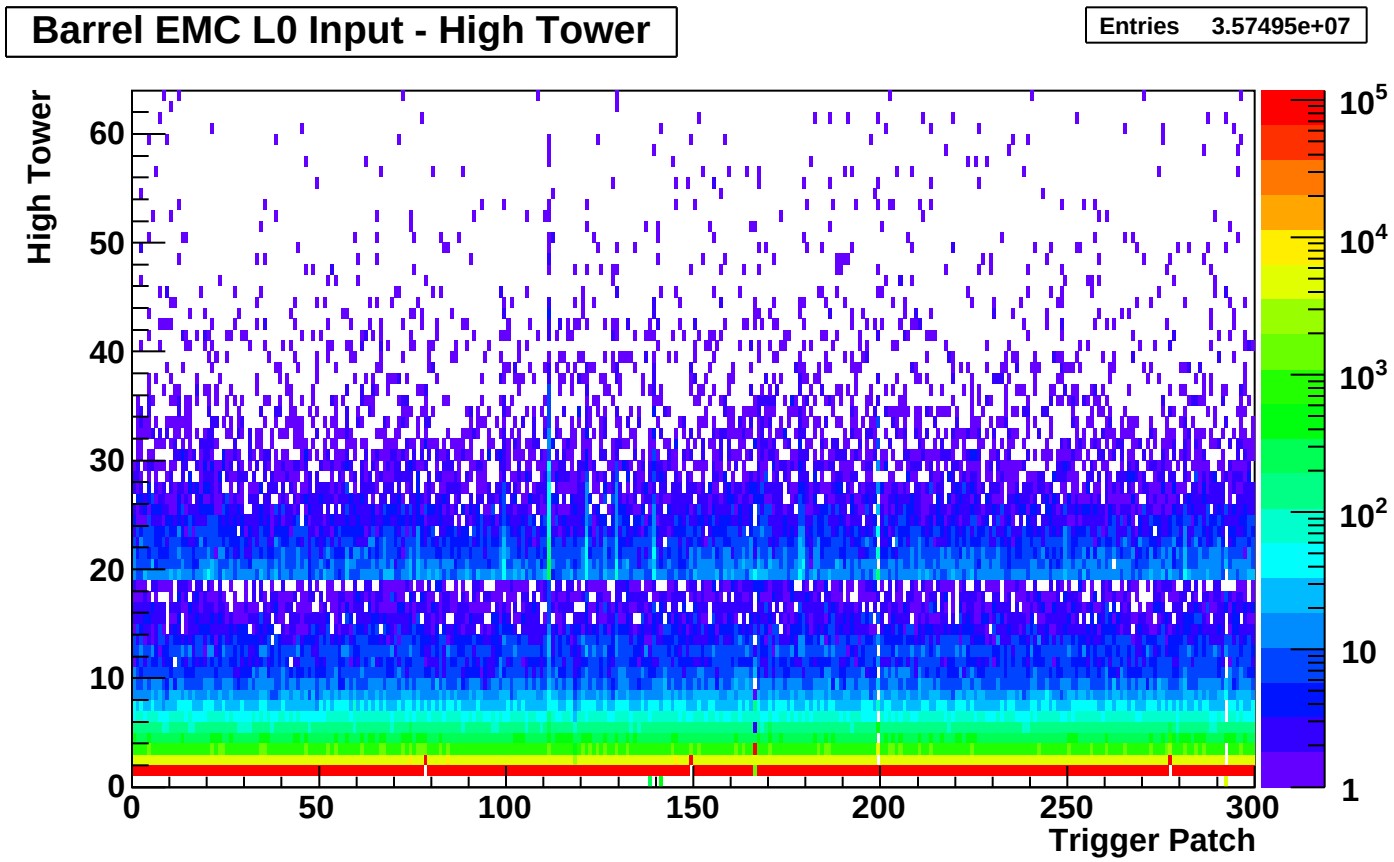

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

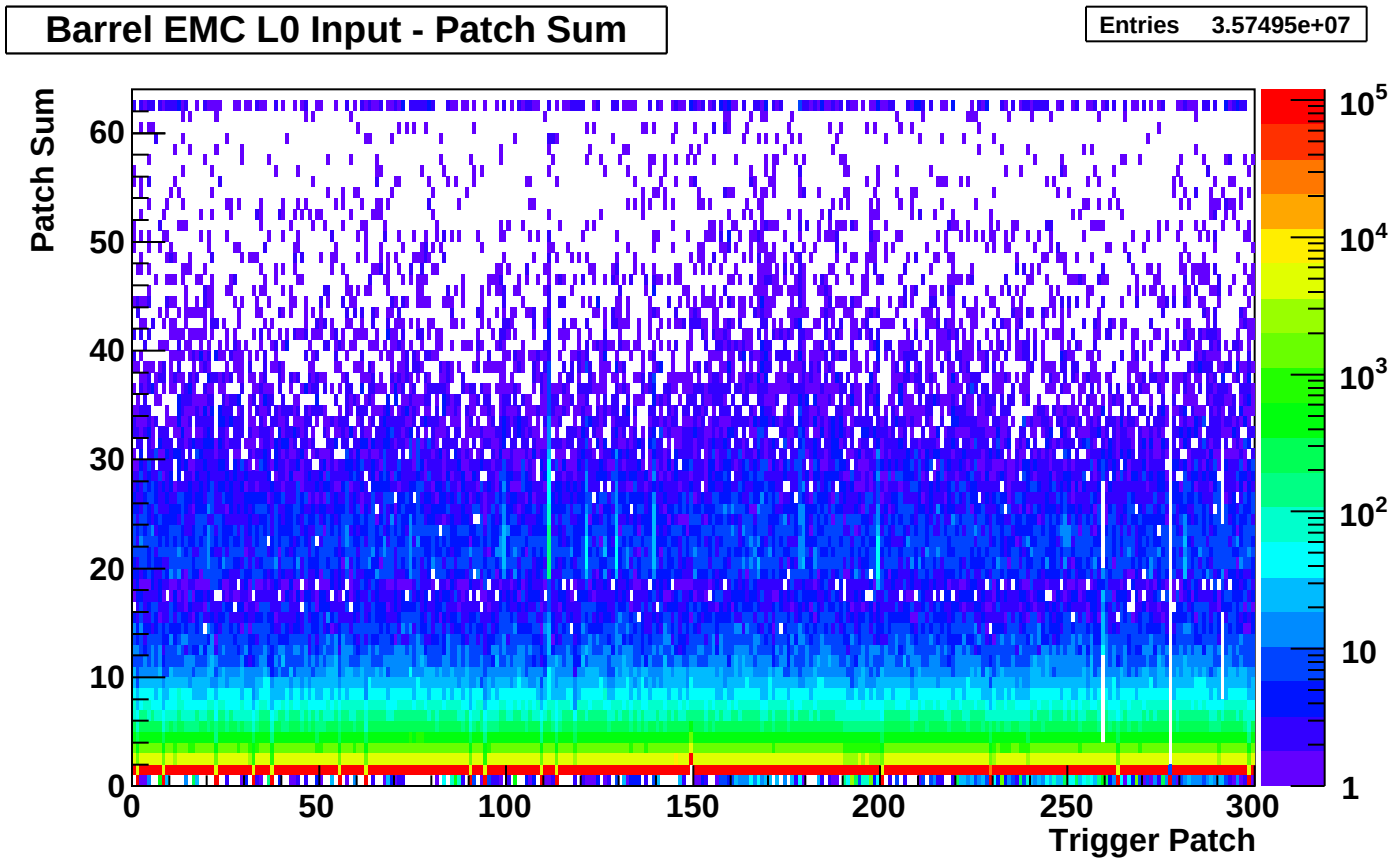

TCU bits
Entries 1906640
TCU bits
119165 119165 119165 119165 119165 119165 119165 119165 119165 119165 119165 119165 119165 119165 119165 119165
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

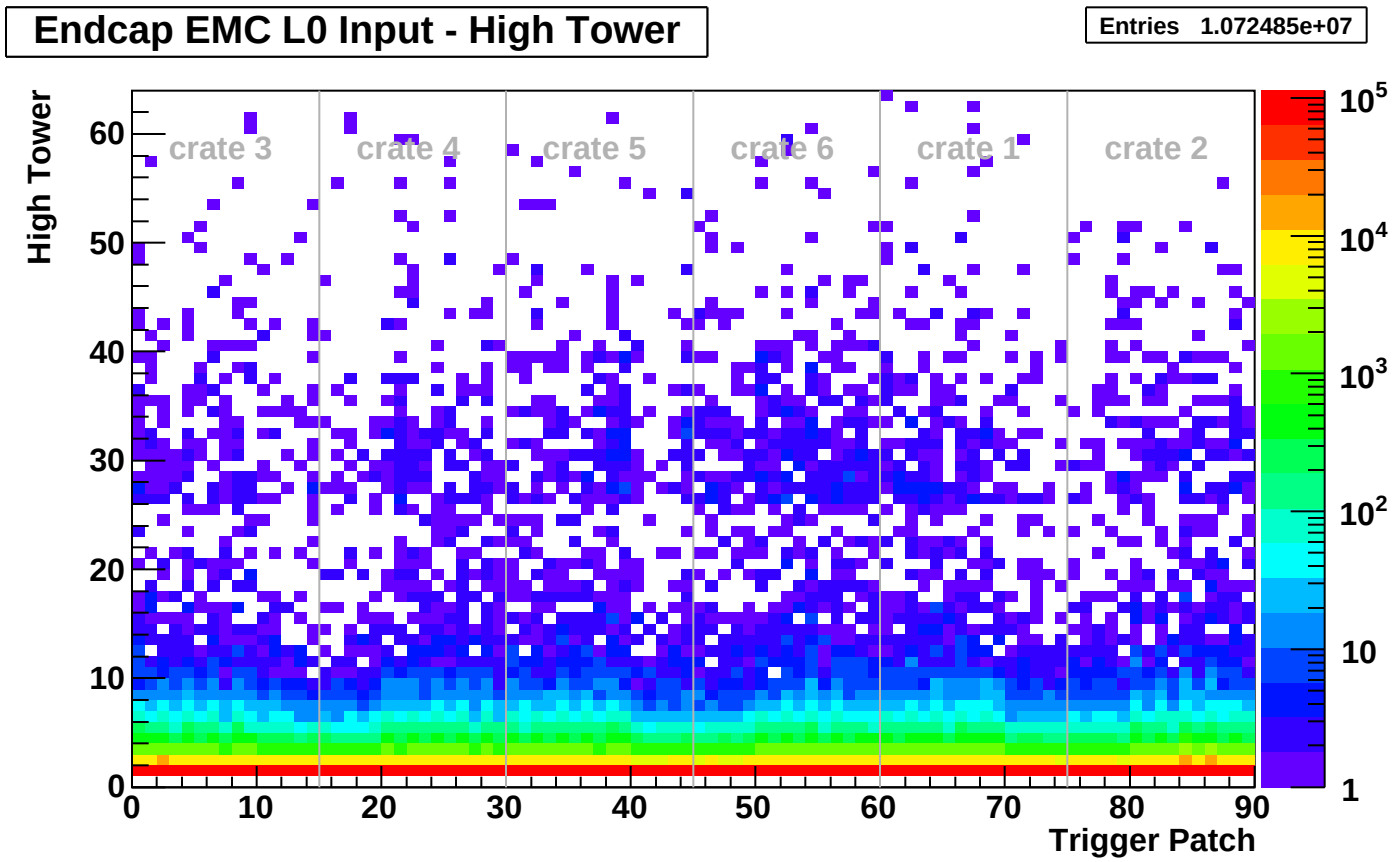

TCU bits
Entries 141400
TCU bits - Simulated
9013 20706 49447 1433 3789 56772 240
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

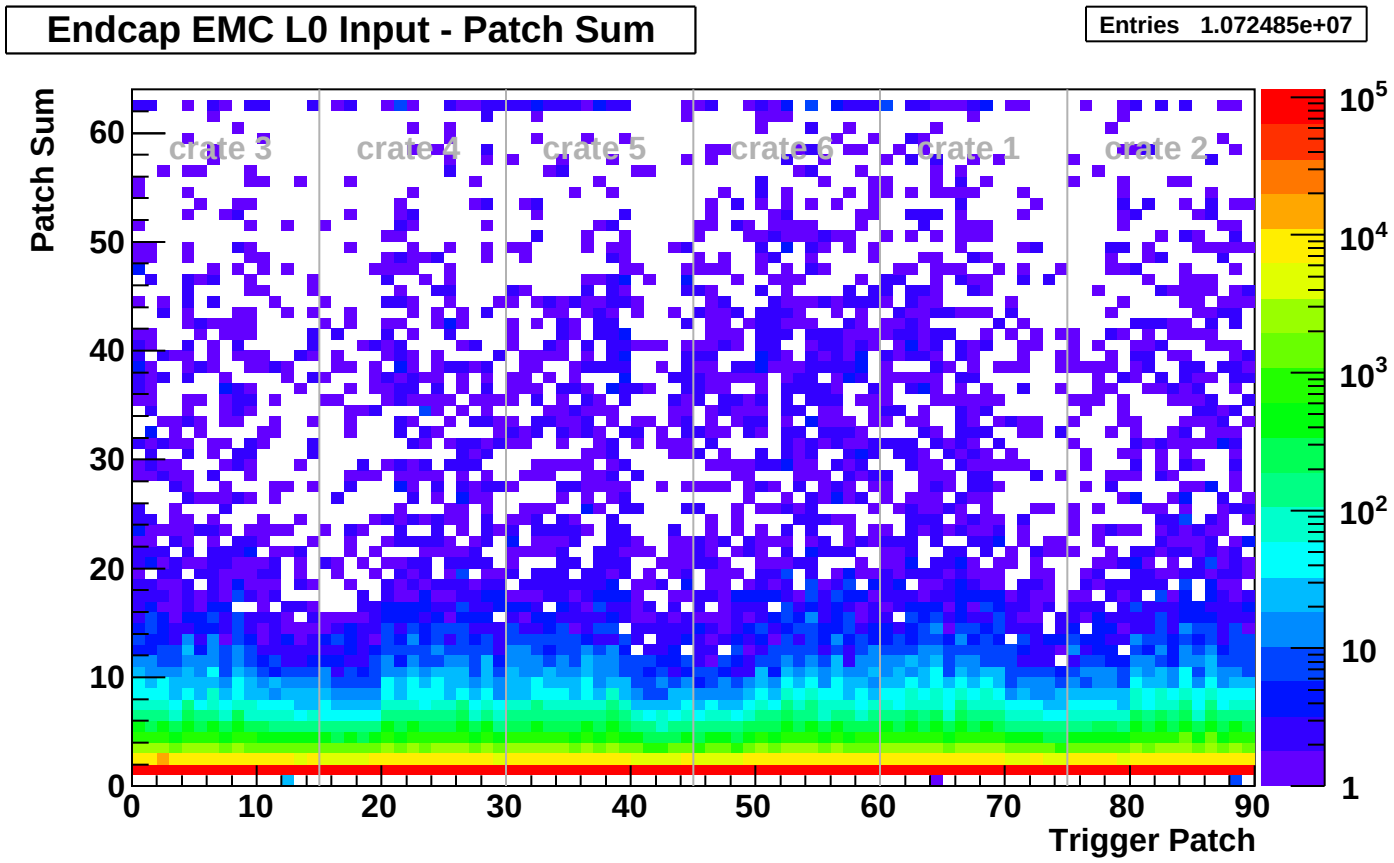

BunchId7Bit (BHT3)
Entries 1584

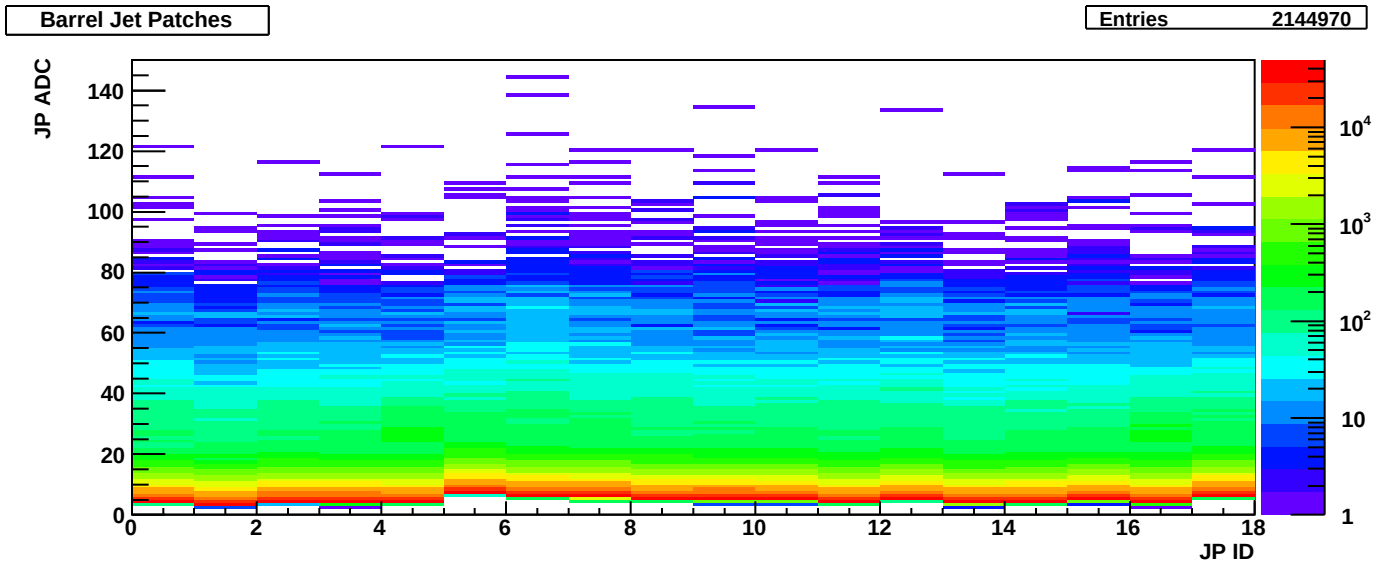

BunchId7Bit (all events)
Entries 119165

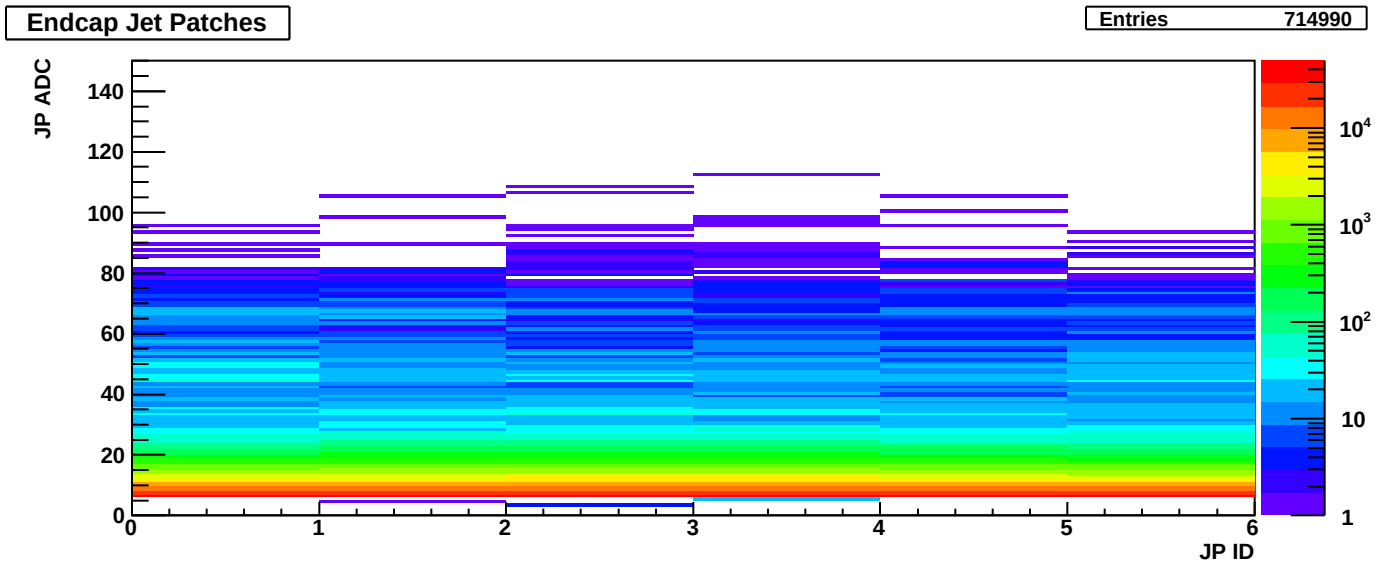

Barrel EMC L0 Input - High Tower
Entries 3.57495e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

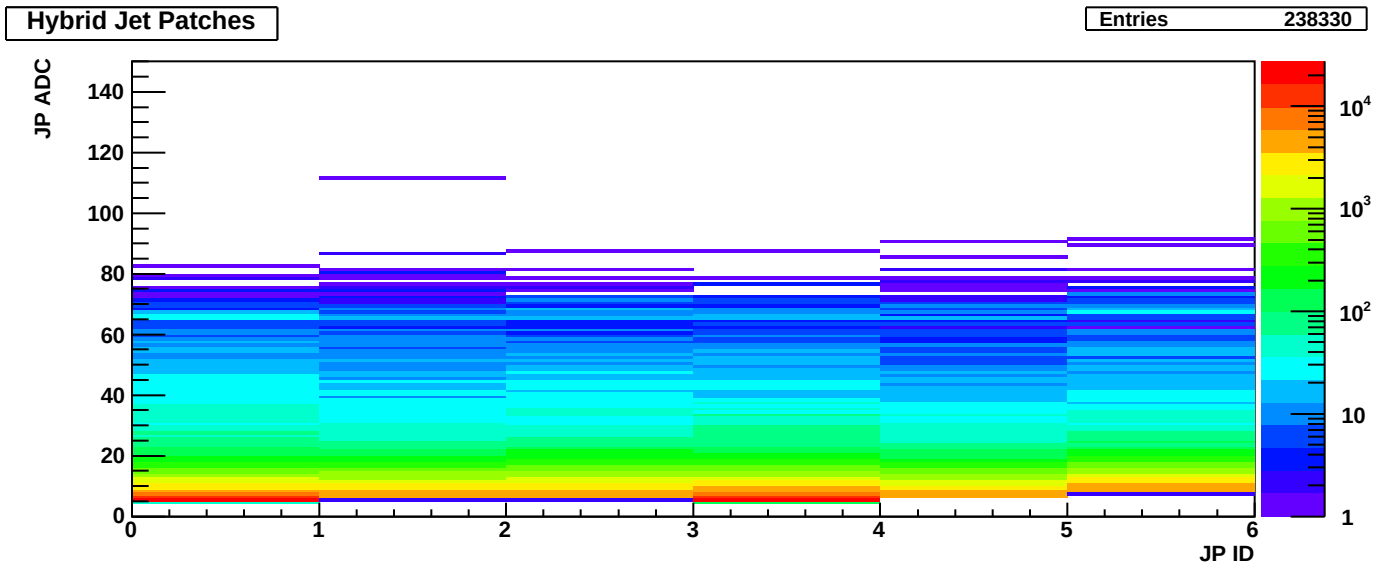

Barrel EMC L0 Input - Patch Sum
Entries 3.57495e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

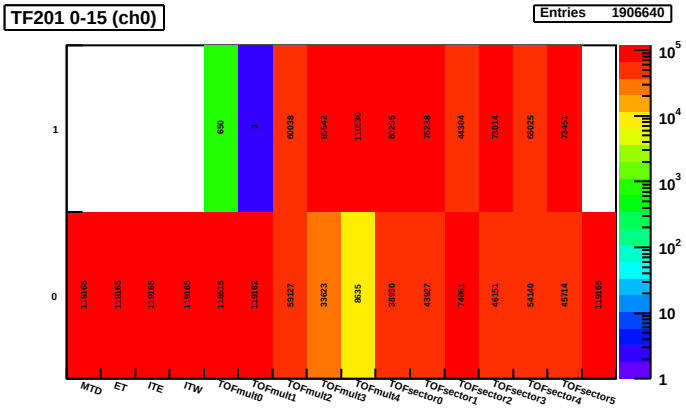

Endcap EMC L0 Input - High Tower
Entries 1.072485e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

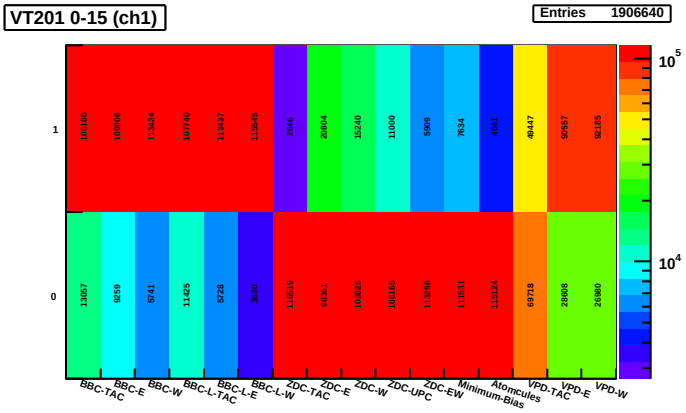

Endcap EMC L0 Input - Patch Sum
Entries 1.072485e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

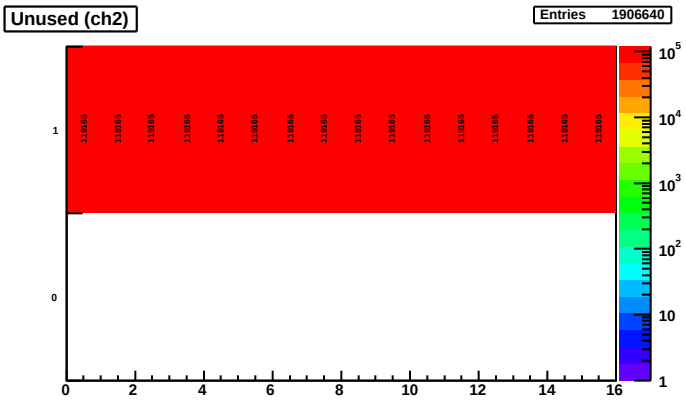

Barrel Jet Patches
Entries 2144970
JP ADC
JP ID

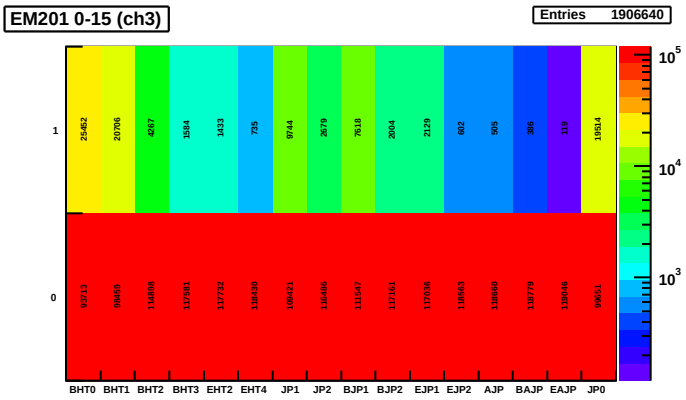

Endcap Jet Patches
Entries 714990
JP ADC
JP ID

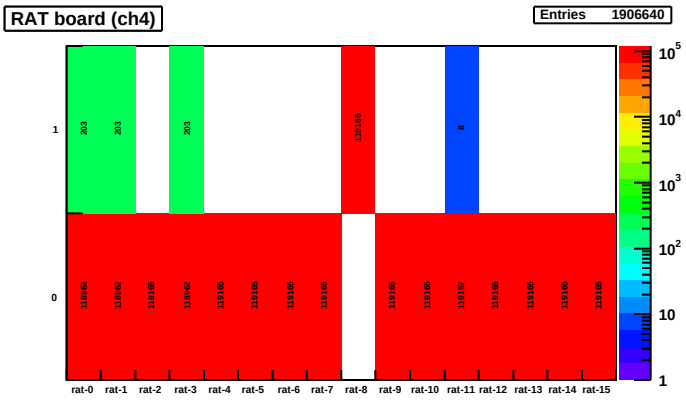

Hybrid Jet Patches
Entries 238330
JP ADC
JP ID

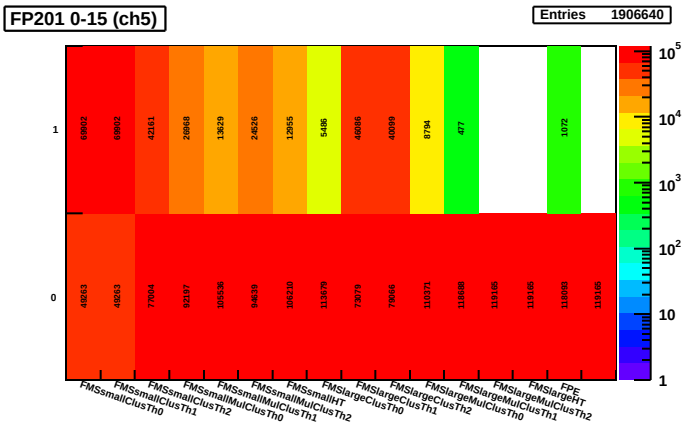

TF201 0-15 (ch0)
Entries 1906640
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

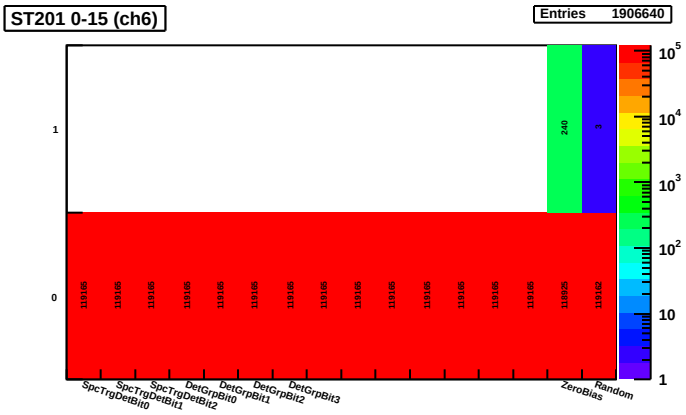

VT201 0-15 (ch1)
Entries 1906640
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

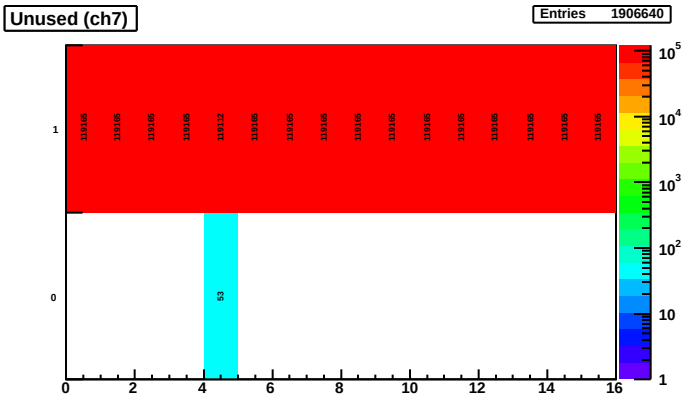

Unused (ch2)
Entries 1906640

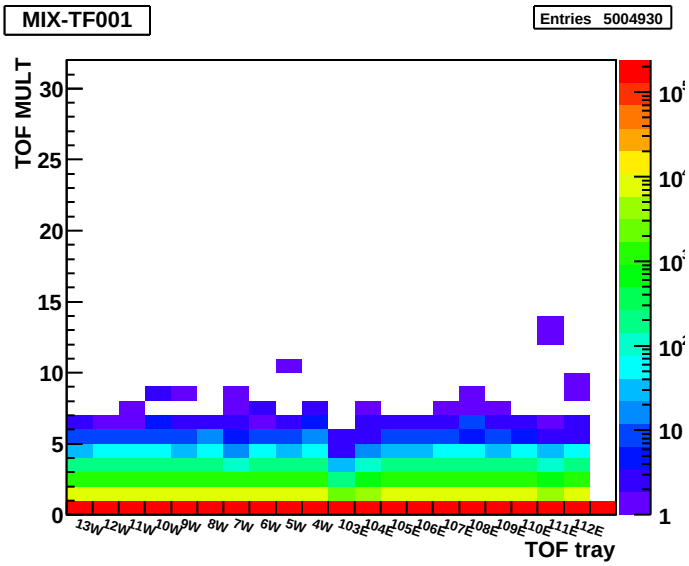

EM201 0-15 (ch3)
Entries 1906640
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

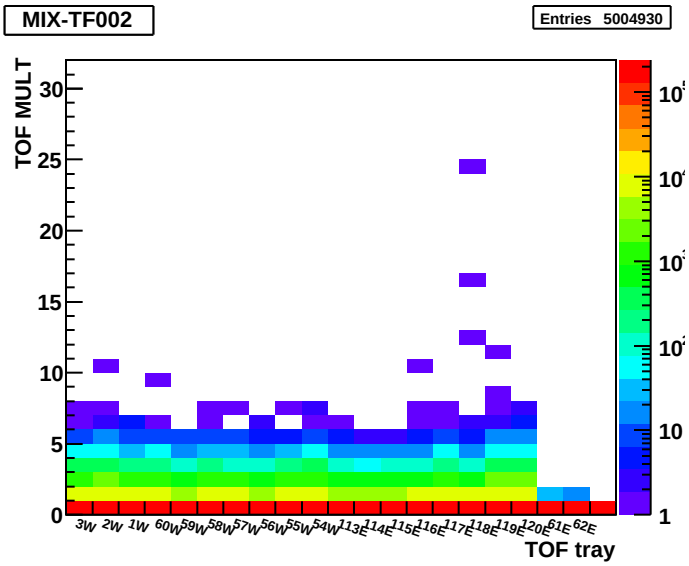

RAT board (ch4)
Entries 1906640
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

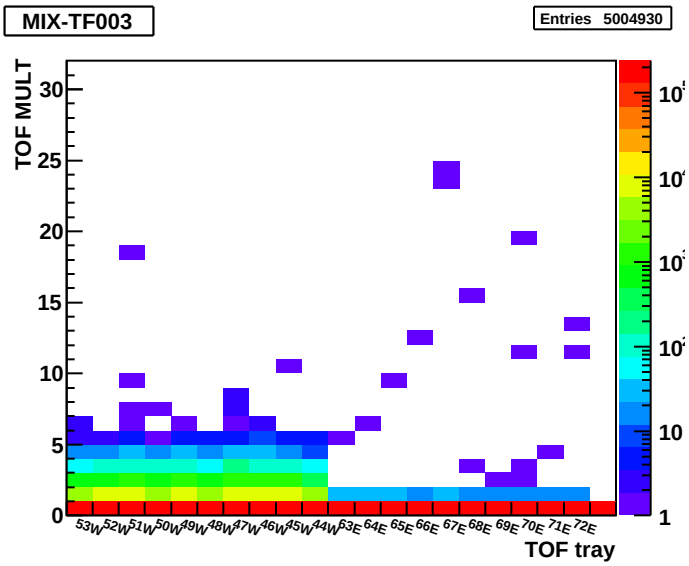

FP201 0-15 (ch5)
Entries 1906640
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

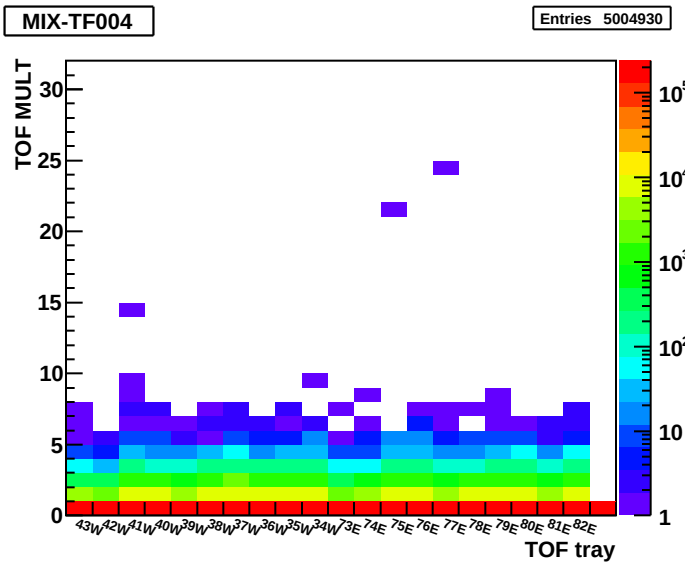

ST201 0-15 (ch6)
Entries 1906640
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

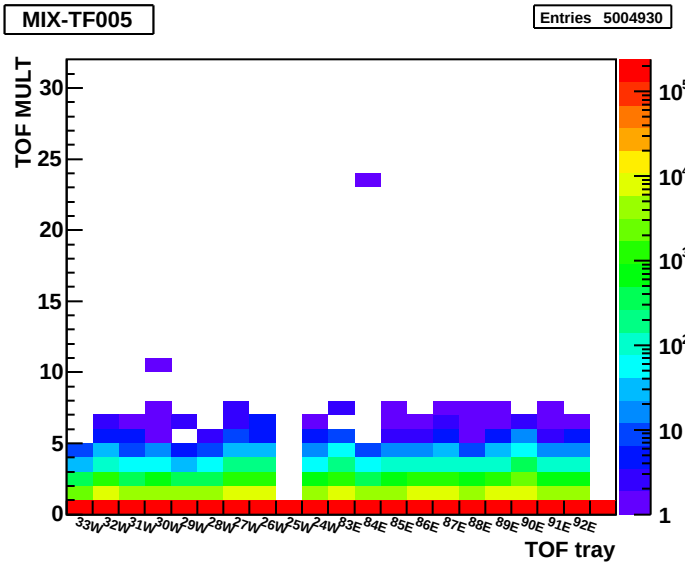

Unused (ch7)
Entries 1906640

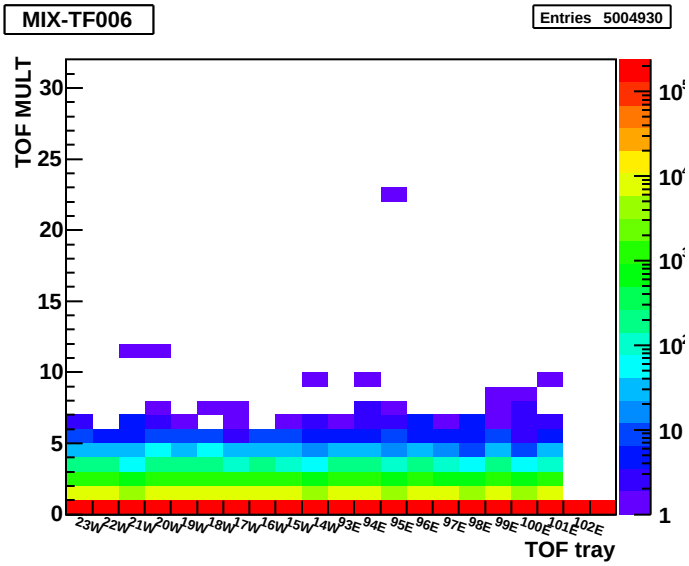

MIX-TF001
Entries 5004930
TOF MULT
TOF tray

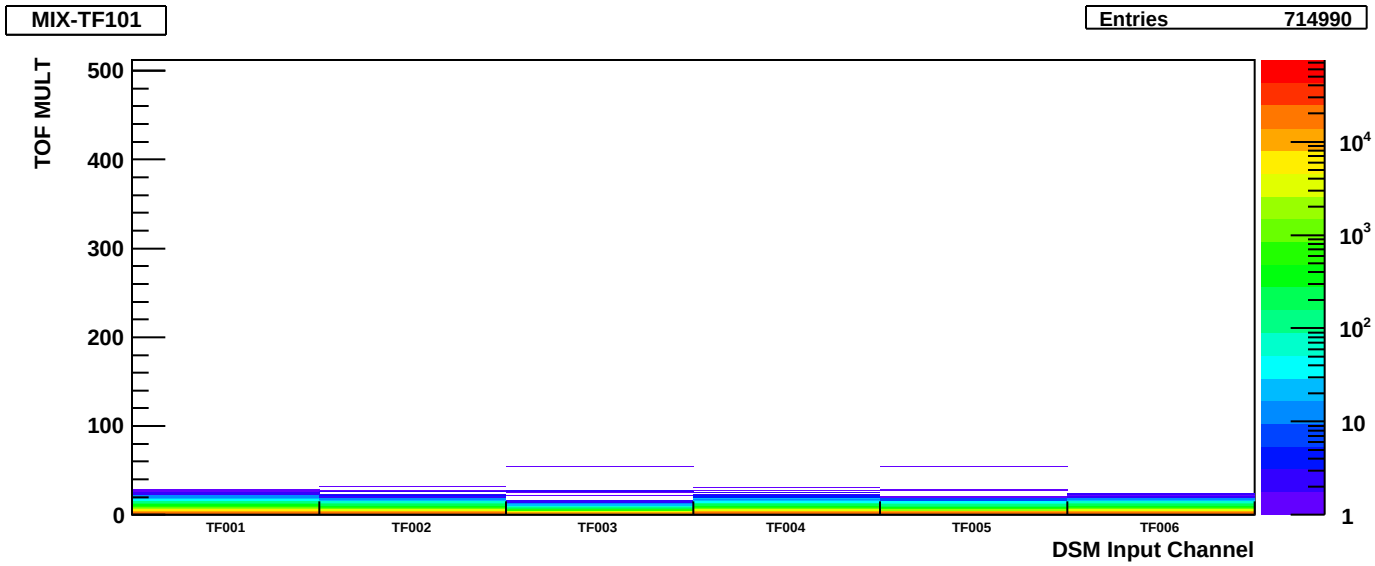

MIX-TF002
Entries 5004930
TOF MULT
TOF tray

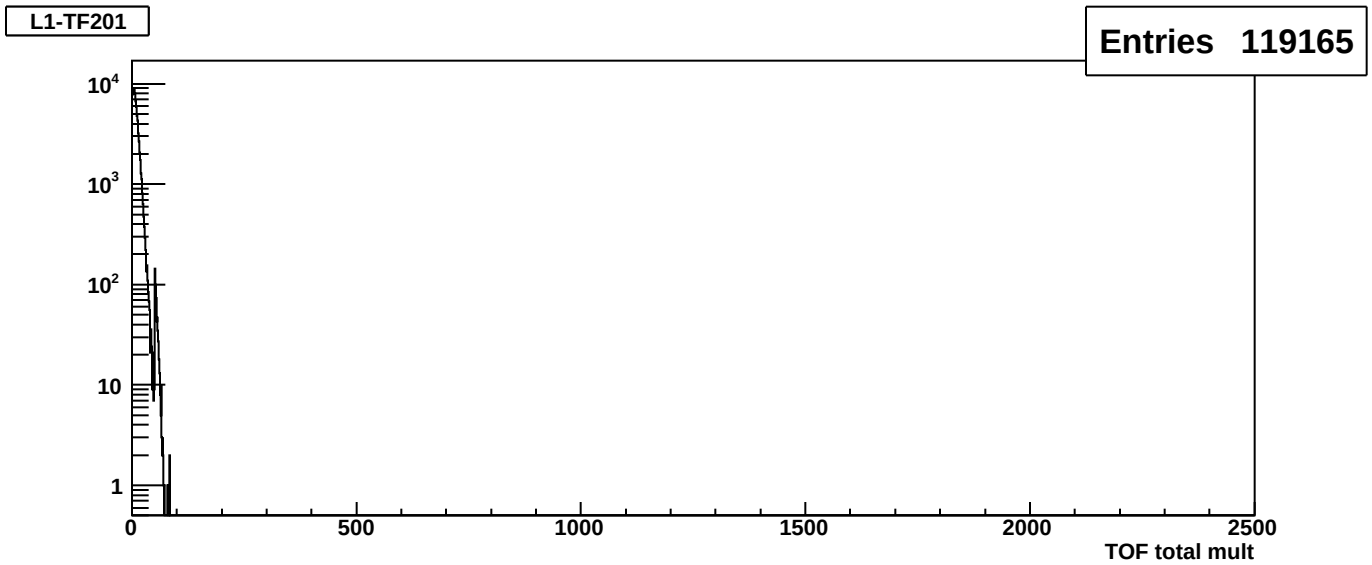

MIX-TF003
Entries 5004930
TOF MULT
TOF tray

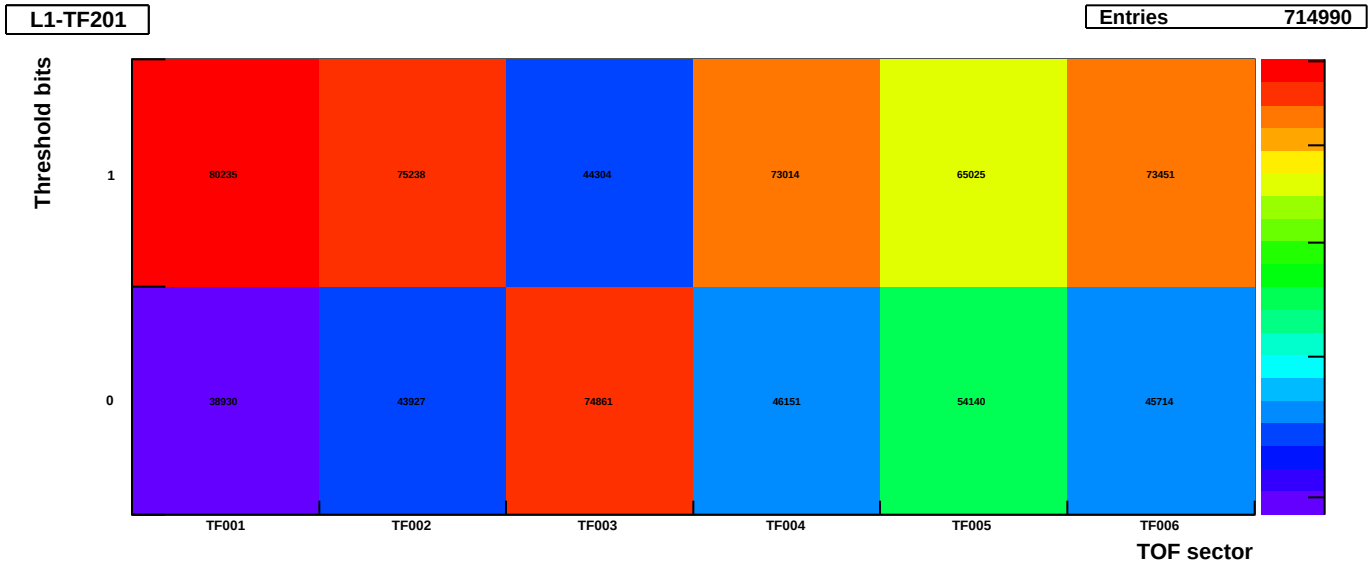

MIX-TF004
Entries 5004930
TOF MULT
TOF tray

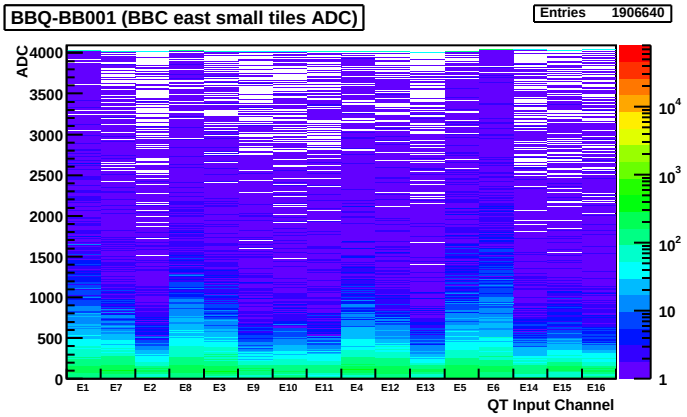

MIX-TF005
Entries 5004930
TOF MULT
TOF tray

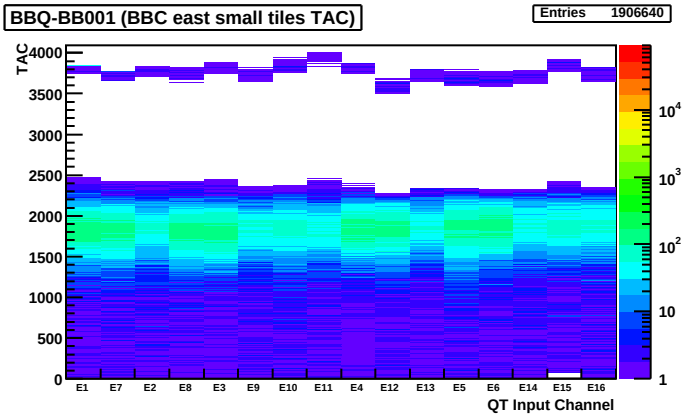

MIX-TF006
Entries 5004930
TOF MULT
TOF tray

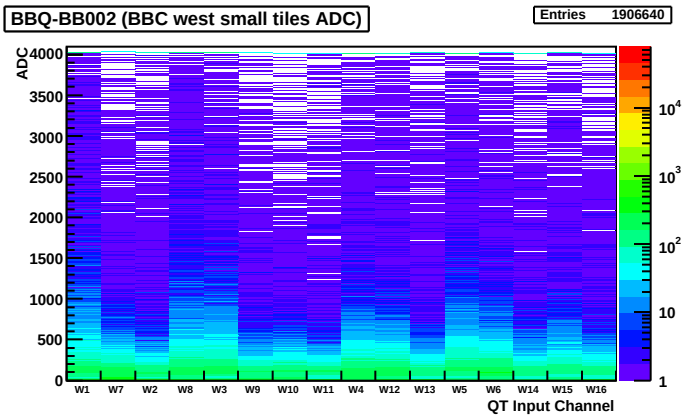

MIX-TF101
Entries 714990
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

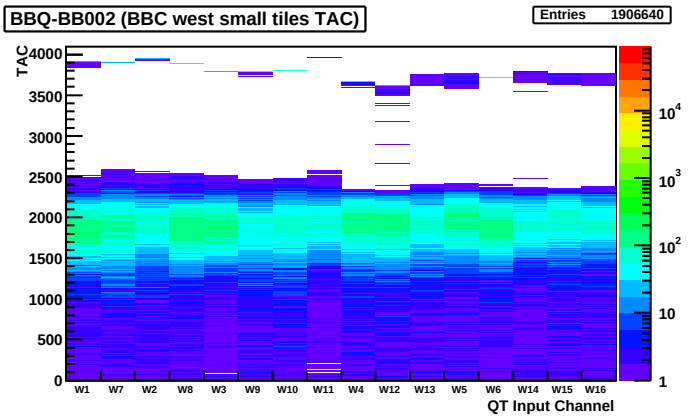

L1-TF201
Entries 119165
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

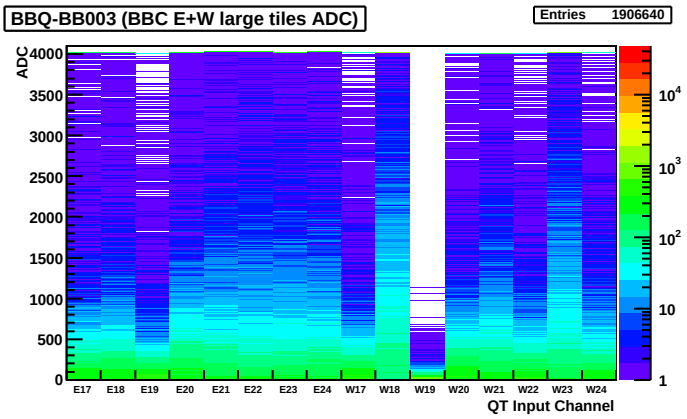

L1-TF201
Entries 714990
Threshold bits
1
0
80235
75238
44304
73014
65025
73451
38930
43927
74861
46151
54140
45714
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

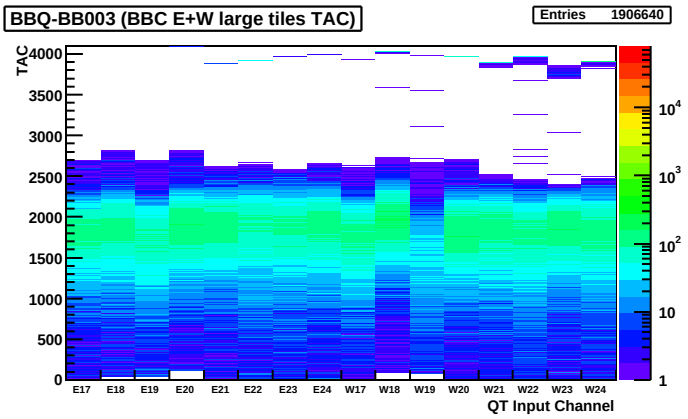

BBQ-BB001 (BBC east small tiles ADC)
Entries 1906640
ADC
QT Input Channel

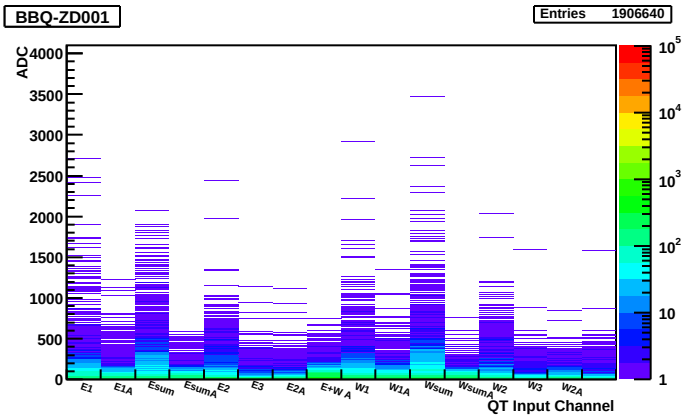

BBQ-BB001 (BBC east small tiles TAC)
Entries 1906640
TAC
QT Input Channel

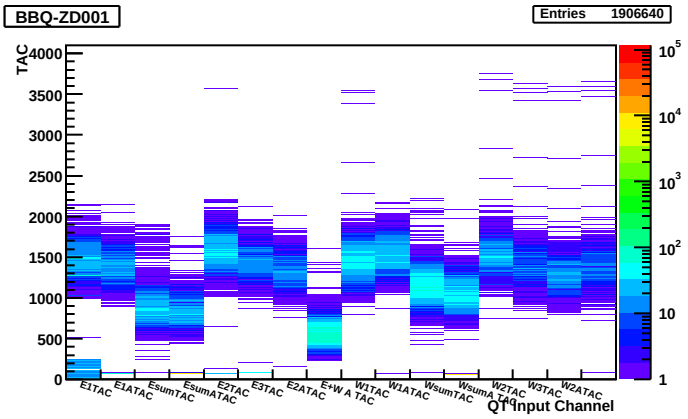

BBQ-BB002 (BBC west small tiles ADC)
Entries 1906640
ADC
QT Input Channel

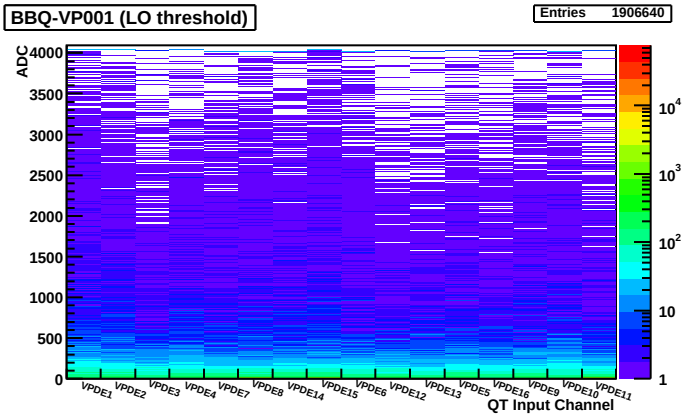

BBQ-BB002 (BBC west small tiles TAC)
Entries 1906640
TAC
QT Input Channel

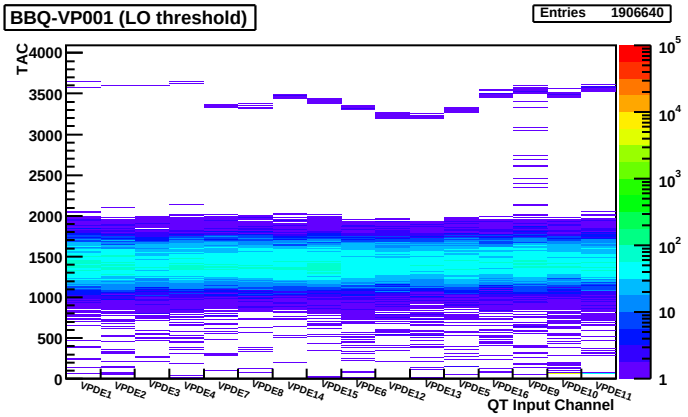

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1906640
ADC
QT Input Channel

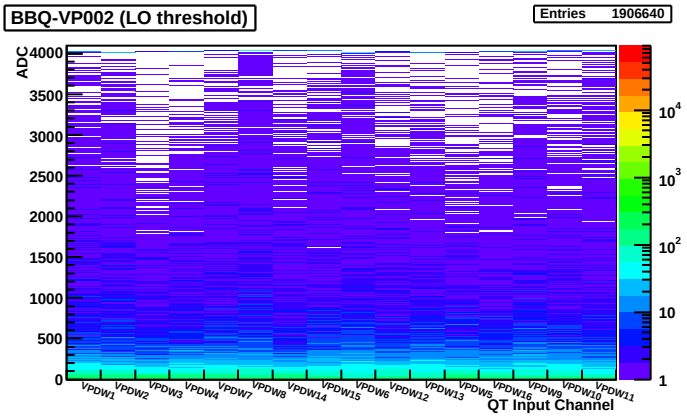

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1906640
TAC
QT Input Channel

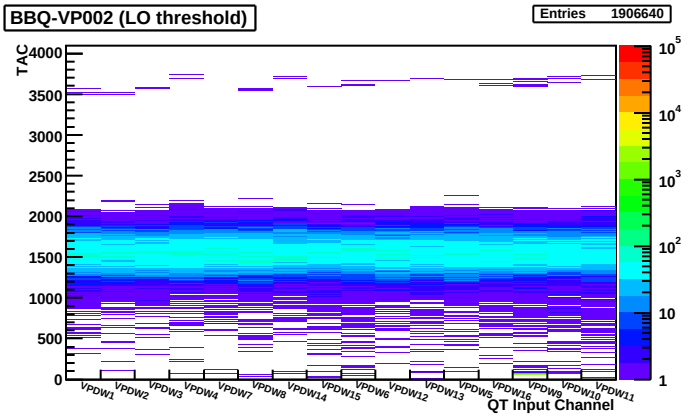

BBQ-ZD001
Entries 1906640
ADC
QT Input Channel

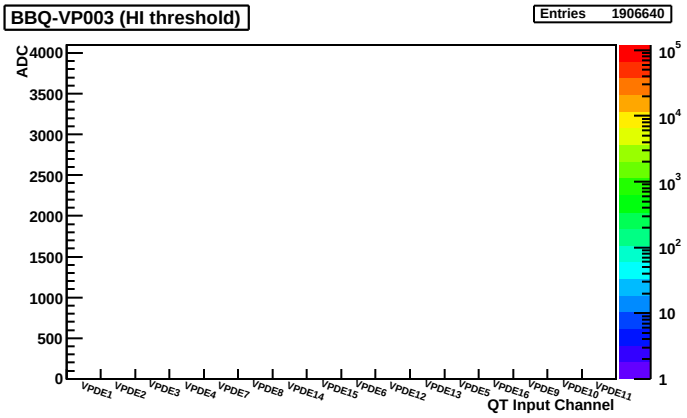

BBQ-ZD001
Entries 1906640
TAC
QT Input Channel

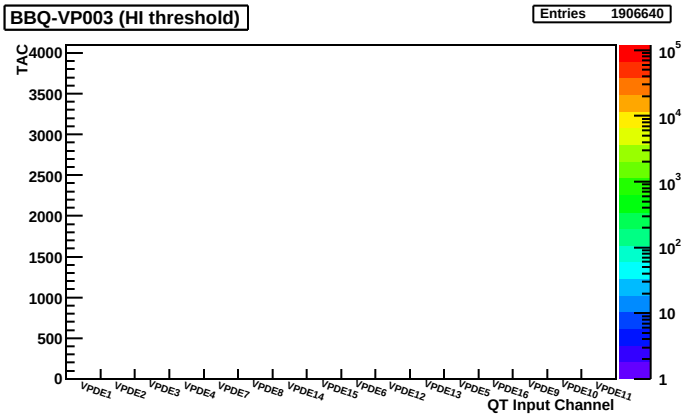

BBQ-VP001 (LO threshold)
Entries 1906640
ADC
QT Input Channel

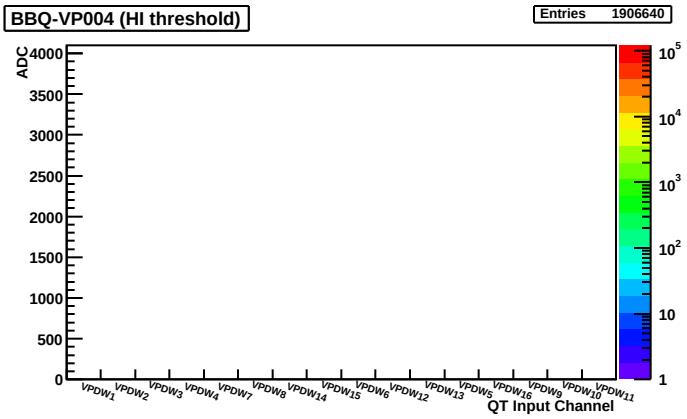

BBQ-VP001 (LO threshold)
Entries 1906640
TAC
QT Input Channel

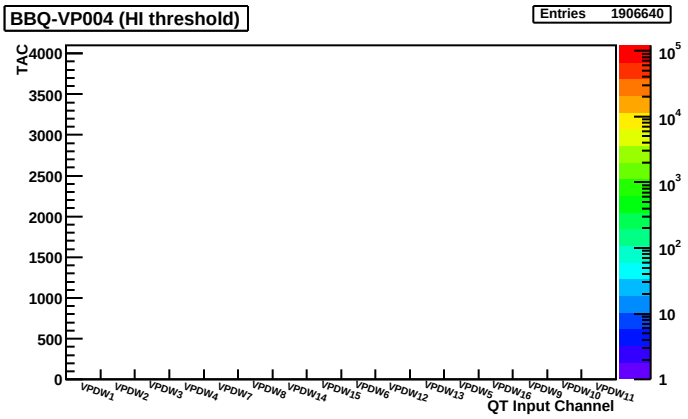

BBQ-VP002 (LO threshold)
Entries 1906640
ADC
QT Input Channel

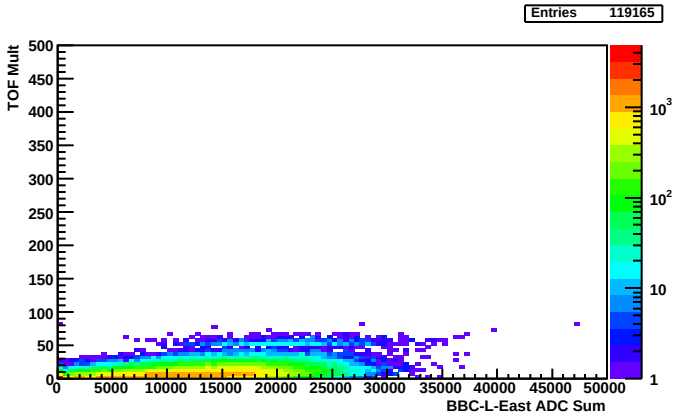

BBQ-VP002 (LO threshold)
Entries 1906640
TAC
QT Input Channel

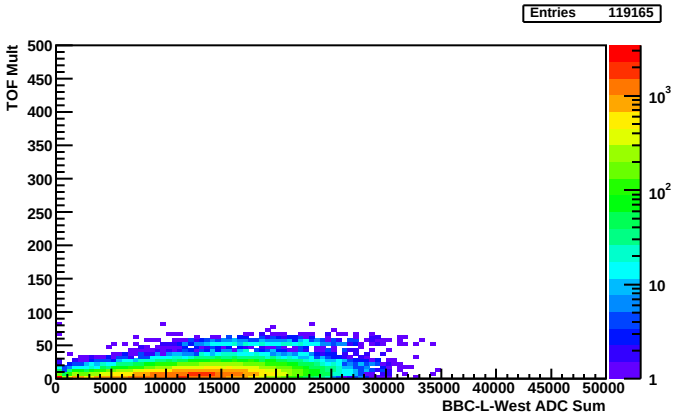

BBQ-VP003 (HI threshold)
Entries 1906640
ADC
QT Input Channel

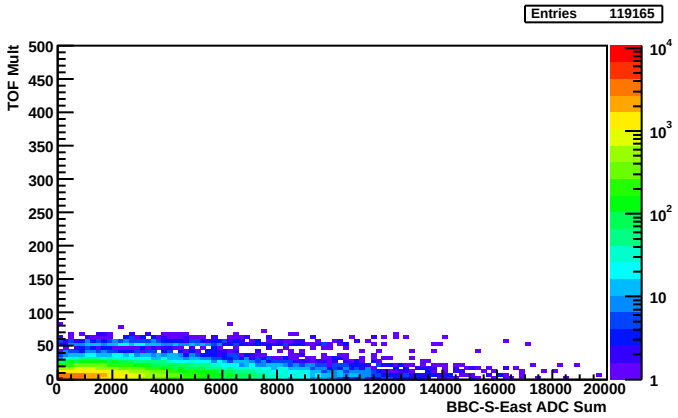

BBQ-VP003 (HI threshold)
Entries 1906640
TAC
QT Input Channel

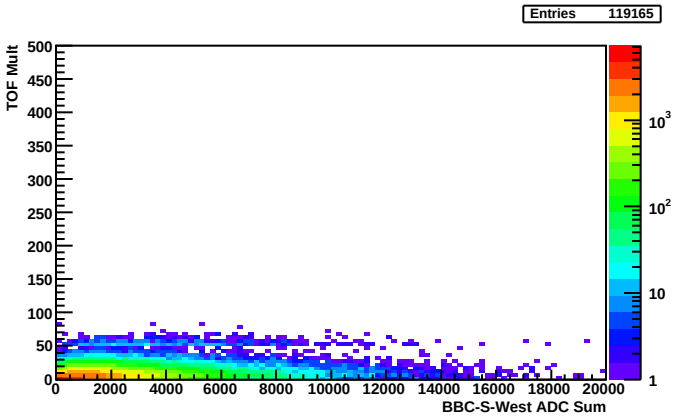

BBQ-VP004 (HI threshold)
Entries 1906640
ADC
QT Input Channel

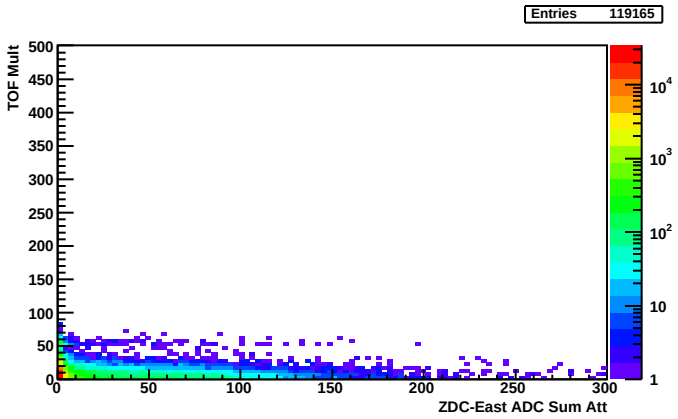

BBQ-VP004 (HI threshold)
Entries 1906640
TAC
QT Input Channel

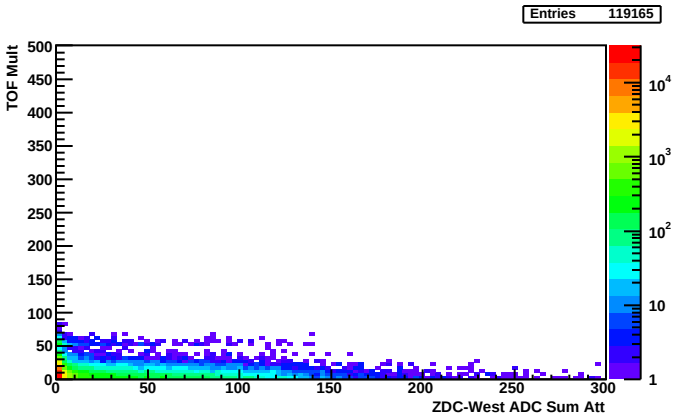

Entries 119165
TOF Mult
BBC-L-East ADC Sum

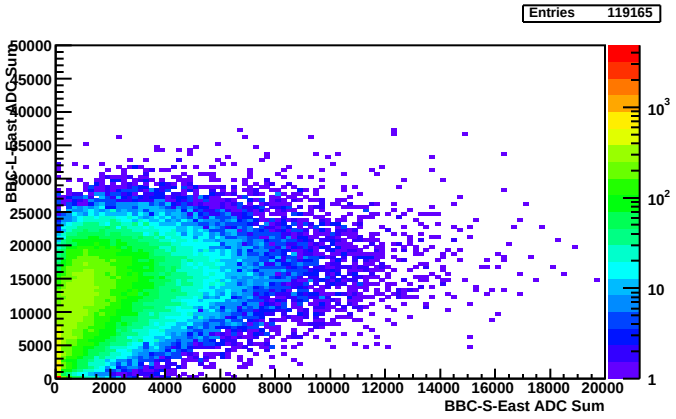

Entries 119165
TOF Mult
BBC-L-West ADC Sum

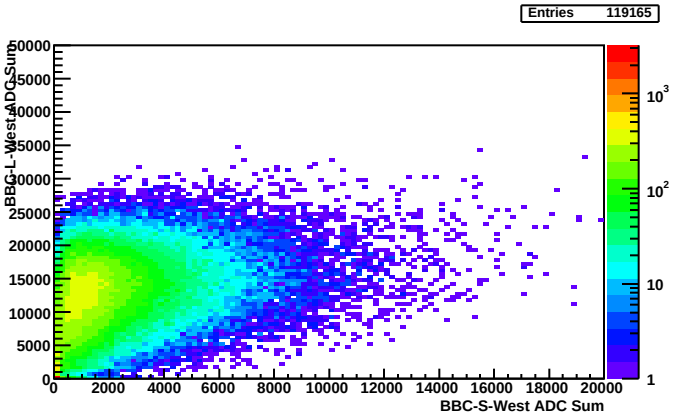

Entries 119165
TOF Mult
BBC-S-East ADC Sum

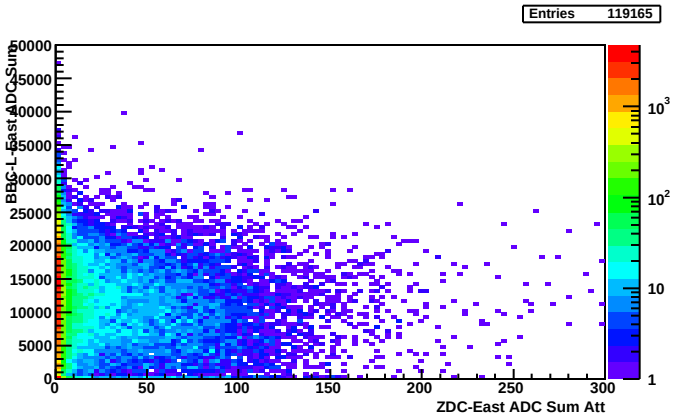

Entries 119165
TOF Mult
BBC-S-West ADC Sum

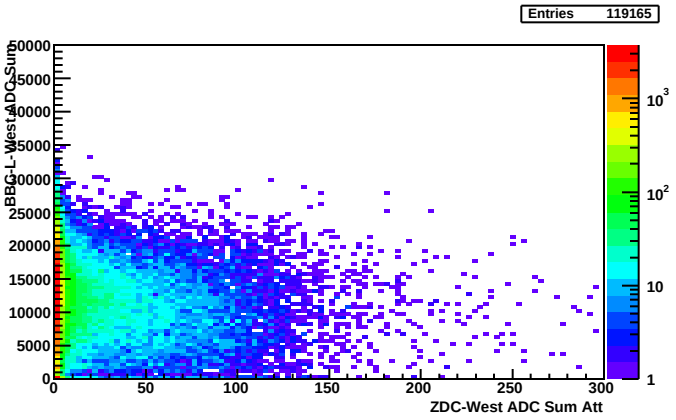

Entries 119165
TOF Mult
ZDC-East ADC Sum Att

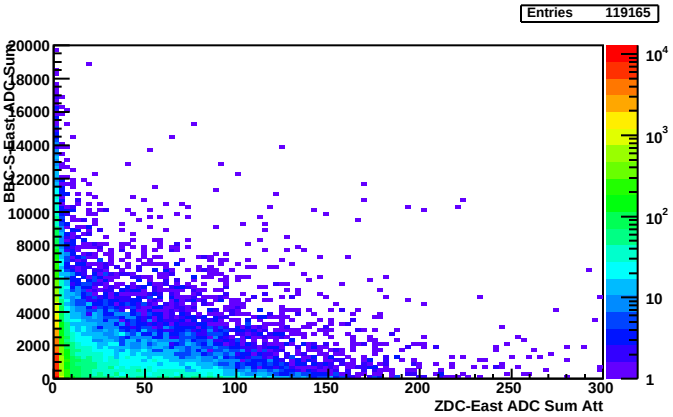

Entries 119165
TOF Mult
ZDC-West ADC Sum Att

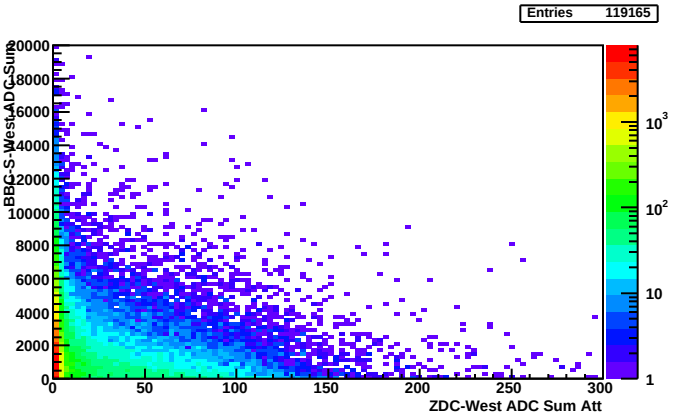

Entries 119165
BBC-L-East ADC Sum
BBC-S-East ADC Sum

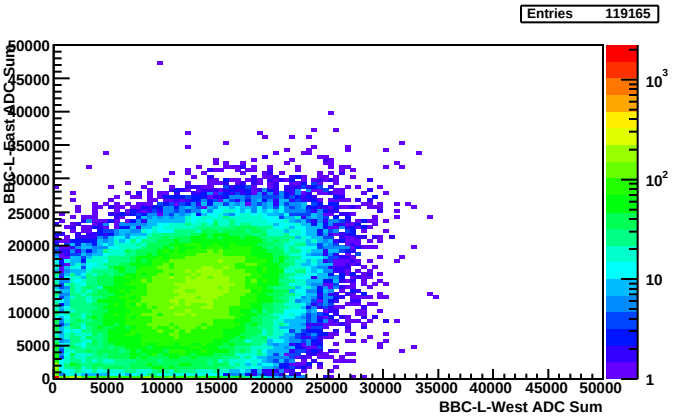

Entries 119165
BBC-L-West ADC Sum
BBC-S-West ADC Sum

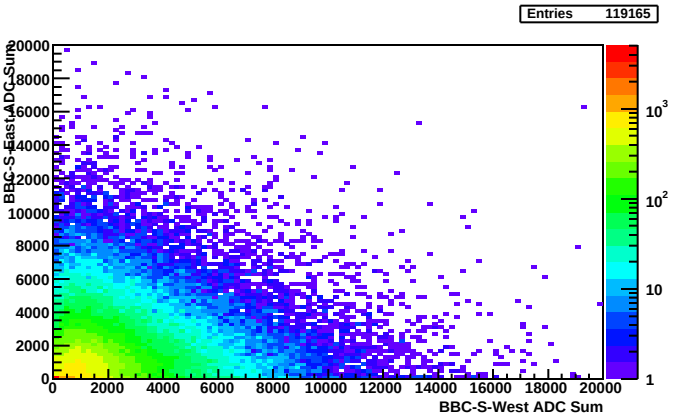

Entries 119165
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

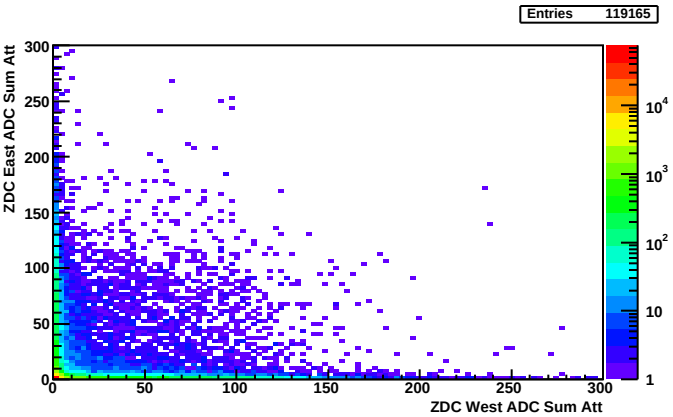

Entries 119165
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

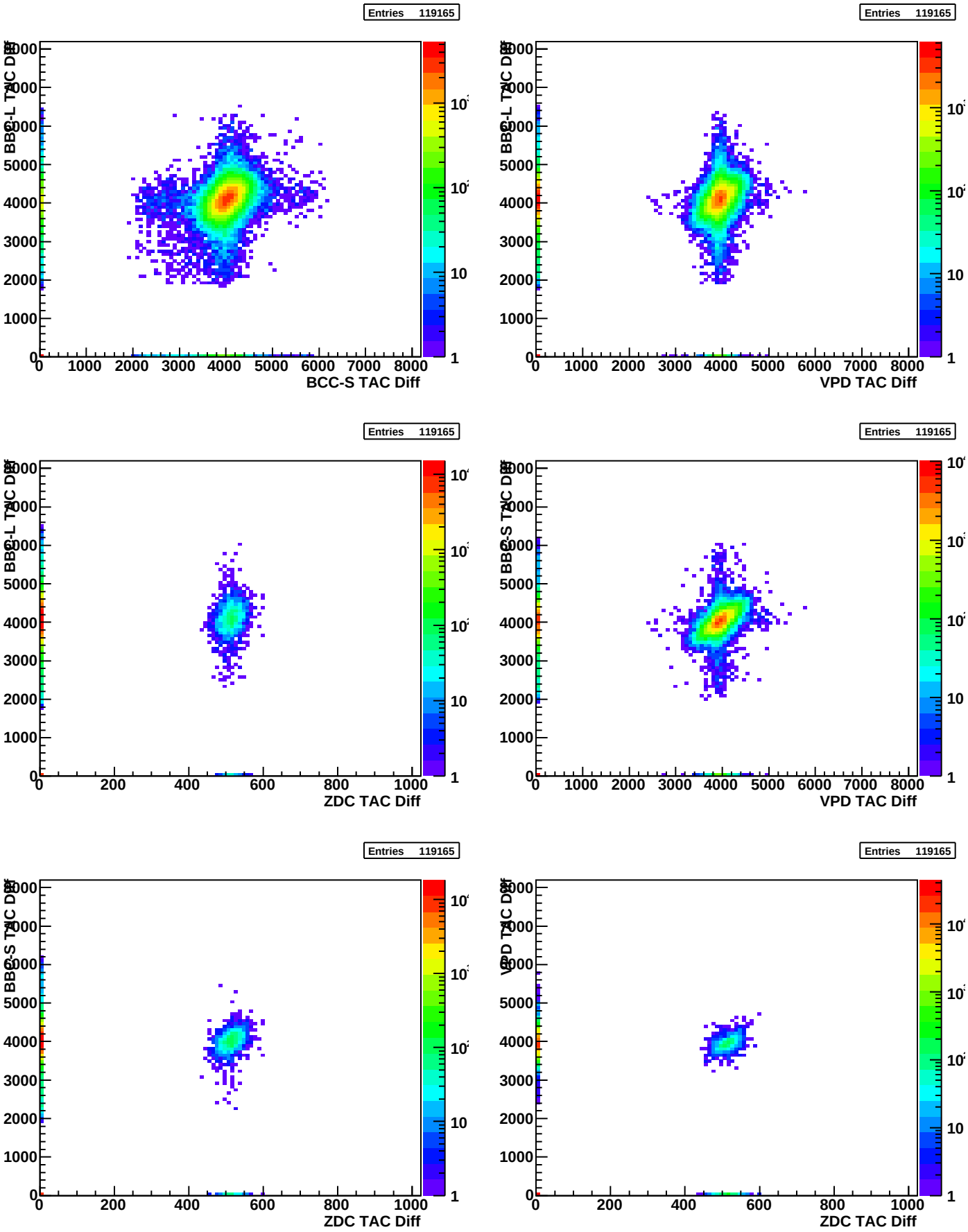

Entries 119165
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

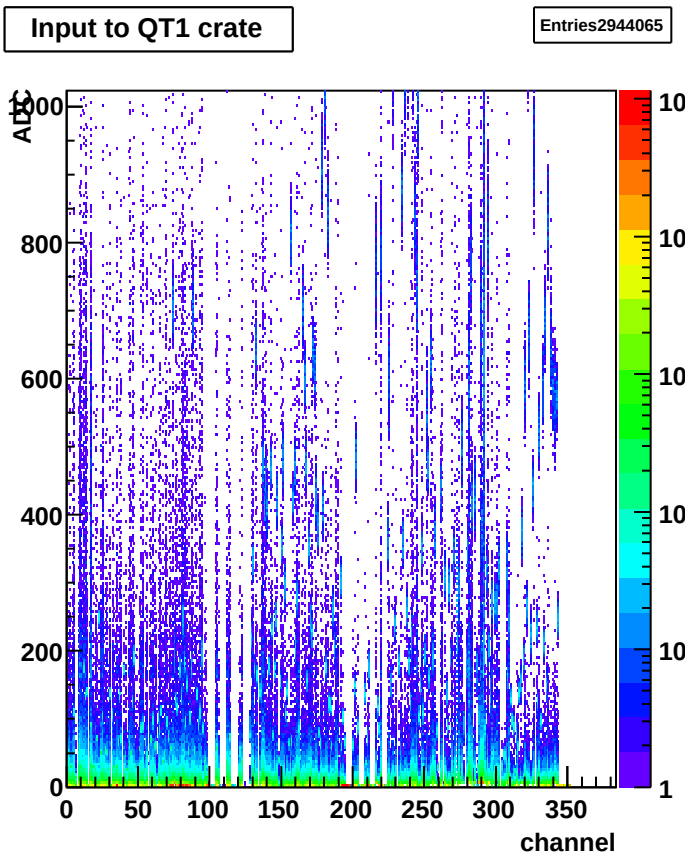

Entries 119165
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

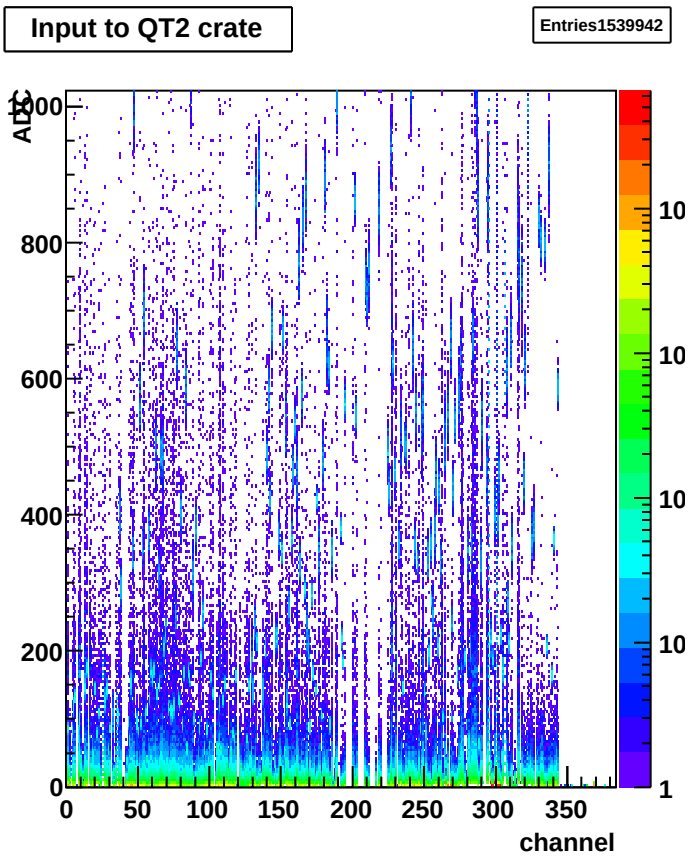

Entries 119165
BBC-L-East ADC Sum
BBC-L-West ADC Sum

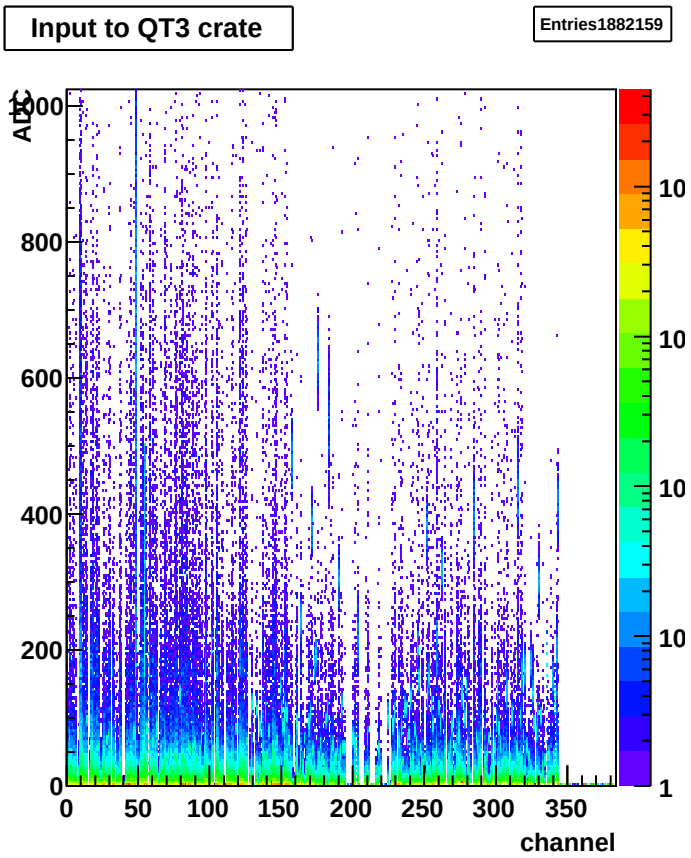

Entries 119165
BBC-S-East ADC Sum
BBC-S-West ADC Sum

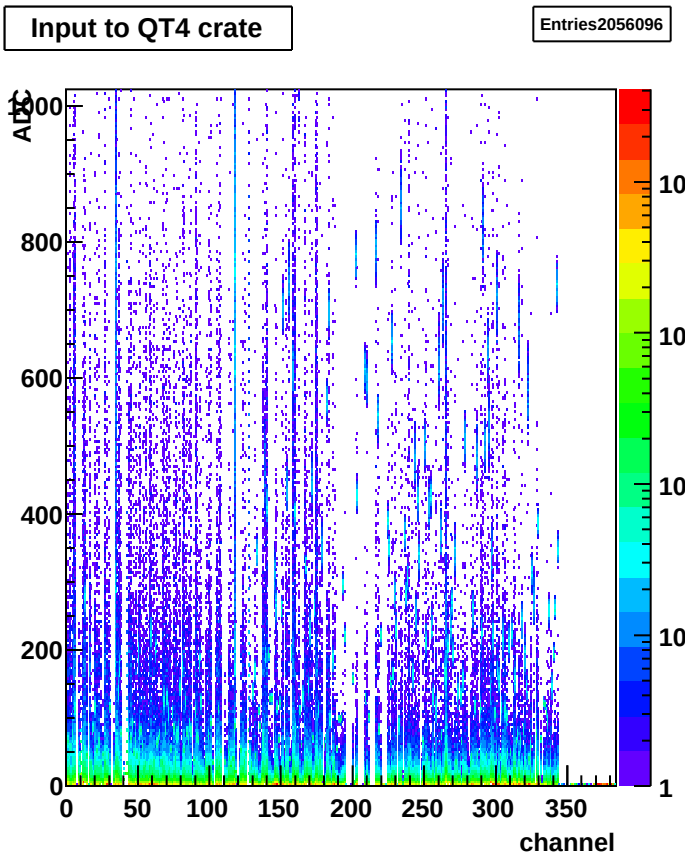

Entries 119165
ZDC East ADC Sum Att
ZDC West ADC Sum Att

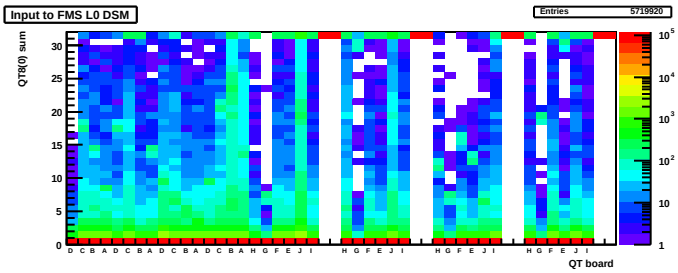

Entries 119165
BBC-L TAC Diff
BCC-S TAC Diff
Entries 119165
BBC-L TAC Diff
VPD TAC Diff
Entries 119165
BBC-L TAC Diff
ZDC TAC Diff
Entries 119165
BBC-S TAC Diff
VPD TAC Diff
Entries 119165
BBC-S TAC Diff
ZDC TAC Diff
Entries 119165
VPD TAC Diff
ZDC TAC Diff

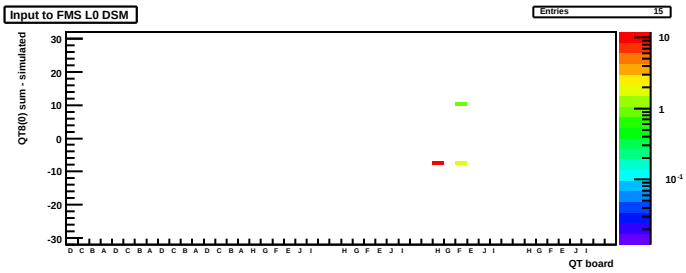

Input to QT1 crate
Entries2944065
ADC
channel

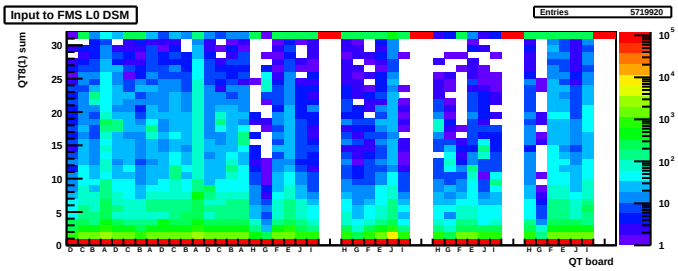

Input to QT2 crate
Entries1539942
ADC
channel

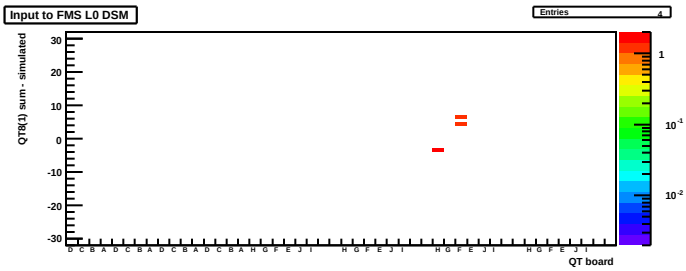

Input to QT3 crate
Entries1882159
ADC
channel

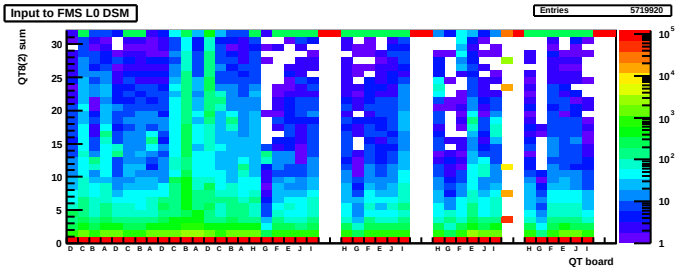

Input to QT4 crate
Entries2056096
ADC
channel

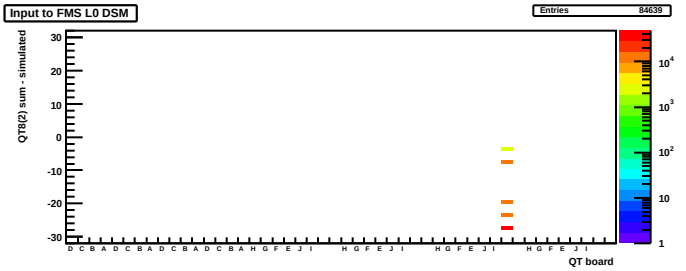

Input to FMS L0 DSM
Entries 5719920
QT8(0) sum
QT board

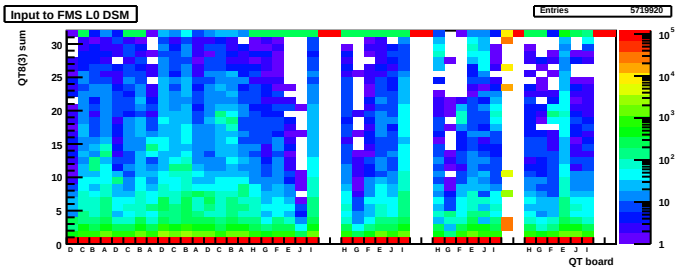

Input to FMS L0 DSM
Entries 15
QT8(0) sum - simulated
QT board

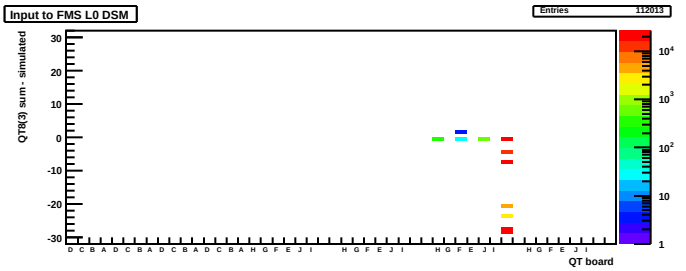

Input to FMS L0 DSM
Entries 5719920
QT8(1) sum
QT board

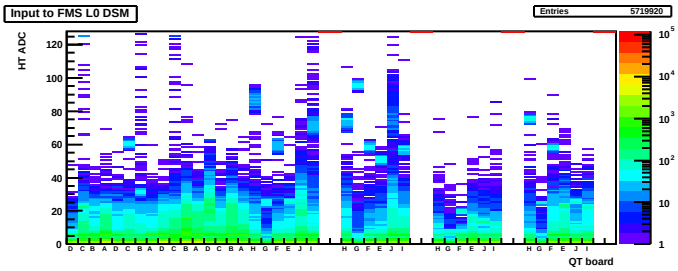

Input to FMS L0 DSM
Entries 4
QT8(1) sum - simulated
QT board

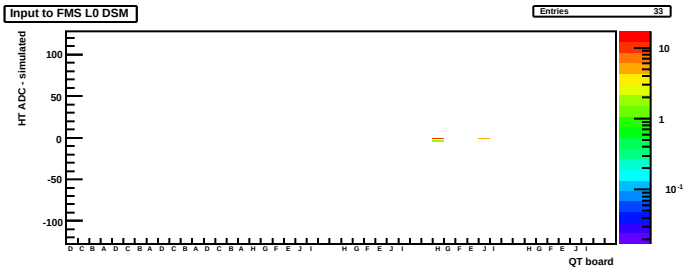

Input to FMS L0 DSM
Entries 5719920
QT8(2) sum
QT board

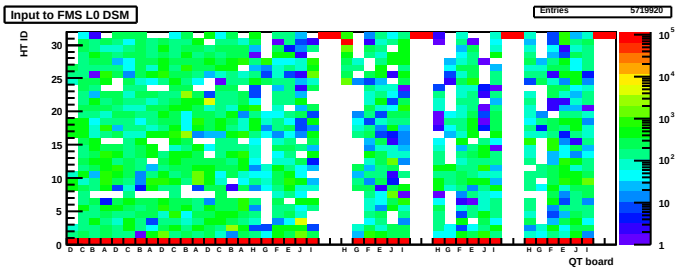

Input to FMS L0 DSM
Entries 84639
QT8(2) sum - simulated
QT board

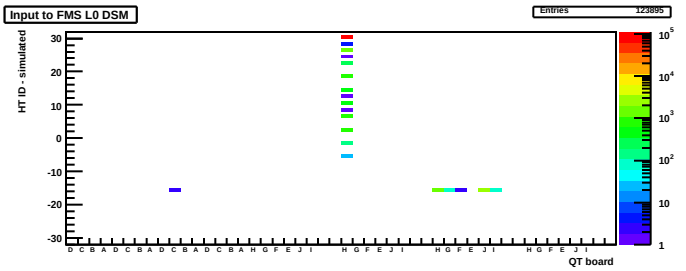

Input to FMS L0 DSM
Entries 5719920
QT8(3) sum
QT board

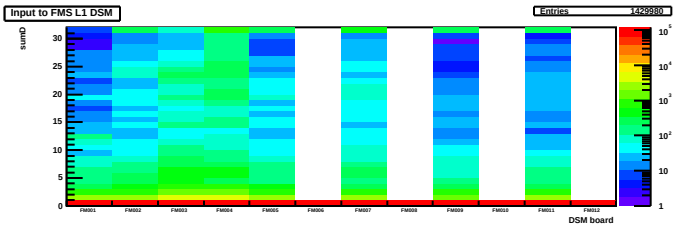

Input to FMS L0 DSM
Entries 112013
QT8(3) sum - simulated
QT board

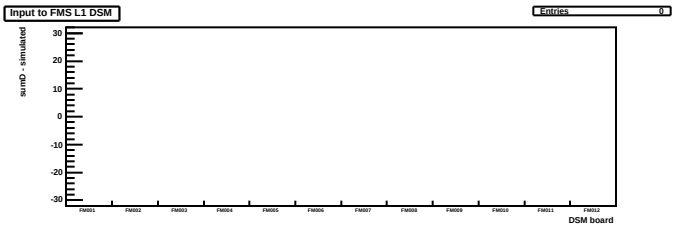

Input to FMS L0 DSM
Entries 5719920
HT ADC
QT board

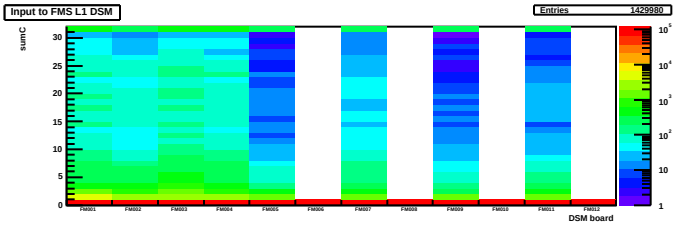

Input to FMS L0 DSM
Entries 33
HT ADC - simulated
QT board

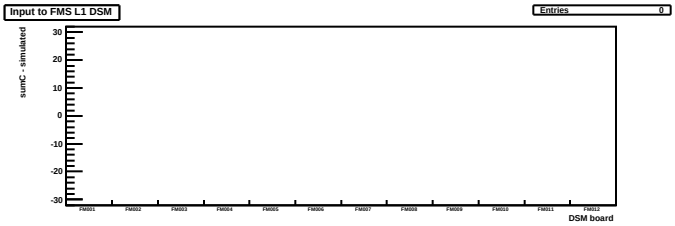

Input to FMS L0 DSM
Entries 5719920
HT ID
QT board

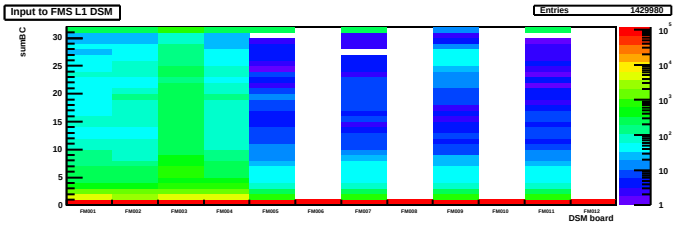

Input to FMS L0 DSM
Entries 123895
HT ID - simulated
QT board

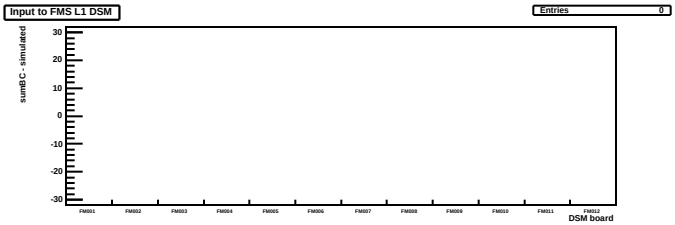

Input to FMS L1 DSM
Entries 1429980
sumD
DSM board

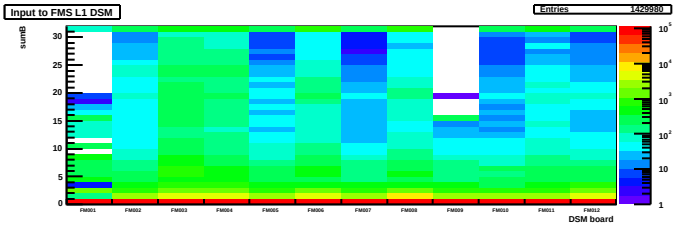

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

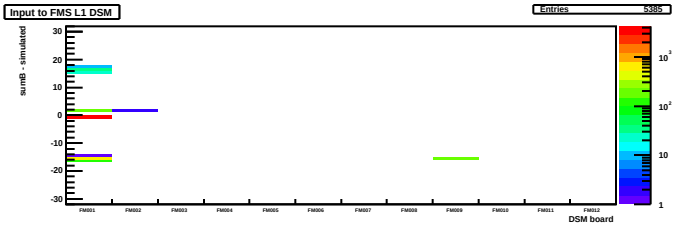

Input to FMS L1 DSM
Entries 1429980
sumC
DSM board

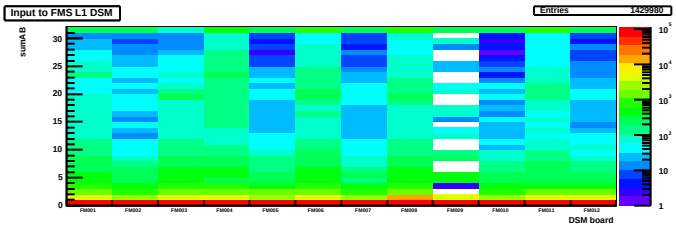

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

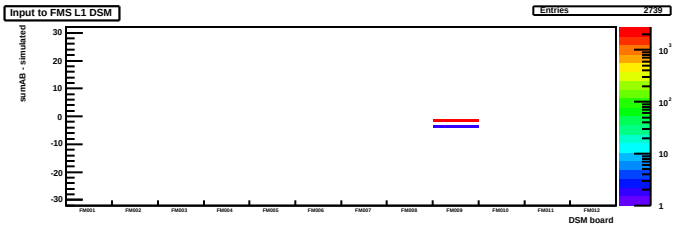

Input to FMS L1 DSM
Entries 1429980
sumBC
DSM board

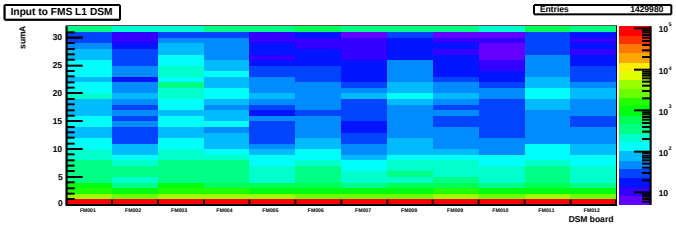

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

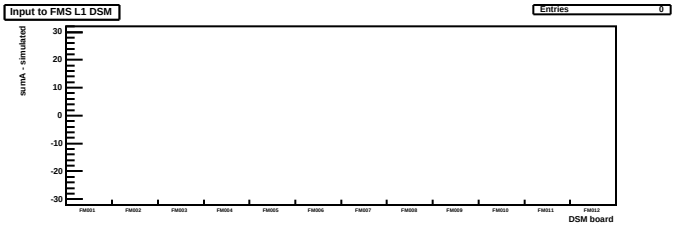

Input to FMS L1 DSM
Entries 1429980
sumB
DSM board

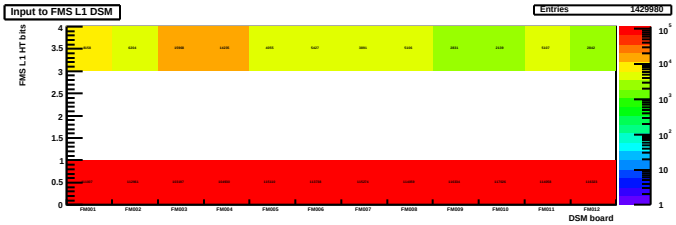

Input to FMS L1 DSM
Entries 5385
sumB - simulated
DSM board

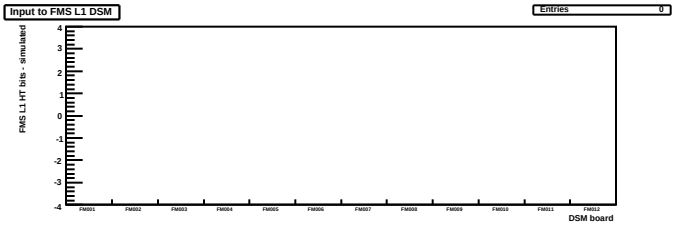

Input to FMS L1 DSM
Entries 1429980
sumAB
DSM board

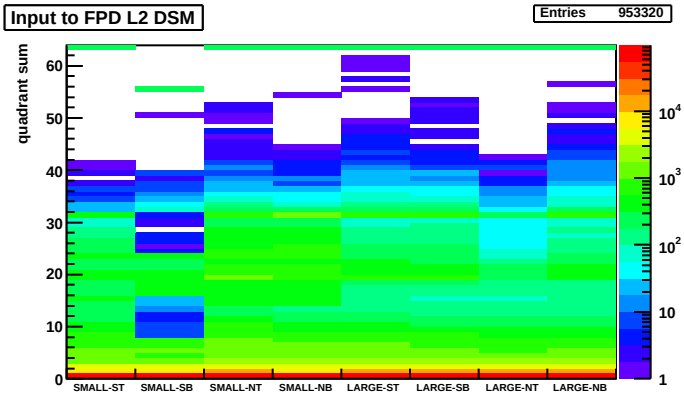

Input to FMS L1 DSM
Entries 2739
sumAB - simulated
DSM board

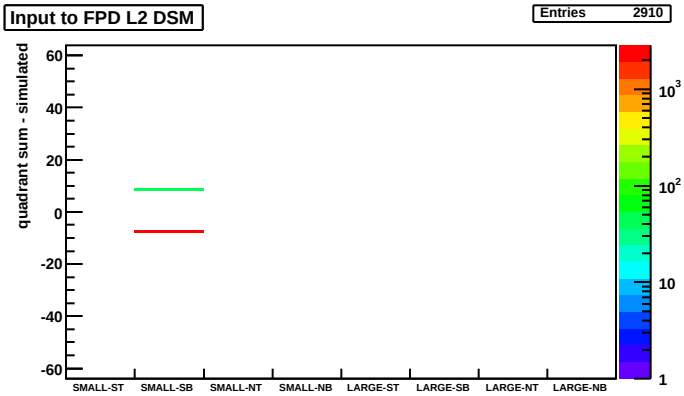

Input to FMS L1 DSM
Entries 1429980
sumA
DSM board

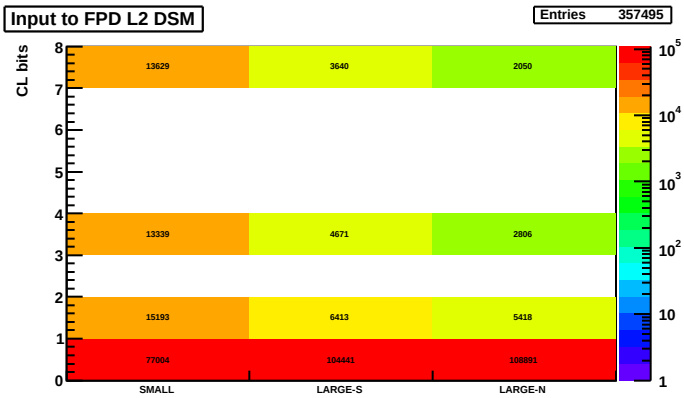

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

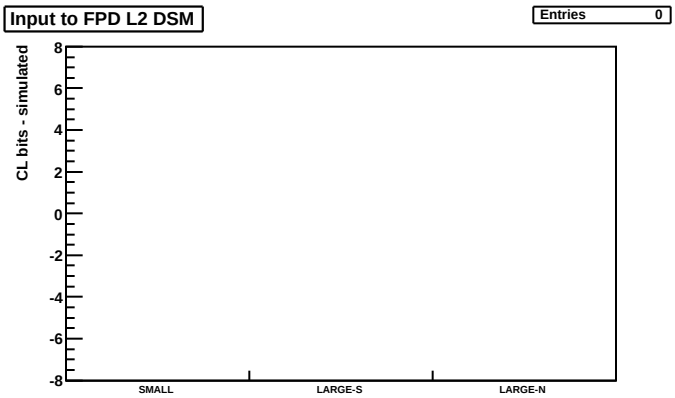

Input to FMS L1 DSM
Entries 1429980
FMS L1 HT bits
DSM board

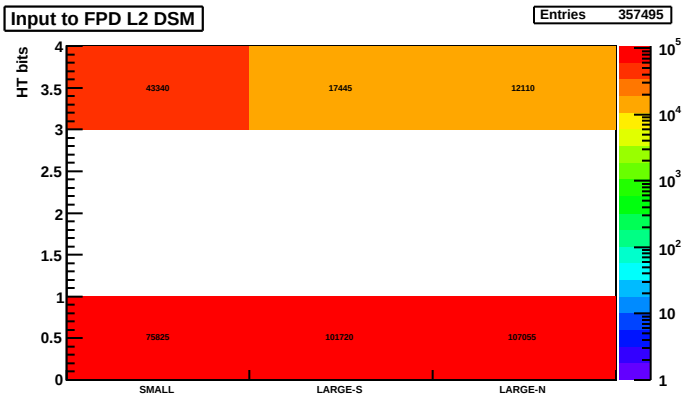

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

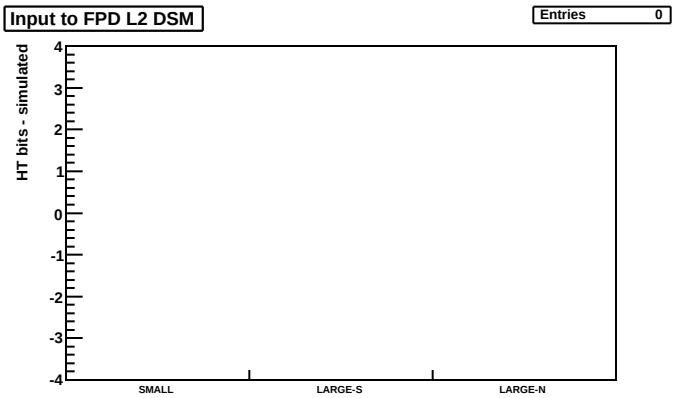

Input to FPD L2 DSM
Entries 953320
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

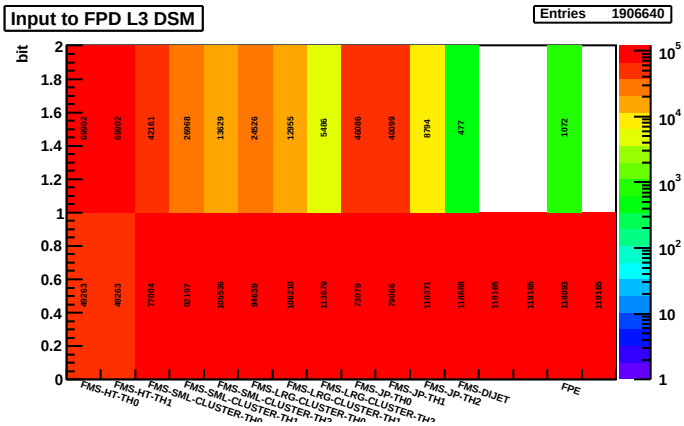

Input to FPD L2 DSM
Entries 2910
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

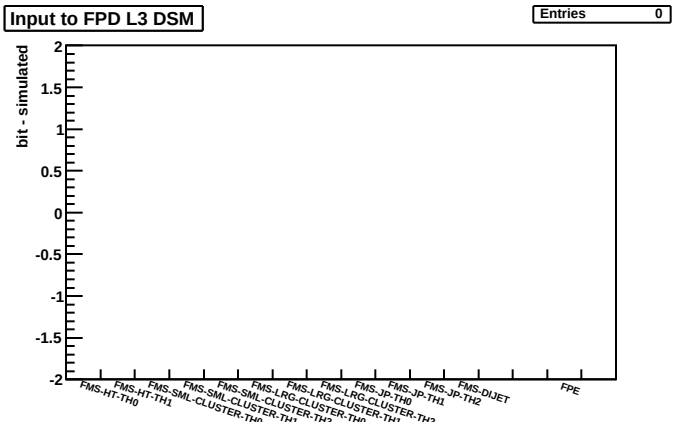

Input to FPD L2 DSM
Entries 357495
CL bits
13629
3640
2050
13339
4671
2806
15193
6413
5418
77004
104441
108891
SMALL
LARGE-S
LARGE-N

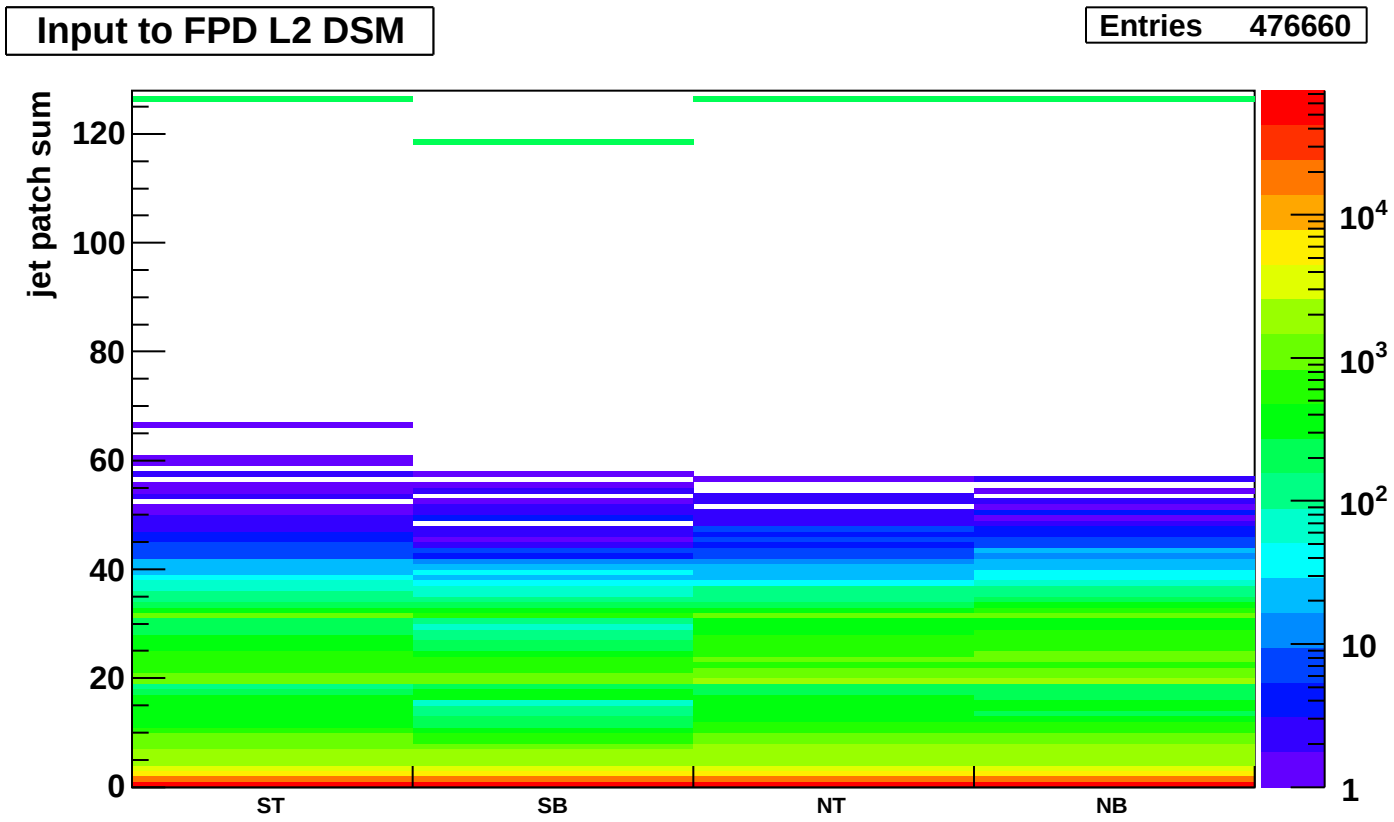

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

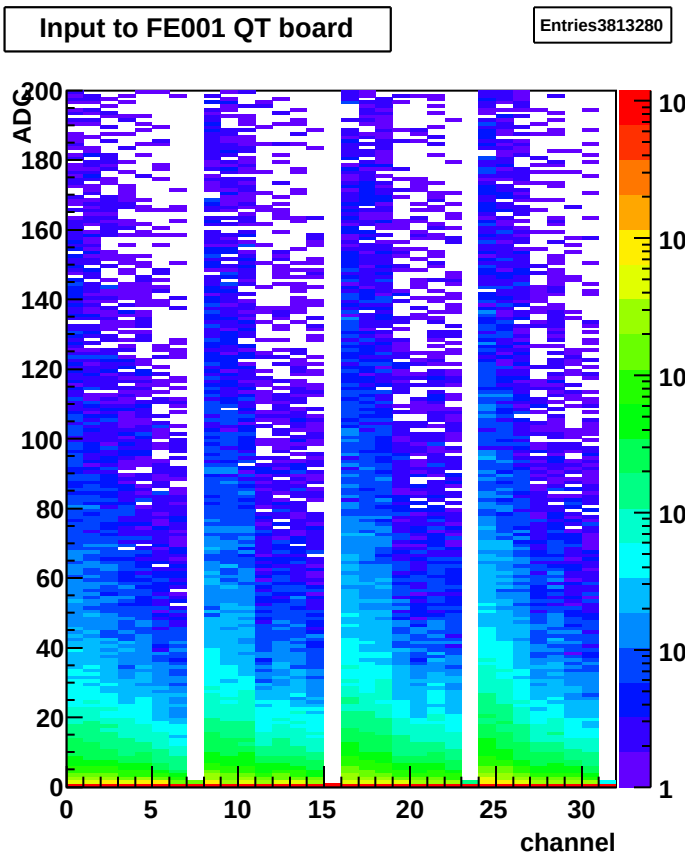

Input to FPD L2 DSM
Entries 357495
HT bits
43340
17445
12110
75825
101720
107055
SMALL
LARGE-S
LARGE-N

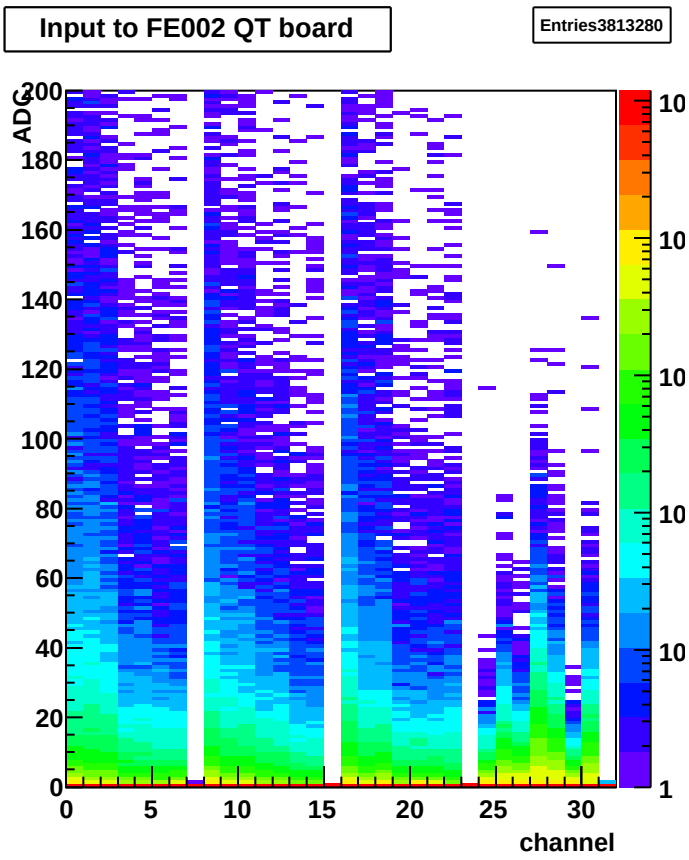

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

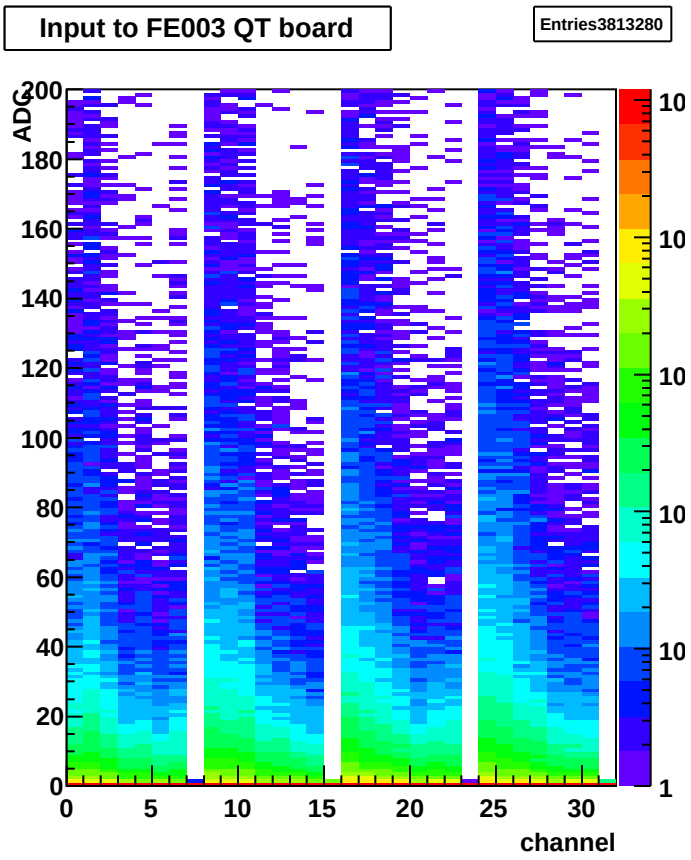

Input to FPD L3 DSM
Entries 1906640
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

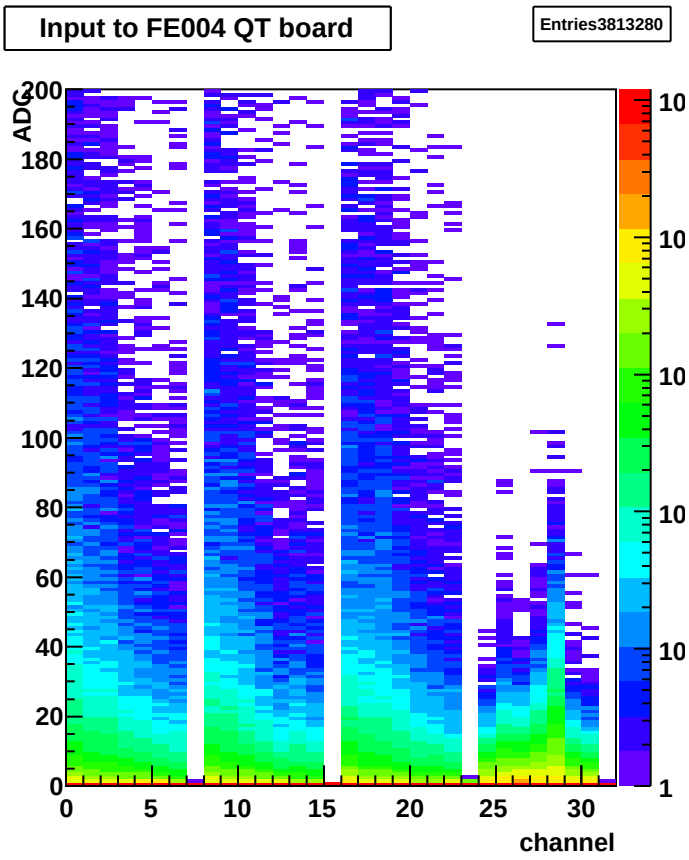

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

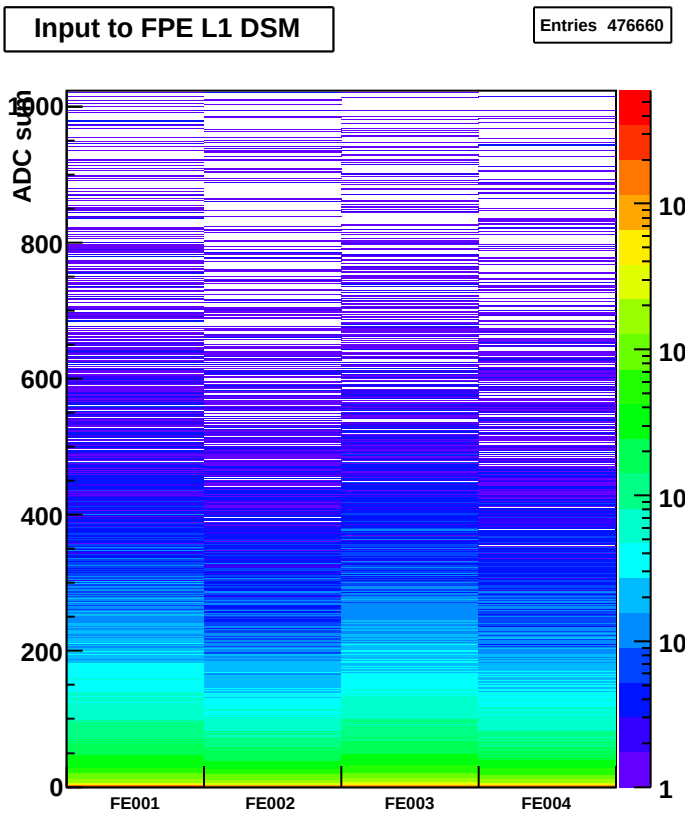

Input to FPD L2 DSM
Entries 476660
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

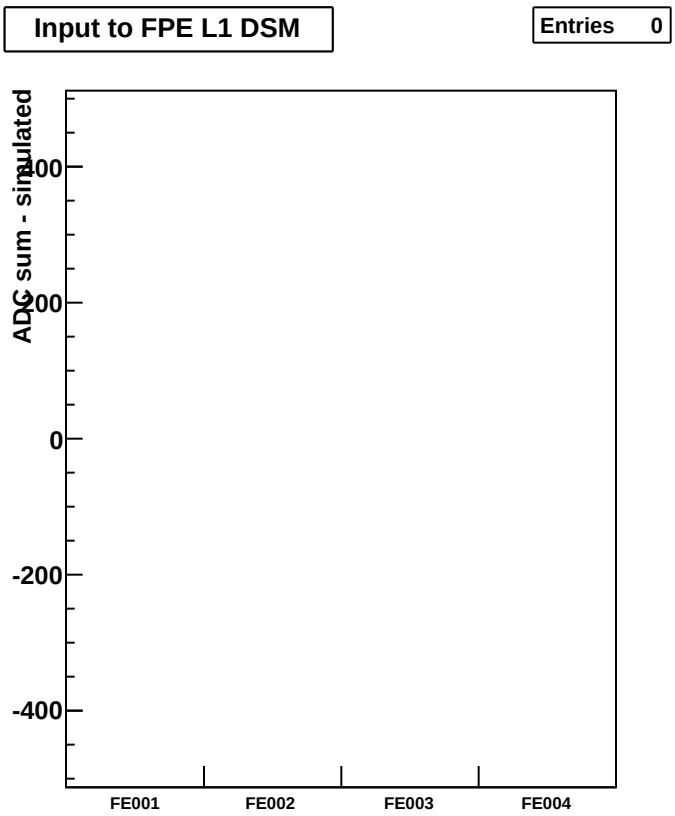

Input to FE001 QT board
Entries3813280
ADC
channel

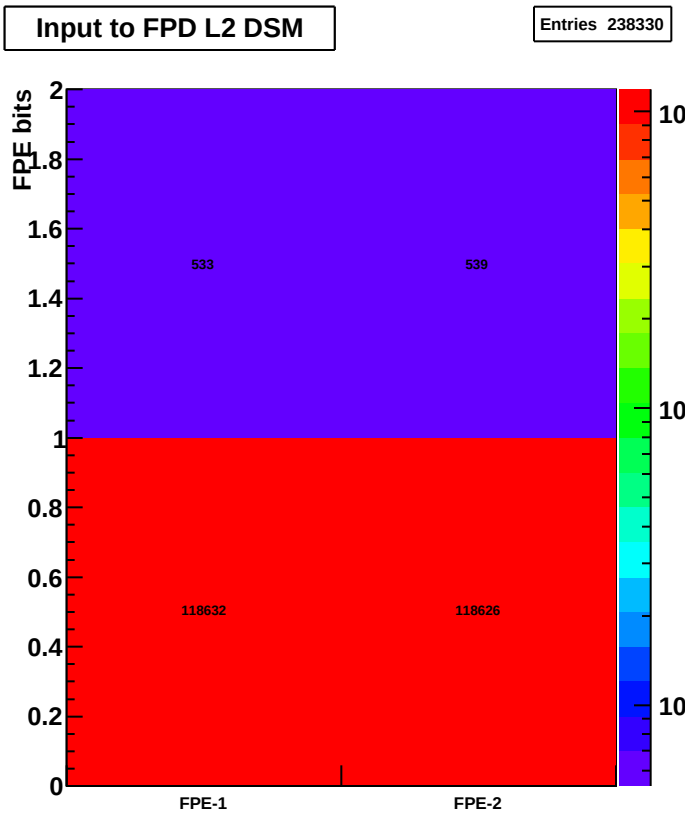

Input to FE002 QT board
Entries3813280
ADC
channel

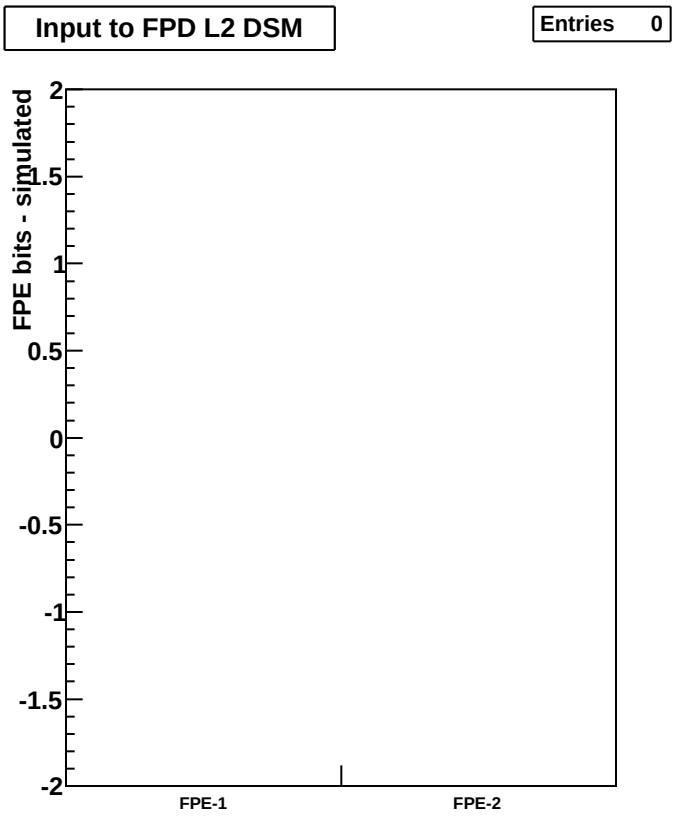

Input to FE003 QT board
Entries3813280
ADC
channel


Input to FE004 QT board
Entries3813280
ADC
channel


Input to FPE L1 DSM
Entries 476660
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 238330
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
533
539
118632
118626
FPE-1
FPE-2
10
10
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2