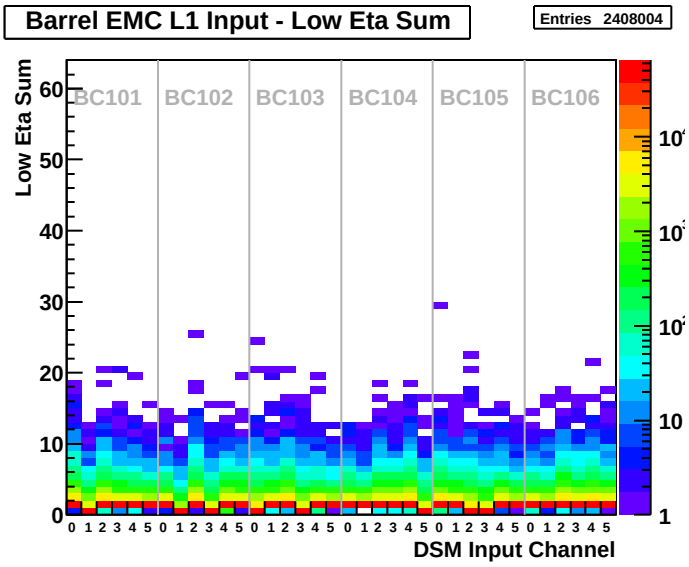
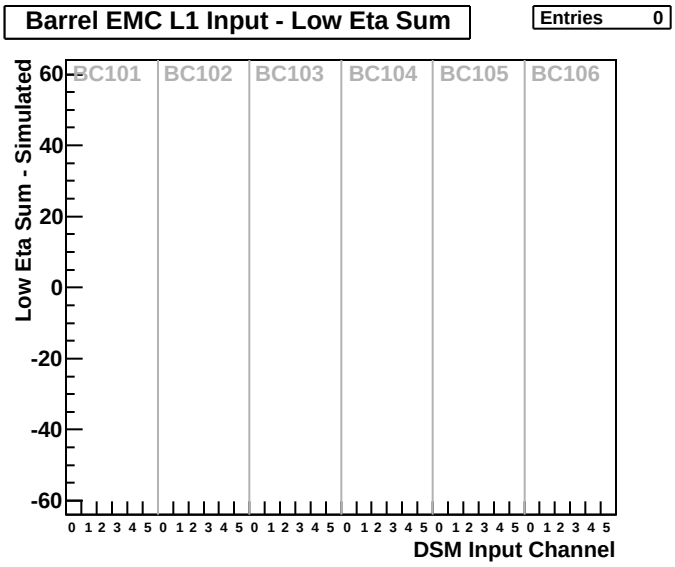
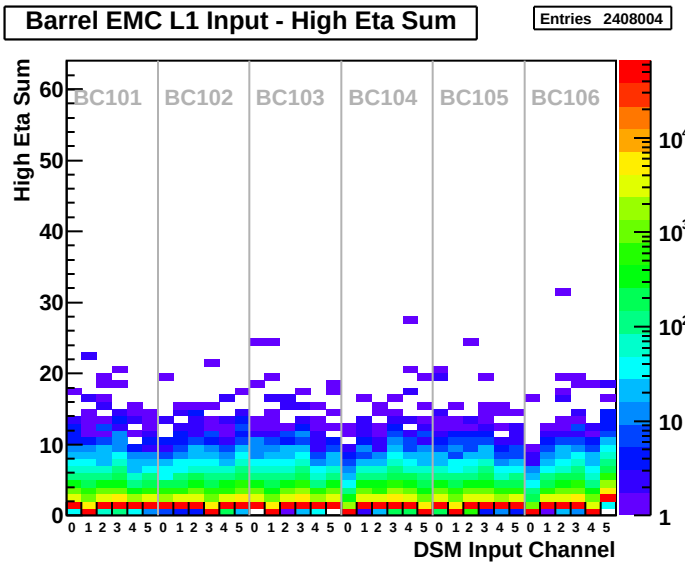
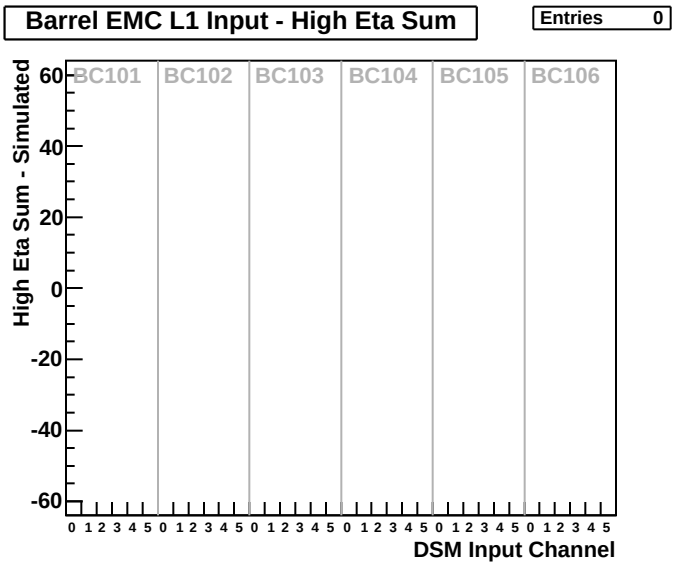
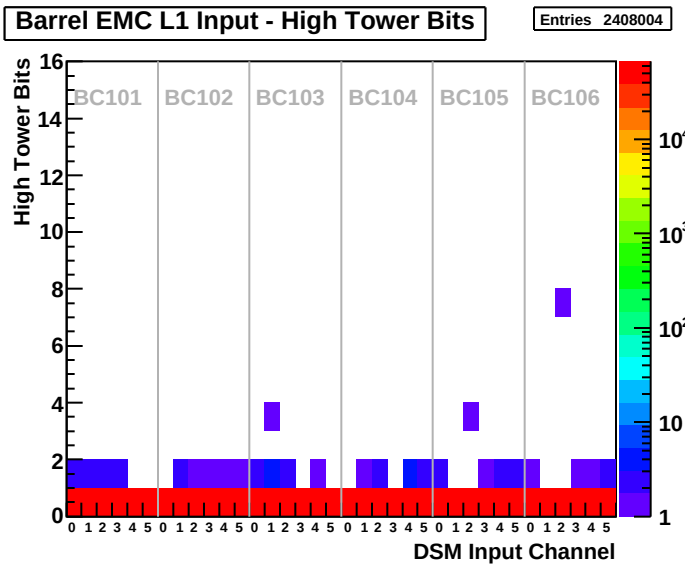
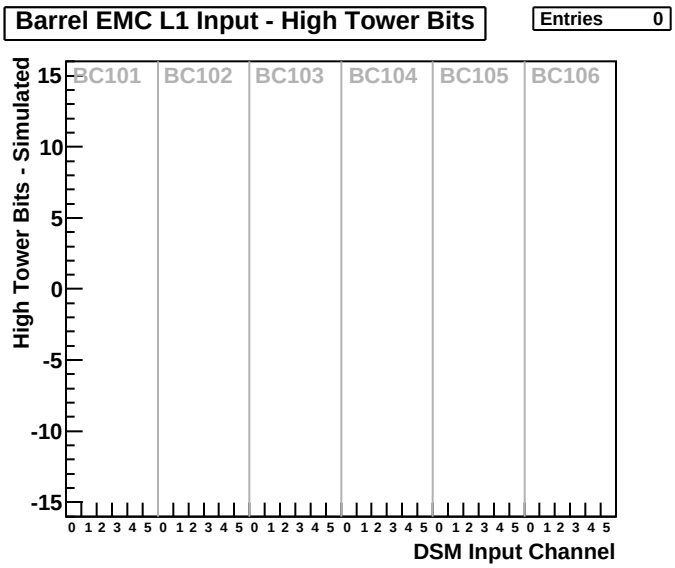

Barrel EMC L1 Input - Low Eta Sum
Entries 2408004
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

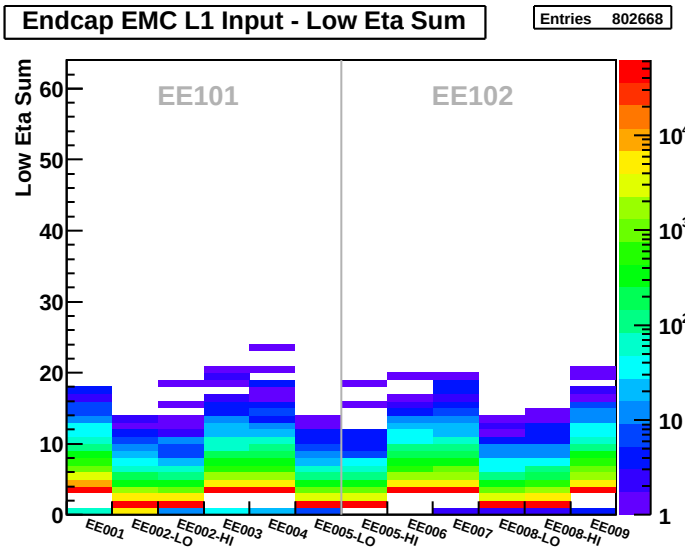

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

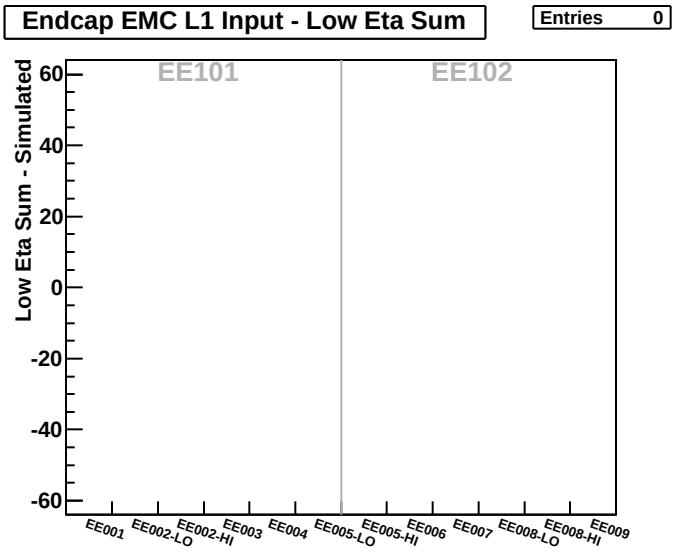

Barrel EMC L1 Input - High Eta Sum
Entries 2408004
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

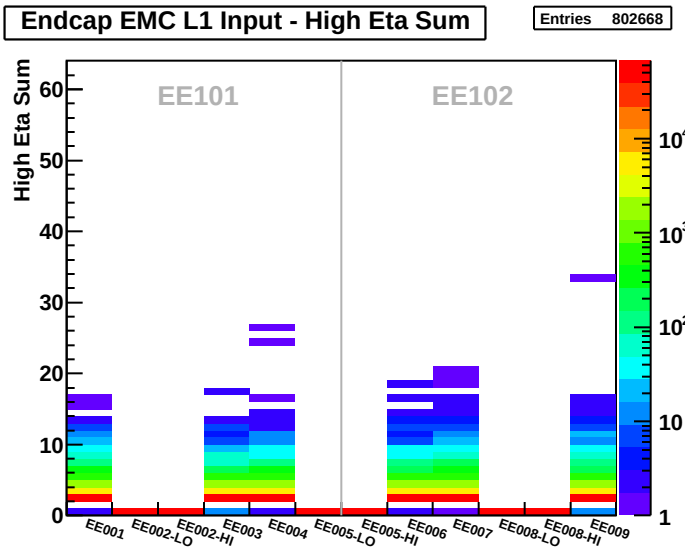

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

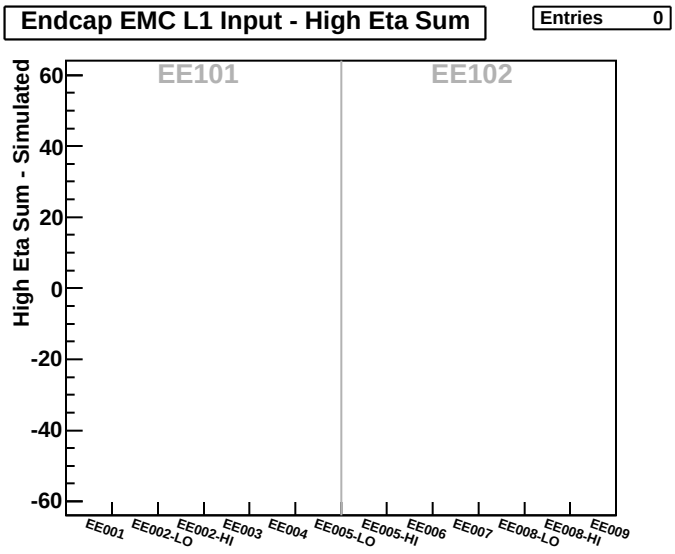

Barrel EMC L1 Input - High Tower Bits
Entries 2408004
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

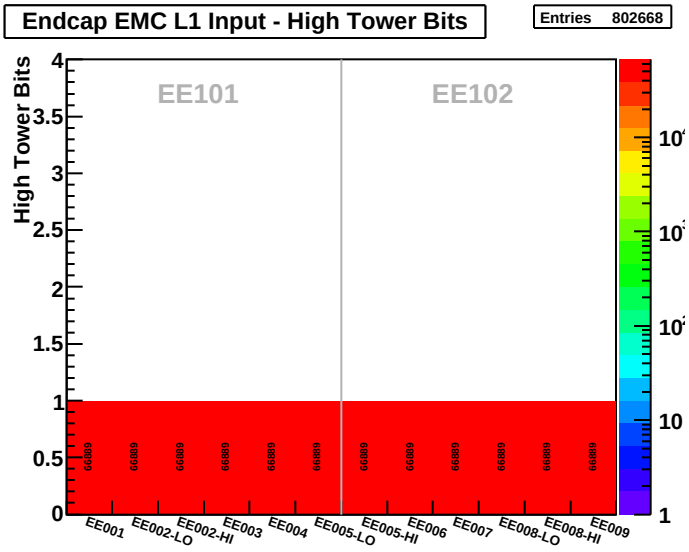

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

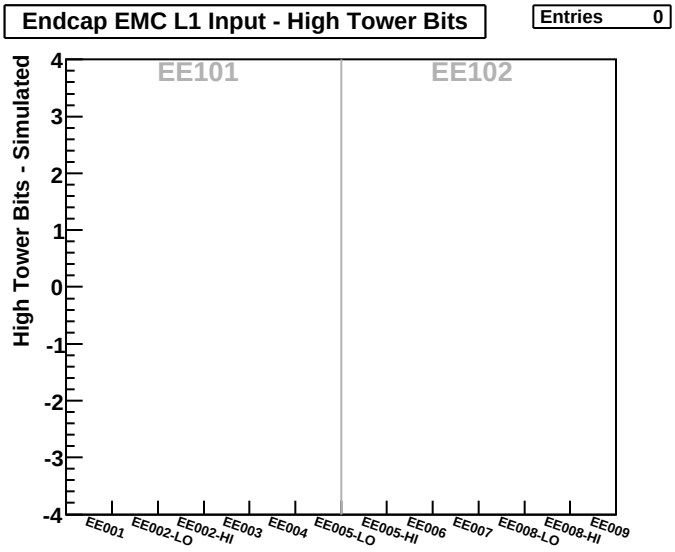

Endcap EMC L1 Input - Low Eta Sum
Entries 802668
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

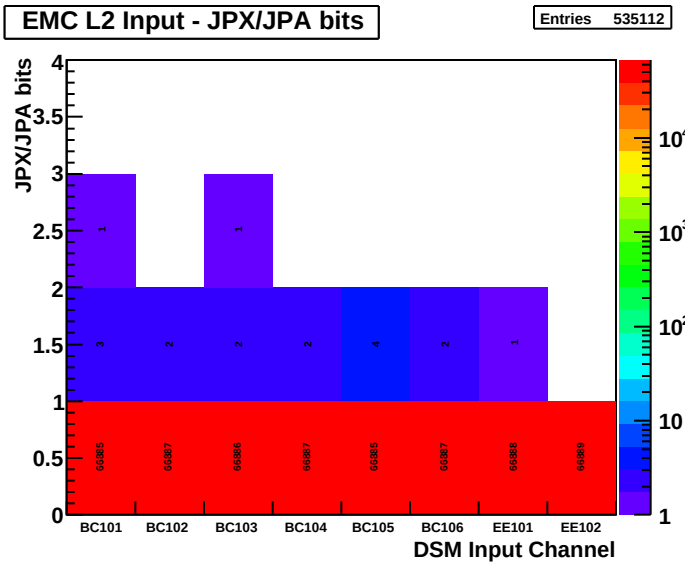

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

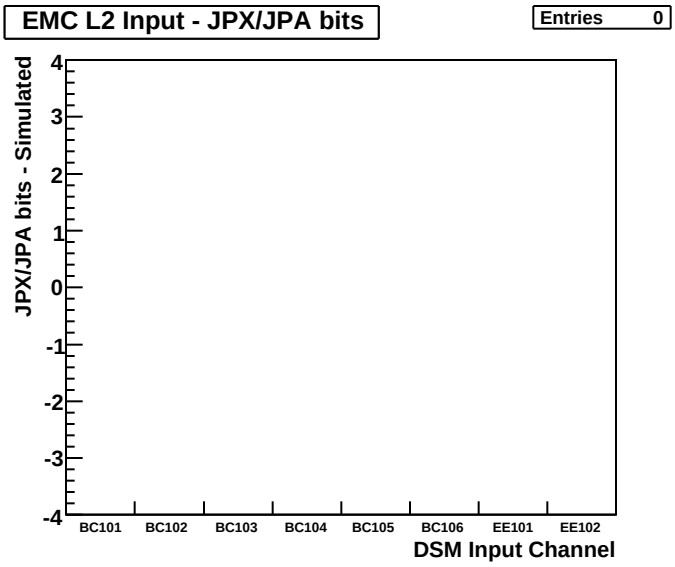

Endcap EMC L1 Input - High Eta Sum
Entries 802668
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

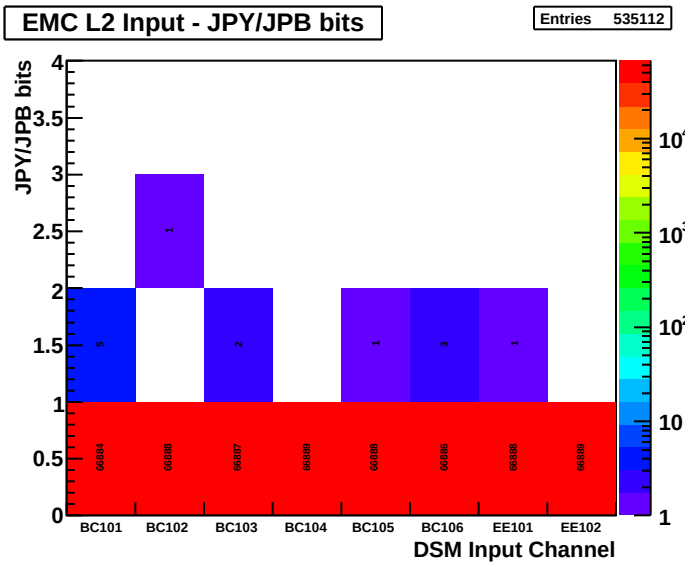

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

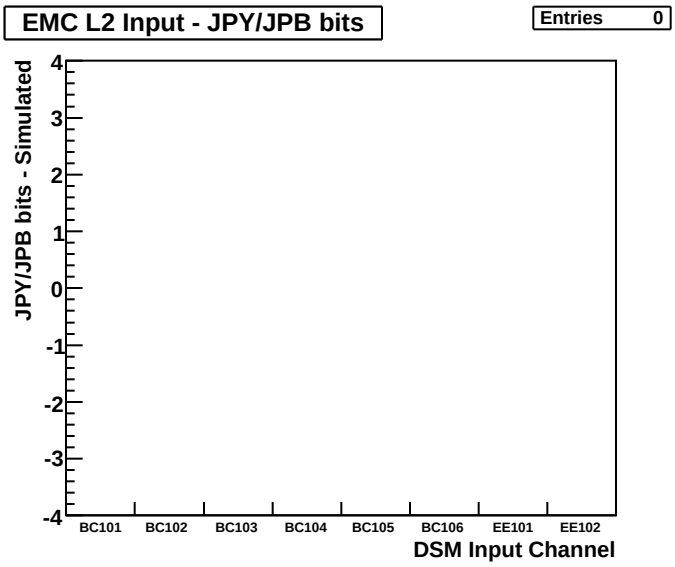

Endcap EMC L1 Input - High Tower Bits
Entries 802668
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

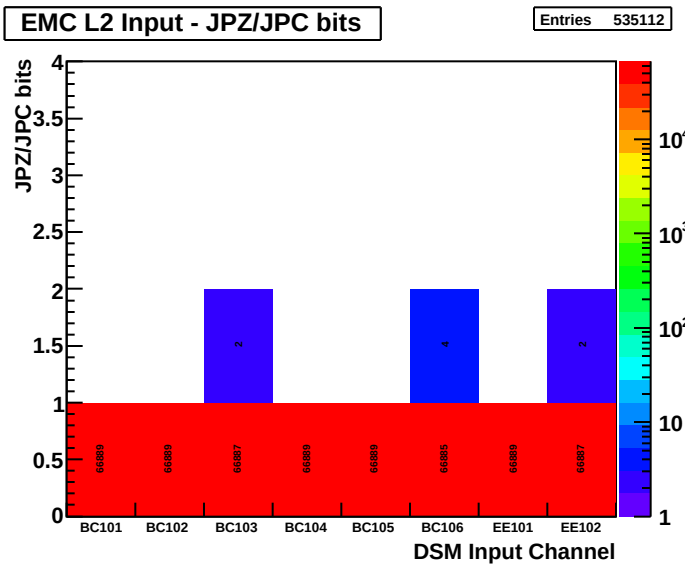

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

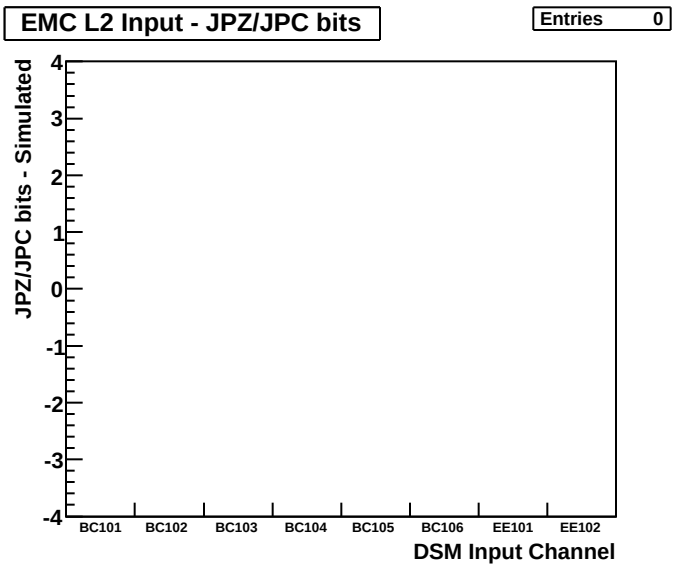

EMC L2 Input - JPX/JPA bits
Entries 535112
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

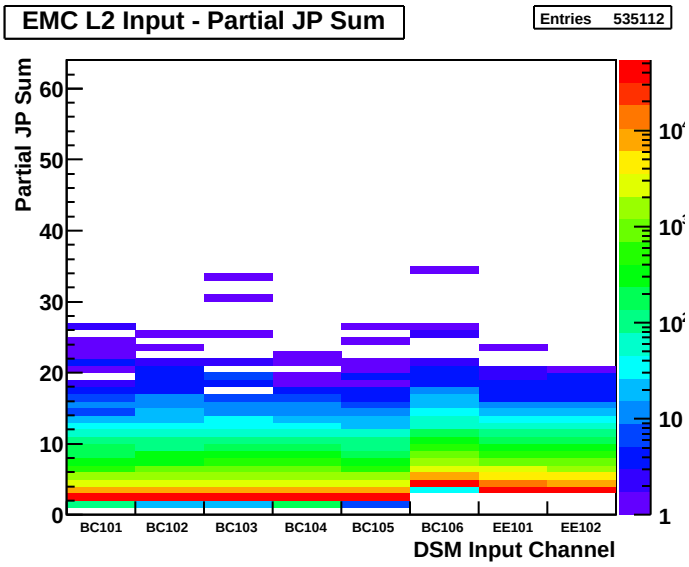

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

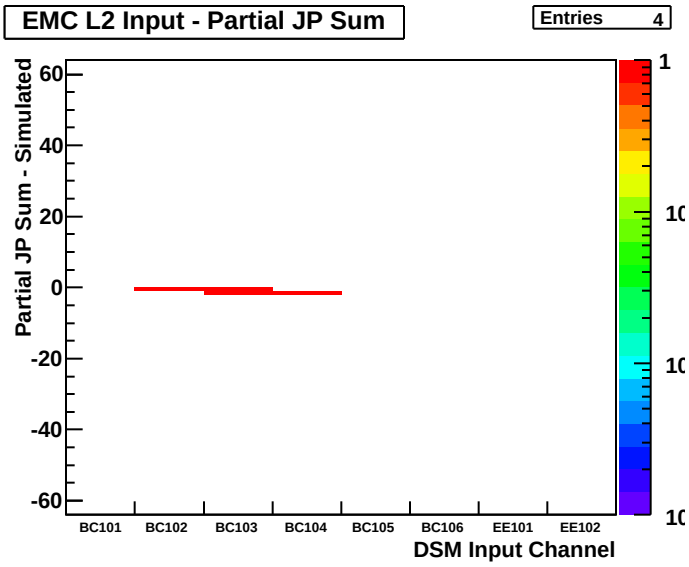

EMC L2 Input - JPY/JPB bits
Entries 535112
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

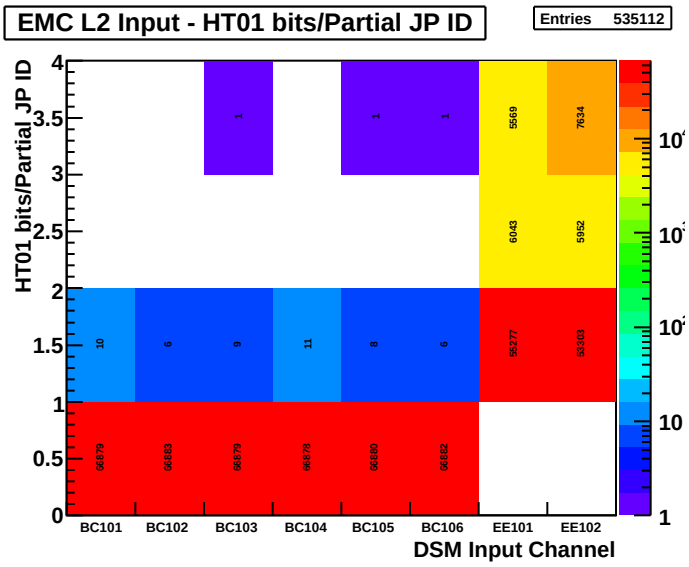

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

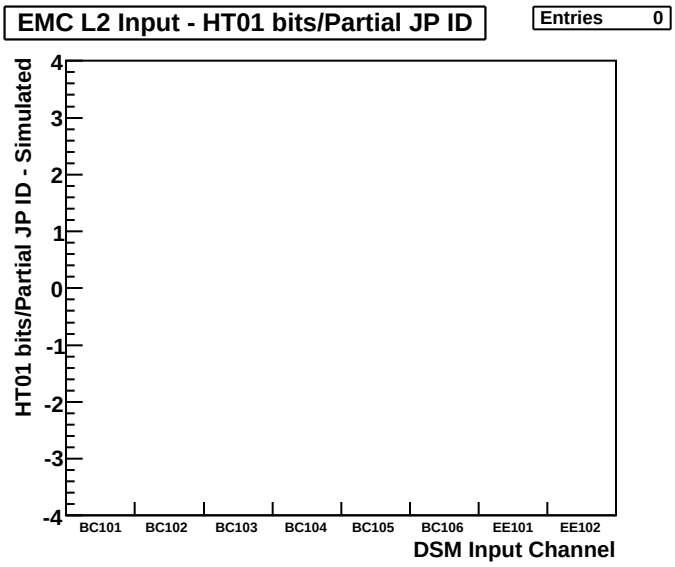

EMC L2 Input - JPZ/JPC bits
Entries 535112
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

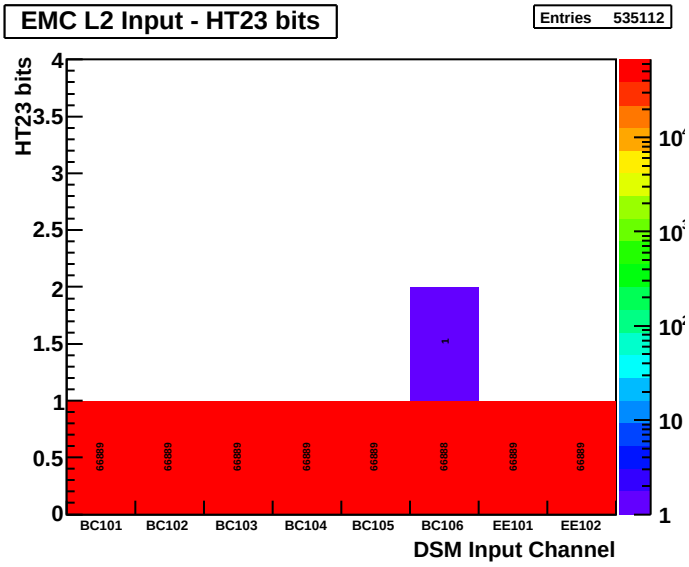

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

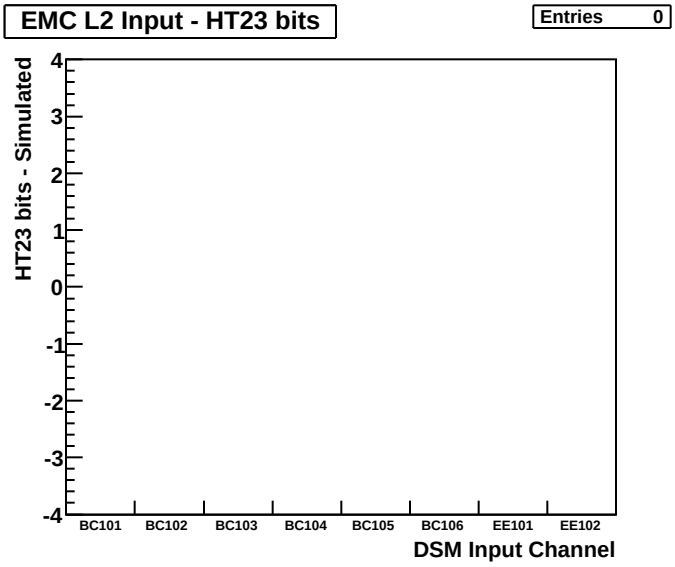

EMC L2 Input - Partial JP Sum
Entries 535112
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

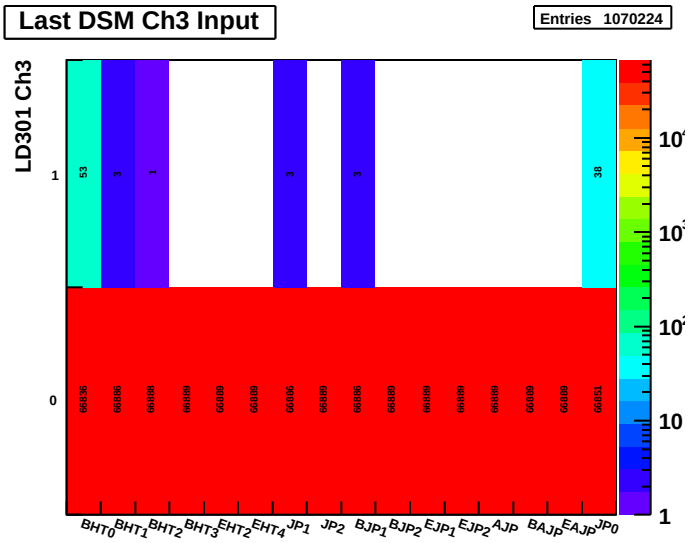

EMC L2 Input - Partial JP Sum
Entries 4
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

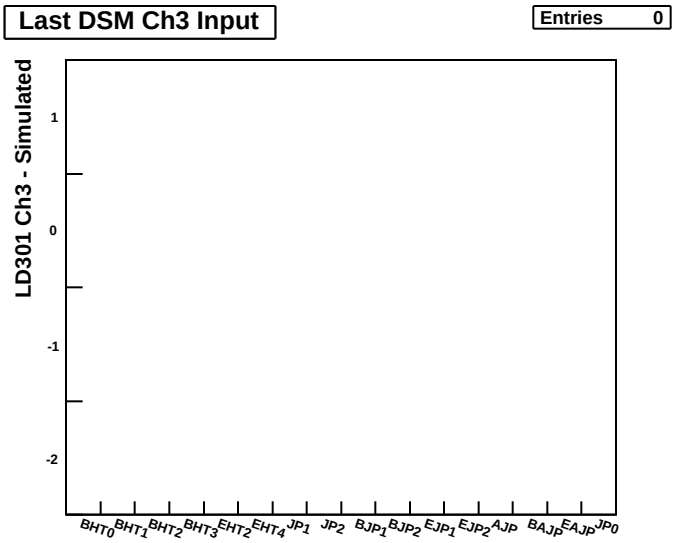

EMC L2 Input - HT01 bits/Partial JP ID
Entries 535112
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

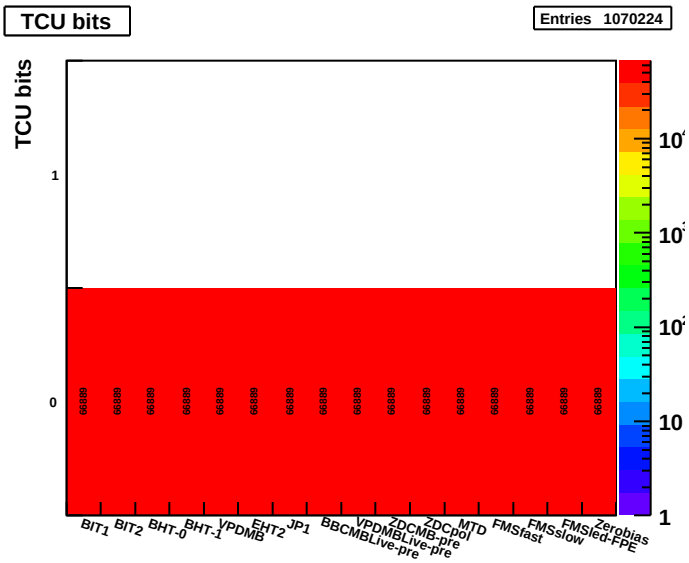

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

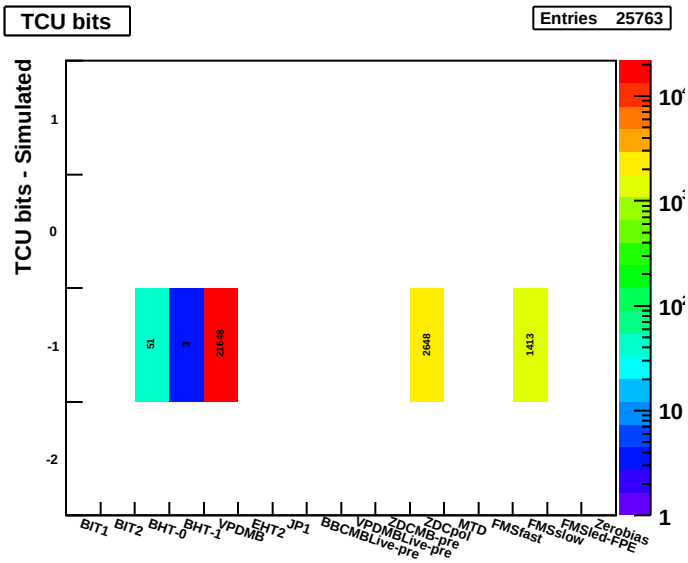

EMC L2 Input - HT23 bits
Entries 535112
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

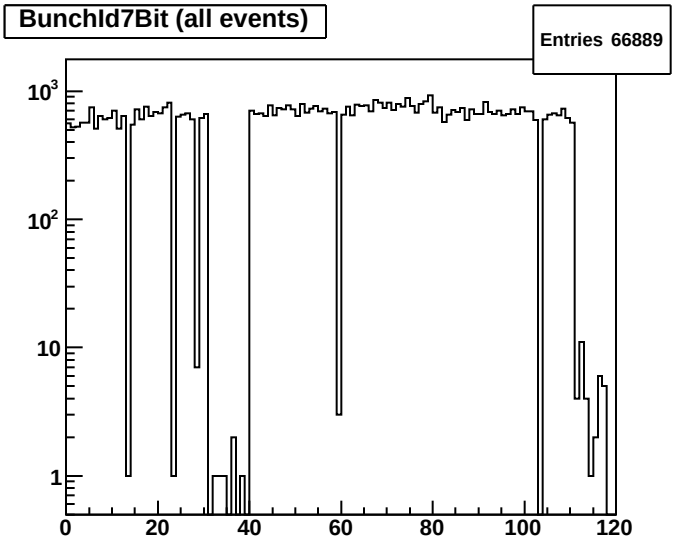

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

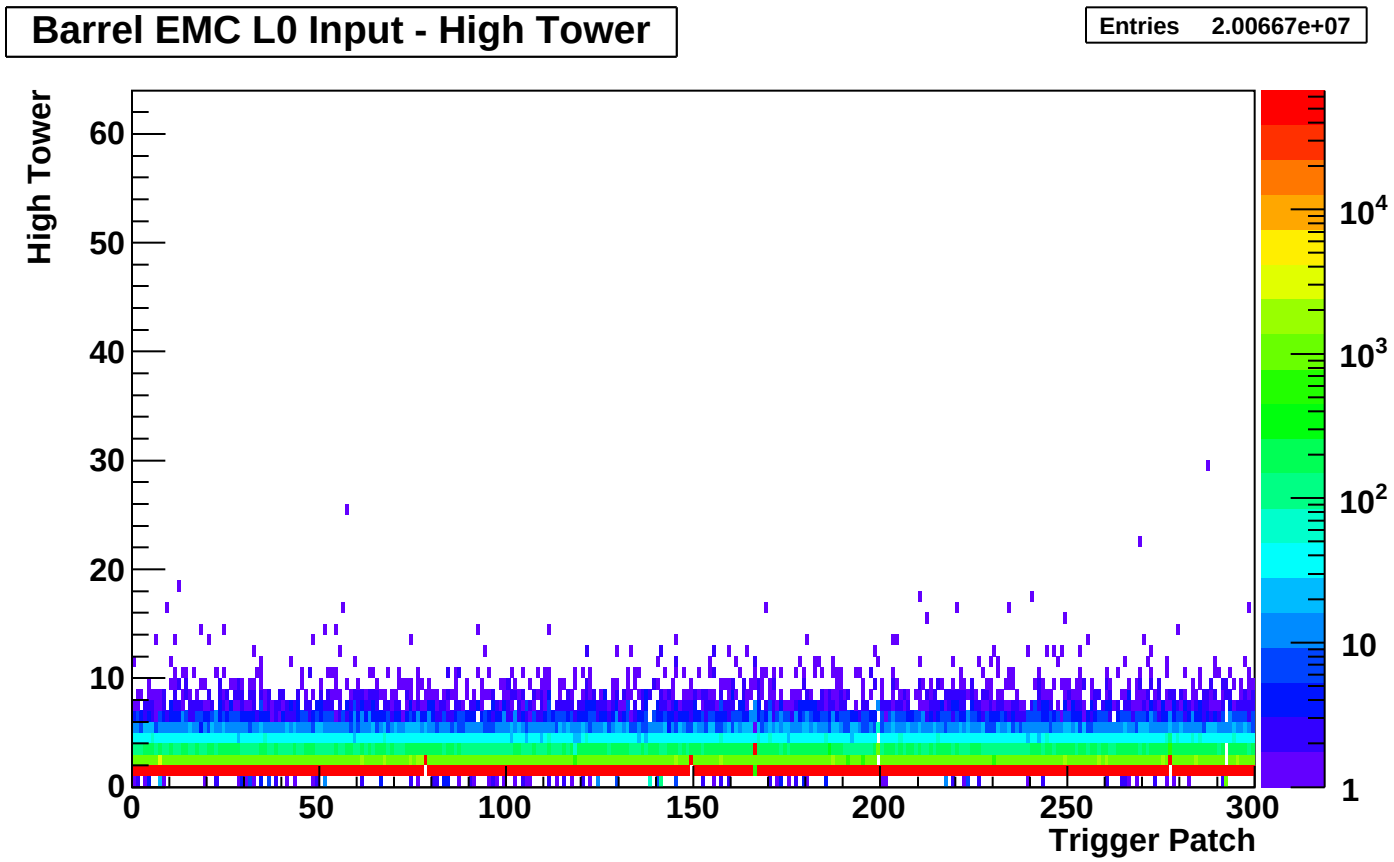

Last DSM Ch3 Input
Entries 1070224
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

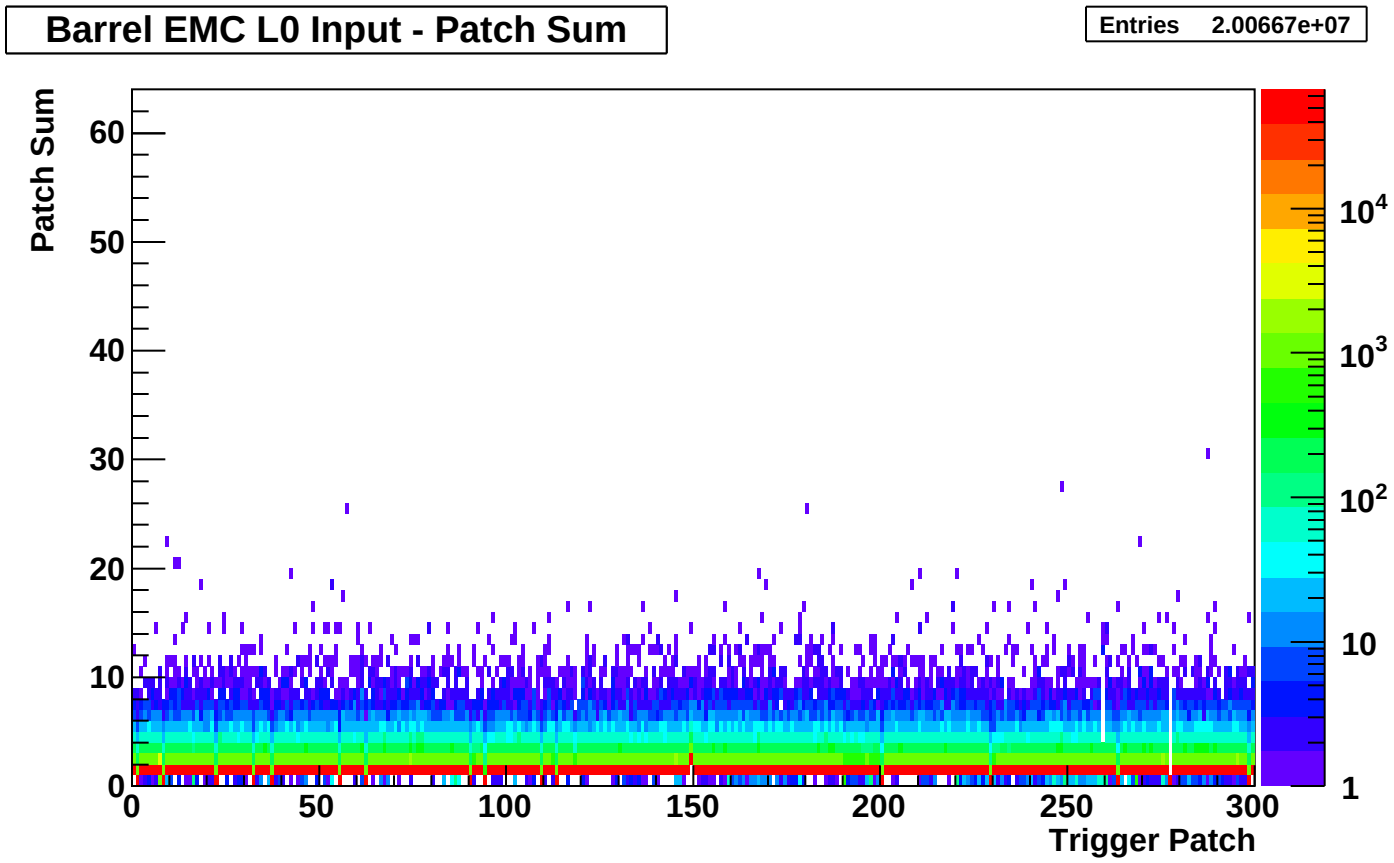

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

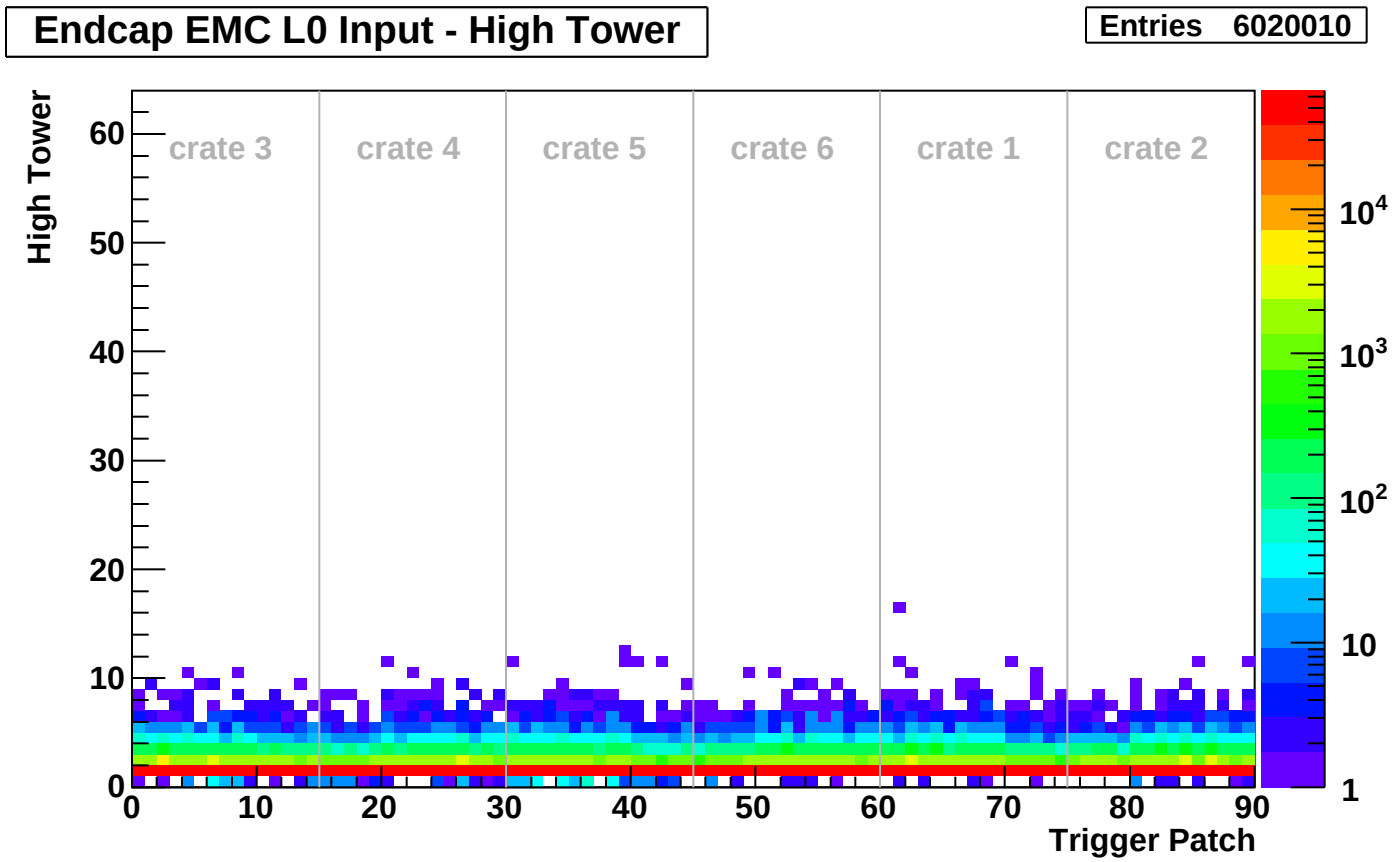

TCU bits
Entries 1070224
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

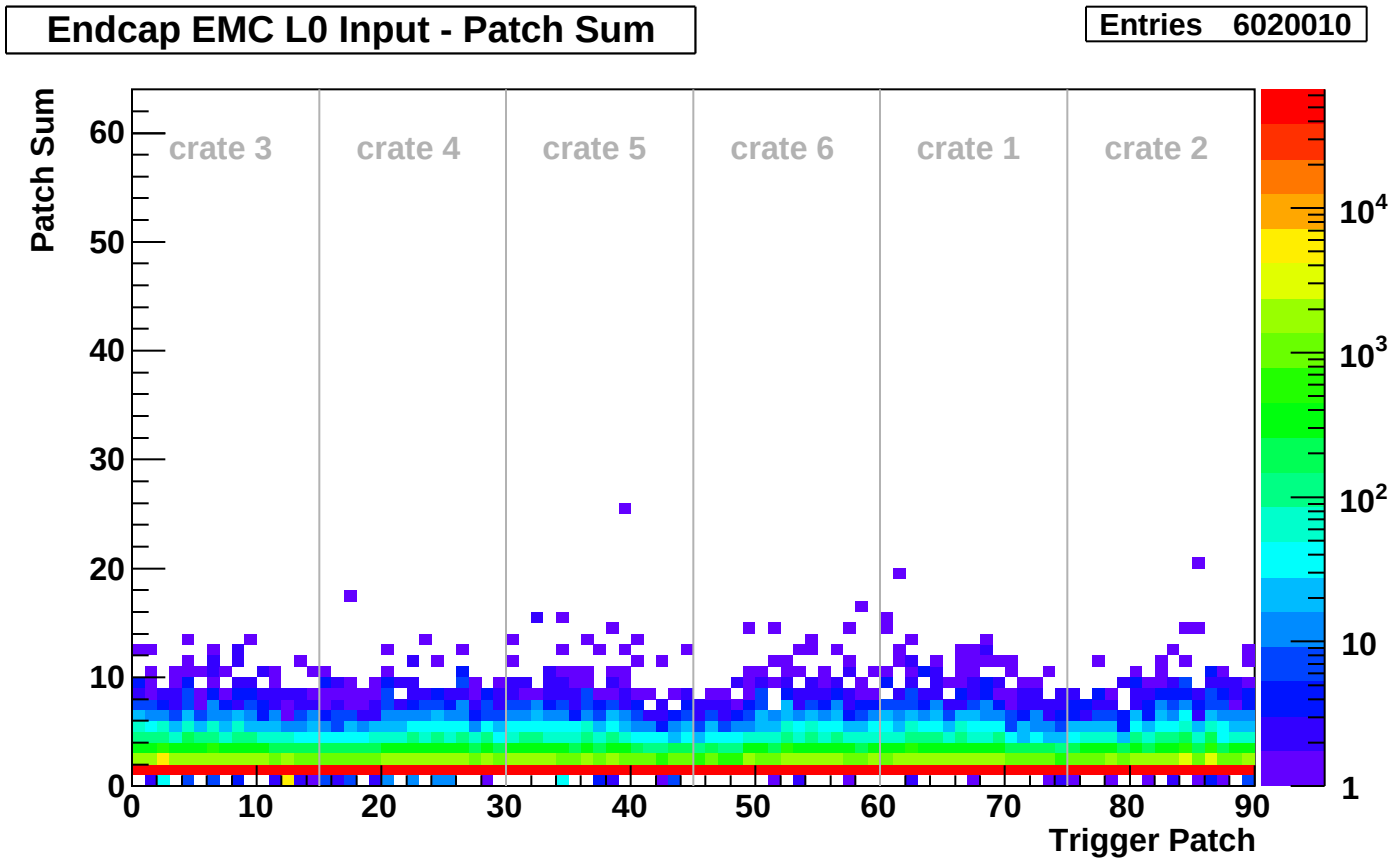

TCU bits
Entries 25763
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

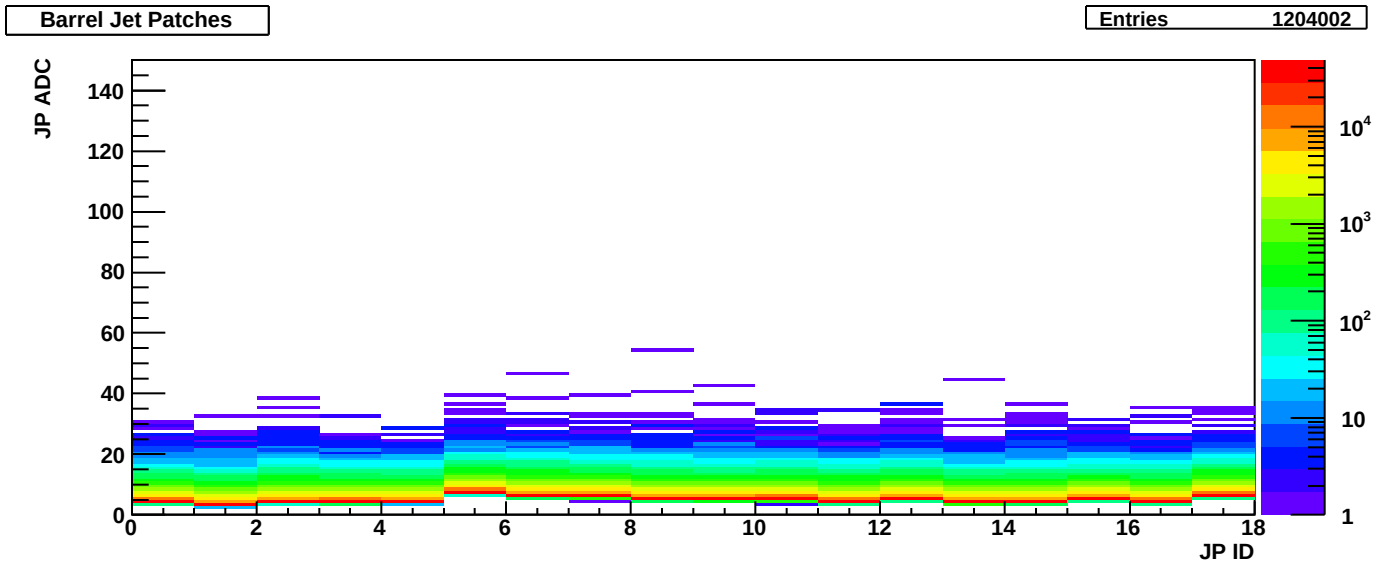

BunchId7Bit (all events)
Entries 66889

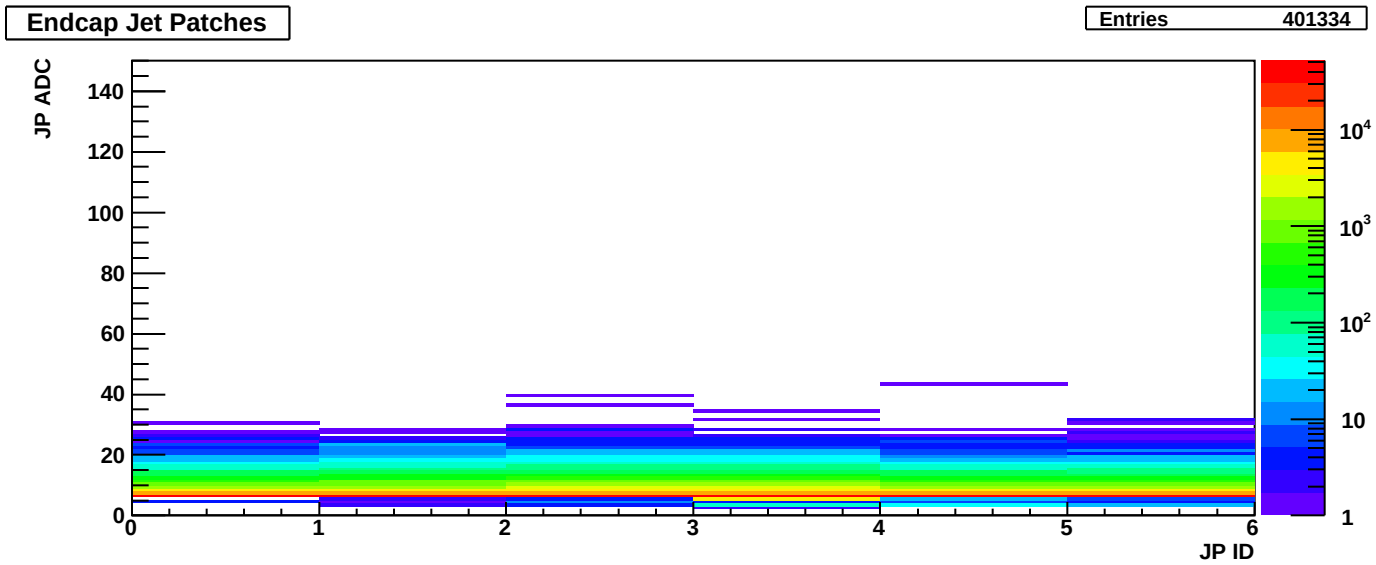

Barrel EMC L0 Input - High Tower
Entries 2.00667e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

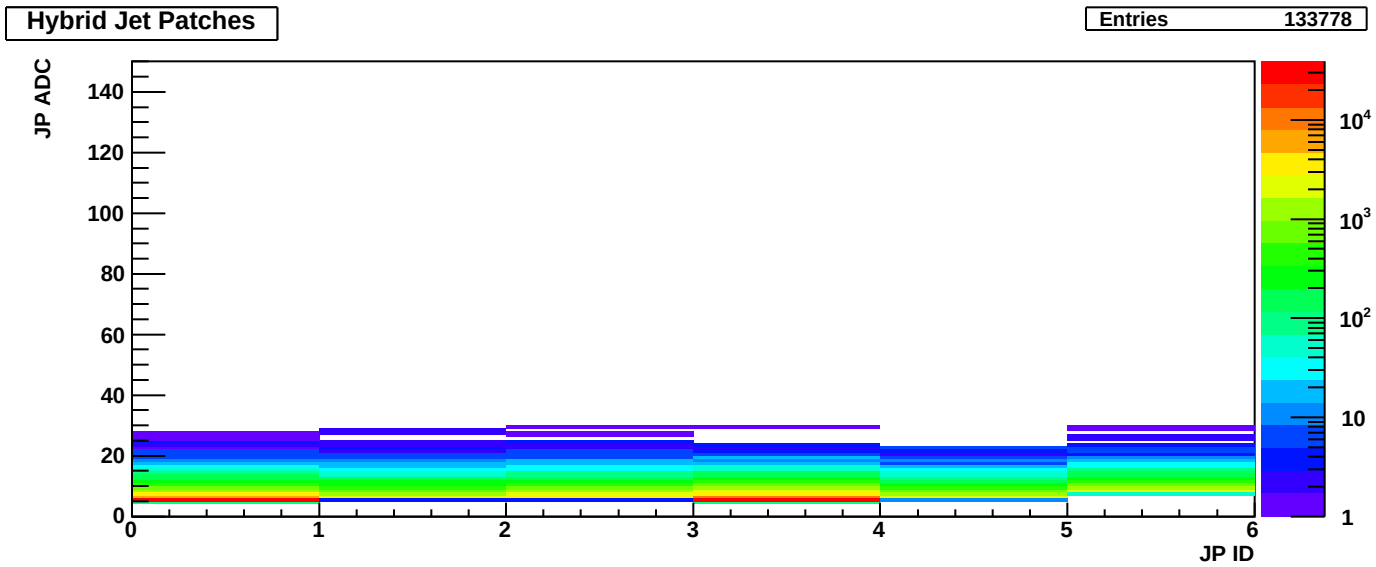

Barrel EMC L0 Input - Patch Sum
Entries 2.00667e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

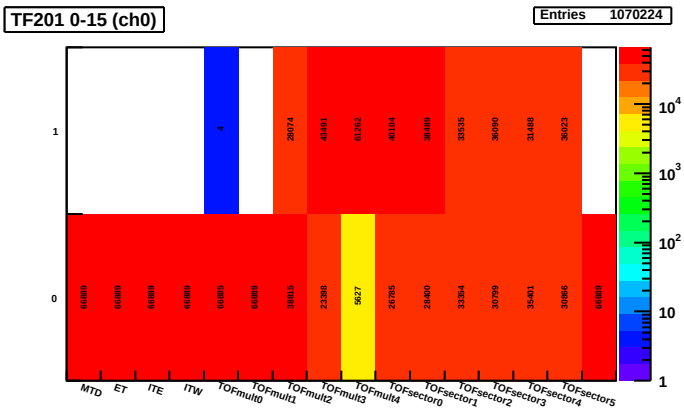

Endcap EMC L0 Input - High Tower
Entries 6020010
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

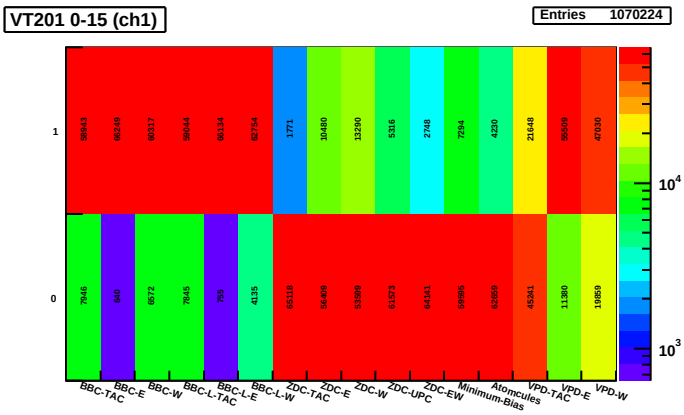

Endcap EMC L0 Input - Patch Sum
Entries 6020010
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch

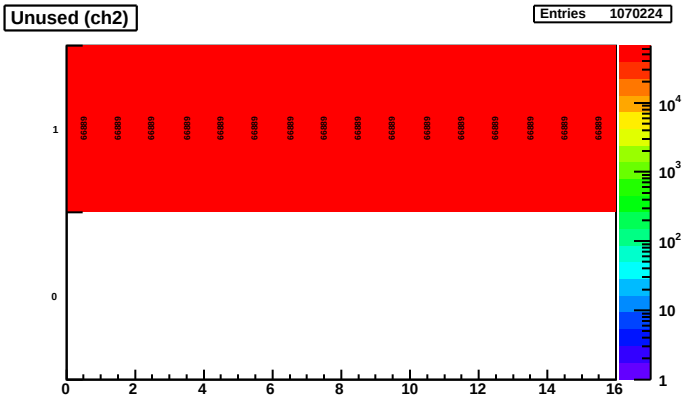

Barrel Jet Patches
Entries 1204002
JP ADC
JP ID

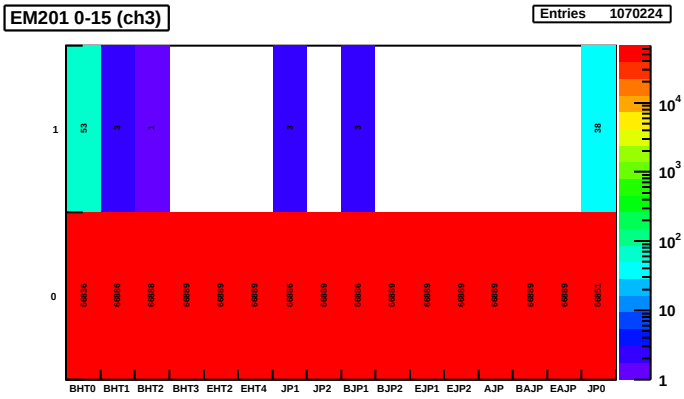

Endcap Jet Patches
Entries 401334
JP ADC
JP ID

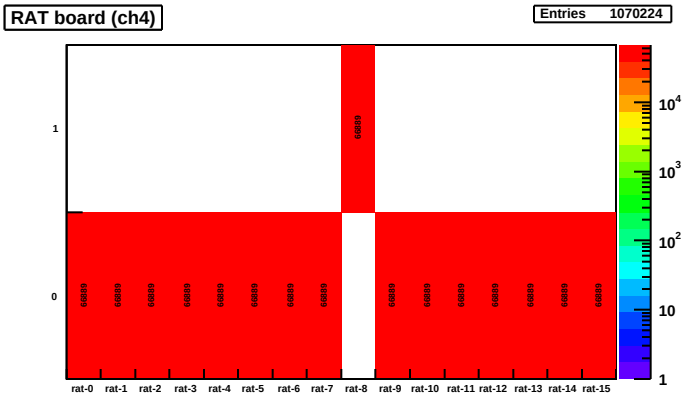

Hybrid Jet Patches
Entries 133778
JP ADC
JP ID

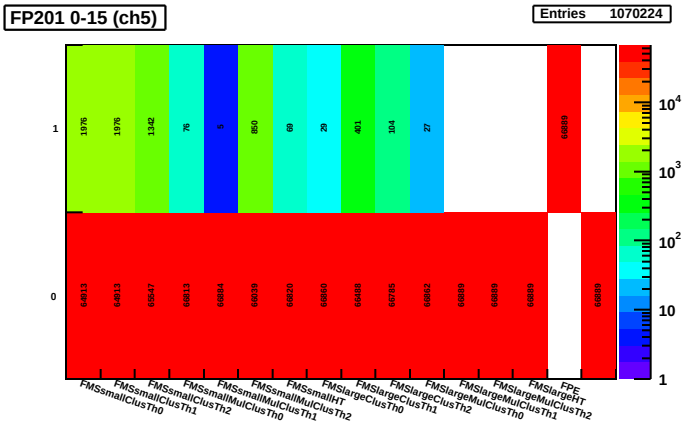

TF201 0-15 (ch0)
Entries 1070224
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

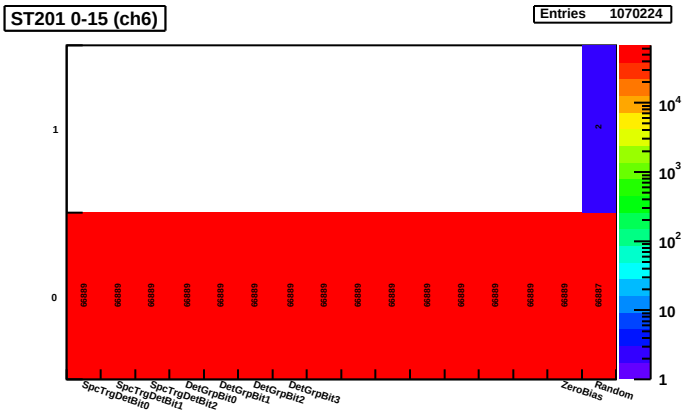

VT201 0-15 (ch1)
Entries 1070224
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Atomcules Minimum-Bias VPD-TAC VPD-E VPD-W

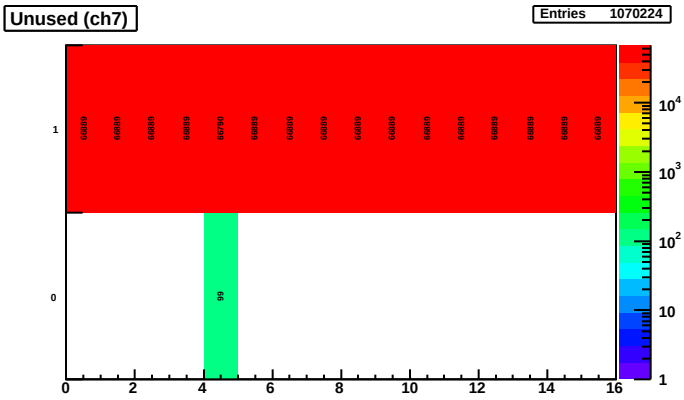

Unused (ch2)
Entries 1070224

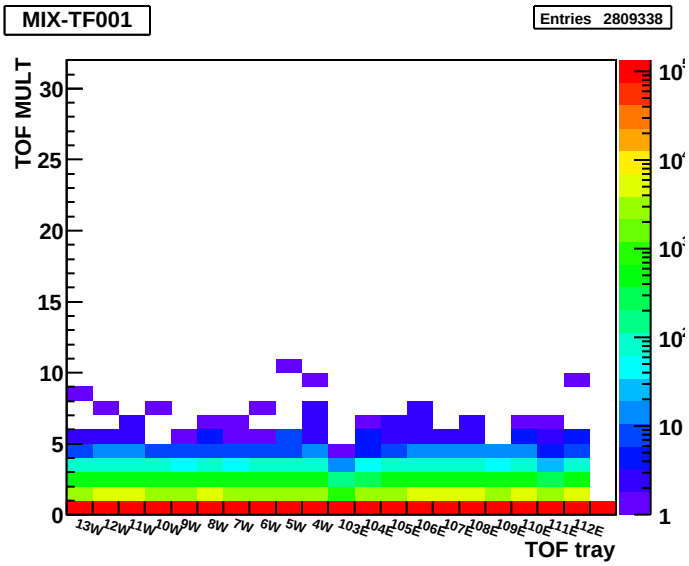

EM201 0-15 (ch3)
Entries 1070224
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

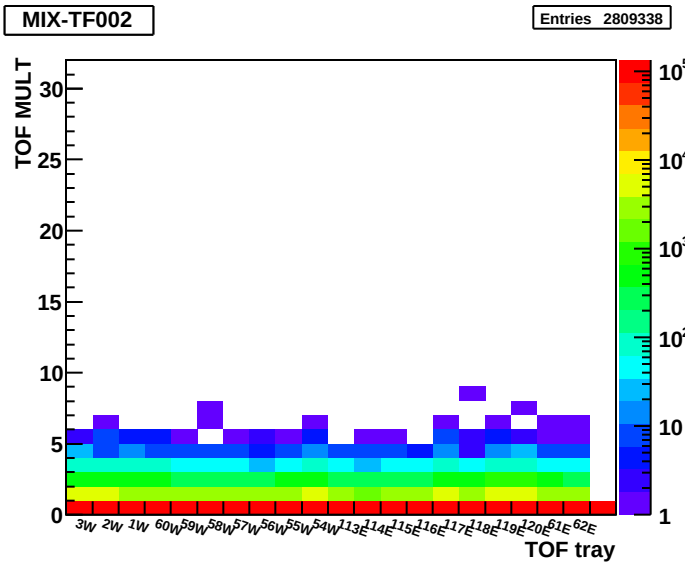

RAT board (ch4)
Entries 1070224
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

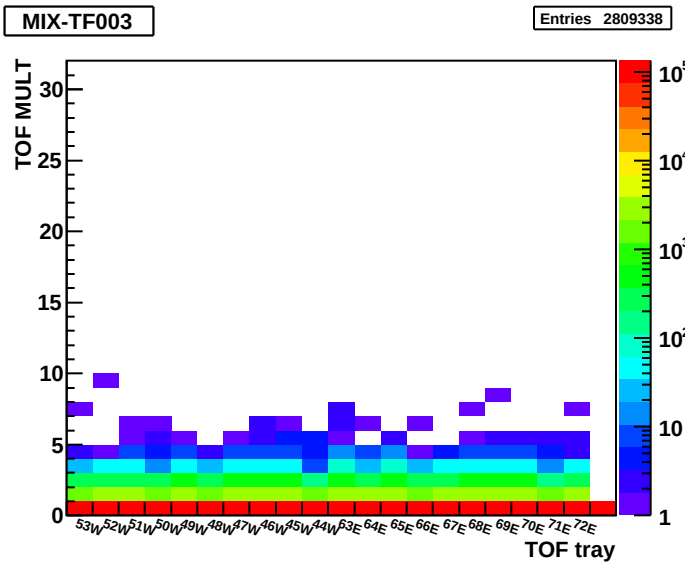

FP201 0-15 (ch5)
Entries 1070224
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

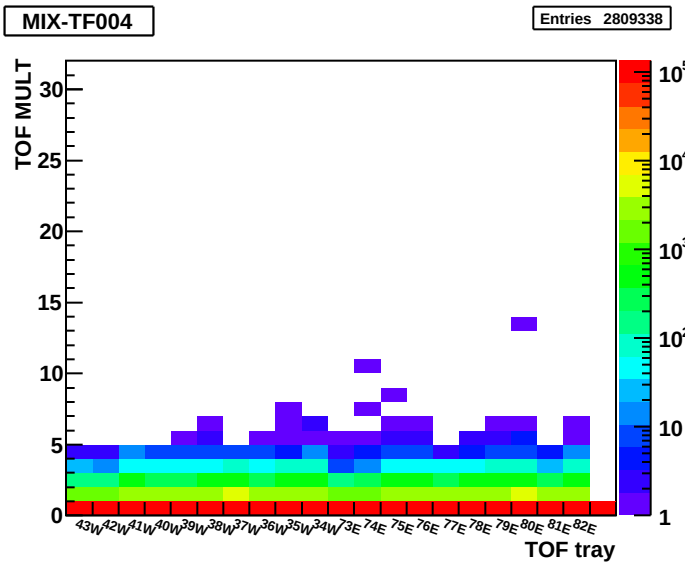

ST201 0-15 (ch6)
Entries 1070224
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

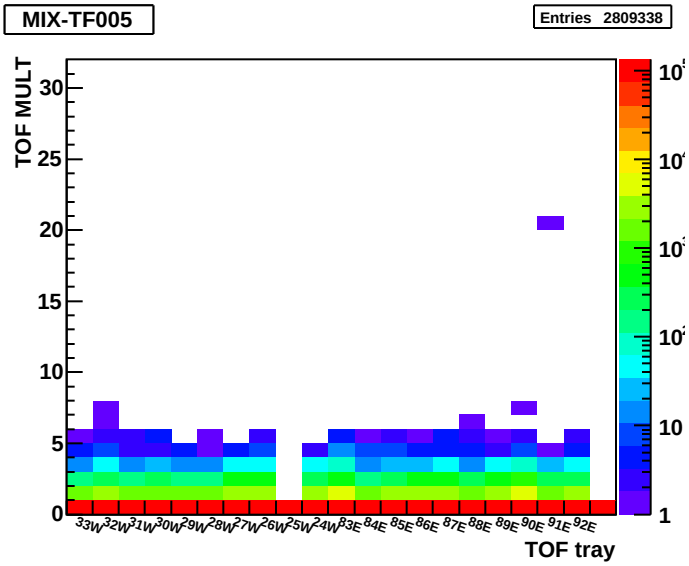

Unused (ch7)
Entries 1070224

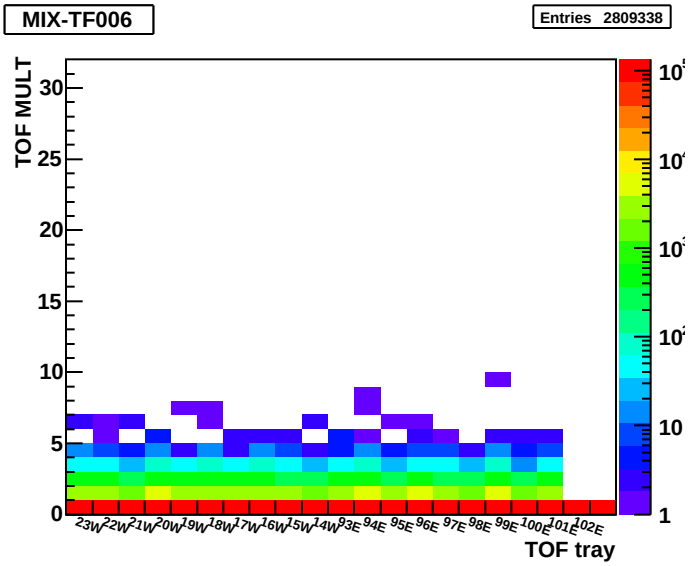

MIX-TF001
Entries 2809338
TOF MULT
TOF tray

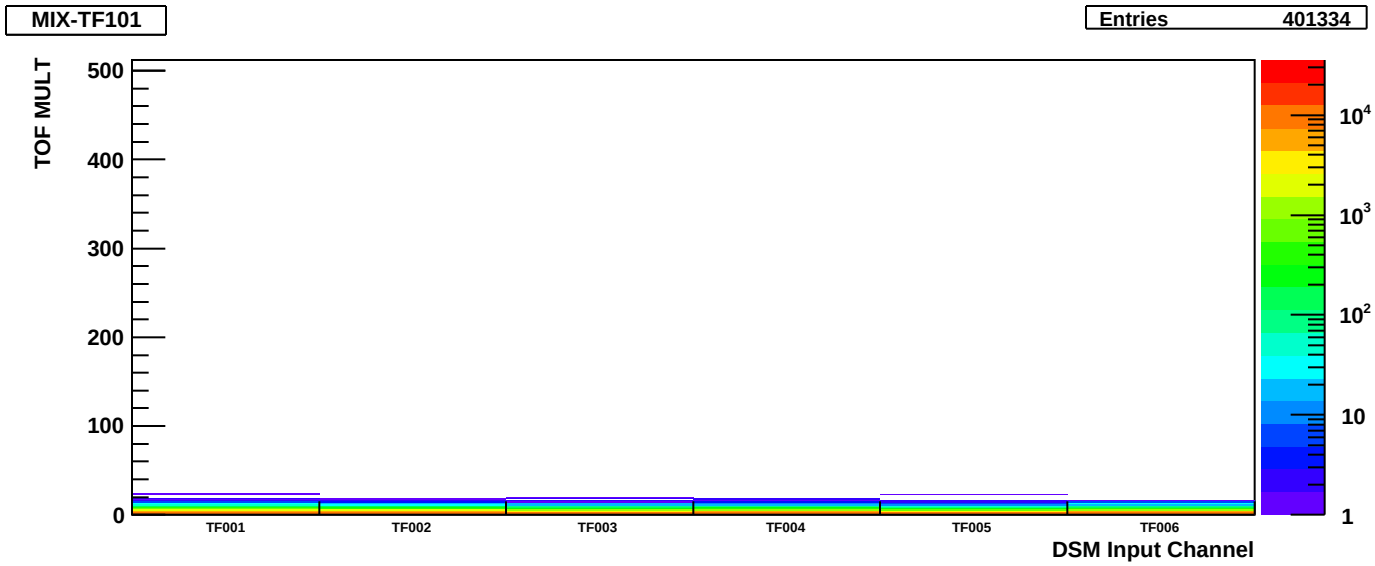

MIX-TF002
Entries 2809338
TOF MULT
TOF tray

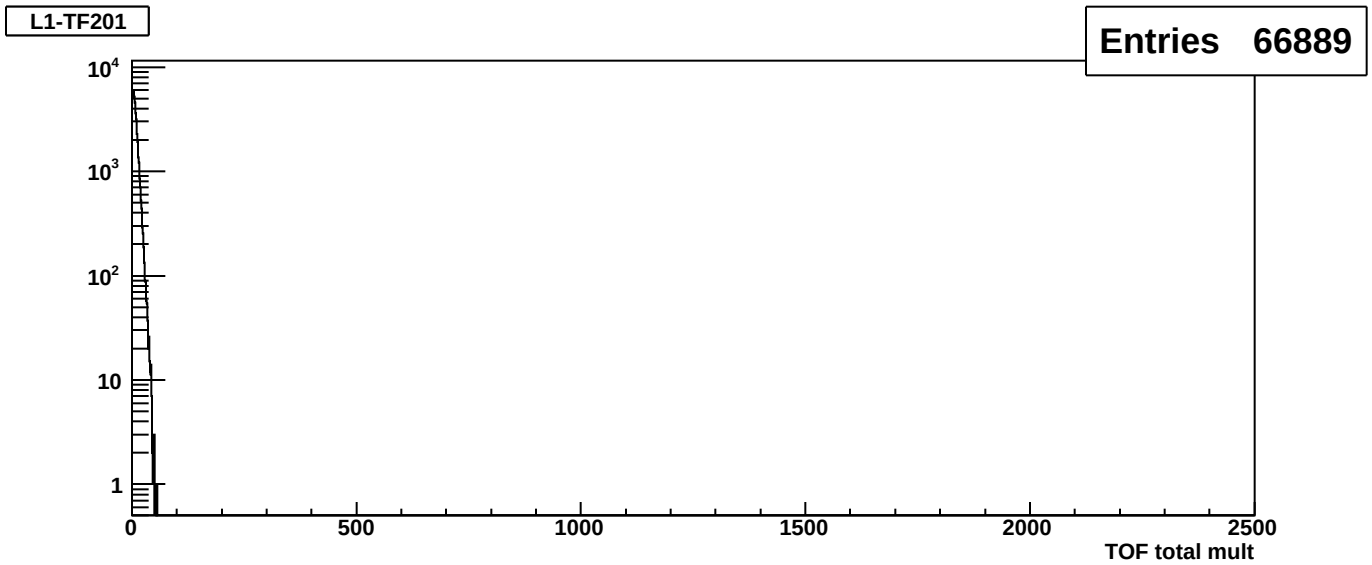

MIX-TF003
Entries 2809338
TOF MULT
TOF tray

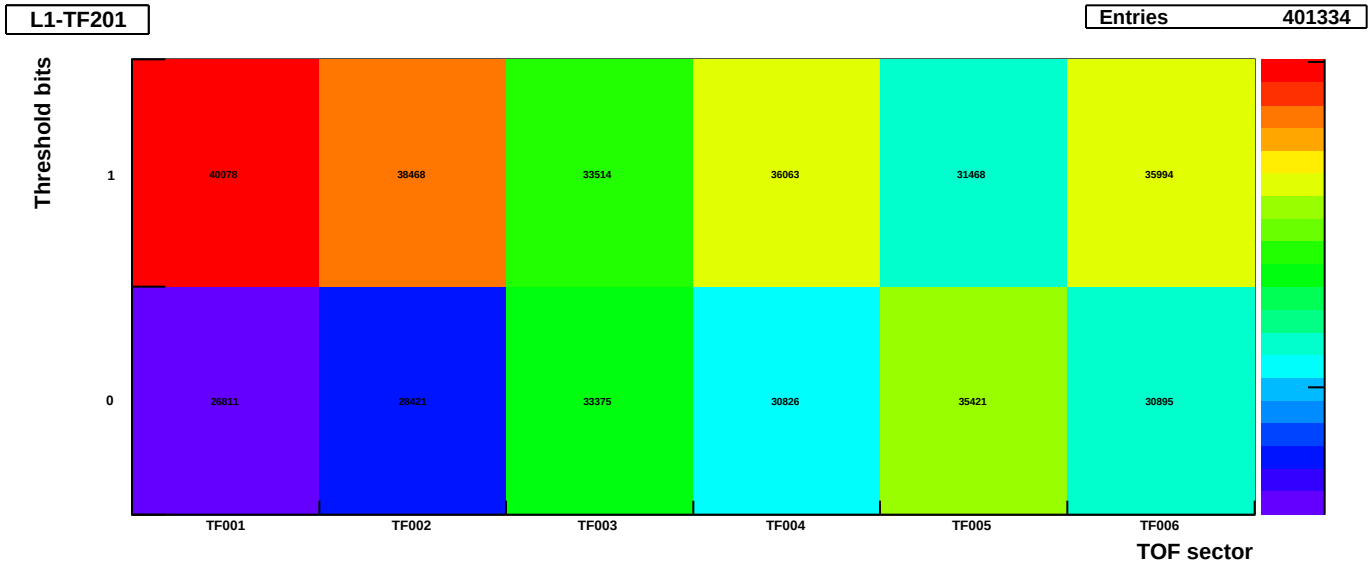

MIX-TF004
Entries 2809338
TOF MULT
TOF tray

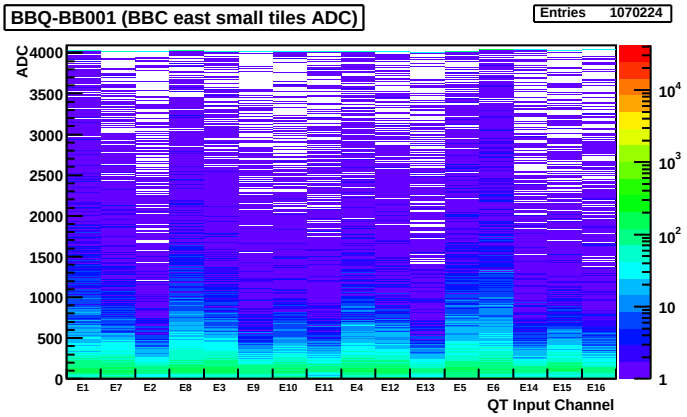

MIX-TF005
Entries 2809338
TOF MULT
TOF tray

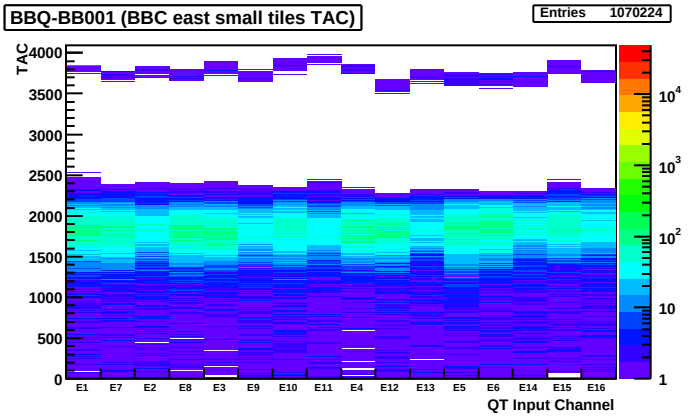

MIX-TF006
Entries 2809338
TOF MULT
TOF tray

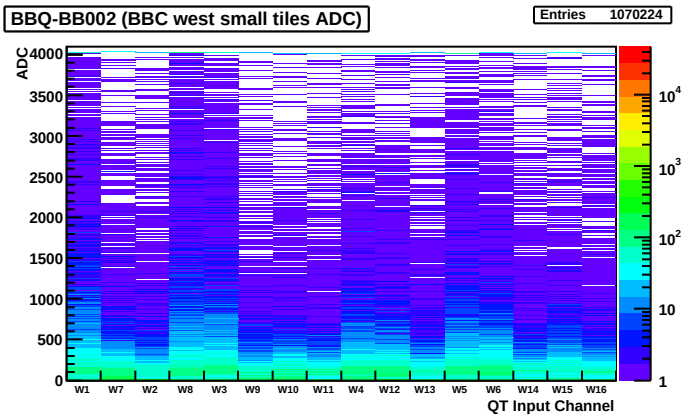

MIX-TF101
Entries 401334
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

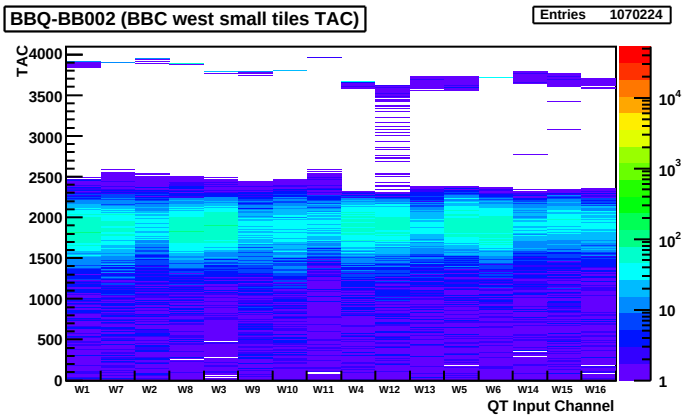

L1-TF201
Entries 66889
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

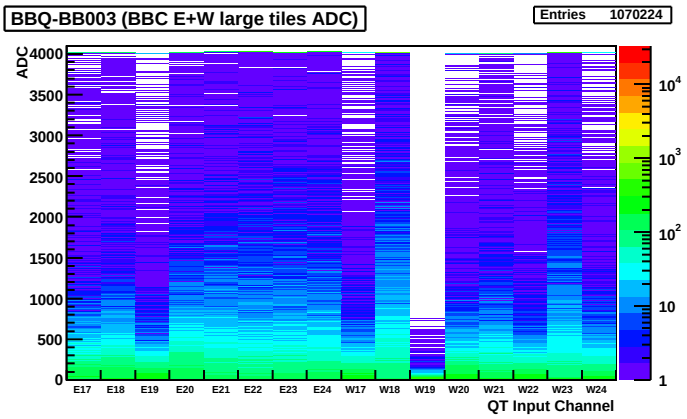

L1-TF201
Entries 401334
Threshold bits
1
0
40078
38468
33514
36063
31468
35994
26811
28421
33375
30826
35421
30895
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

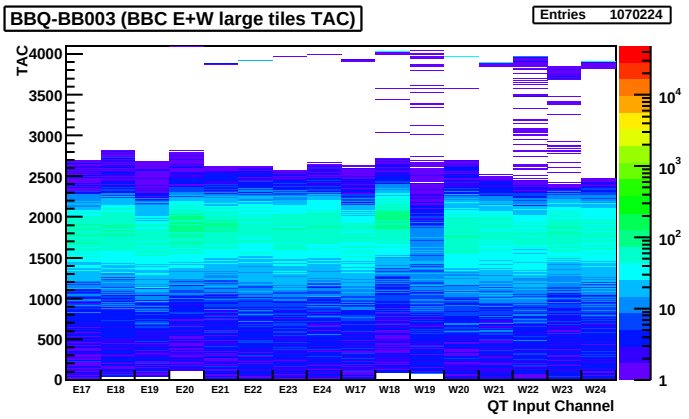

BBQ-BB001 (BBC east small tiles ADC)
Entries 1070224
ADC
QT Input Channel

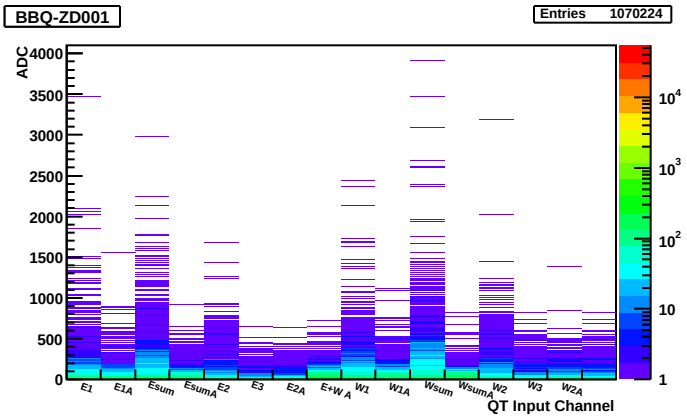

BBQ-BB001 (BBC east small tiles TAC)
Entries 1070224
TAC
QT Input Channel

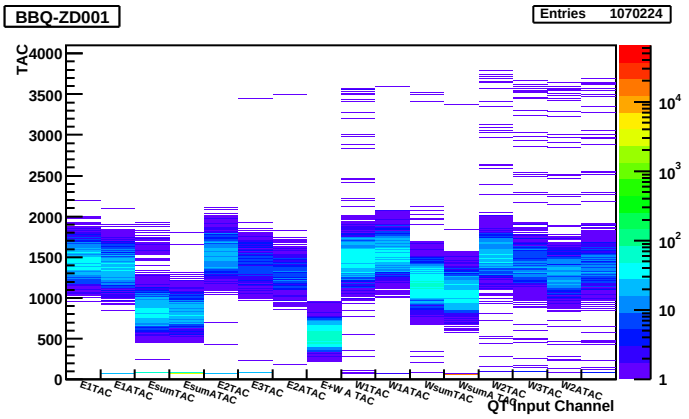

BBQ-BB002 (BBC west small tiles ADC)
Entries 1070224
ADC
QT Input Channel

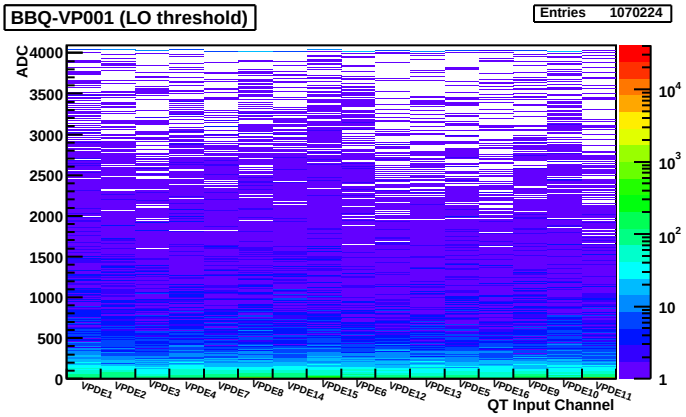

BBQ-BB002 (BBC west small tiles TAC)
Entries 1070224
TAC
QT Input Channel

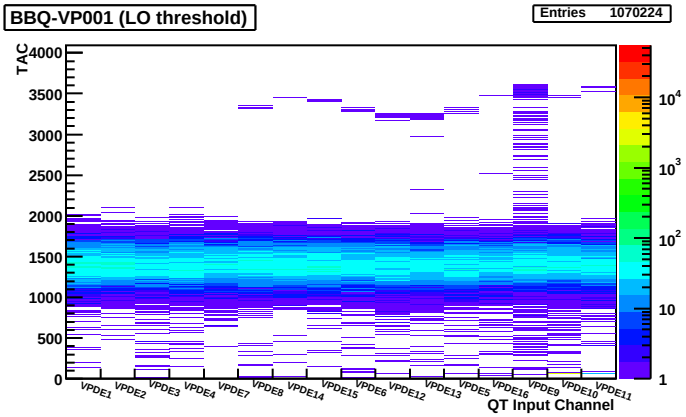

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1070224
ADC
QT Input Channel

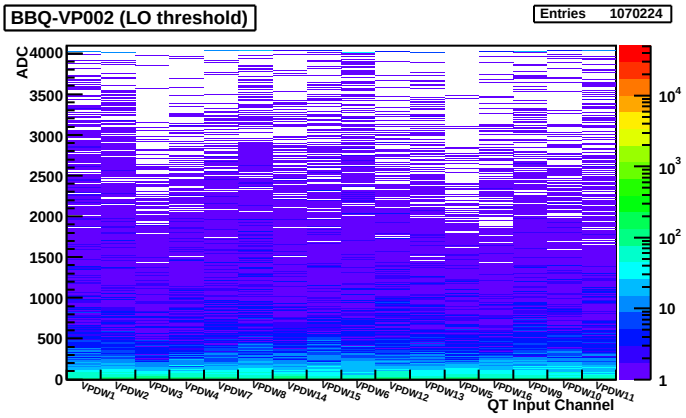

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1070224
TAC
QT Input Channel

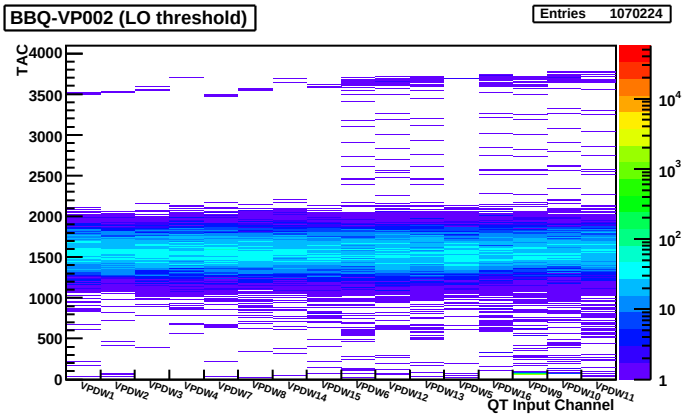

BBQ-ZD001
Entries 1070224
ADC
QT Input Channel

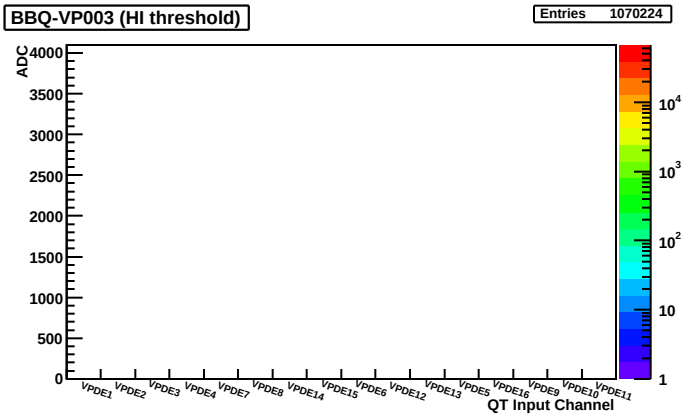

BBQ-ZD001
Entries 1070224
TAC
QT Input Channel

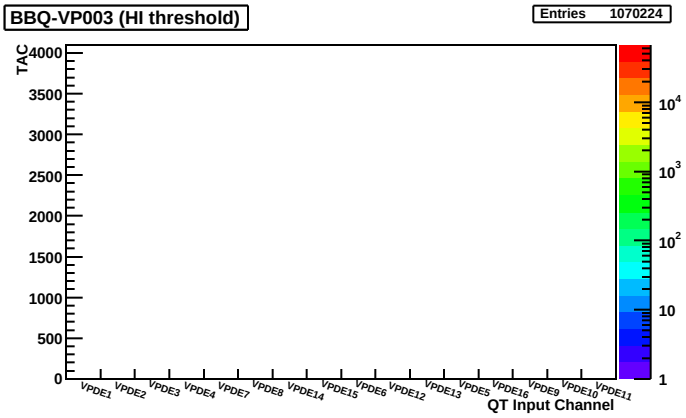

BBQ-VP001 (LO threshold)
Entries 1070224
ADC
QT Input Channel

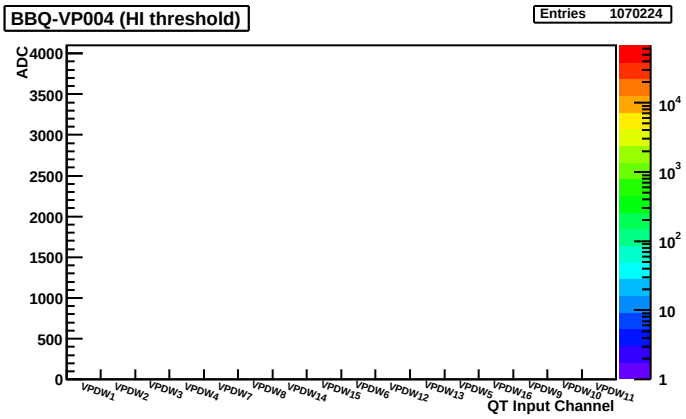

BBQ-VP001 (LO threshold)
Entries 1070224
TAC
QT Input Channel

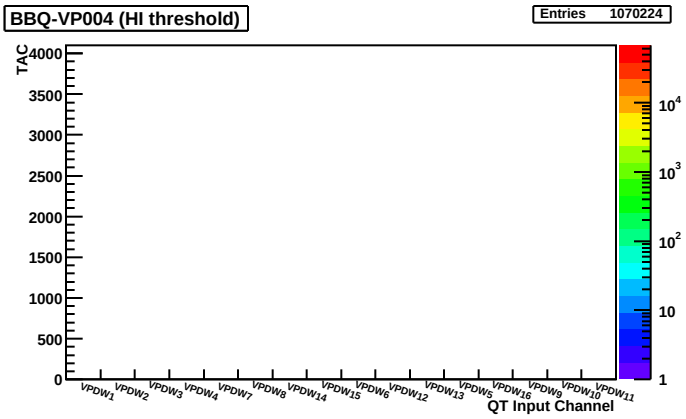

BBQ-VP002 (LO threshold)
Entries 1070224
ADC
QT Input Channel

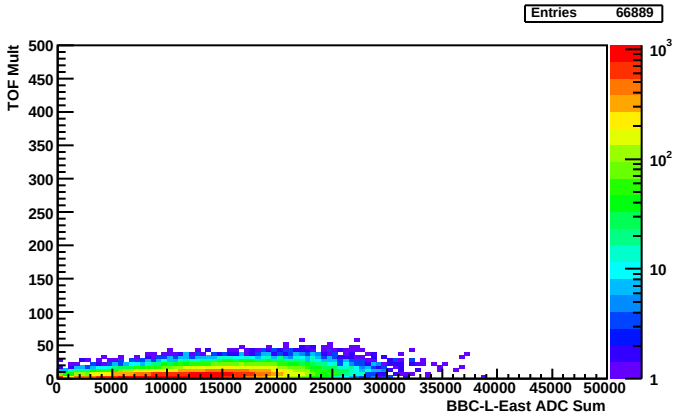

BBQ-VP002 (LO threshold)
Entries 1070224
TAC
QT Input Channel

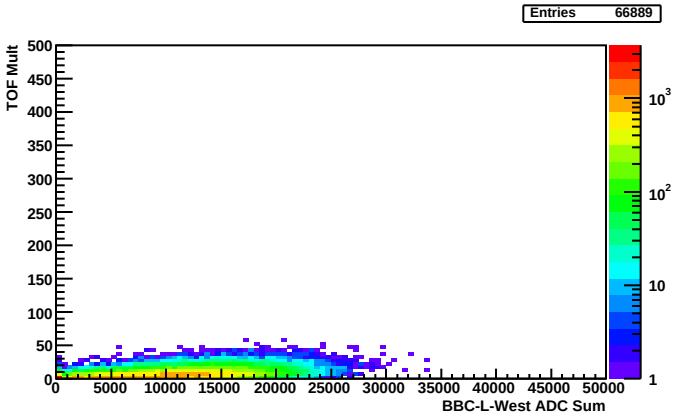

BBQ-VP003 (HI threshold)
Entries 1070224
ADC
QT Input Channel

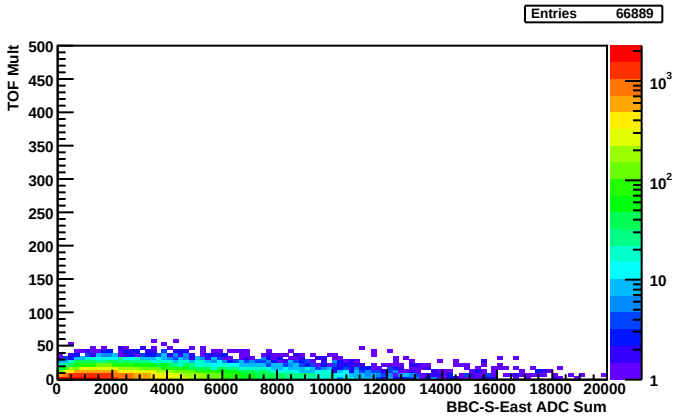

BBQ-VP003 (HI threshold)
Entries 1070224
TAC
QT Input Channel

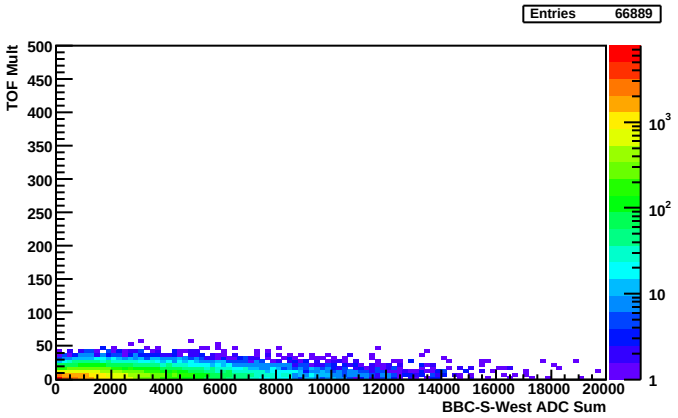

BBQ-VP004 (HI threshold)
Entries 1070224
ADC
QT Input Channel

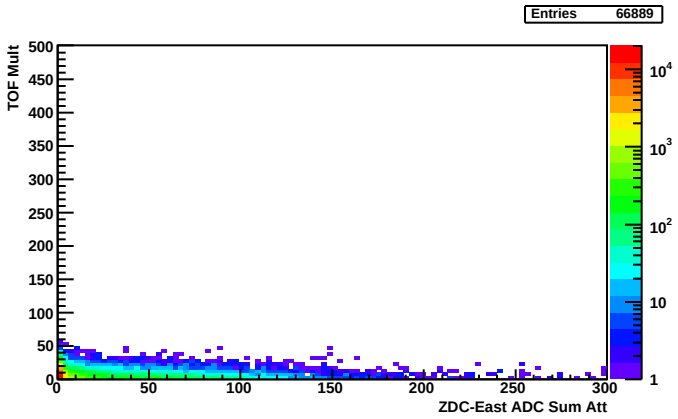

BBQ-VP004 (HI threshold)
Entries 1070224
TAC
QT Input Channel

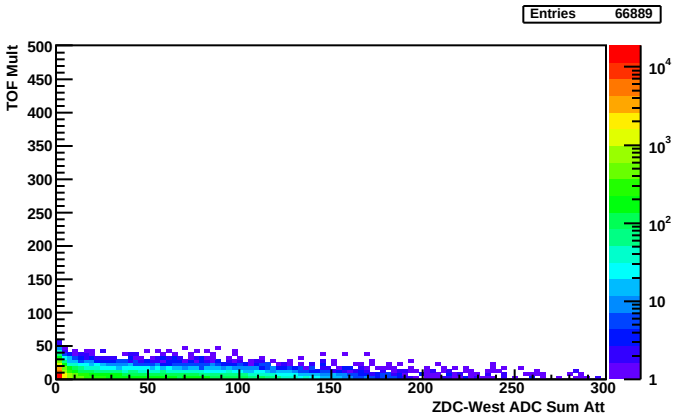

Entries 66889
TOF Mult
BBC-L-East ADC Sum

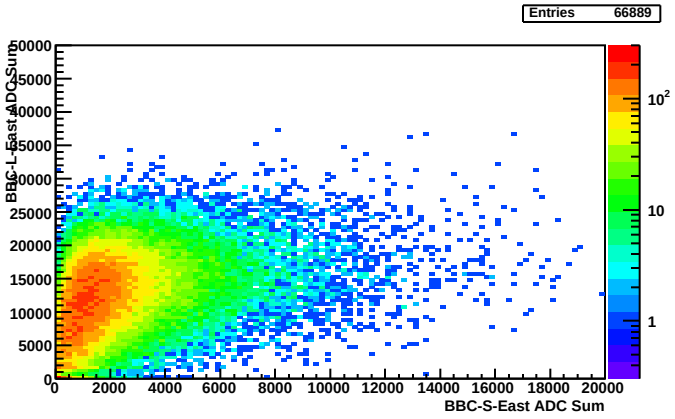

Entries 66889
TOF Mult
BBC-L-West ADC Sum

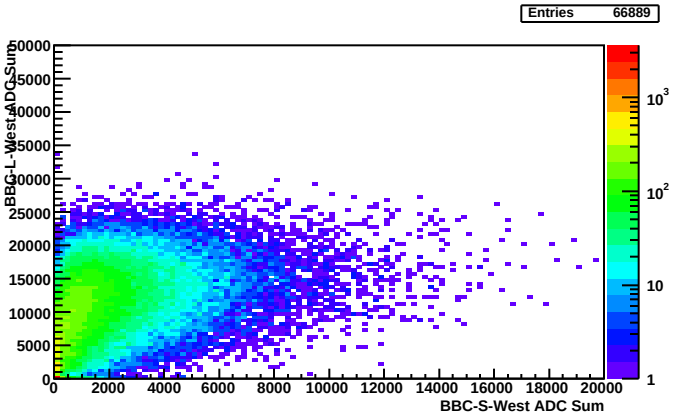

Entries 66889
TOF Mult
BBC-S-East ADC Sum

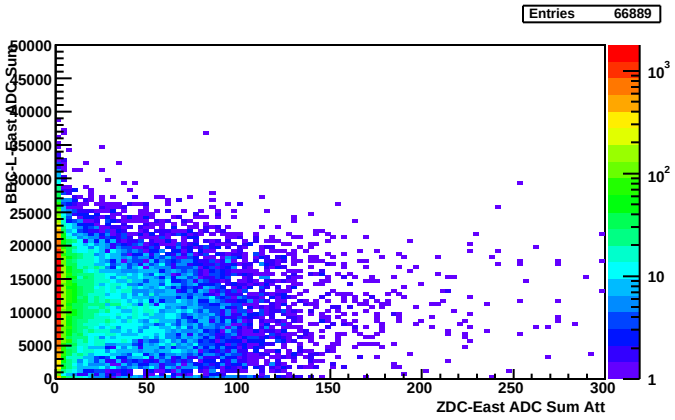

Entries 66889
TOF Mult
BBC-S-West ADC Sum

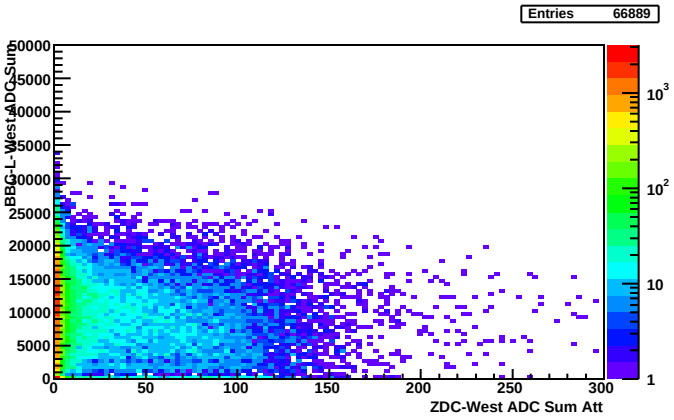

Entries 66889
TOF Mult
ZDC-East ADC Sum Att

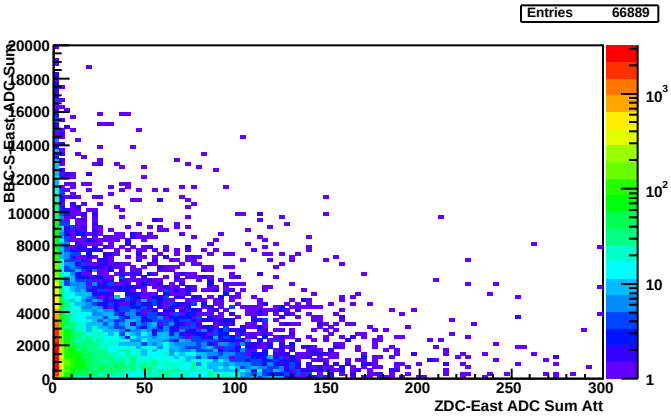

Entries 66889
TOF Mult
ZDC-West ADC Sum Att

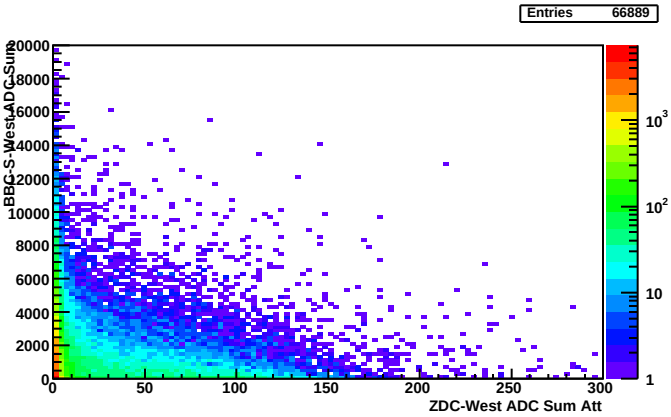

Entries 66889
BBC-L-East ADC Sum
BBC-S-East ADC Sum

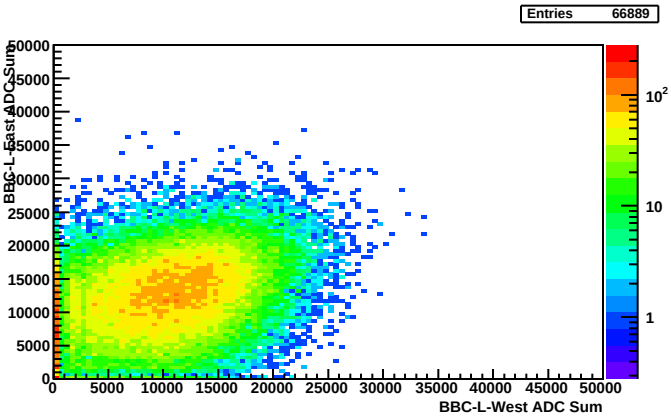

Entries 66889
BBC-L-West ADC Sum
BBC-S-West ADC Sum

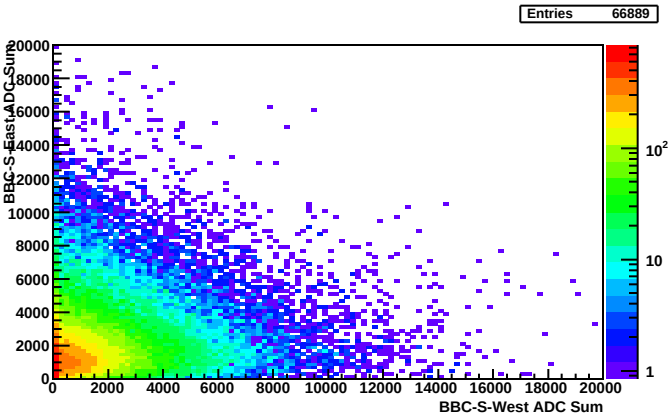

Entries 66889
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

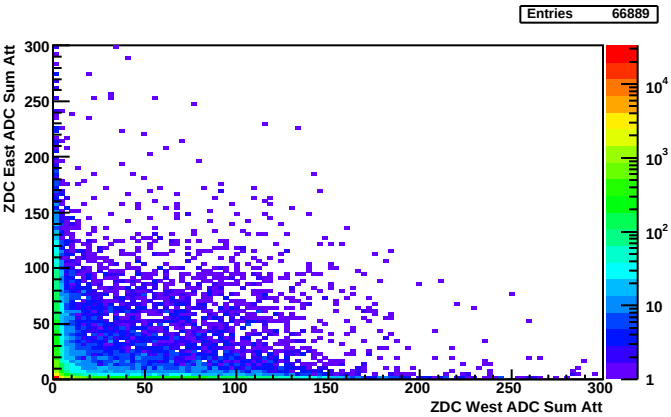

Entries 66889
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

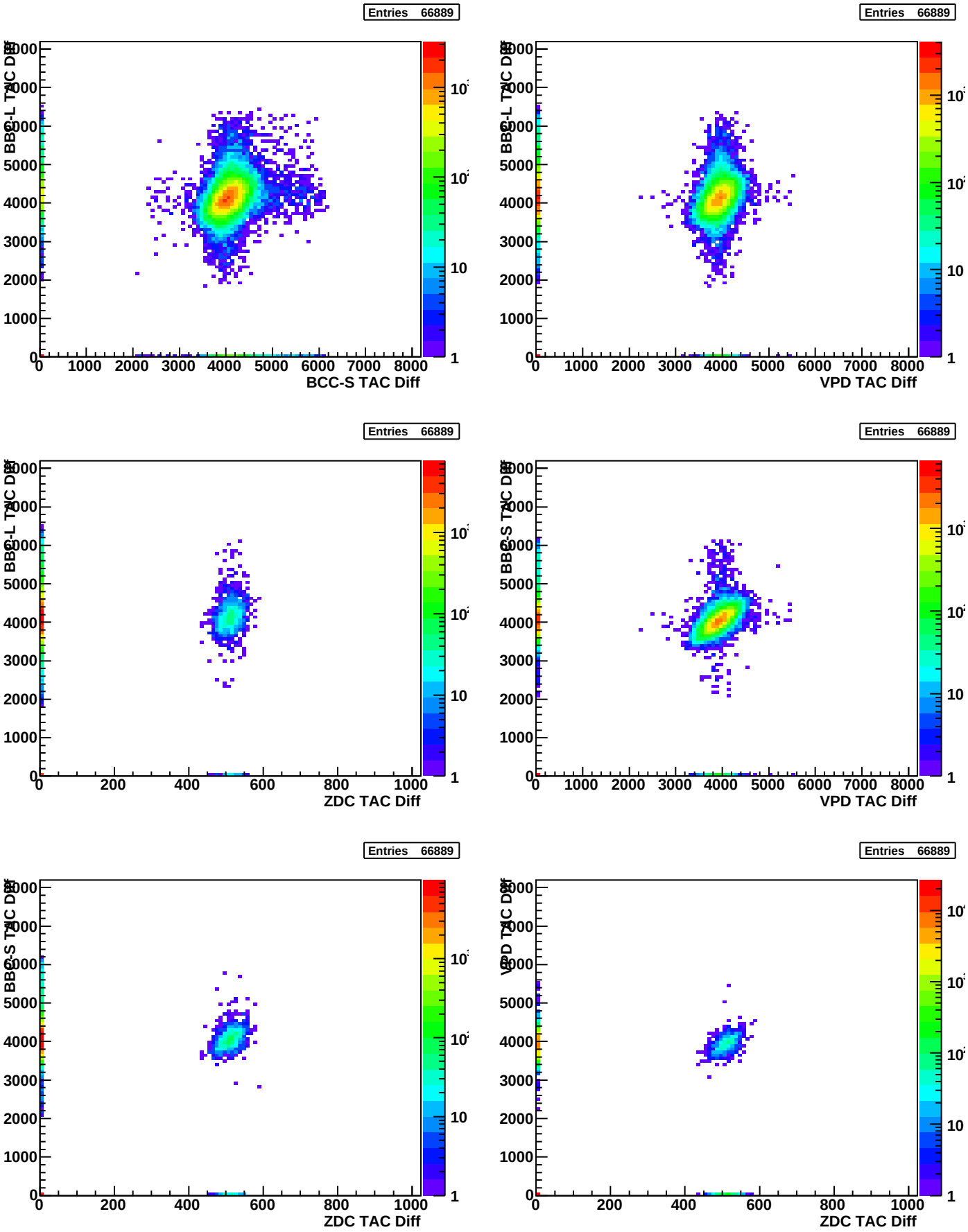

Entries 66889
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

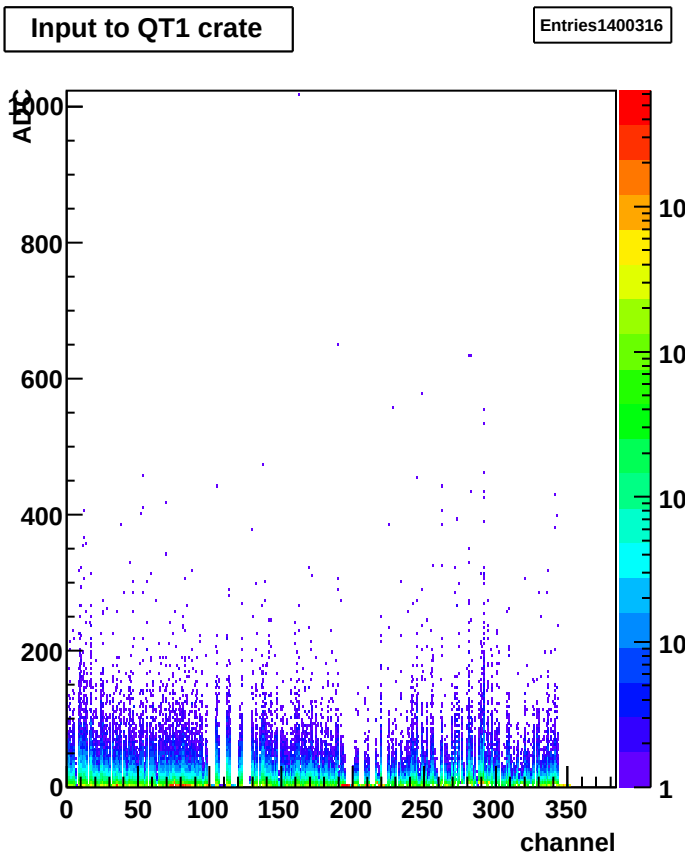

Entries 66889
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

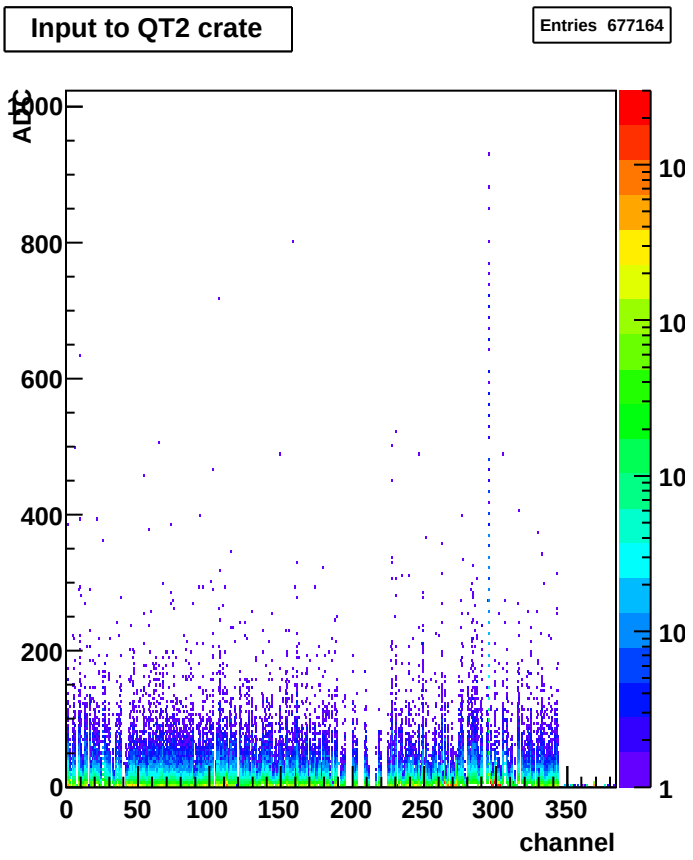

Entries 66889
BBC-L-East ADC Sum
BBC-L-West ADC Sum

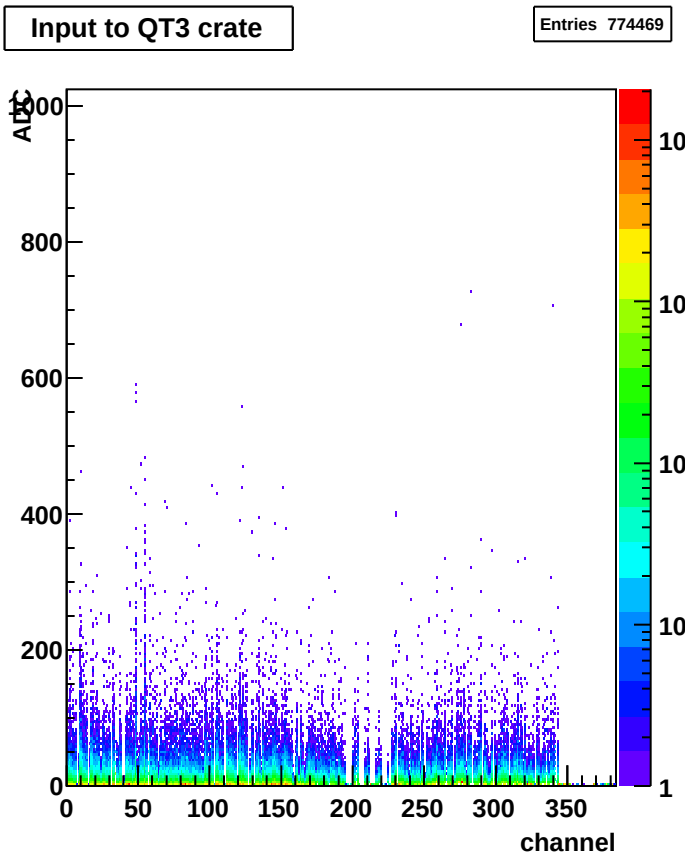

Entries 66889
BBC-S-East ADC Sum
BBC-S-West ADC Sum

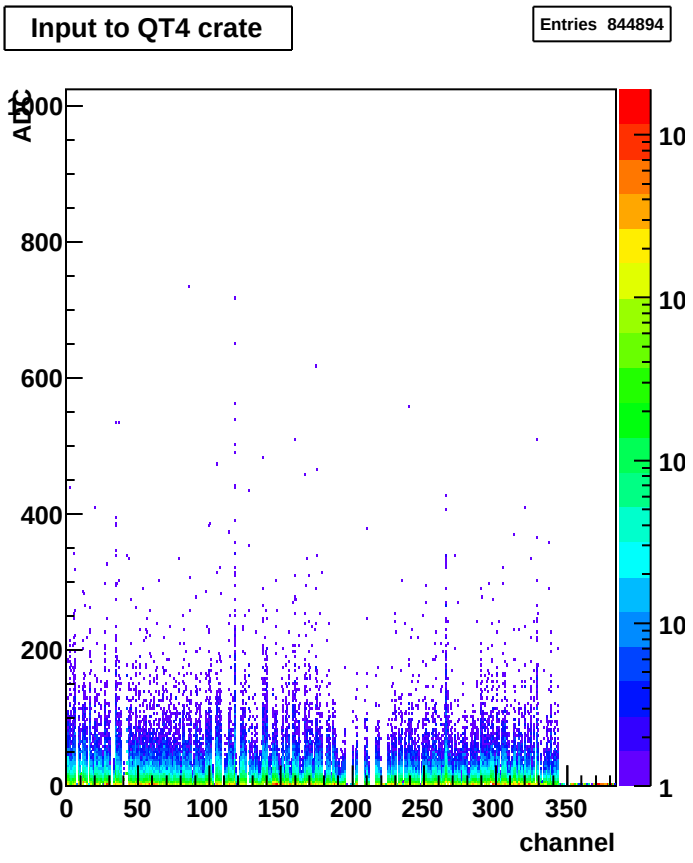

Entries 66889
ZDC East ADC Sum Att
ZDC West ADC Sum Att

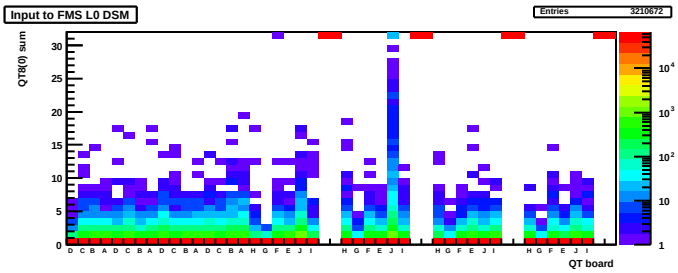

Entries 66889
BBC-L TAC Diff
BCC-S TAC Diff
Entries 66889
BBC-L TAC Diff
VPD TAC Diff
Entries 66889
BBC-L TAC Diff
ZDC TAC Diff
Entries 66889
BBC-S TAC Diff
VPD TAC Diff
Entries 66889
BBC-S TAC Diff
ZDC TAC Diff
Entries 66889
VPD TAC Diff
ZDC TAC Diff

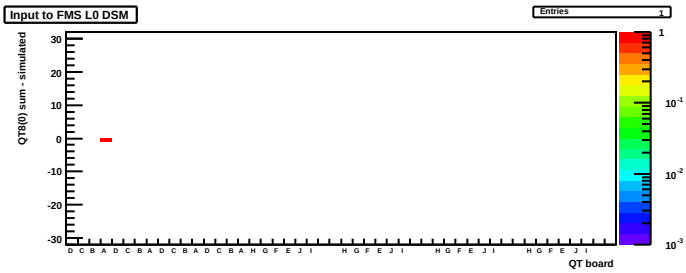

Input to QT1 crate
Entries1400316
ADC
channel

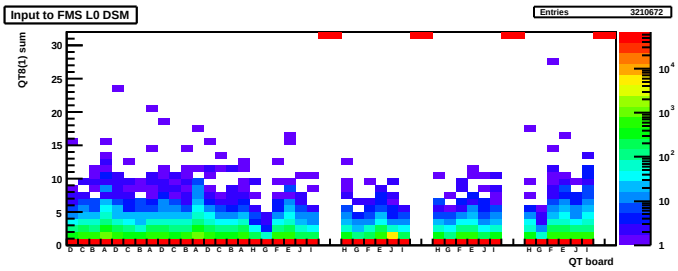

Input to QT2 crate
Entries 677164
ADC
channel

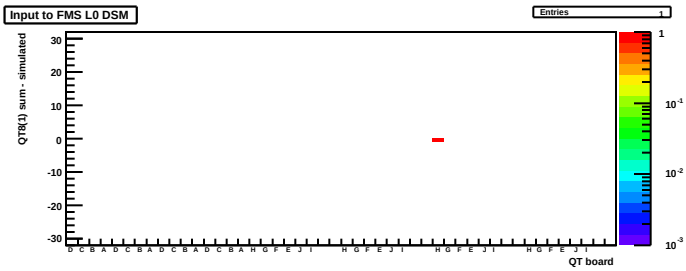

Input to QT3 crate
Entries 774469
ADC
channel

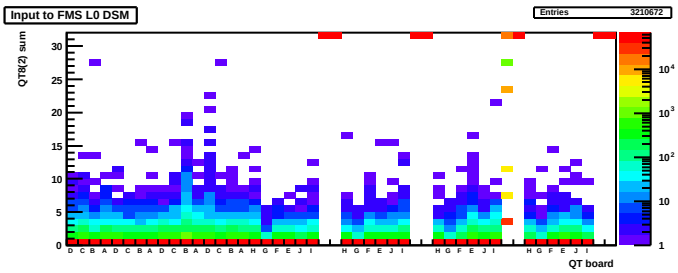

Input to QT4 crate
Entries 844894
ADC
channel

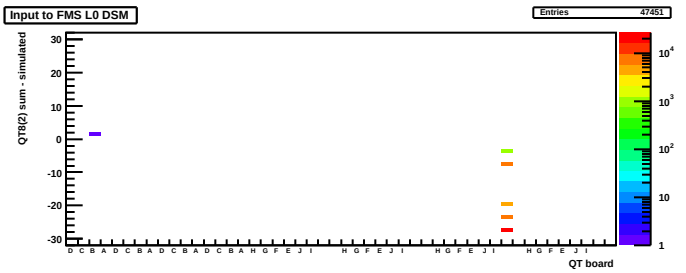

Input to FMS L0 DSM
Entries 3210672
QT8(0) sum
QT board

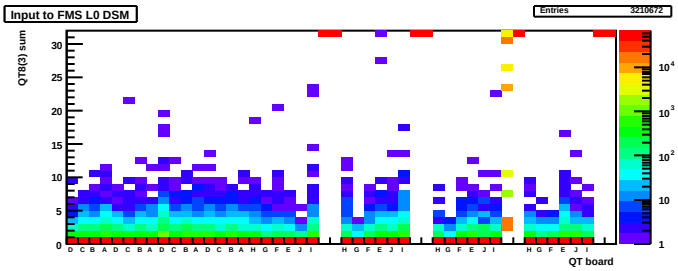

Input to FMS L0 DSM
Entries 1
QT8(0) sum - simulated
QT board

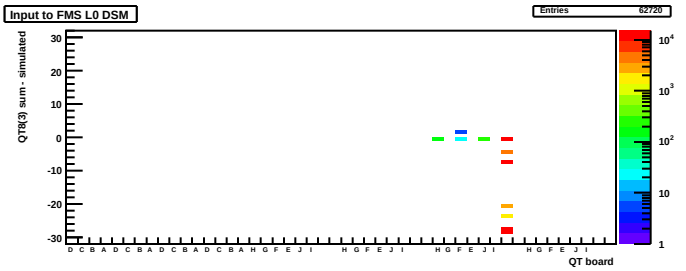

Input to FMS L0 DSM
Entries 3210672
QT8(1) sum
QT board

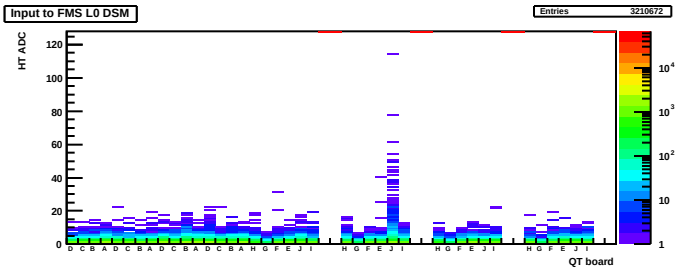

Input to FMS L0 DSM
Entries 1
QT8(1) sum - simulated
QT board

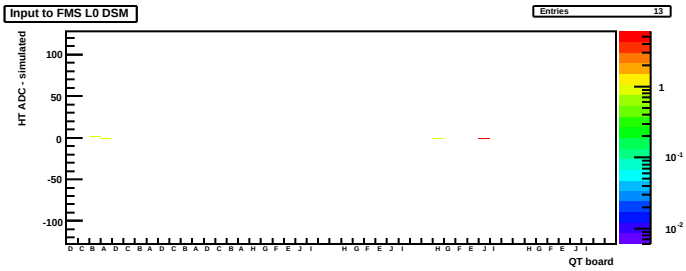

Input to FMS L0 DSM
Entries 3210672
QT8(2) sum
QT board

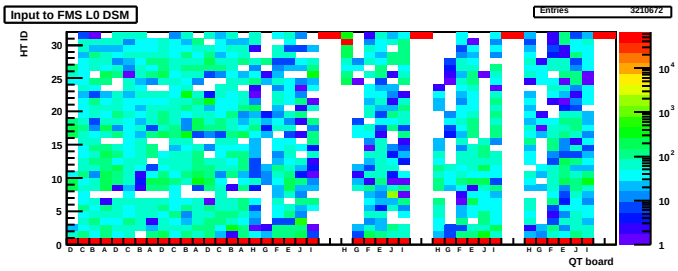

Input to FMS L0 DSM
Entries 47451
QT8(2) sum - simulated
QT board

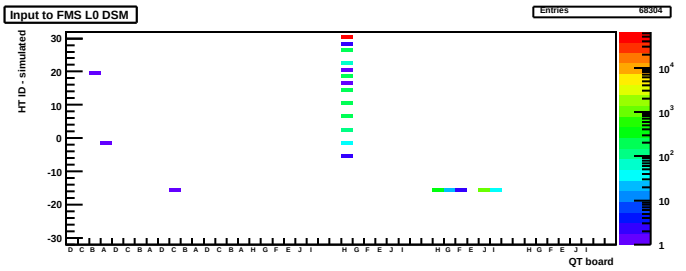

Input to FMS L0 DSM
Entries 3210672
QT8(3) sum
QT board

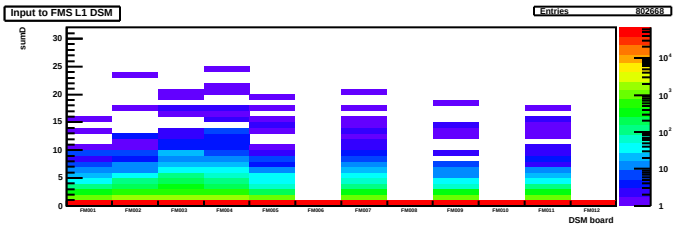

Input to FMS L0 DSM
Entries 62720
QT8(3) sum - simulated
QT board

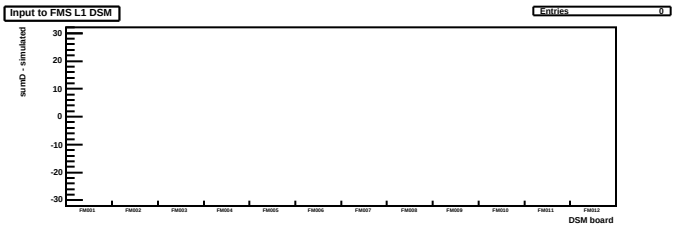

Input to FMS L0 DSM
Entries 3210672
HT ADC
QT board

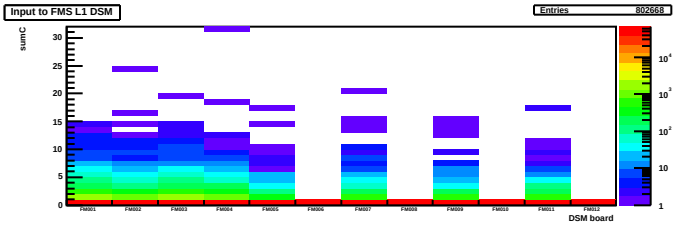

Input to FMS L0 DSM
Entries 13
HT ADC - simulated
QT board

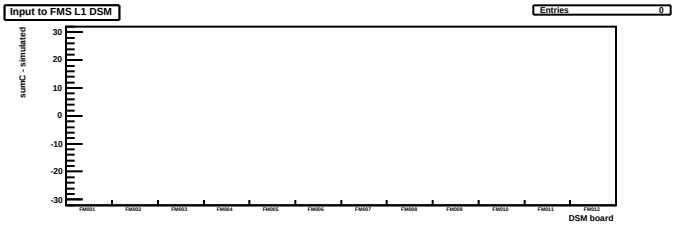

Input to FMS L0 DSM
Entries 3210672
HT ID
QT board

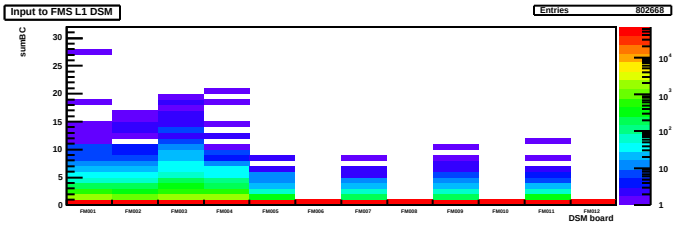

Input to FMS L0 DSM
Entries 68304
HT ID - simulated
QT board

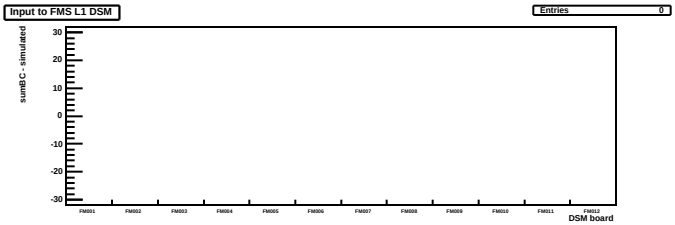

Input to FMS L1 DSM
Entries 802668
sumD
DSM board

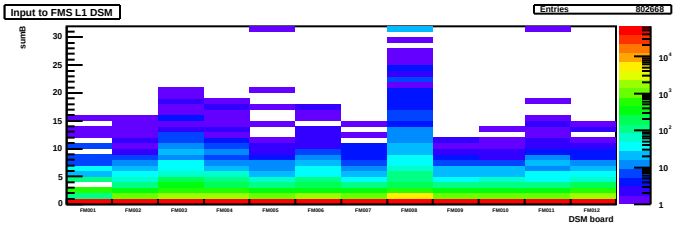

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

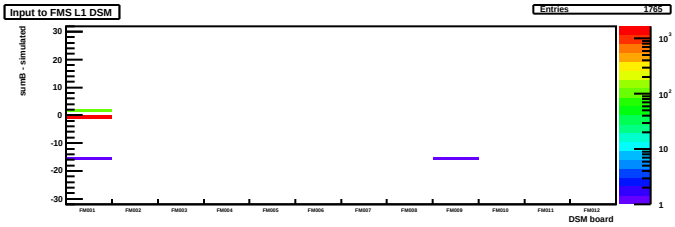

Input to FMS L1 DSM
Entries 802668
sumC
DSM board

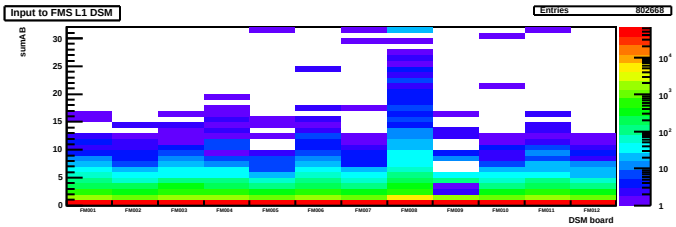

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

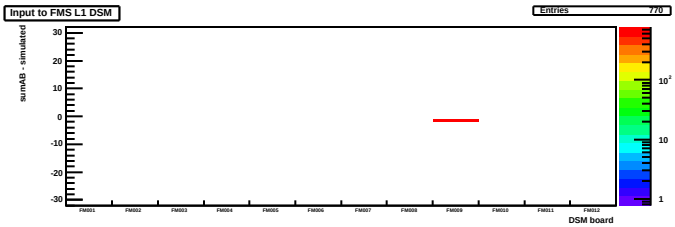

Input to FMS L1 DSM
Entries 802668
sumBC
DSM board

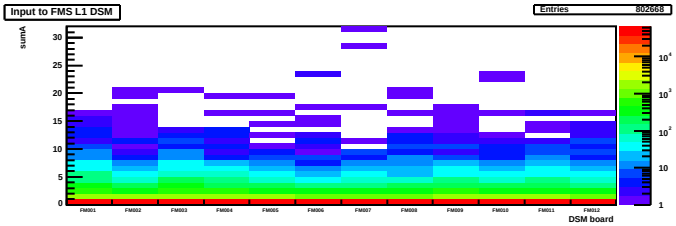

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

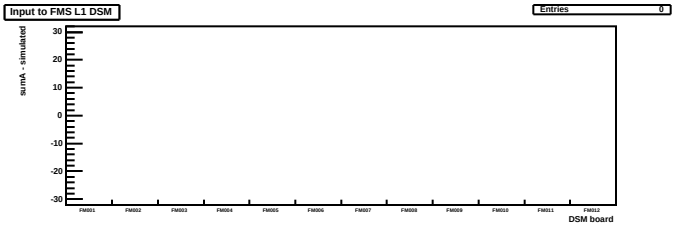

Input to FMS L1 DSM
Entries 802668
sumB
DSM board

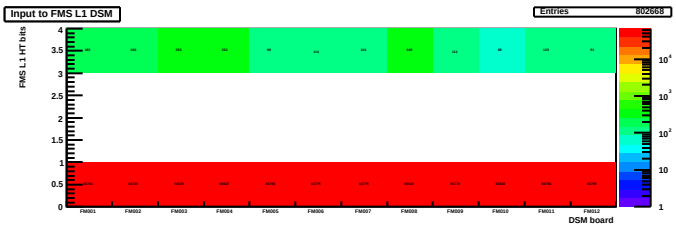

Input to FMS L1 DSM
Entries 1785
sumB - simulated
DSM board

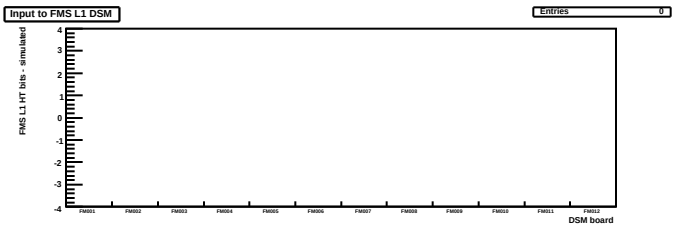

Input to FMS L1 DSM
Entries 802668
sumAB
DSM board

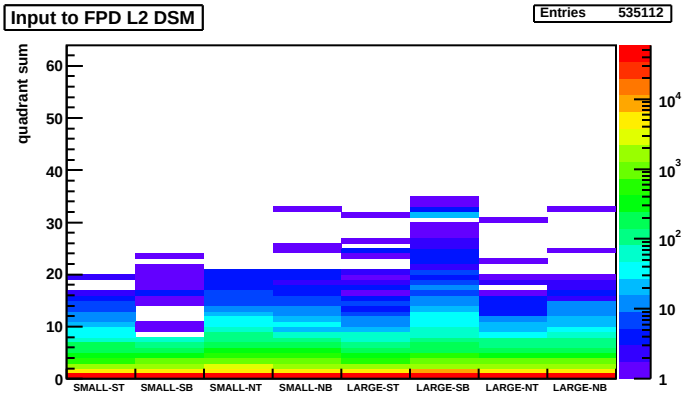

Input to FMS L1 DSM
Entries 770
sumAB - simulated
DSM board

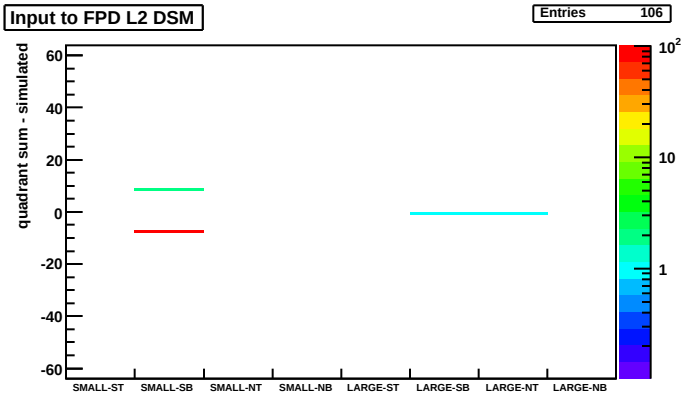

Input to FMS L1 DSM
Entries 802668
sumA
DSM board

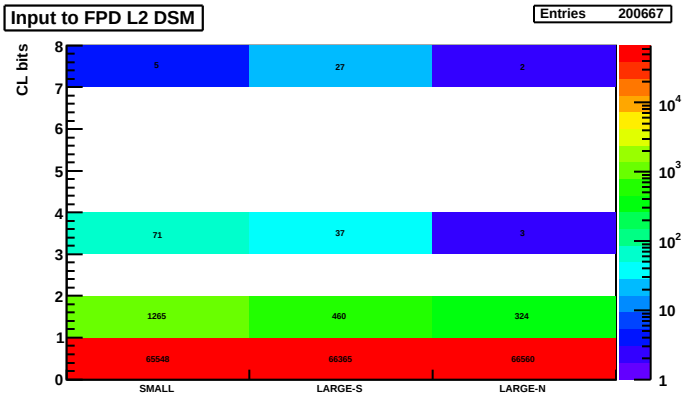

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

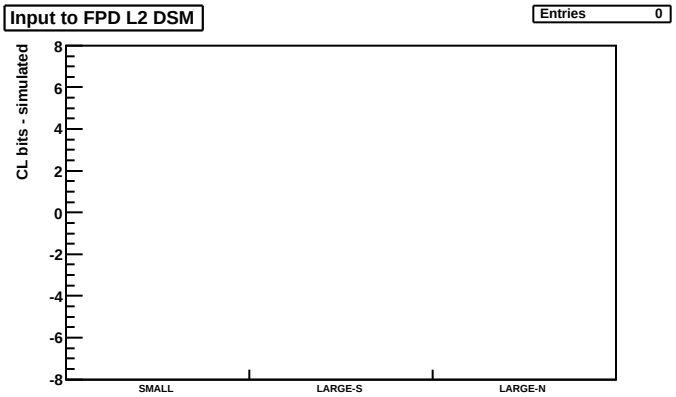

Input to FMS L1 DSM
Entries 802668
FMS L1 HT bits
DSM board

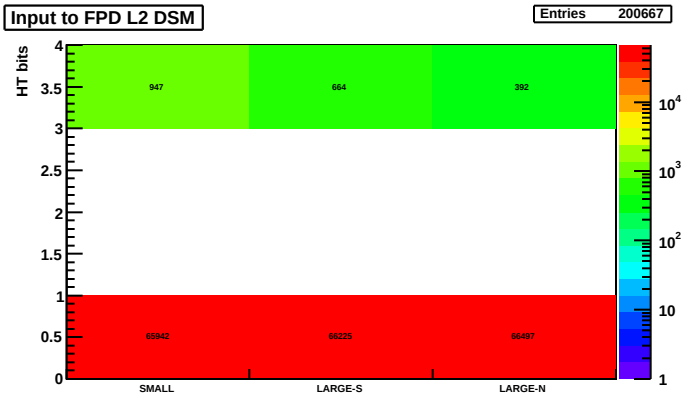

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

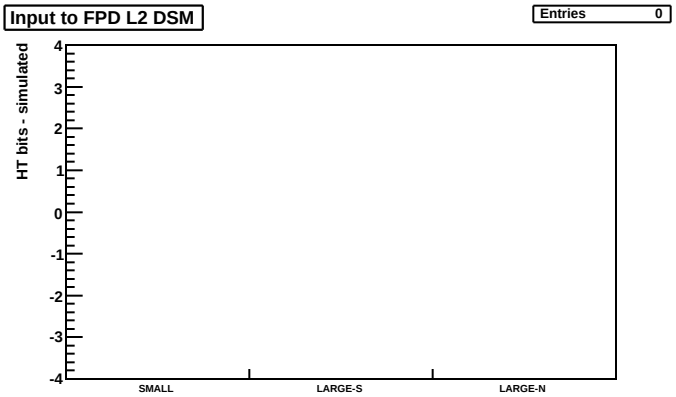

Input to FPD L2 DSM
Entries 535112
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

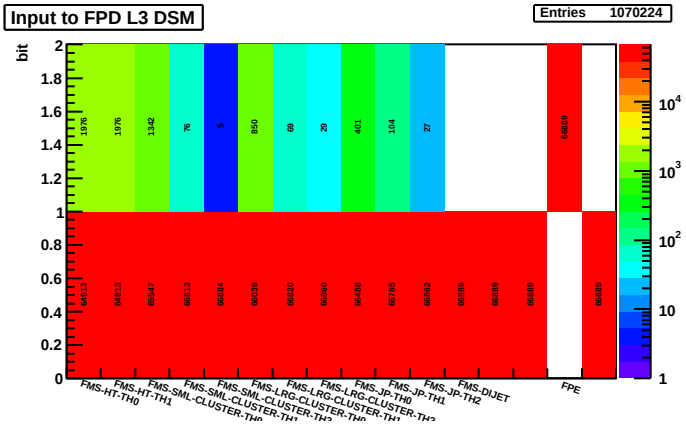

Input to FPD L2 DSM
Entries 106
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

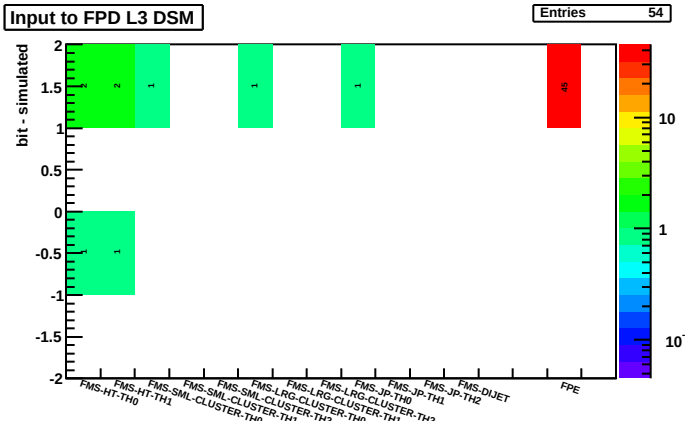

Input to FPD L2 DSM
Entries 200667
CL bits
5
27
2
71
37
3
1265
460
324
65548
66365
66560
SMALL
LARGE-S
LARGE-N

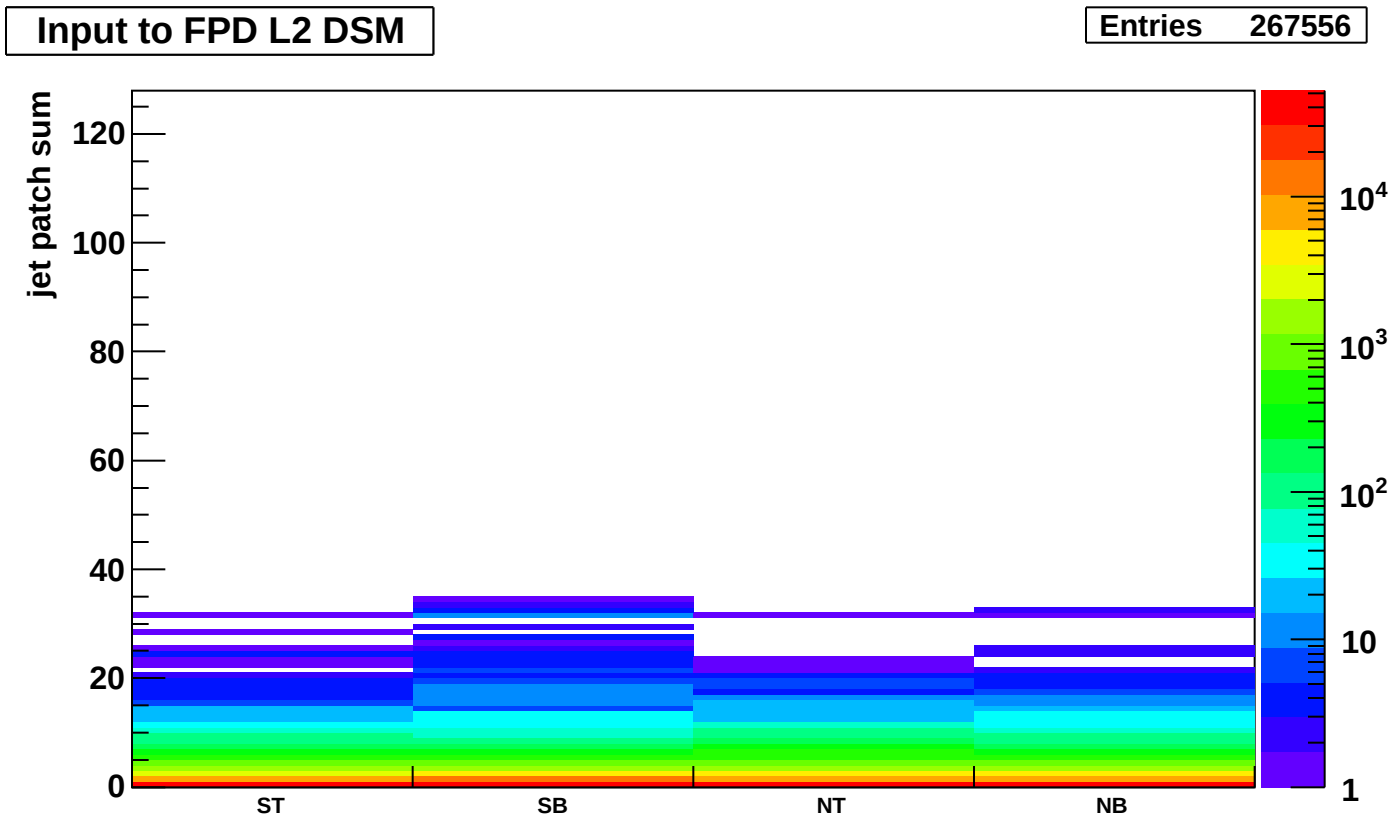

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

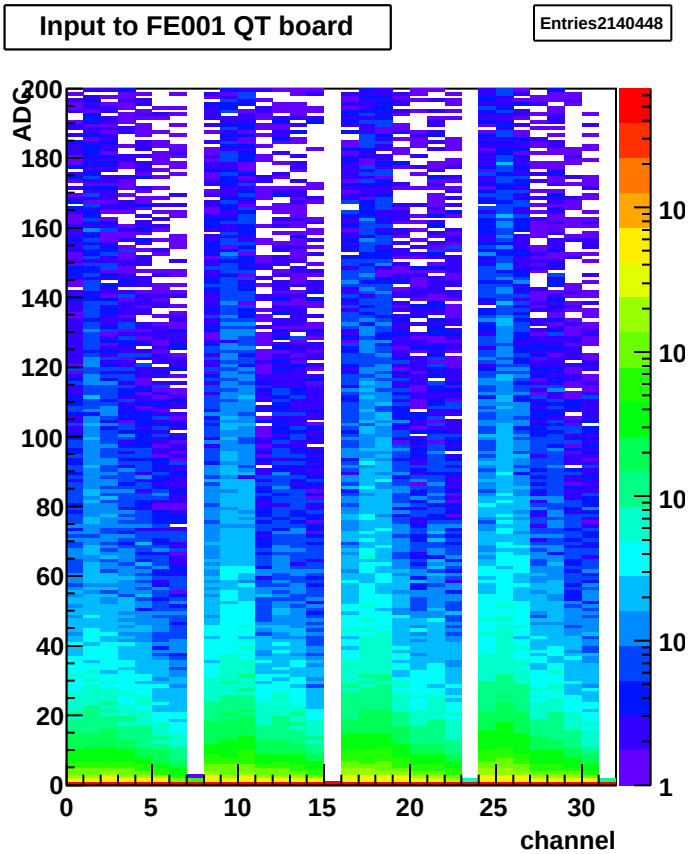

Input to FPD L2 DSM
Entries 200667
HT bits
947
664
392
65942
66225
66497
SMALL
LARGE-S
LARGE-N

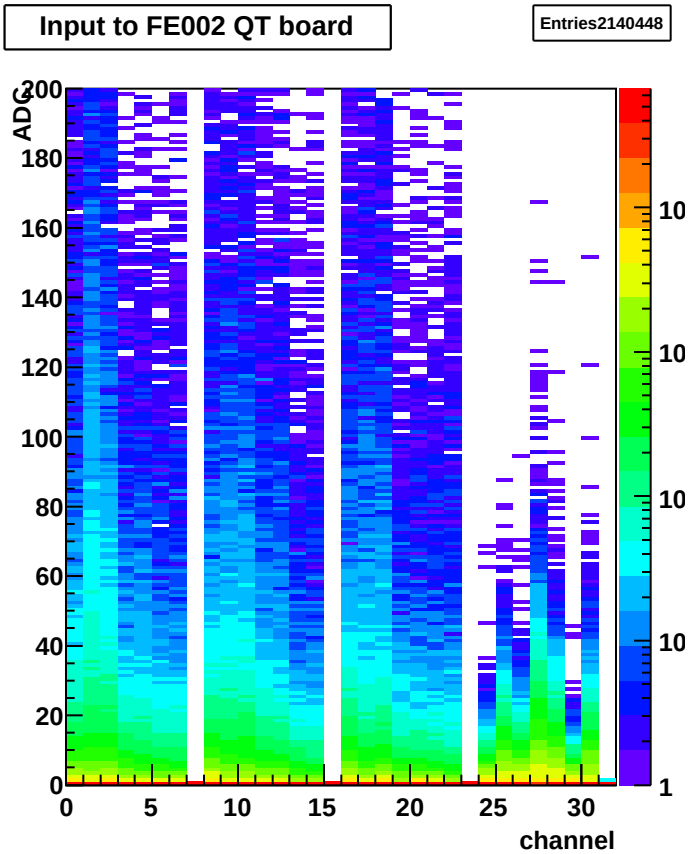

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

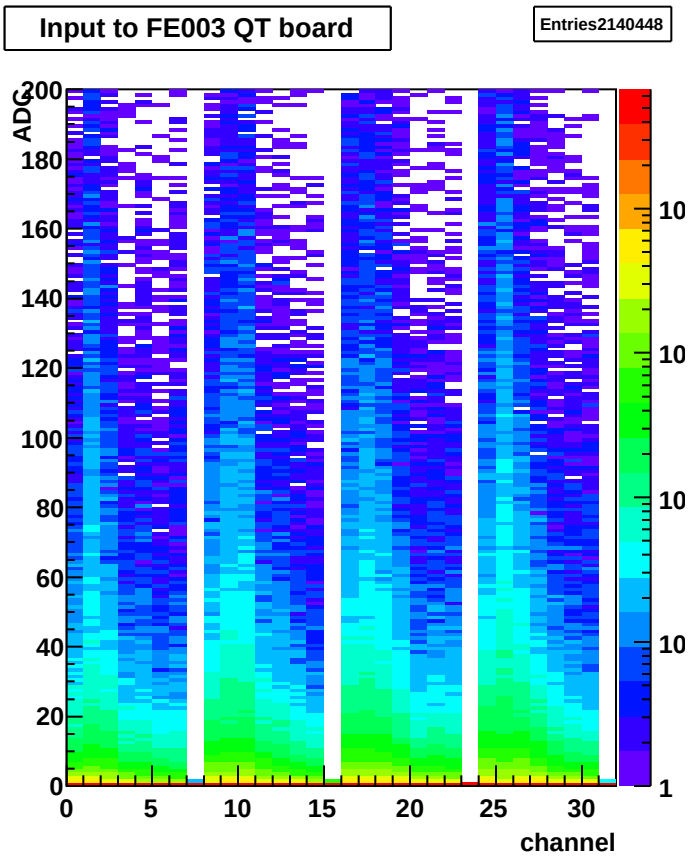

Input to FPD L3 DSM
Entries 1070224
bit
1975
1976
1342
76
5
850
69
29
401
104
27
66839
64913
64913
65547
66813
66834
66039
66820
66860
66488
66785
66862
66839
66839
66839
66839
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

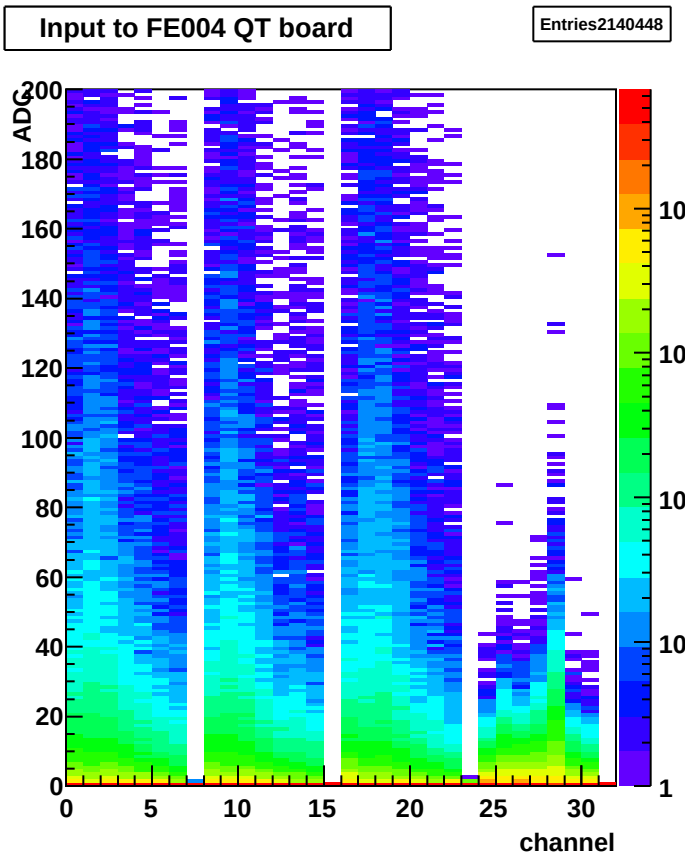

Input to FPD L3 DSM
Entries 54
bit - simulated
2
2
1
1
1
45
1
1
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

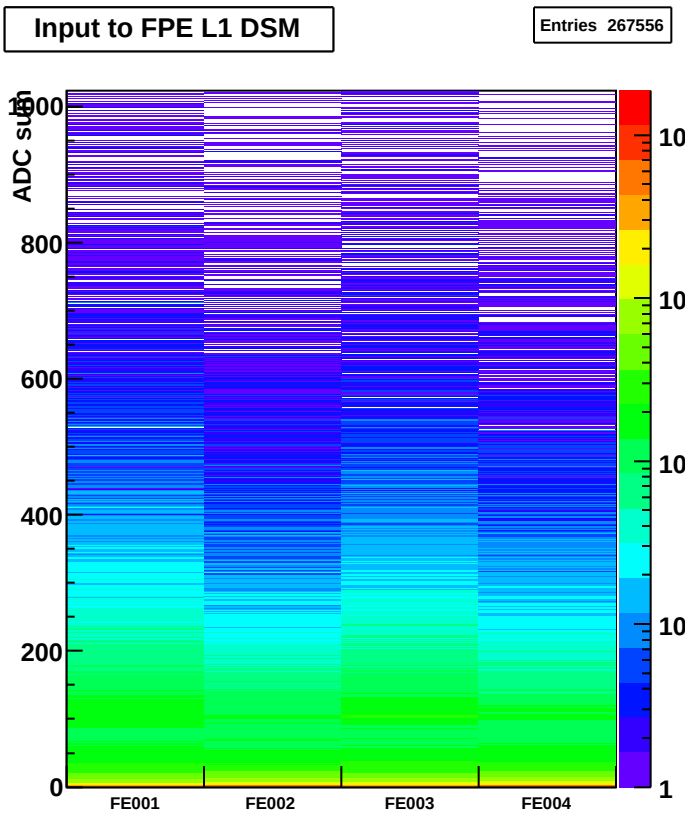

Input to FPD L2 DSM
Entries 267556
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4

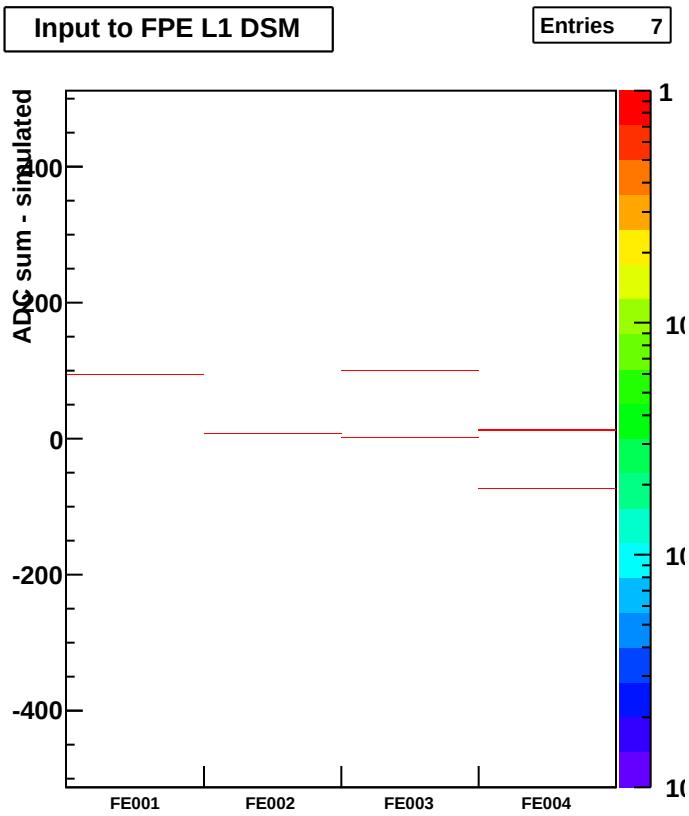

Input to FE001 QT board
Entries2140448
ADC
channel

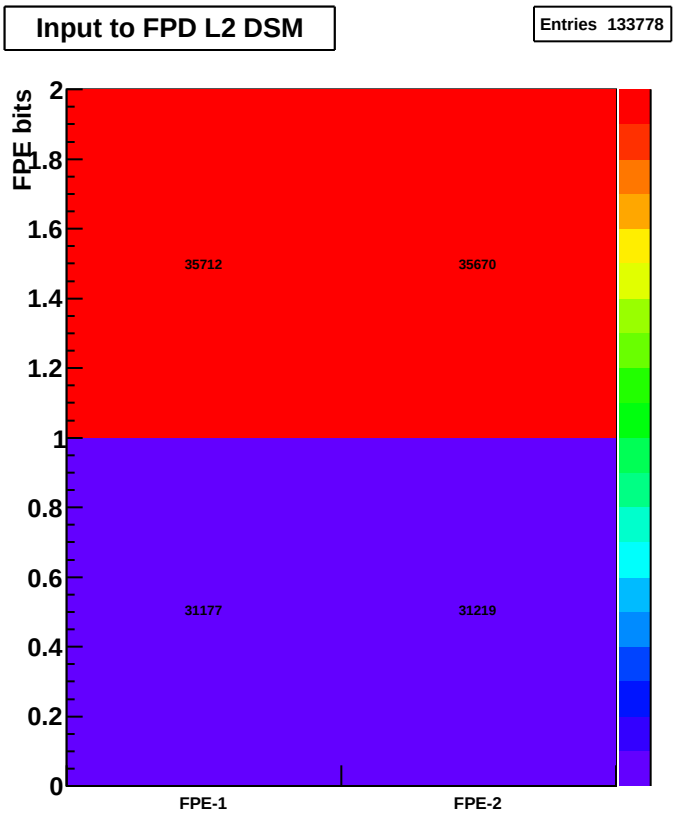

Input to FE002 QT board
Entries2140448
ADC
channel

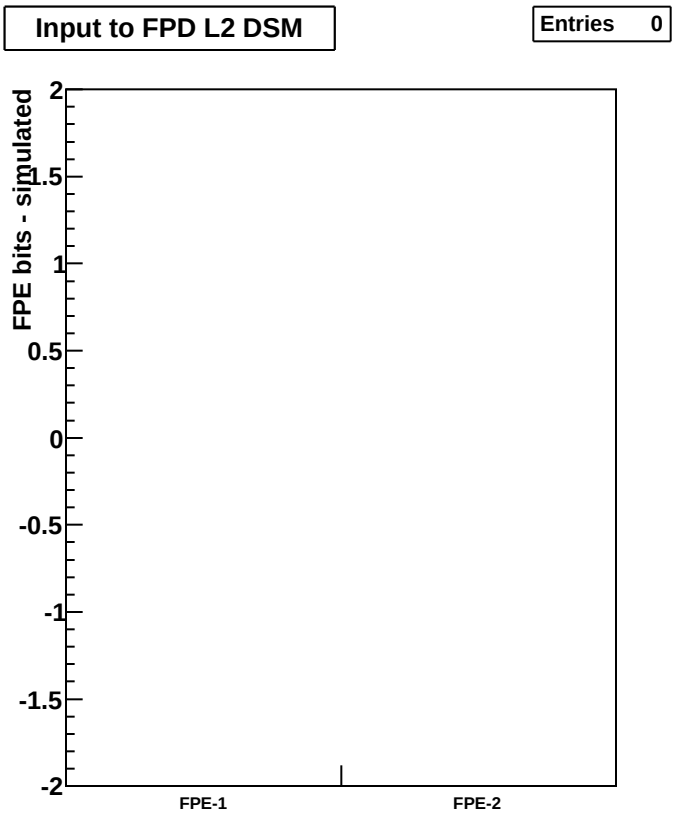

Input to FE003 QT board
Entries2140448
ADC
channel


Input to FE004 QT board
Entries2140448
ADC
channel


Input to FPE L1 DSM
Entries 267556
ADC sum
FE001
FE002
FE003
FE004


Input to FPE L1 DSM
Entries 7
ADC sum - simulated
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 133778
FPE bits
35712
35670
31177
31219
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
FPE-1
FPE-2