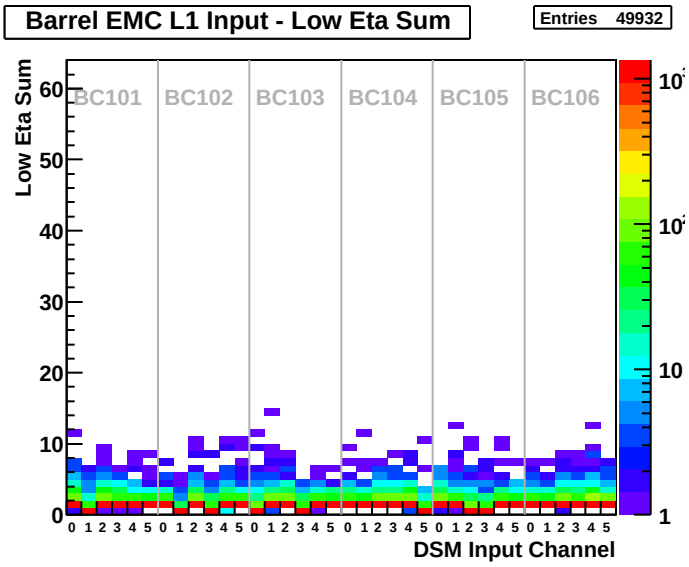
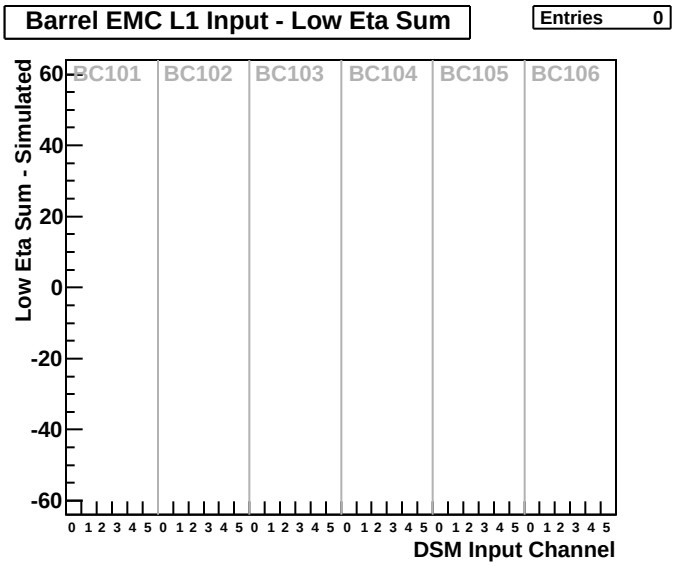
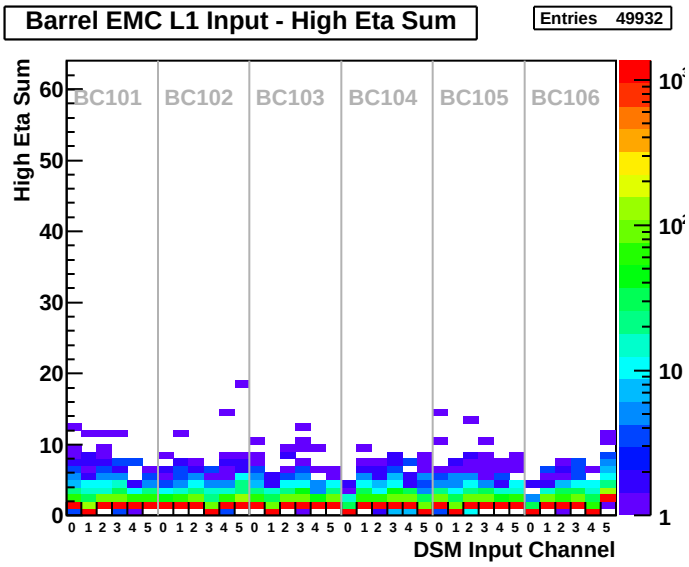
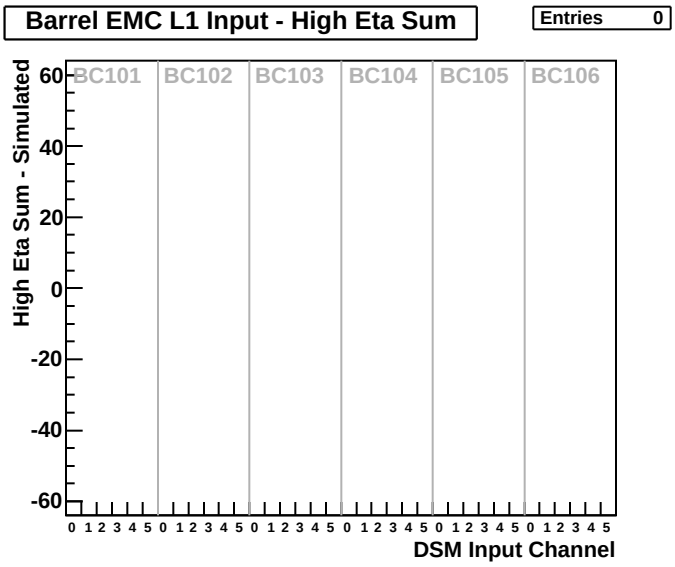
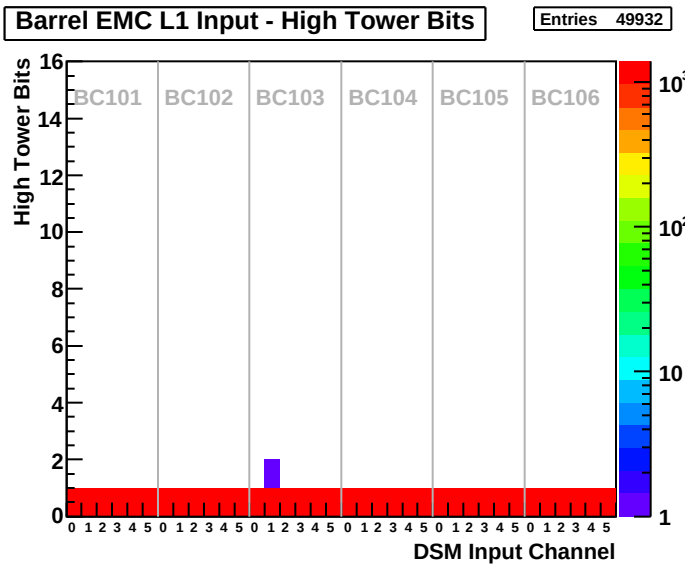
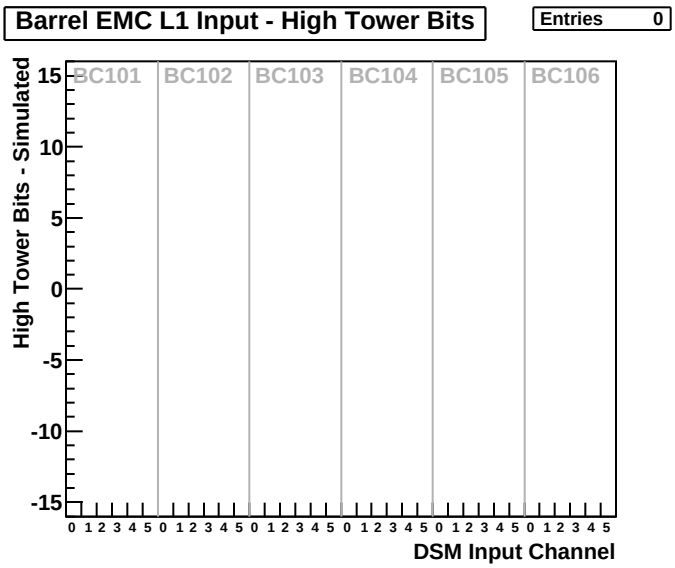

Barrel EMC L1 Input - Low Eta Sum
Entries 49932
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

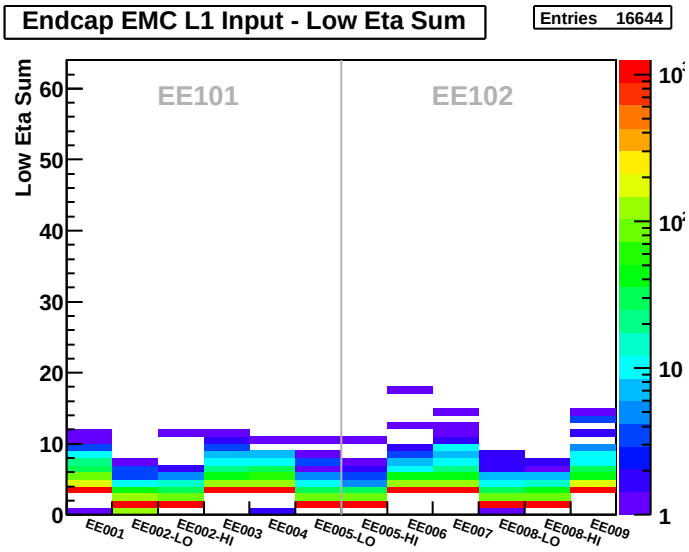

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

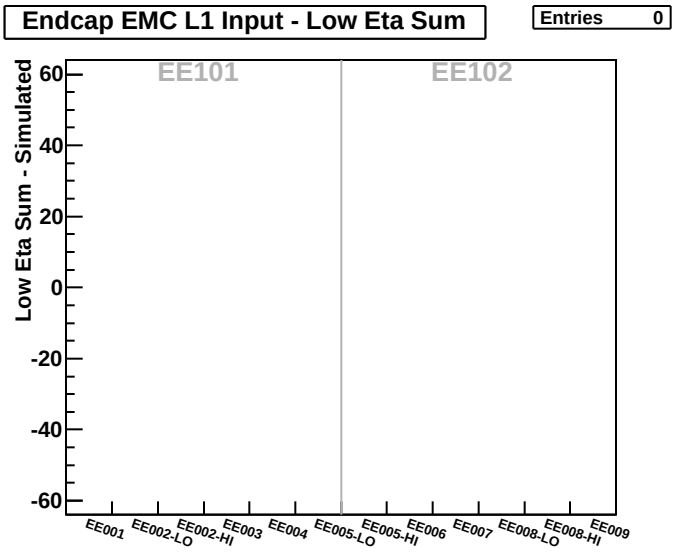

Barrel EMC L1 Input - High Eta Sum
Entries 49932
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

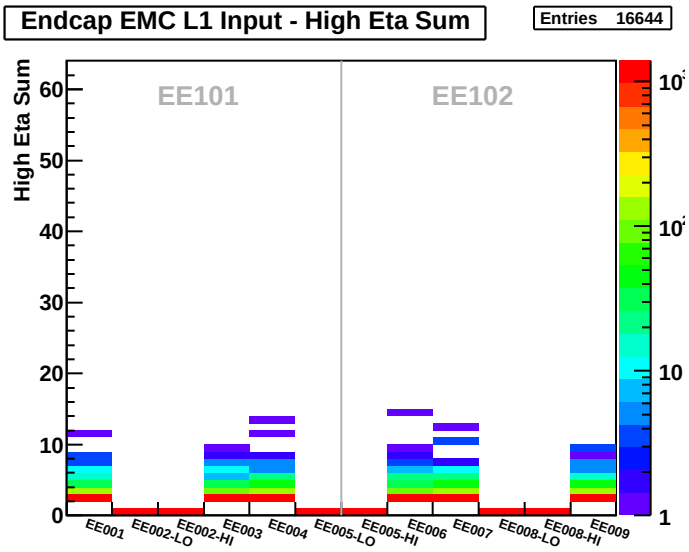

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

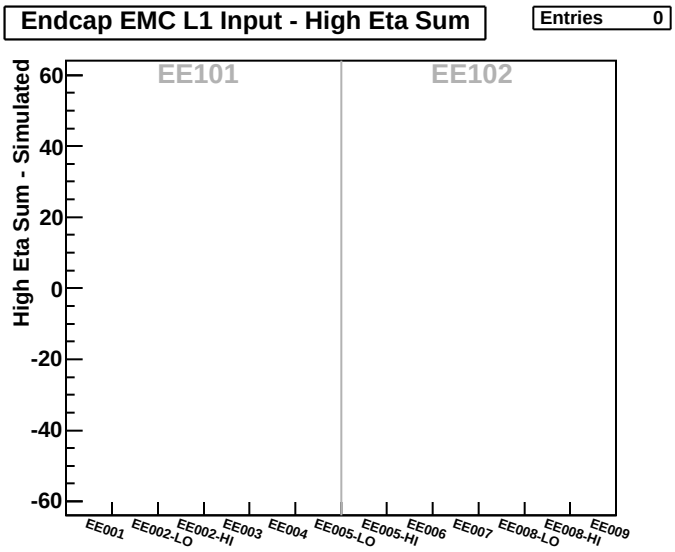

Barrel EMC L1 Input - High Tower Bits
Entries 49932
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

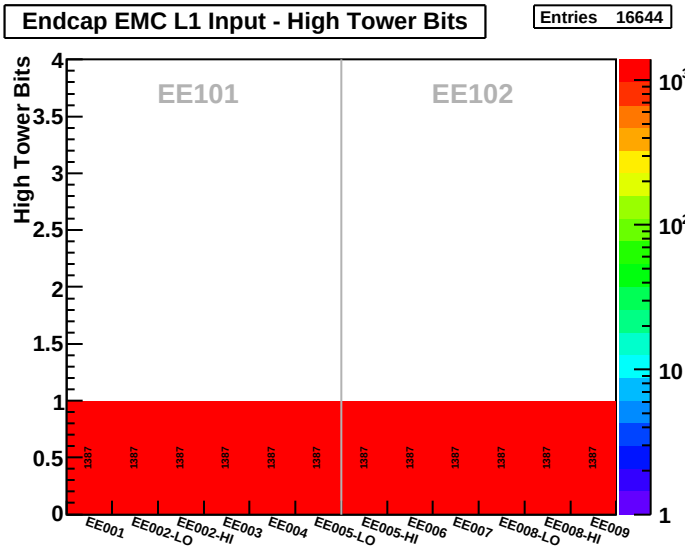

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

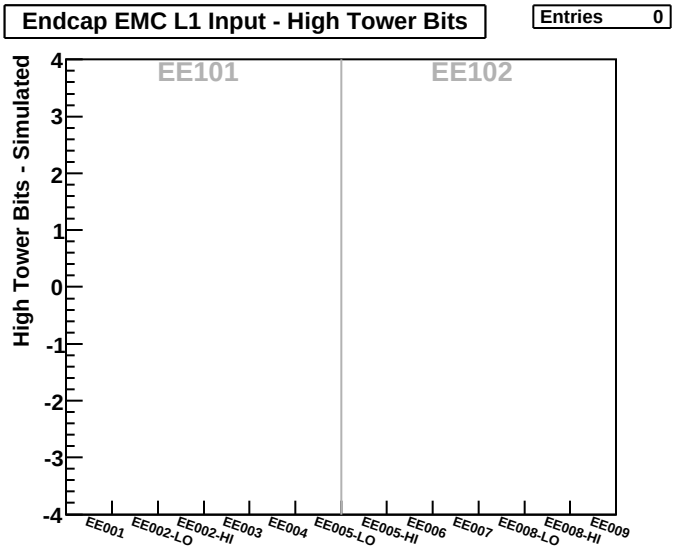

Endcap EMC L1 Input - Low Eta Sum
Entries 16644
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

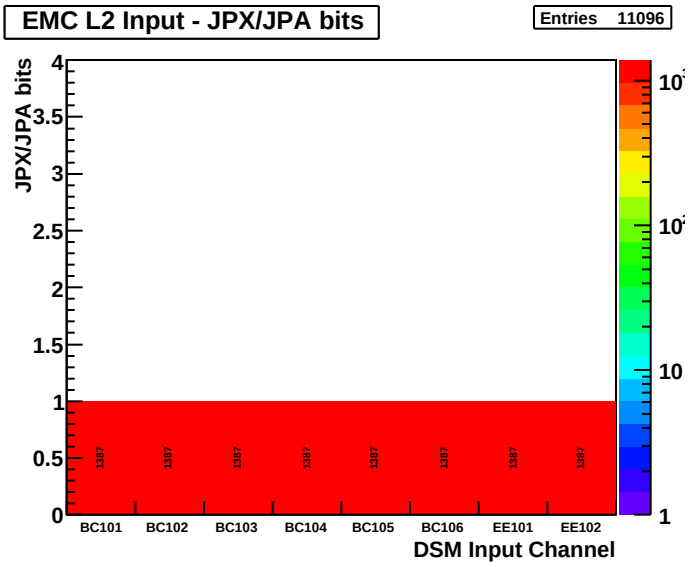

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

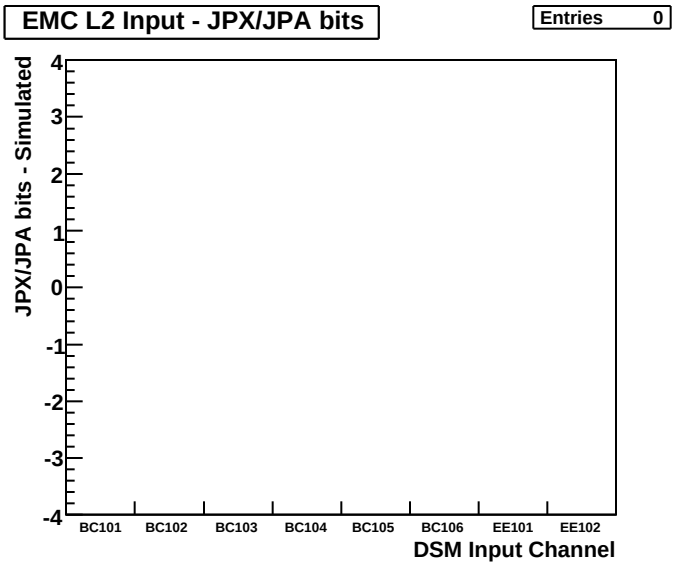

Endcap EMC L1 Input - High Eta Sum
Entries 16644
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

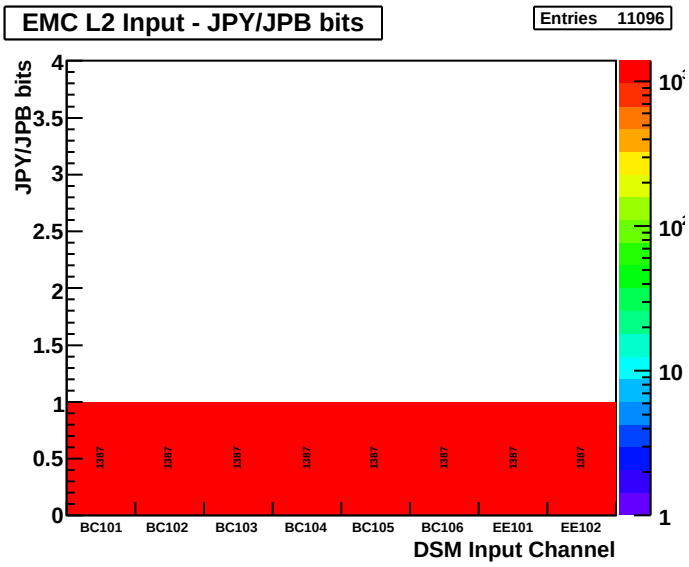

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

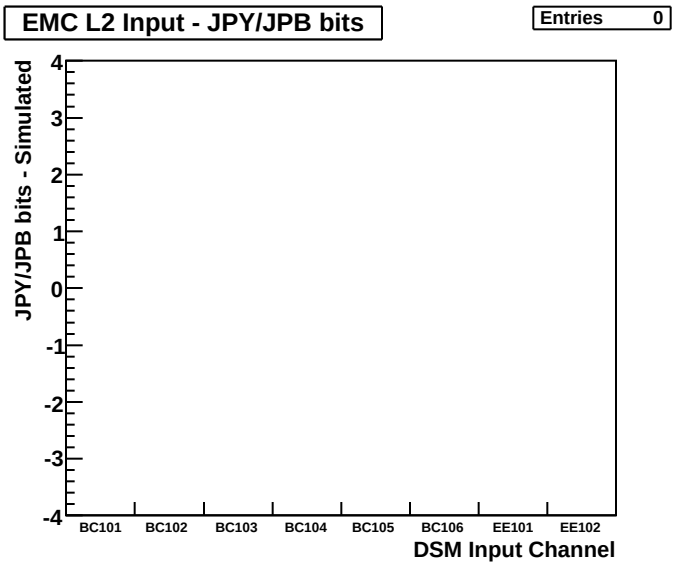

Endcap EMC L1 Input - High Tower Bits
Entries 16644
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

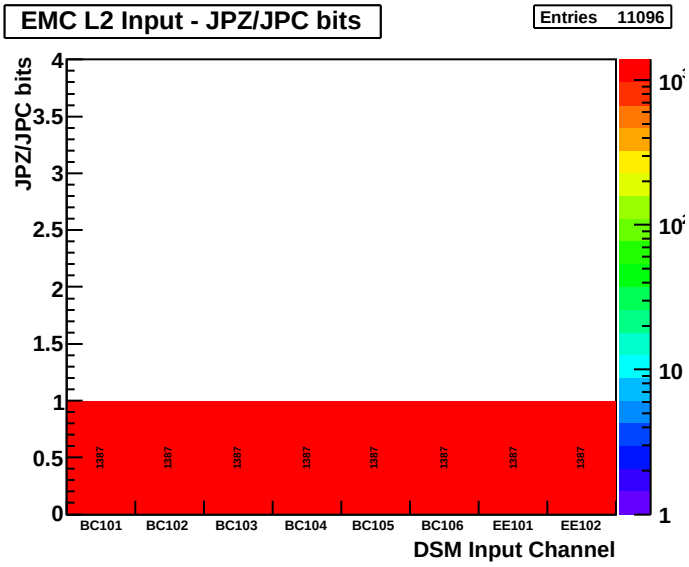

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

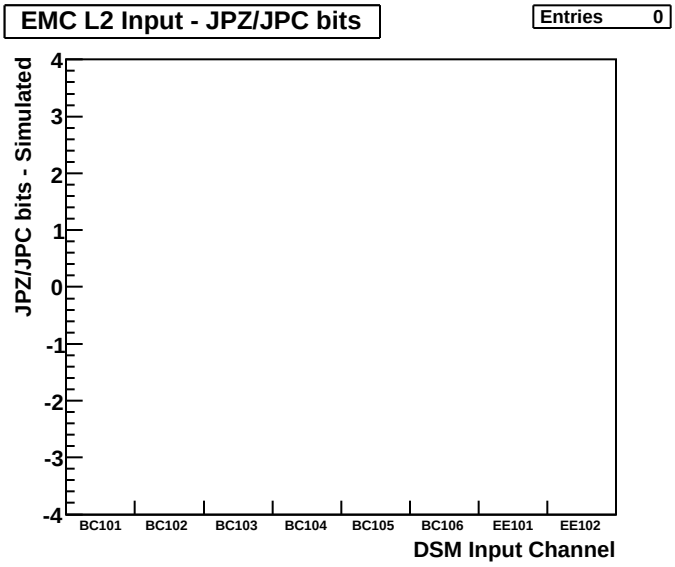

EMC L2 Input - JPX/JPA bits
Entries 11096
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1387

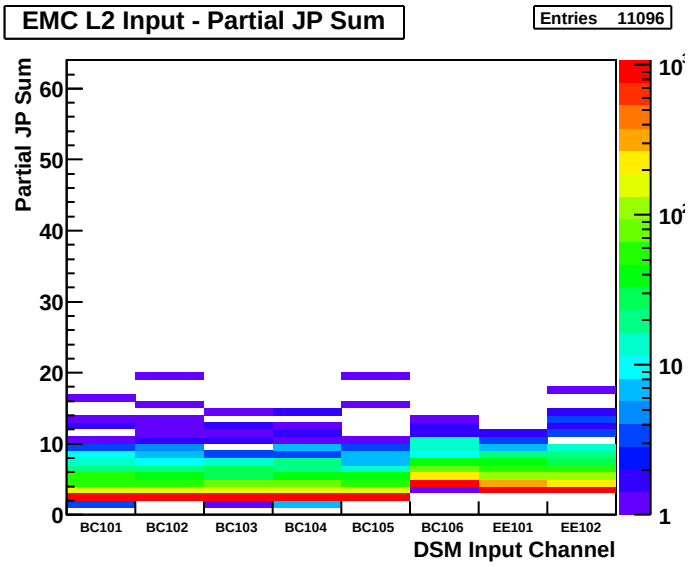

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

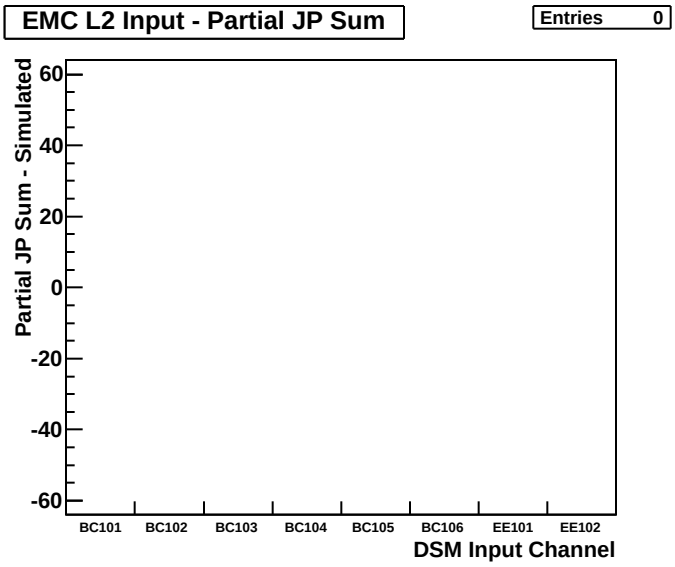

EMC L2 Input - JPY/JPB bits
Entries 11096
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1387

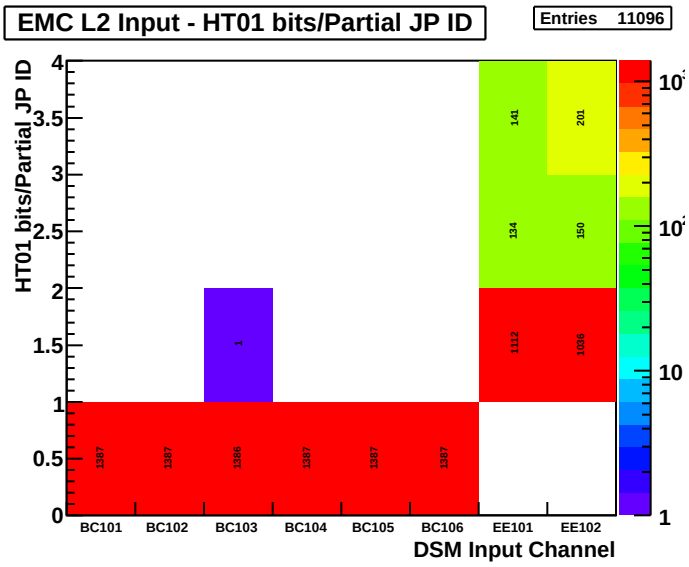

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

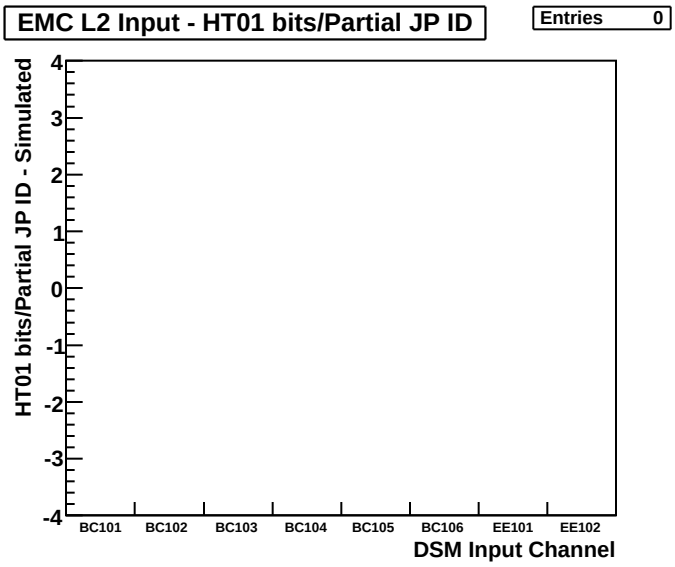

EMC L2 Input - JPZ/JPC bits
Entries 11096
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1387

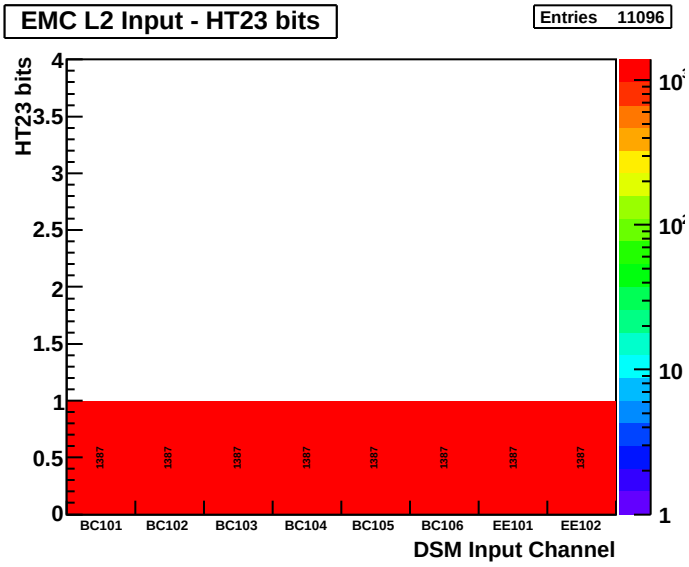

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

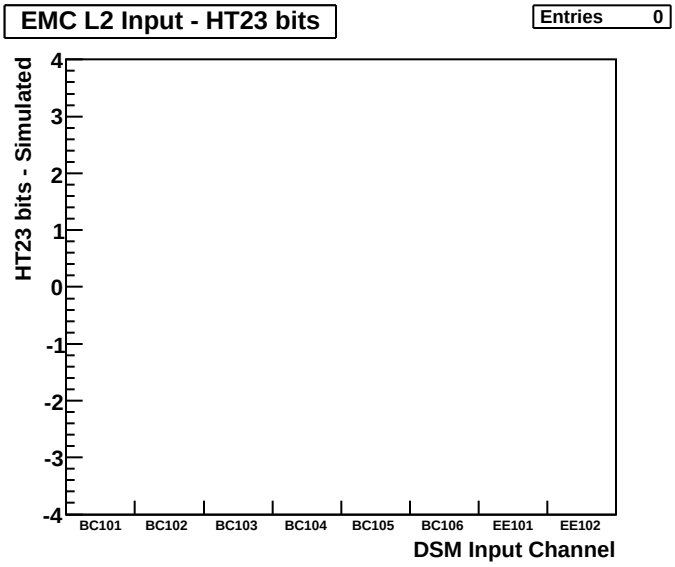

EMC L2 Input - Partial JP Sum
Entries 11096
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

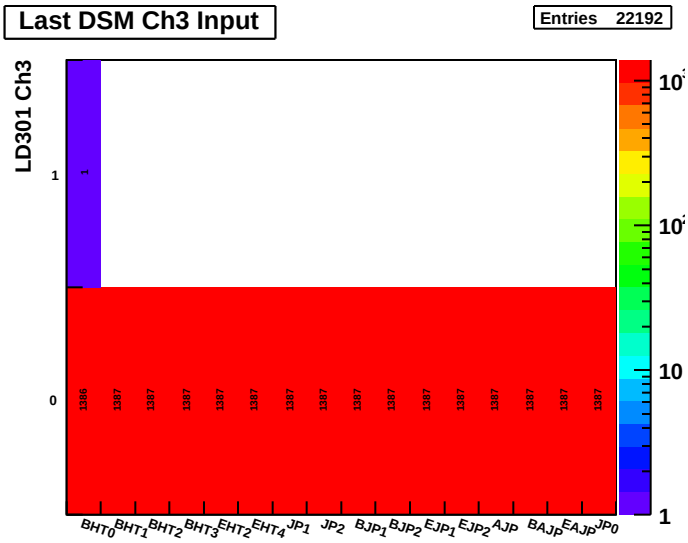

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

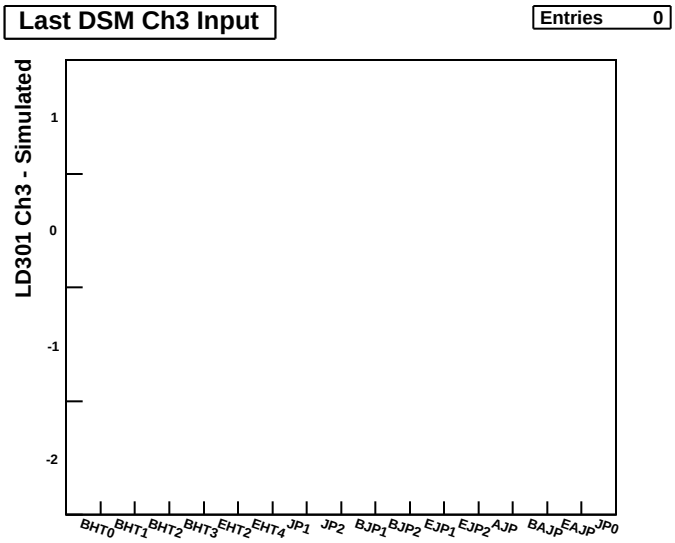

EMC L2 Input - HT01 bits/Partial JP ID
Entries 11096
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

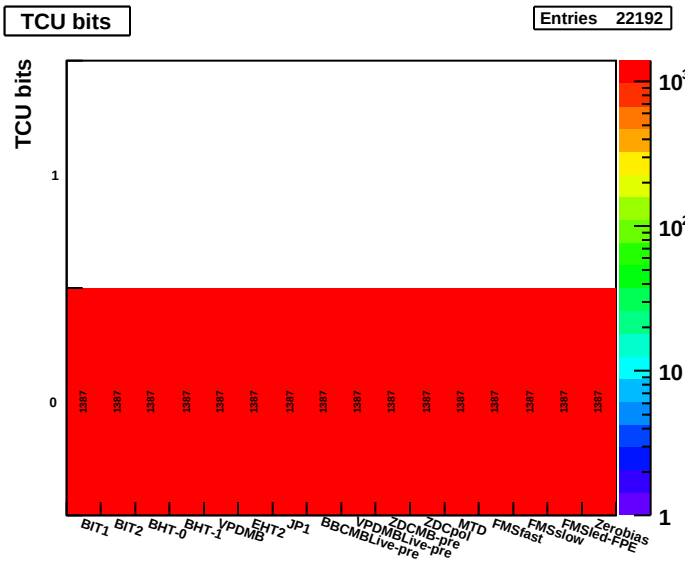

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

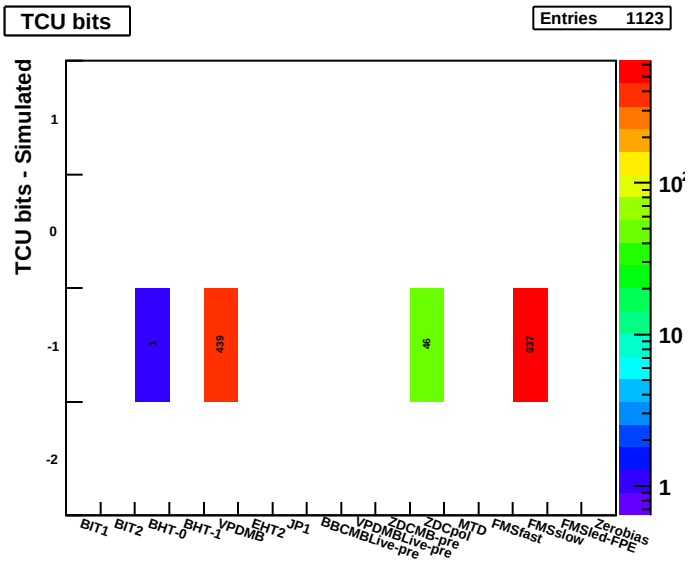

EMC L2 Input - HT23 bits
Entries 11096
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

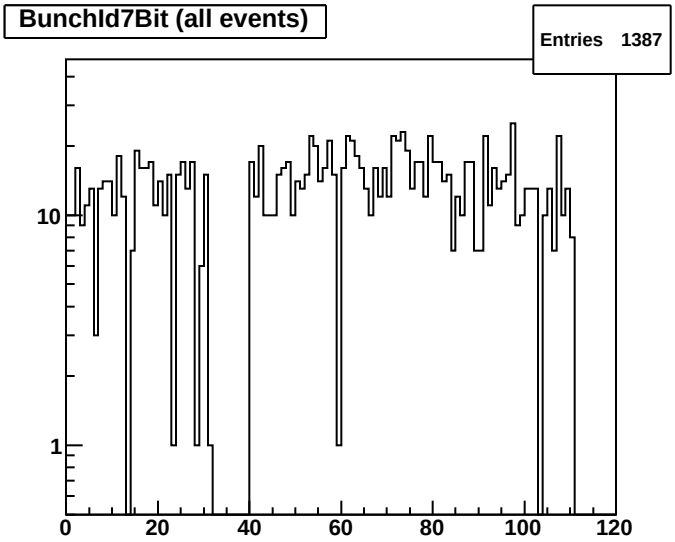

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

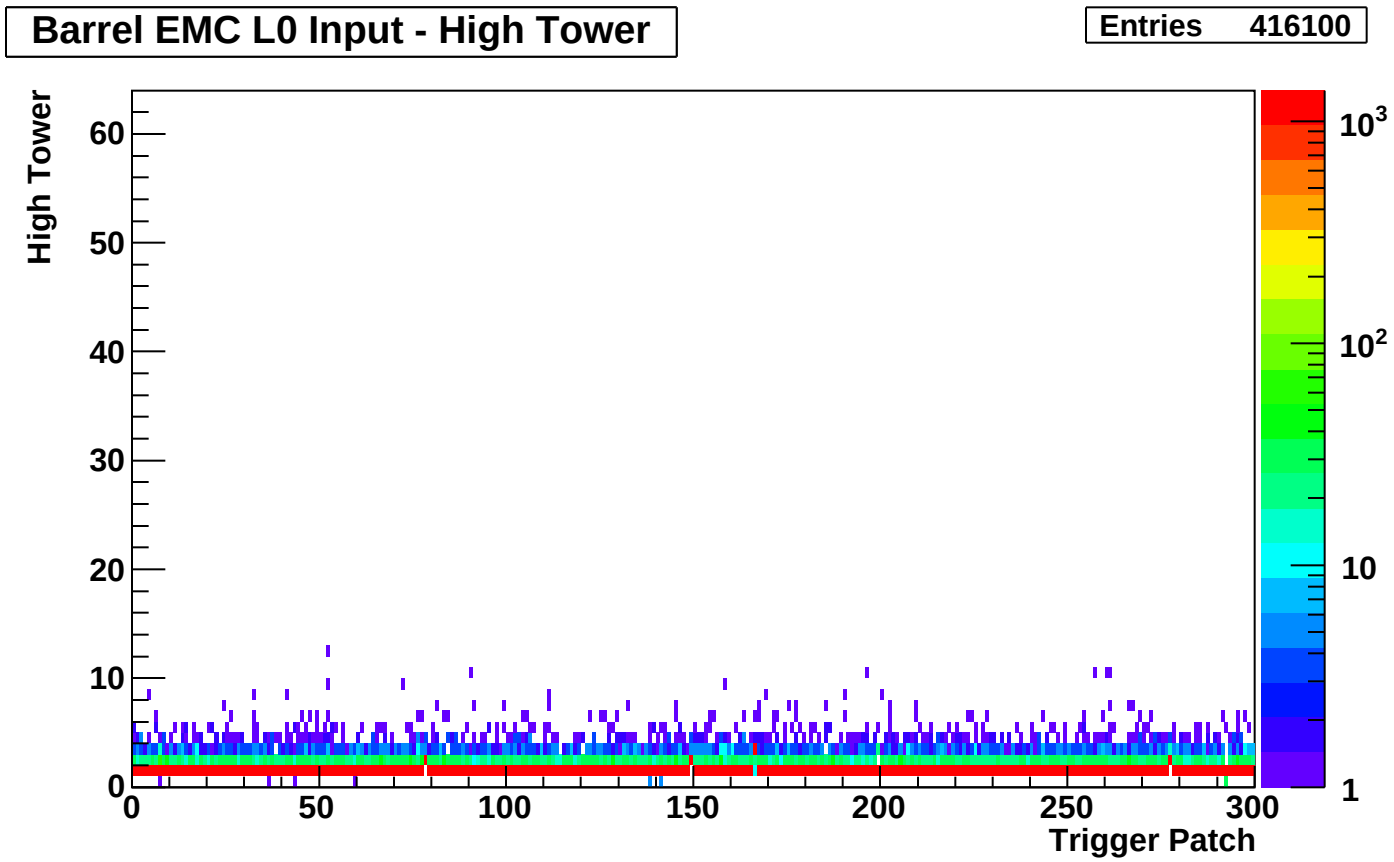

Last DSM Ch3 Input
Entries 22192
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

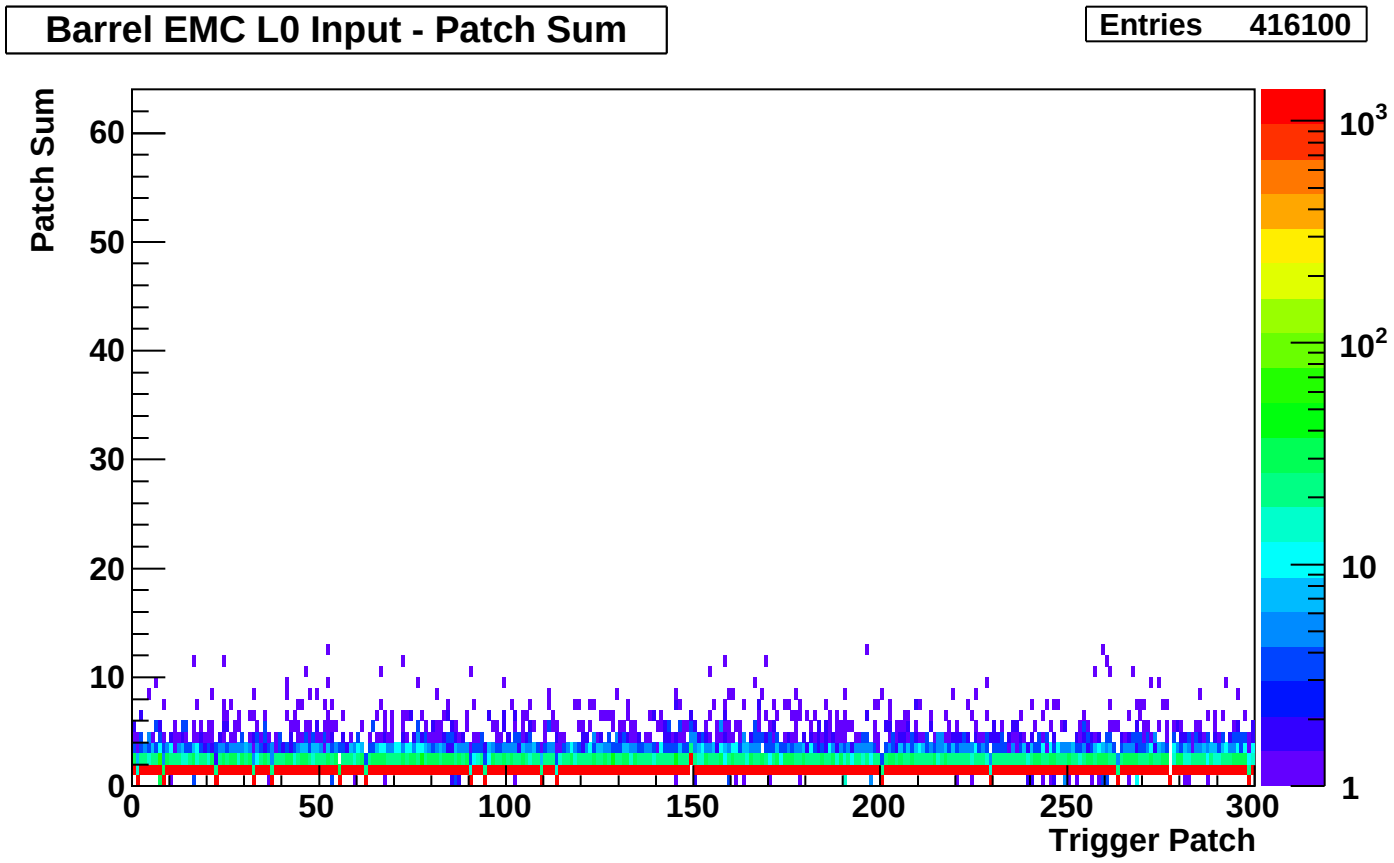

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

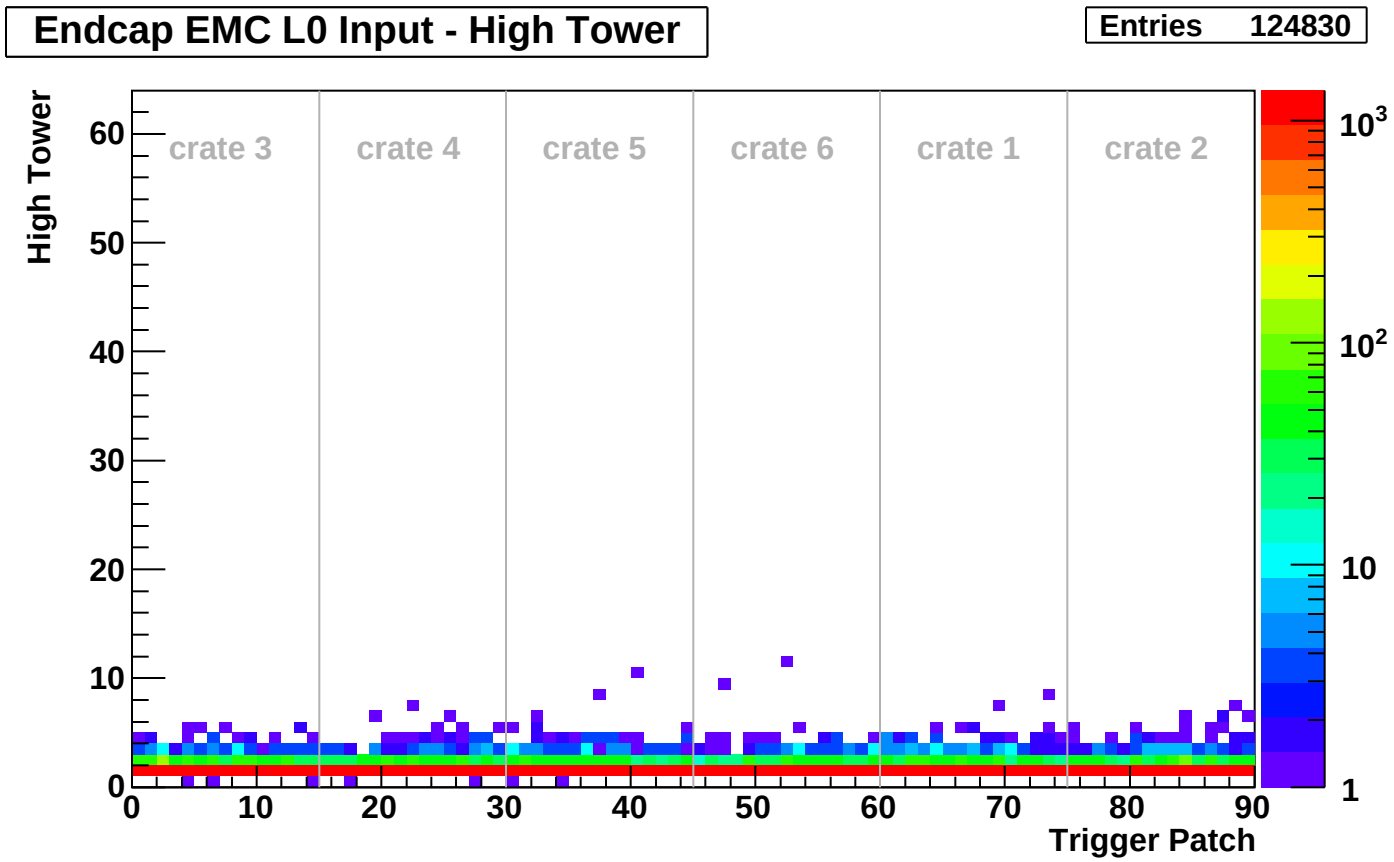

TCU bits
Entries 22192
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

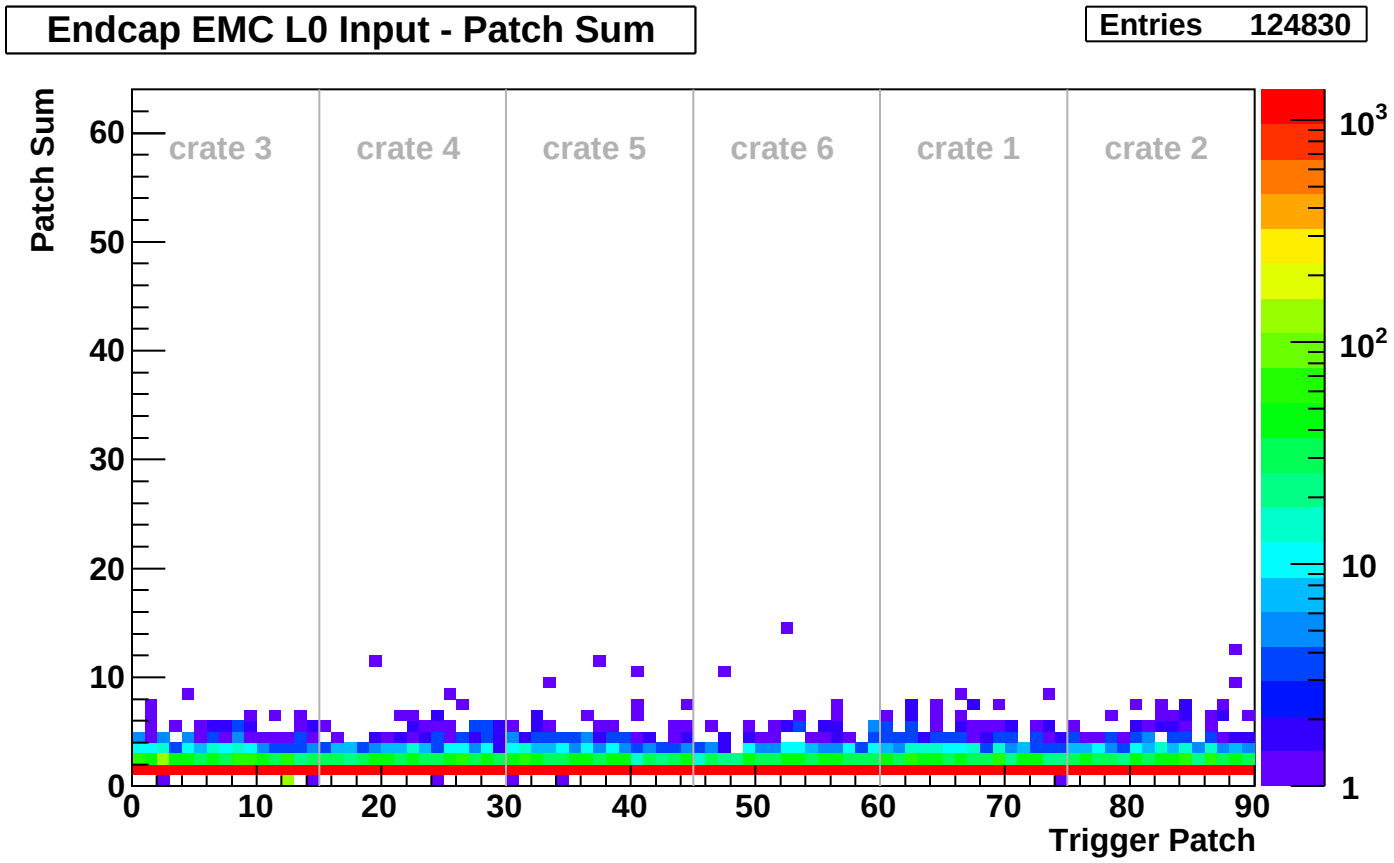

TCU bits
Entries 1123
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

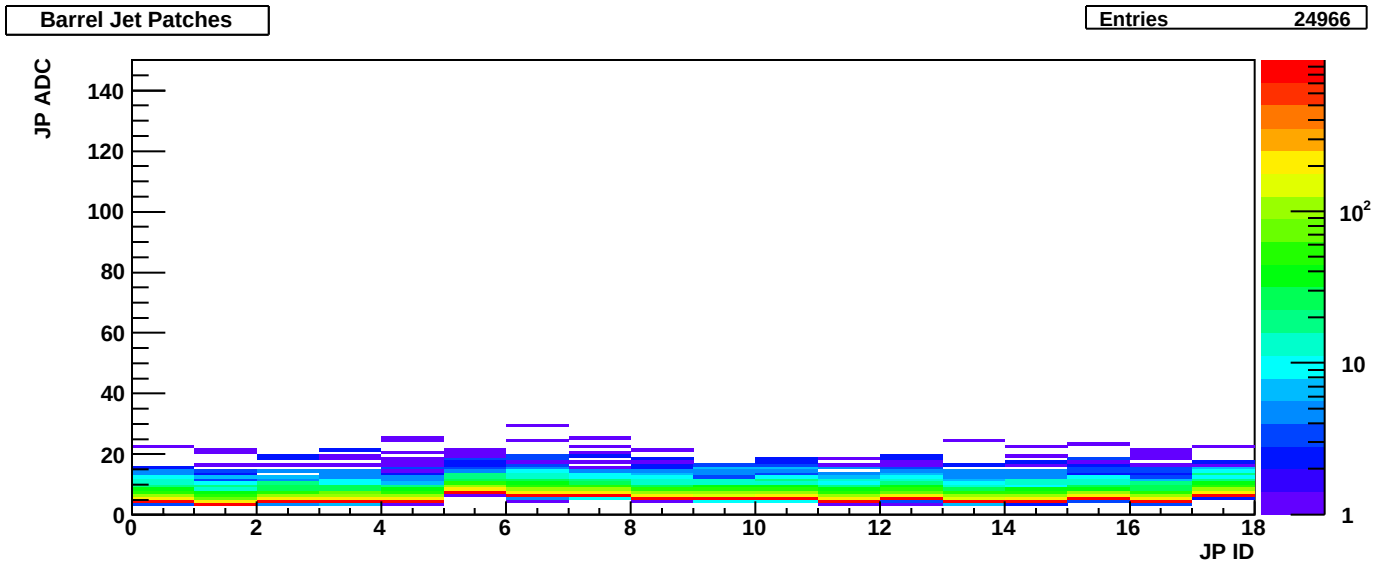

BunchId7Bit (all events)
Entries 1387

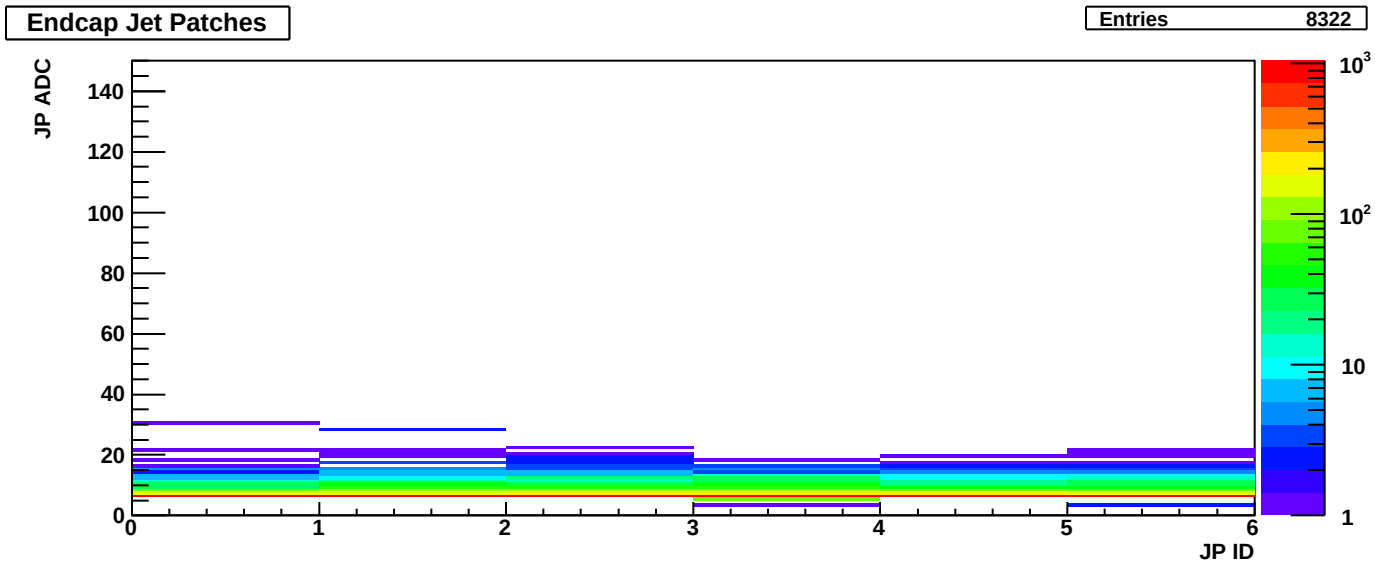

Barrel EMC L0 Input - High Tower
Entries 416100
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

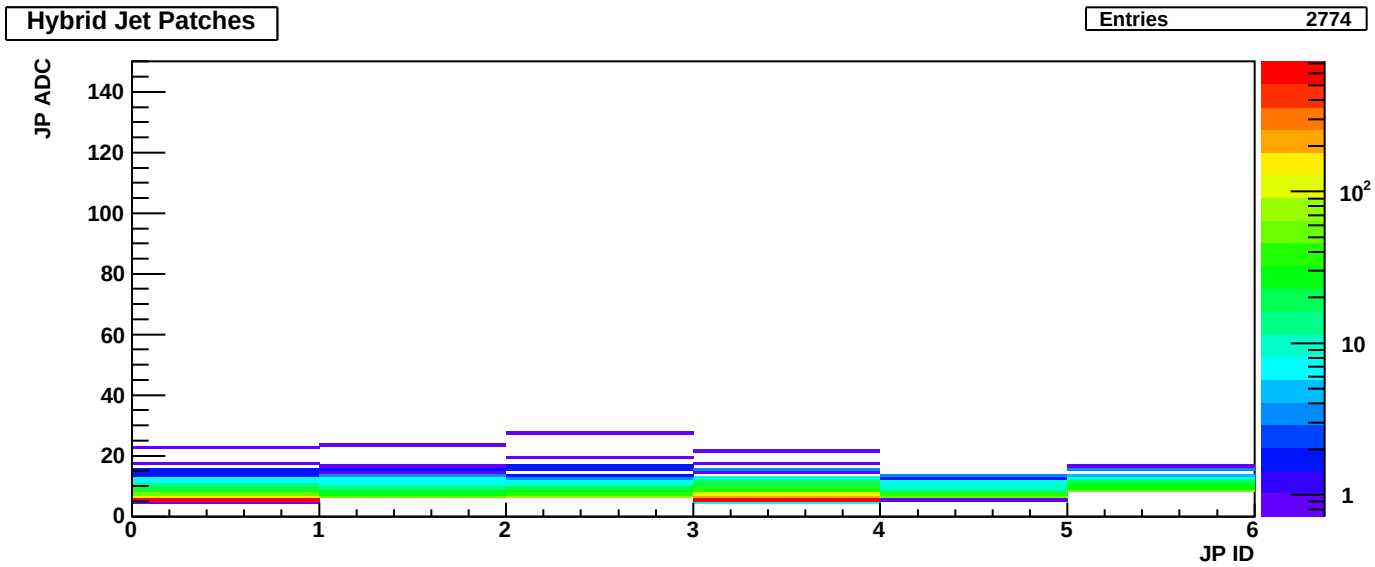

Barrel EMC L0 Input - Patch Sum
Entries 416100
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3

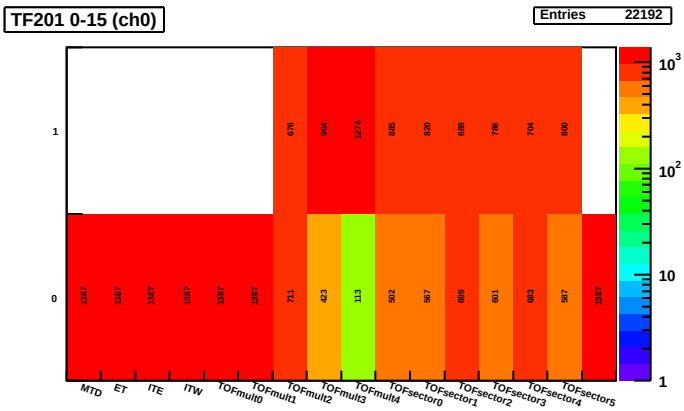

Endcap EMC L0 Input - High Tower
Entries 124830
High Tower
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

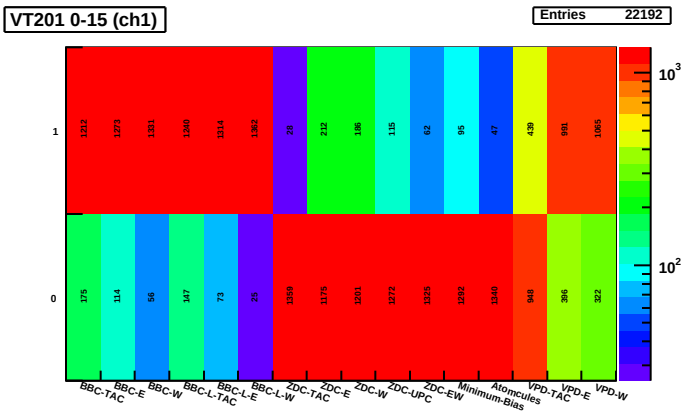

Endcap EMC L0 Input - Patch Sum
Entries 124830
Patch Sum
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2
Trigger Patch
0
10
20
30
40
50
60
70
80
90
1
10
10^2
10^3

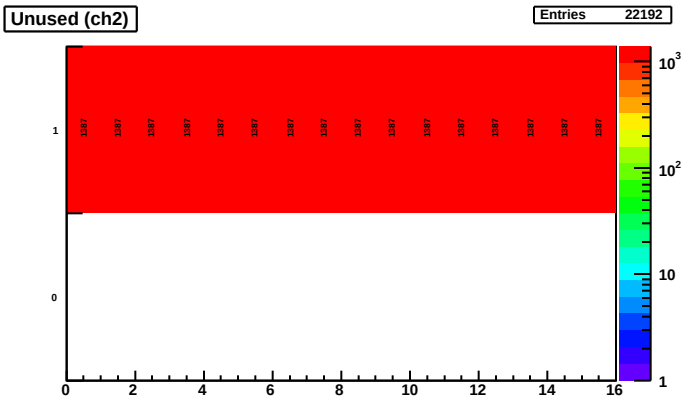

Barrel Jet Patches
Entries 24966
JP ADC
JP ID

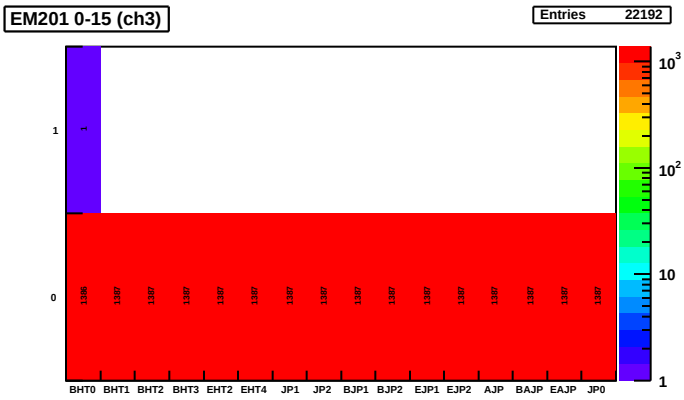

Endcap Jet Patches
Entries 8322
JP ADC
JP ID

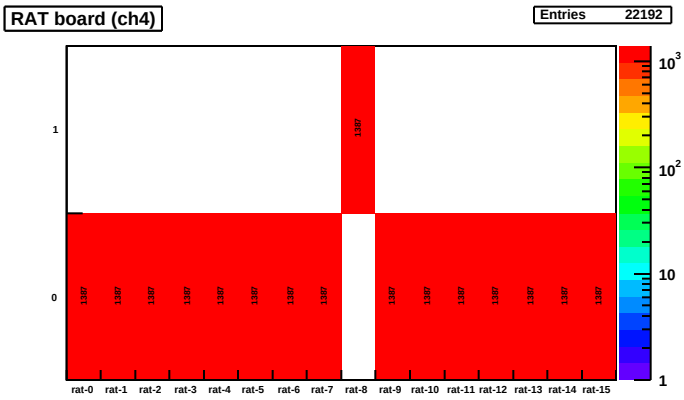

Hybrid Jet Patches
Entries 2774
JP ADC
JP ID

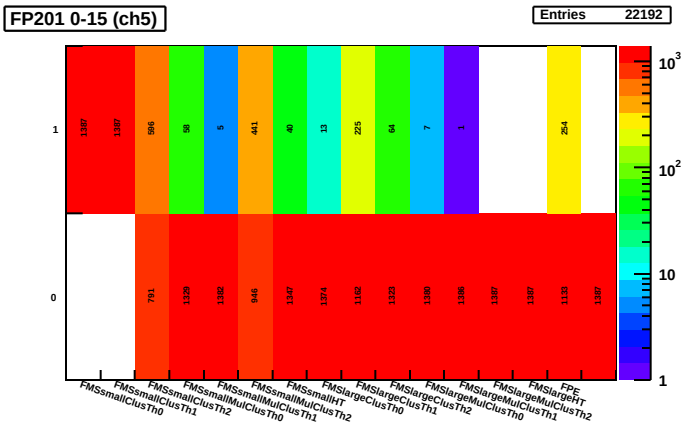

TF201 0-15 (ch0)
Entries 22192
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

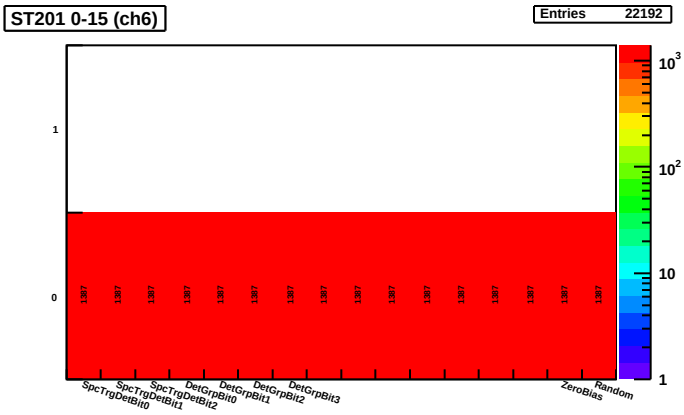

VT201 0-15 (ch1)
Entries 22192
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

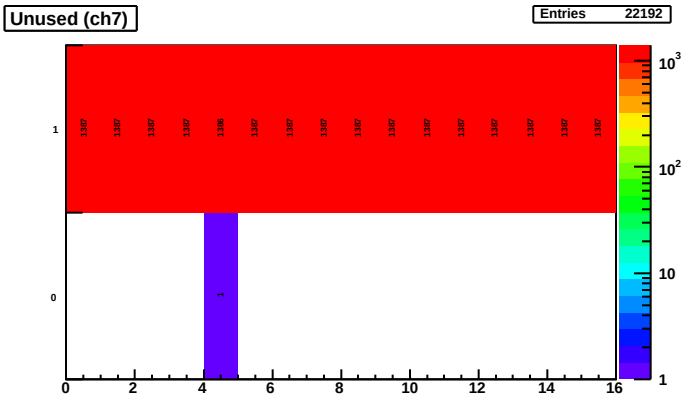

Unused (ch2)
Entries 22192

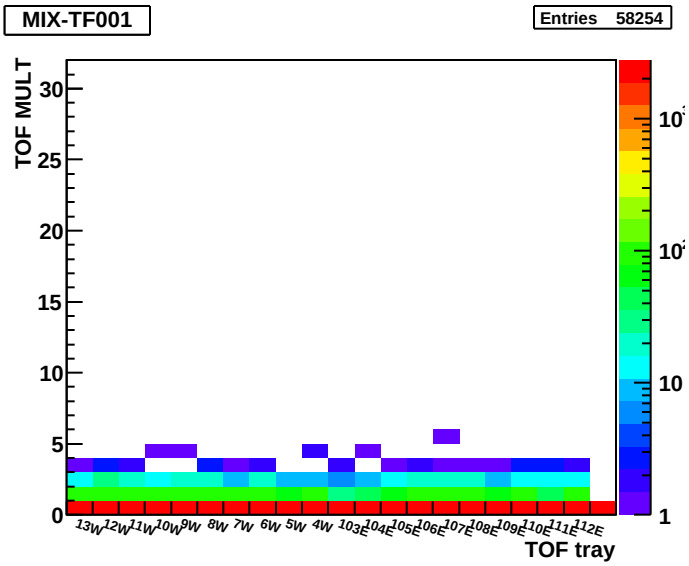

EM201 0-15 (ch3)
Entries 22192
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

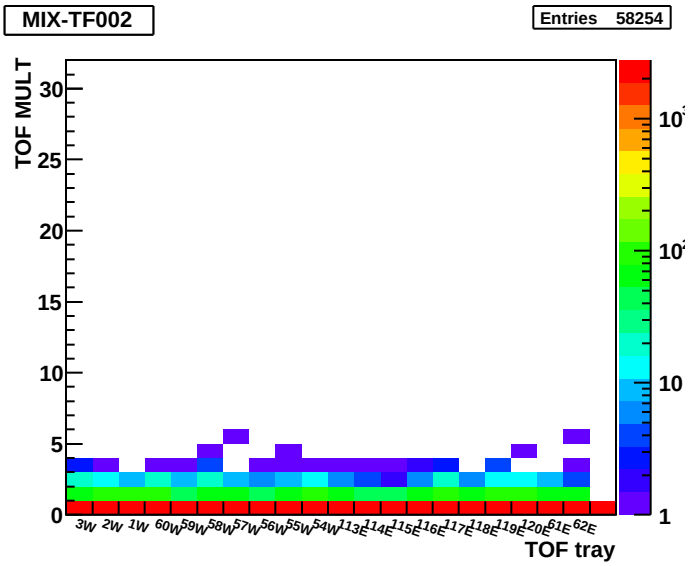

RAT board (ch4)
Entries 22192
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

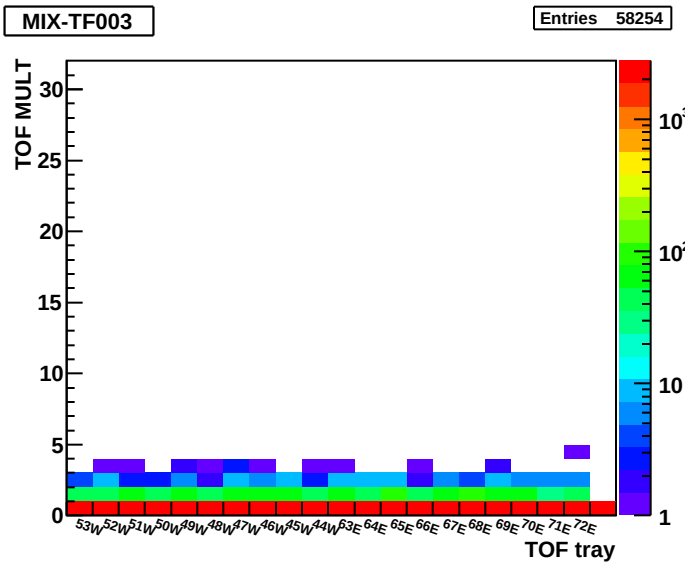

FP201 0-15 (ch5)
Entries 22192
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

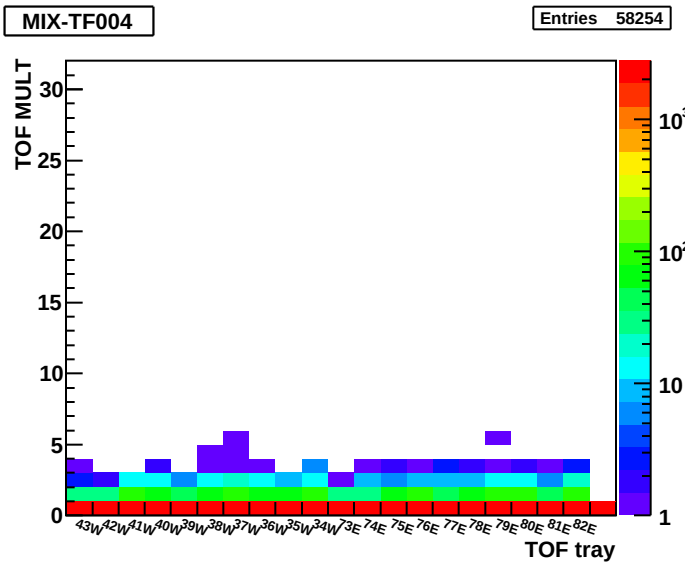

ST201 0-15 (ch6)
Entries 22192
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

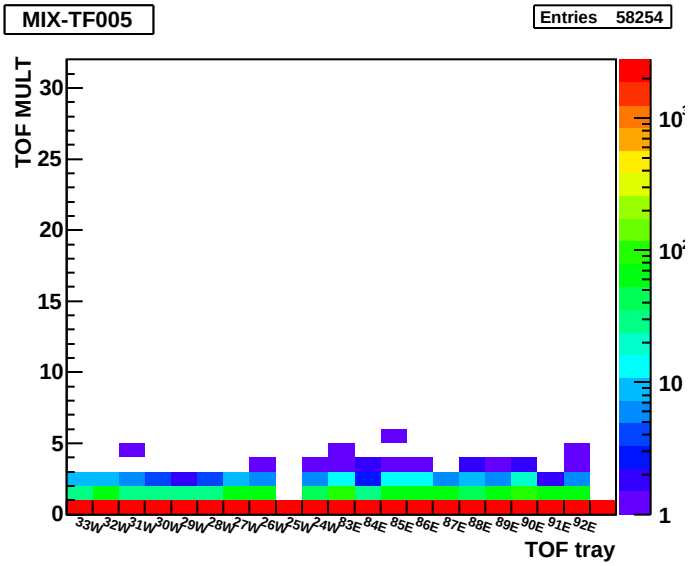

Unused (ch7)
Entries 22192

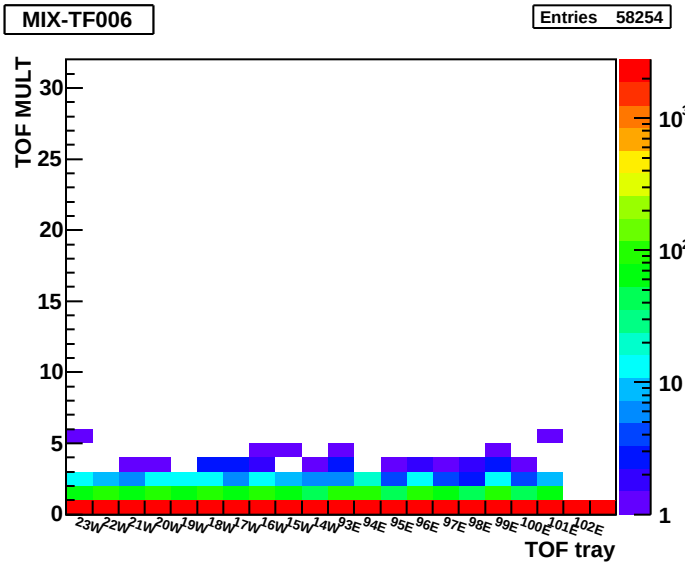

MIX-TF001
Entries 58254
TOF MULT
TOF tray

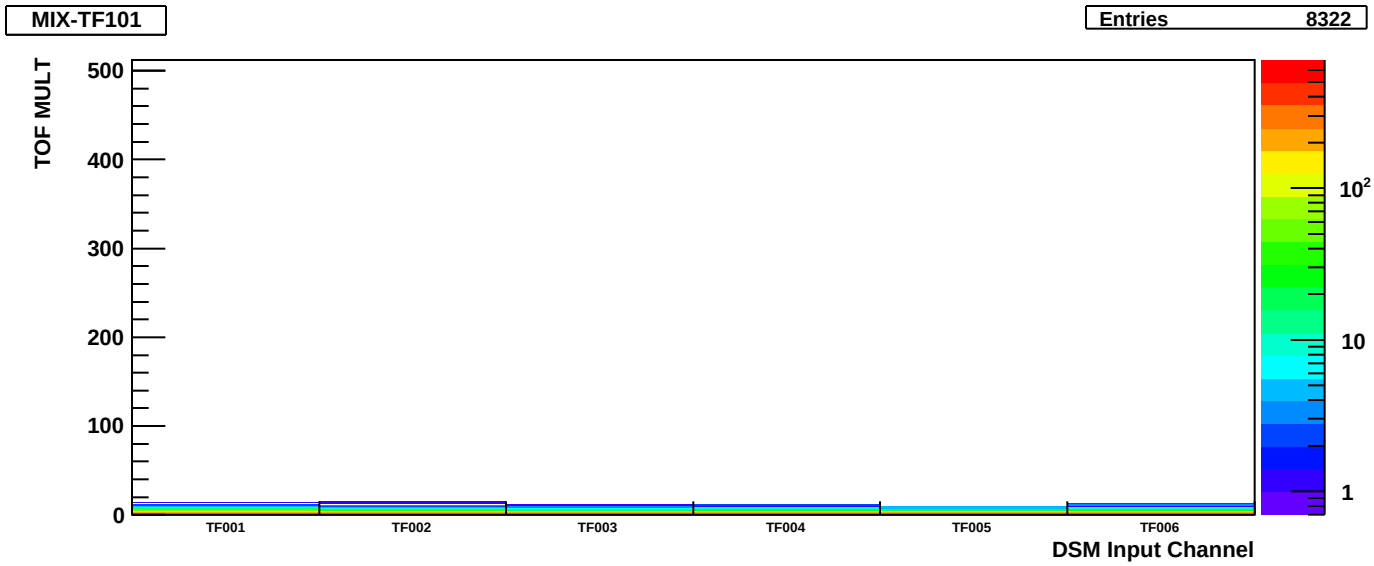

MIX-TF002
Entries 58254
TOF MULT
TOF tray

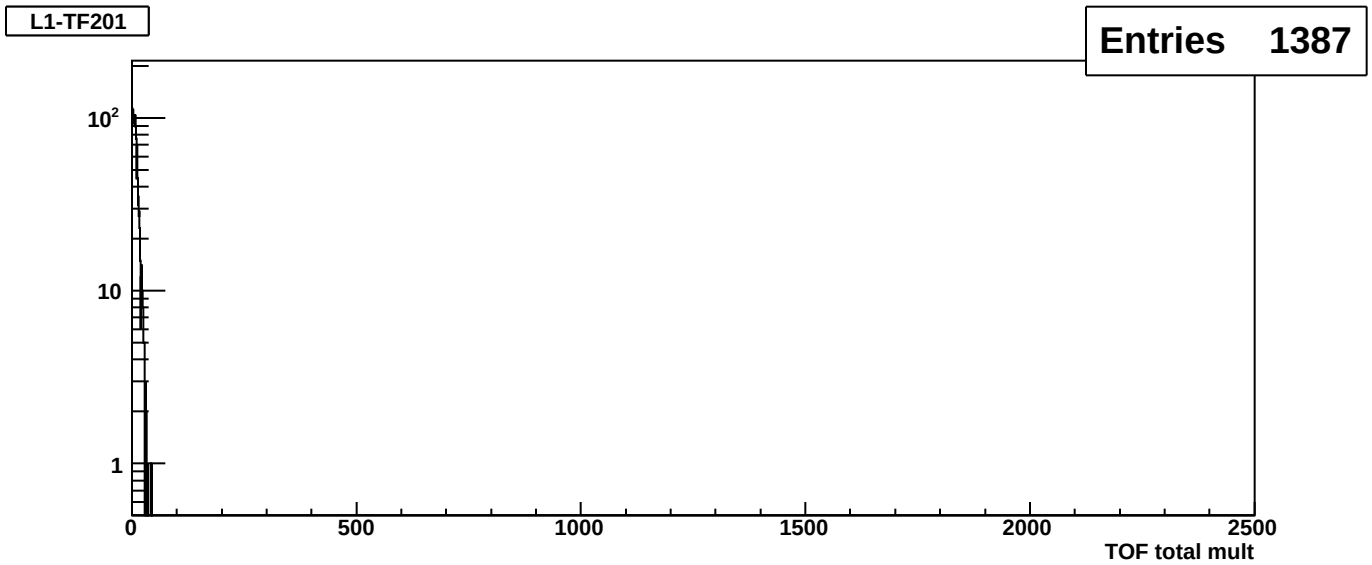

MIX-TF003
Entries 58254
TOF MULT
TOF tray

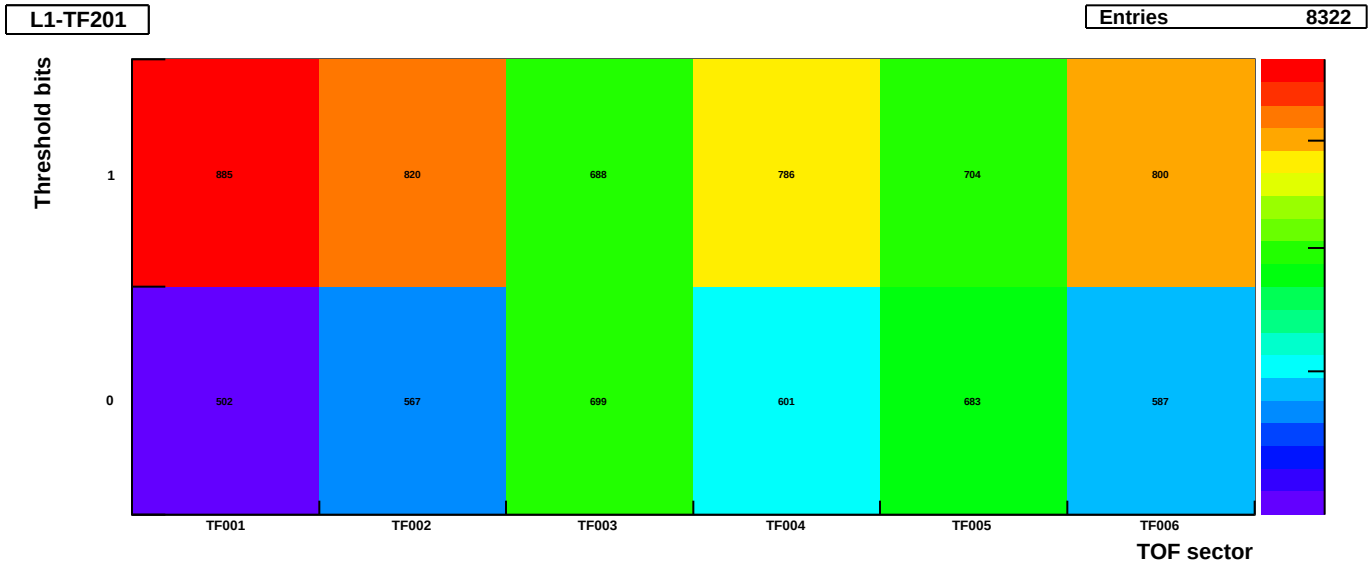

MIX-TF004
Entries 58254
TOF MULT
TOF tray

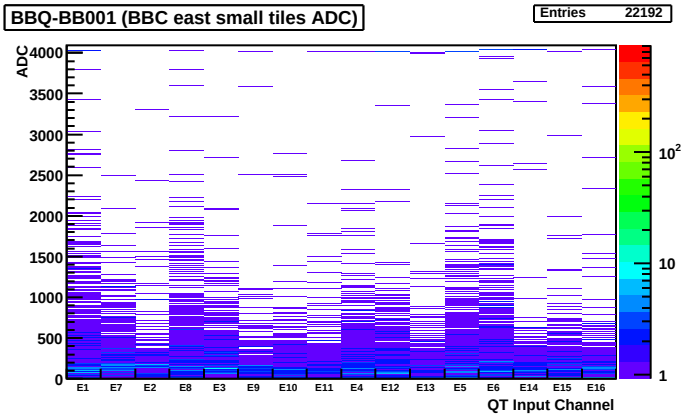

MIX-TF005
Entries 58254
TOF MULT
TOF tray

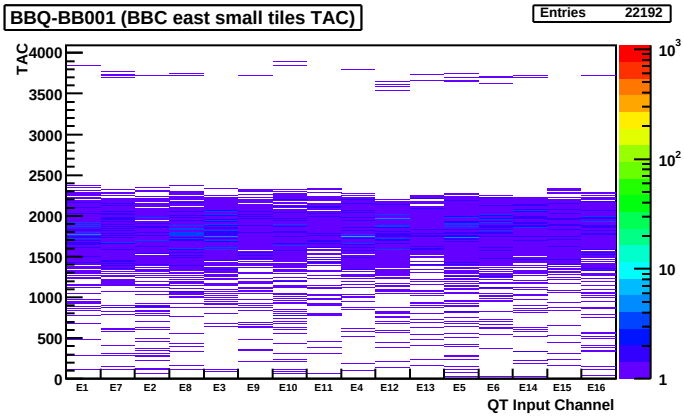

MIX-TF006
Entries 58254
TOF MULT
TOF tray

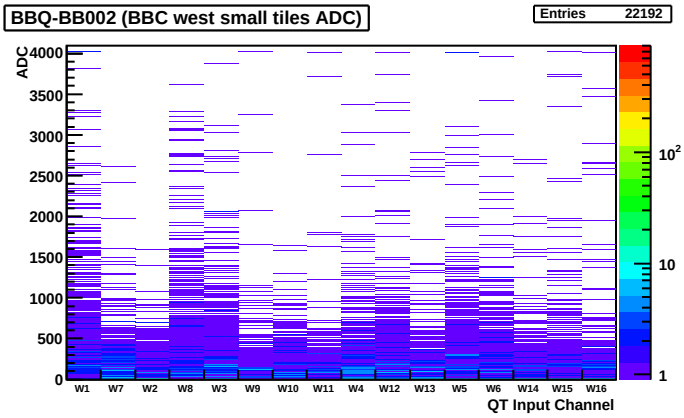

MIX-TF101
Entries 8322
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^2
10
1

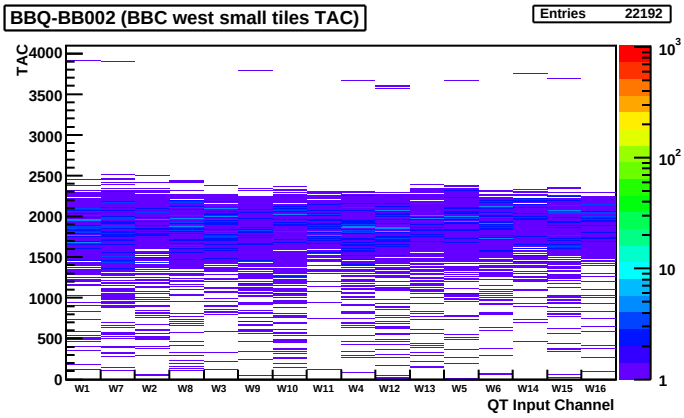

L1-TF201
Entries 1387
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

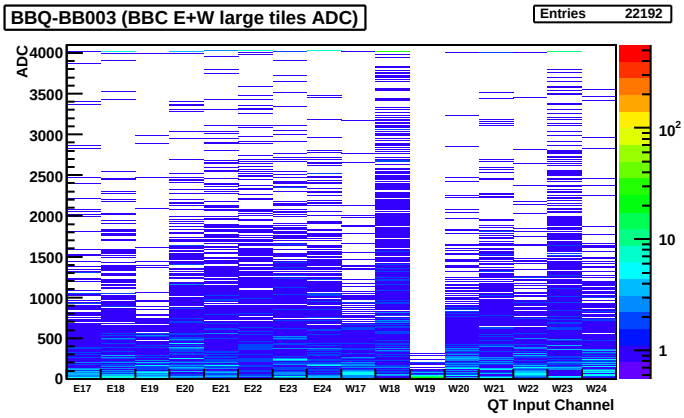

L1-TF201
Entries 8322
Threshold bits
1
0
885
820
688
786
704
800
502
567
699
601
683
587
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

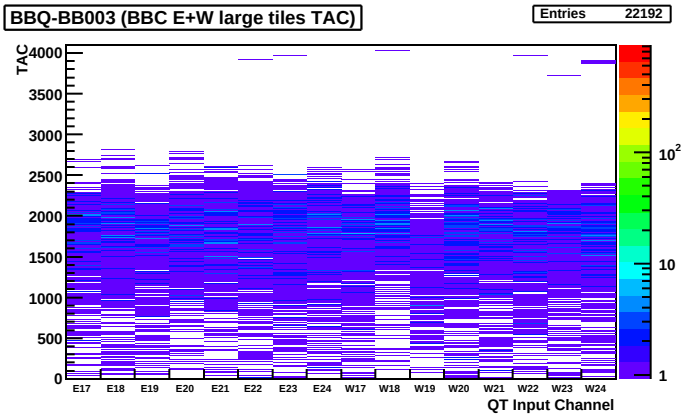

BBQ-BB001 (BBC east small tiles ADC)
Entries 22192
ADC
QT Input Channel

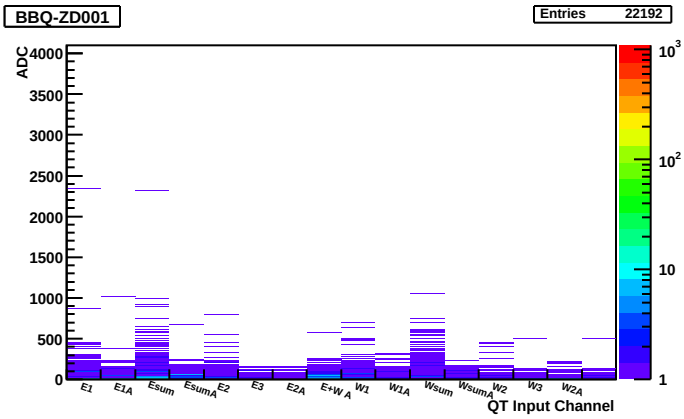

BBQ-BB001 (BBC east small tiles TAC)
Entries 22192
TAC
QT Input Channel

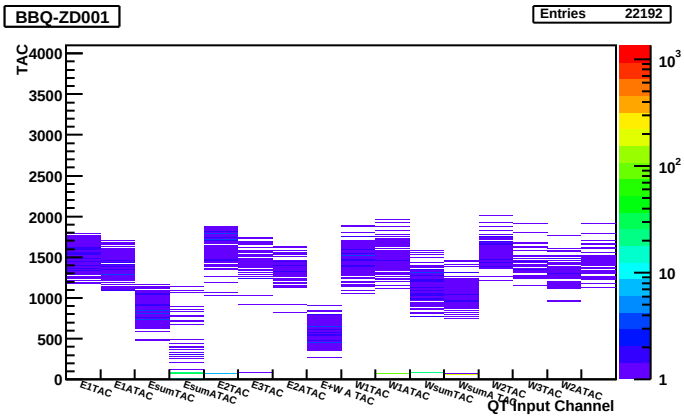

BBQ-BB002 (BBC west small tiles ADC)
Entries 22192
ADC
QT Input Channel

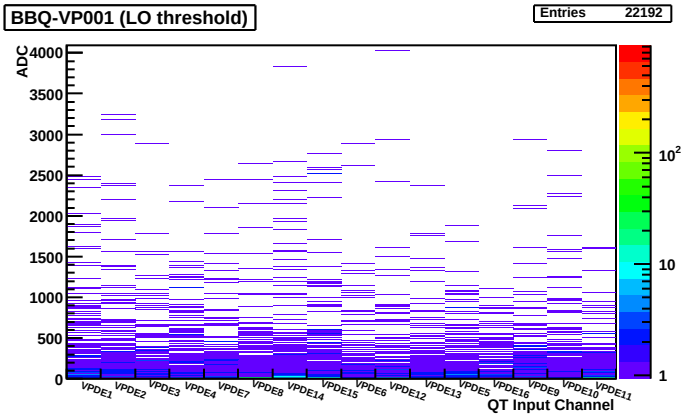

BBQ-BB002 (BBC west small tiles TAC)
Entries 22192
TAC
QT Input Channel

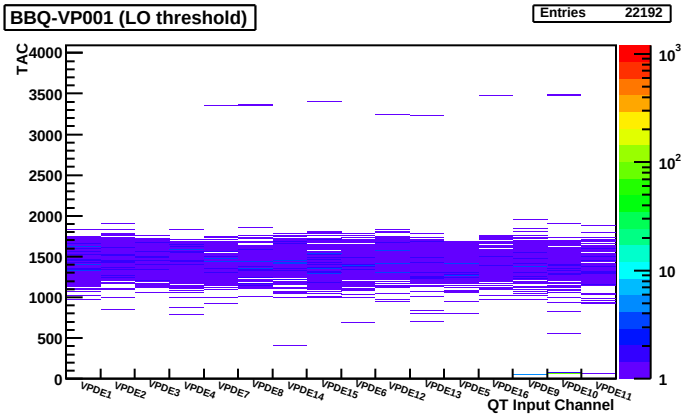

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 22192
ADC
QT Input Channel

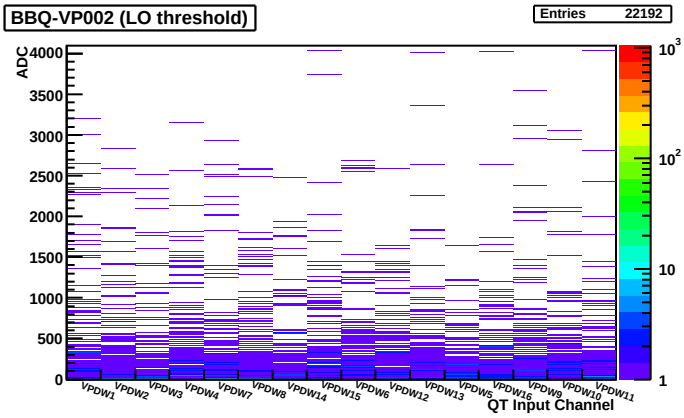

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 22192
TAC
QT Input Channel

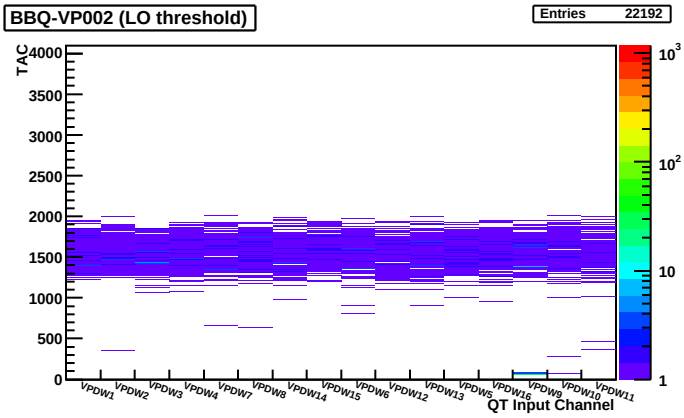

BBQ-ZD001
Entries 22192
ADC
QT Input Channel

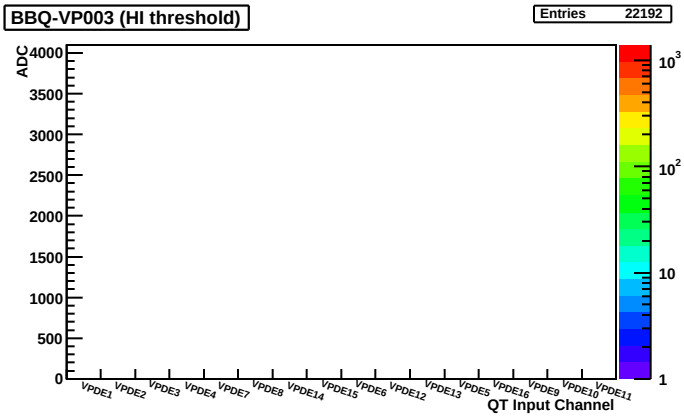

BBQ-ZD001
Entries 22192
TAC
QT Input Channel

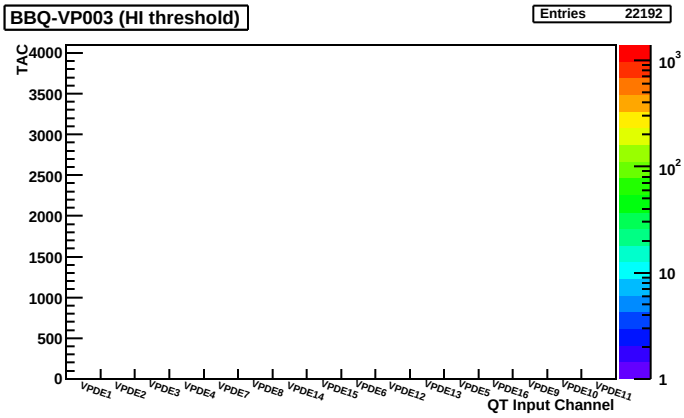

BBQ-VP001 (LO threshold)
Entries 22192
ADC
QT Input Channel

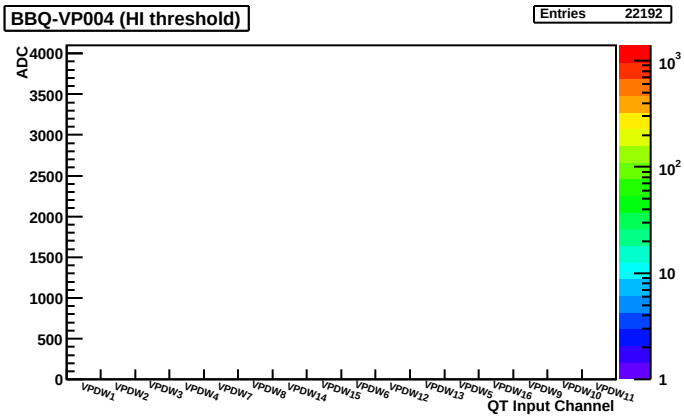

BBQ-VP001 (LO threshold)
Entries 22192
TAC
QT Input Channel

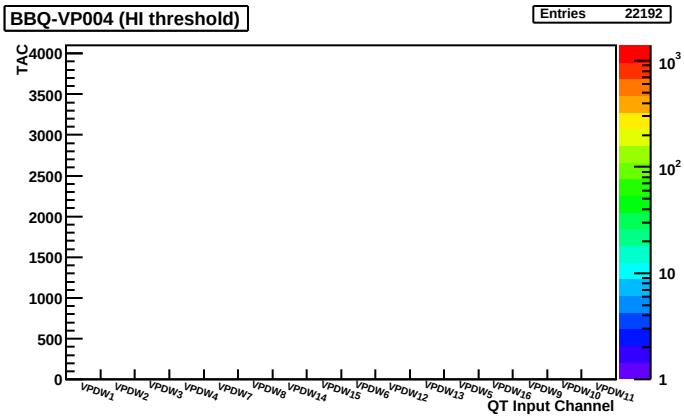

BBQ-VP002 (LO threshold)
Entries 22192
ADC
QT Input Channel

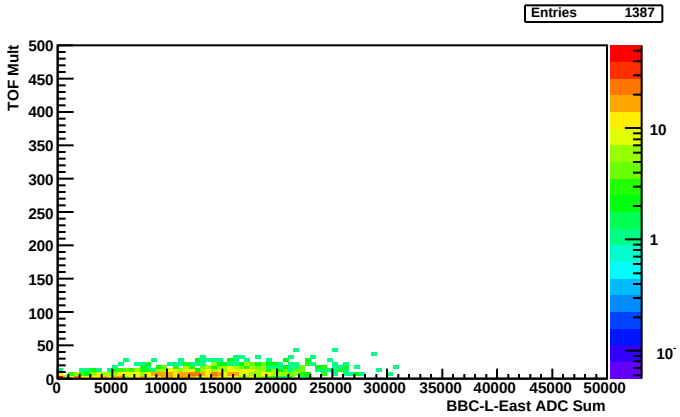

BBQ-VP002 (LO threshold)
Entries 22192
TAC
QT Input Channel

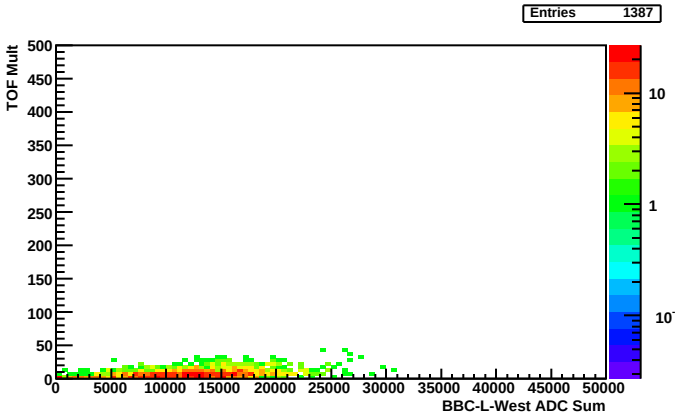

BBQ-VP003 (HI threshold)
Entries 22192
ADC
QT Input Channel

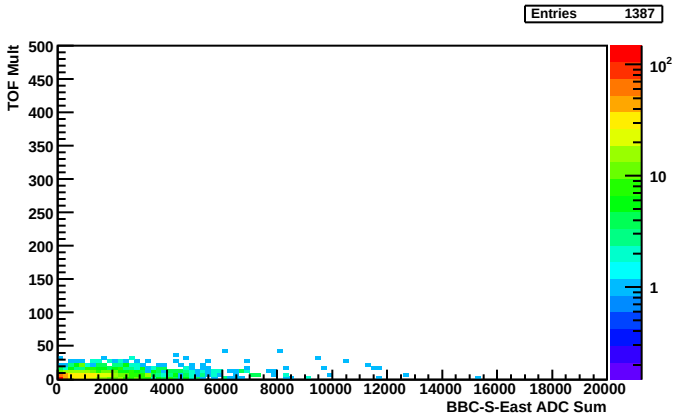

BBQ-VP003 (HI threshold)
Entries 22192
TAC
QT Input Channel

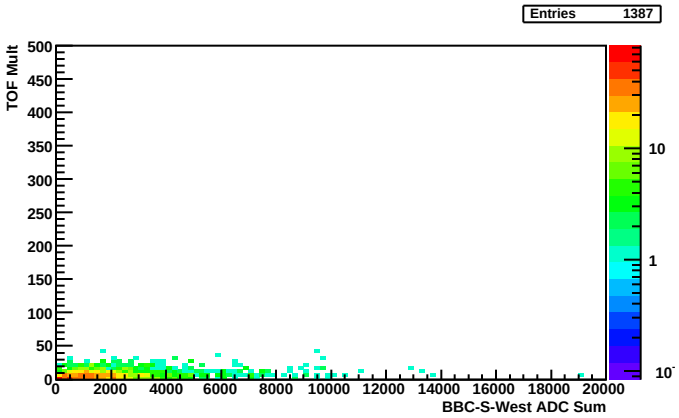

BBQ-VP004 (HI threshold)
Entries 22192
ADC
QT Input Channel

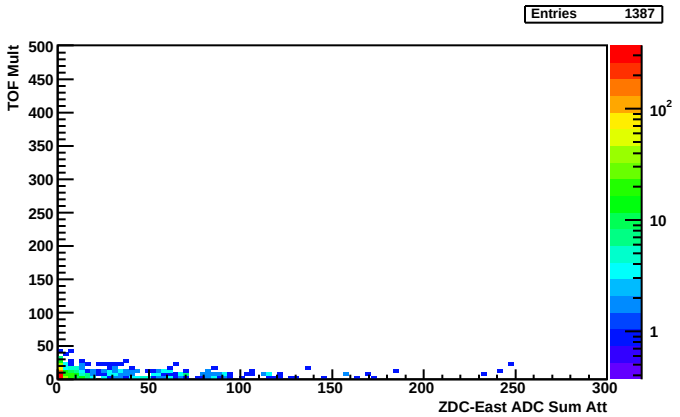

BBQ-VP004 (HI threshold)
Entries 22192
TAC
QT Input Channel

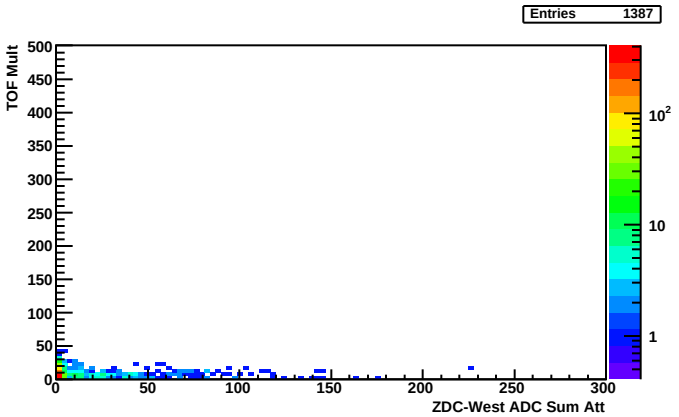

Entries 1387
TOF Mult
BBC-L-East ADC Sum

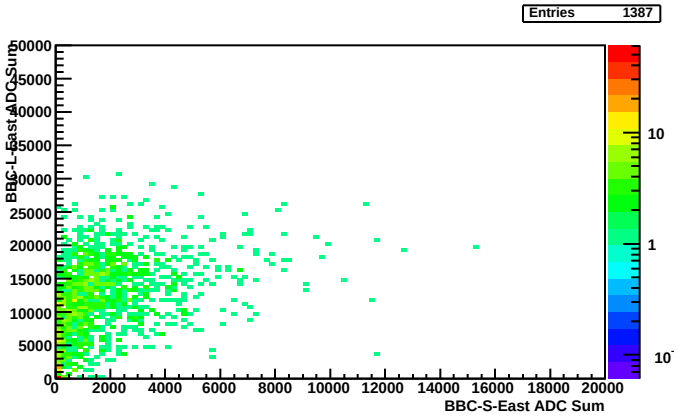

Entries 1387
TOF Mult
BBC-L-West ADC Sum

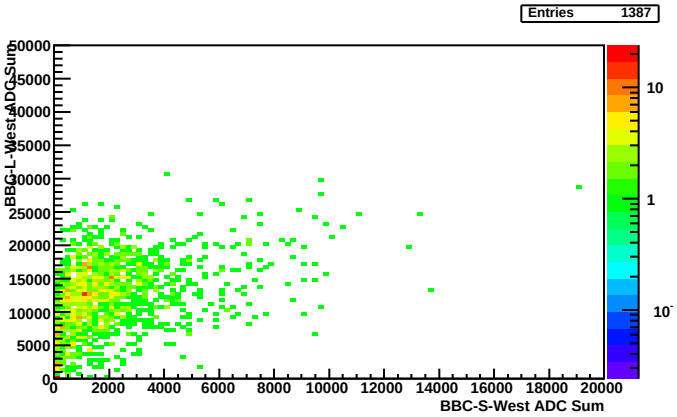

Entries 1387
TOF Mult
BBC-S-East ADC Sum

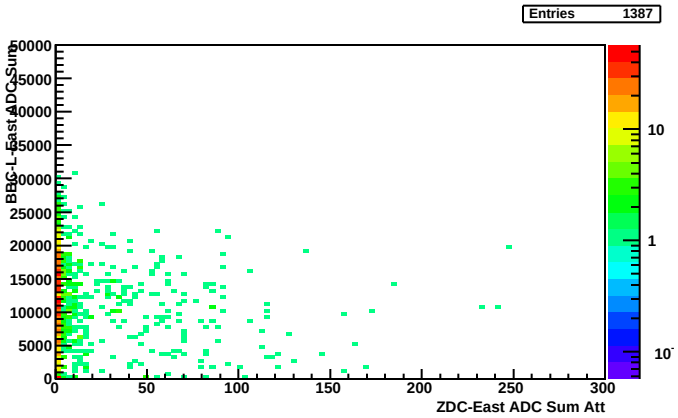

Entries 1387
TOF Mult
BBC-S-West ADC Sum

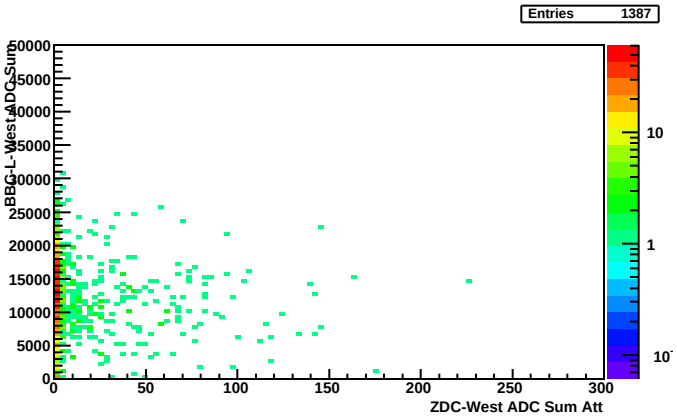

Entries 1387
TOF Mult
ZDC-East ADC Sum Att

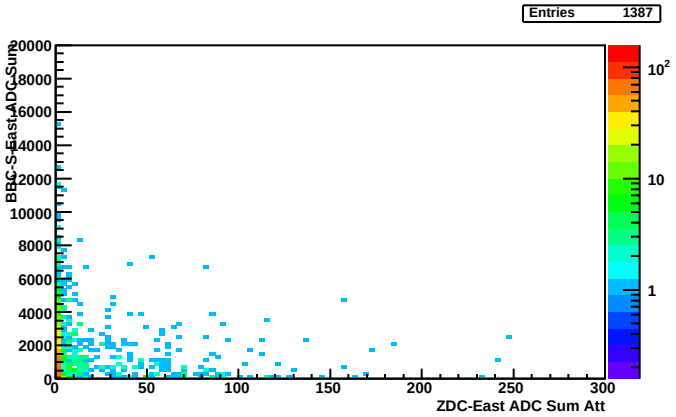

Entries 1387
TOF Mult
ZDC-West ADC Sum Att

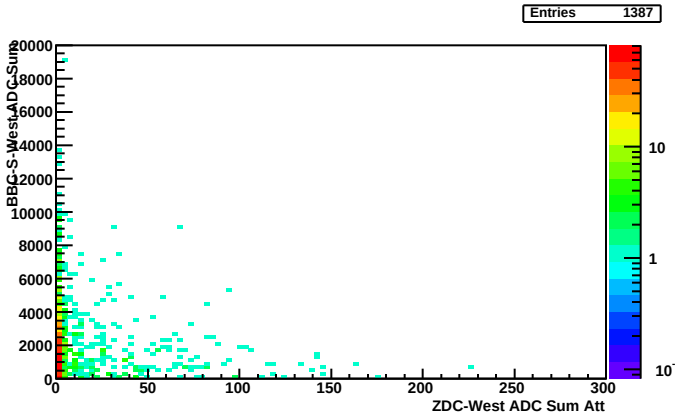

Entries 1387
BBC-L-East ADC Sum
BBC-S-East ADC Sum

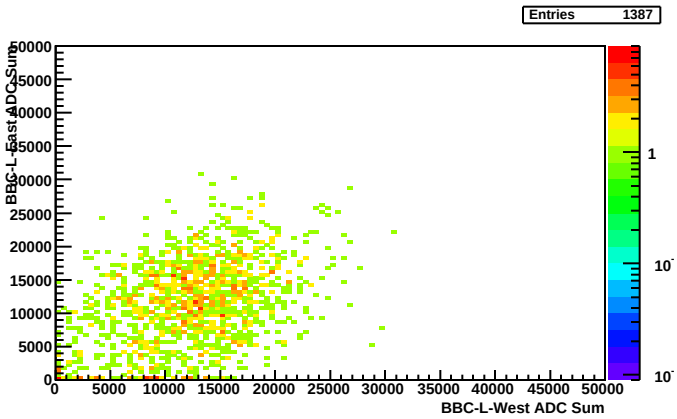

Entries 1387
BBC-L-West ADC Sum
BBC-S-West ADC Sum

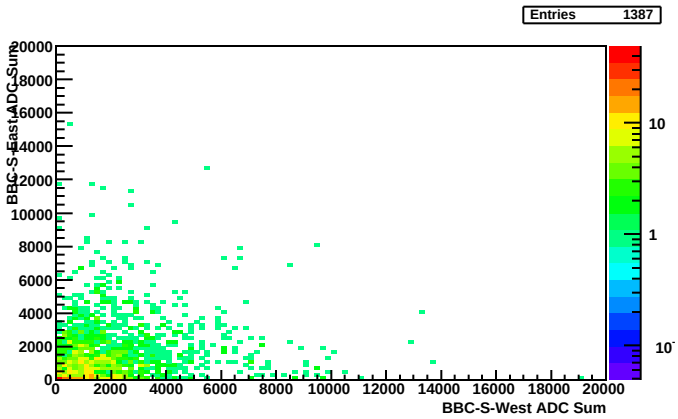

Entries 1387
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

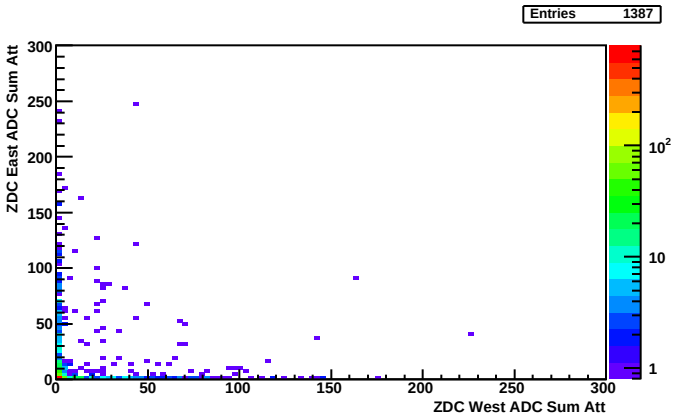

Entries 1387
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

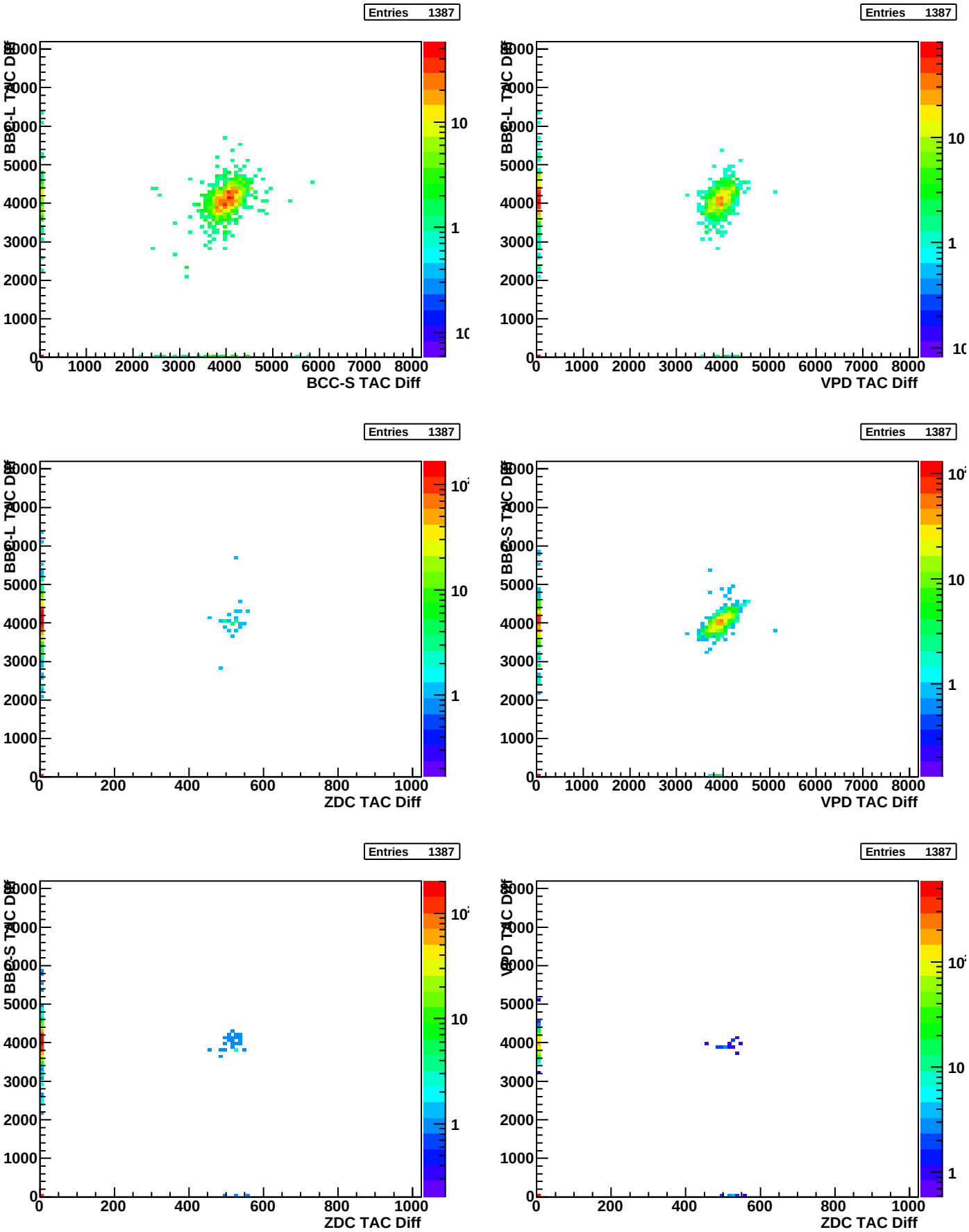

Entries 1387
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

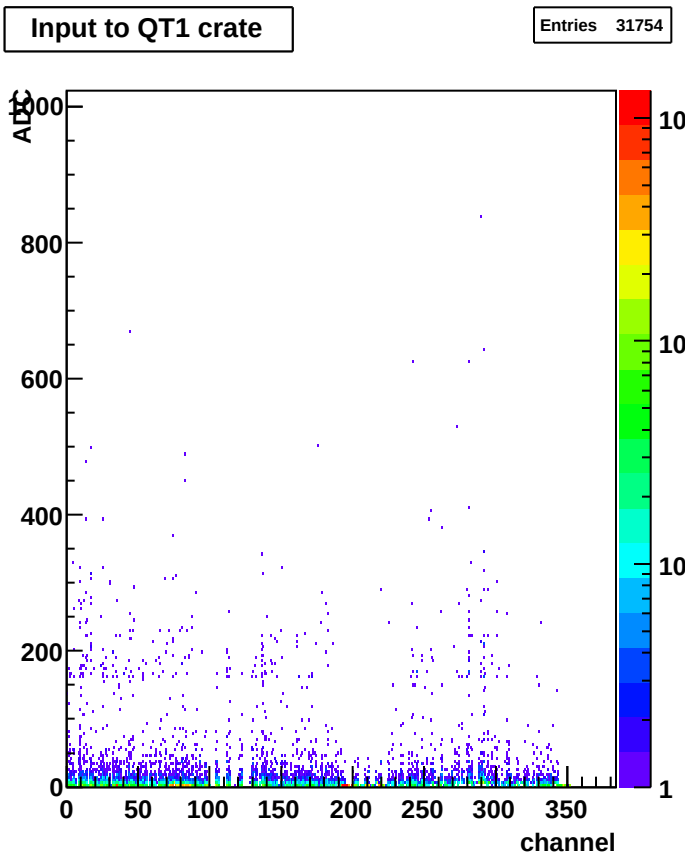

Entries 1387
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

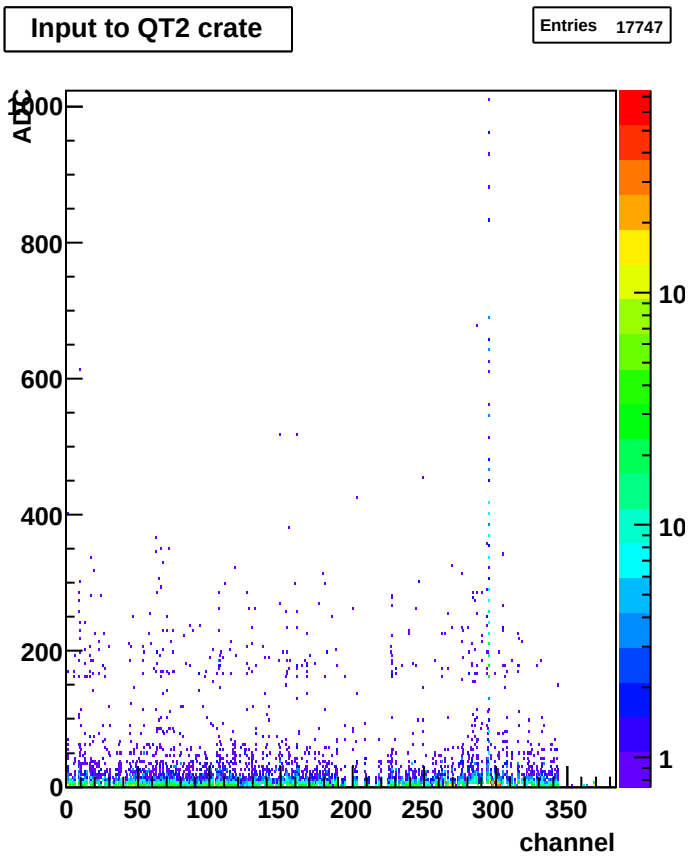

Entries 1387
BBC-L-East ADC Sum
BBC-L-West ADC Sum

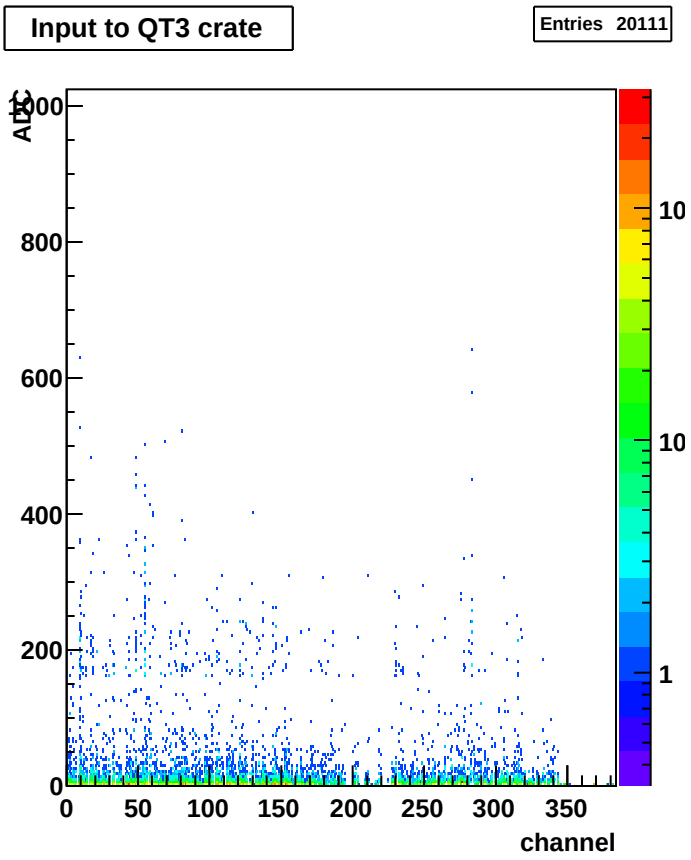

Entries 1387
BBC-S-East ADC Sum
BBC-S-West ADC Sum

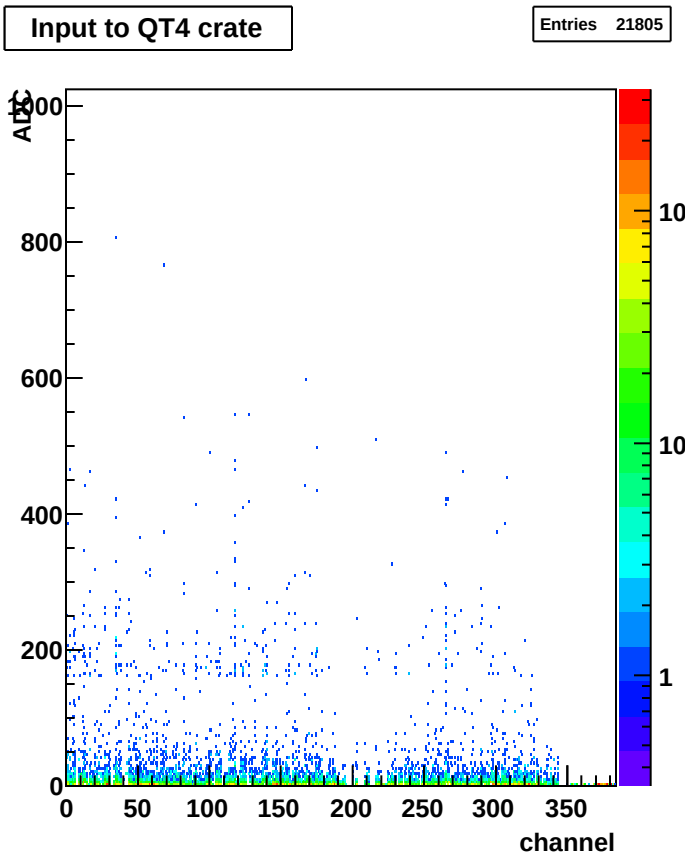

Entries 1387
ZDC East ADC Sum Att
ZDC West ADC Sum Att

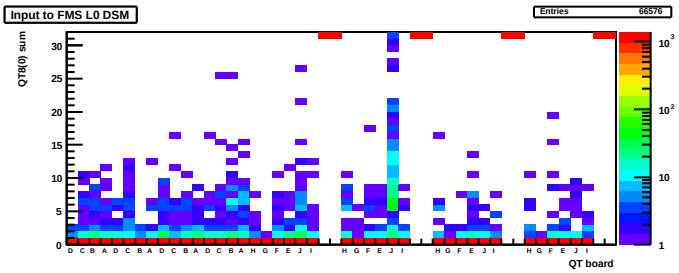

Entries 1387
BBC-L TAC Diff
BCC-S TAC Diff
Entries 1387
BBC-L TAC Diff
VPD TAC Diff
Entries 1387
BBC-L TAC Diff
ZDC TAC Diff
Entries 1387
BBC-S TAC Diff
VPD TAC Diff
Entries 1387
BBC-S TAC Diff
ZDC TAC Diff
Entries 1387
VPD TAC Diff
ZDC TAC Diff

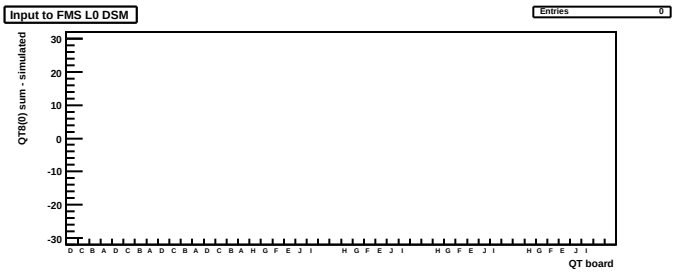

Input to QT1 crate
Entries 31754
ADC
channel

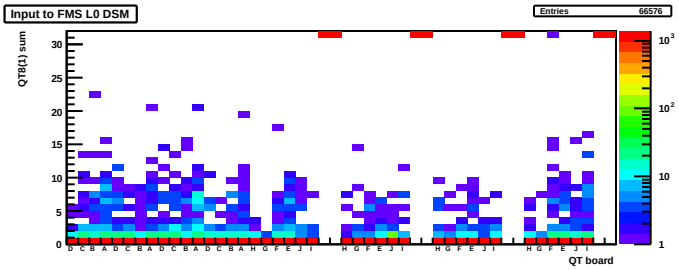

Input to QT2 crate
Entries 17747
ADC
channel

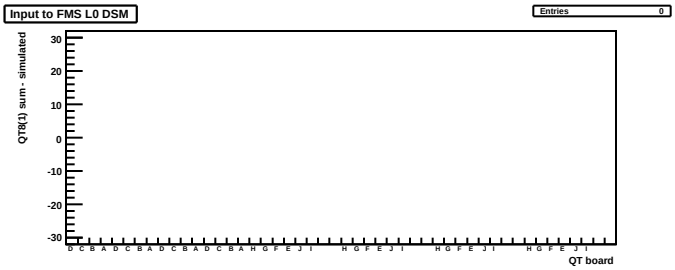

Input to QT3 crate
Entries 20111
ADC
channel

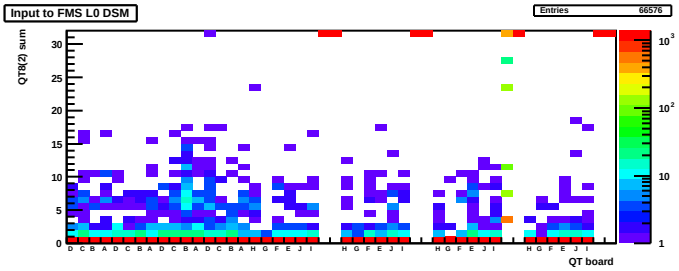

Input to QT4 crate
Entries 21805
ADC
channel

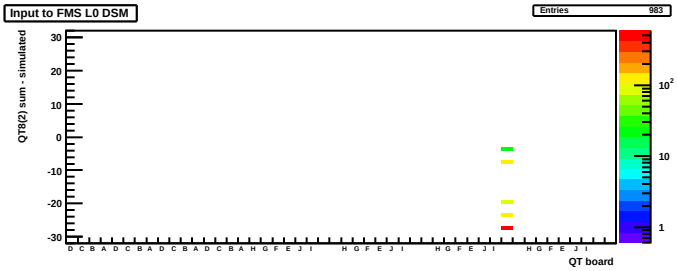

Input to FMS L0 DSM
Entries 66576
QT8(0) sum
QT board

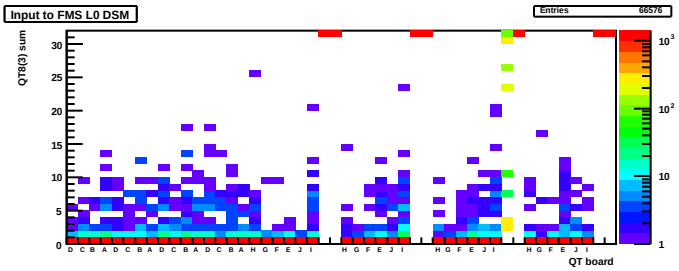

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

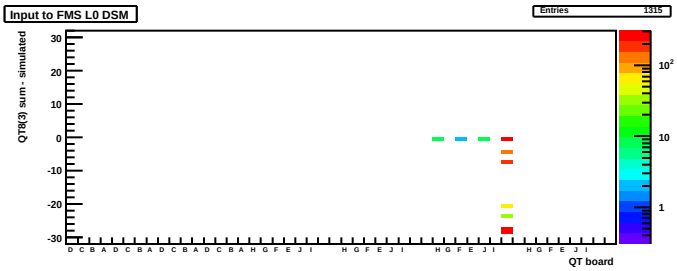

Input to FMS L0 DSM
Entries 66576
QT8(1) sum
QT board

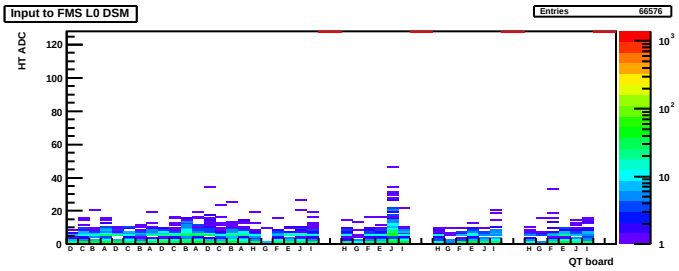

Input to FMS L0 DSM
Entries 0
QT8(1) sum - simulated
QT board

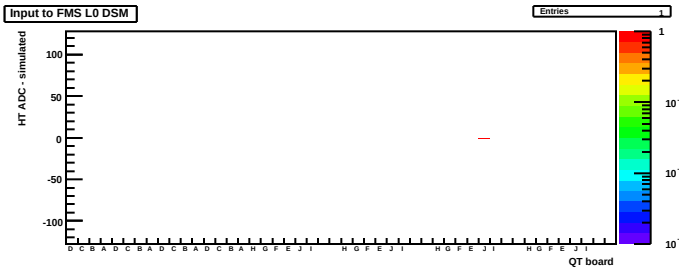

Input to FMS L0 DSM
Entries 66576
QT8(2) sum
QT board

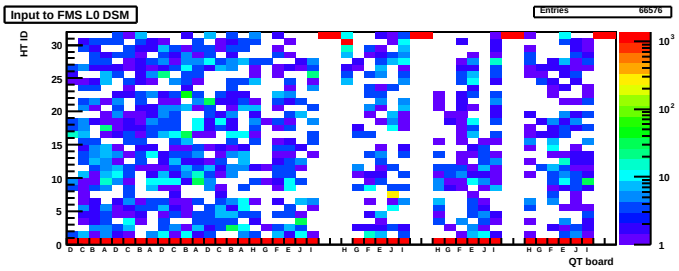

Input to FMS L0 DSM
Entries 983
QT8(2) sum - simulated
QT board

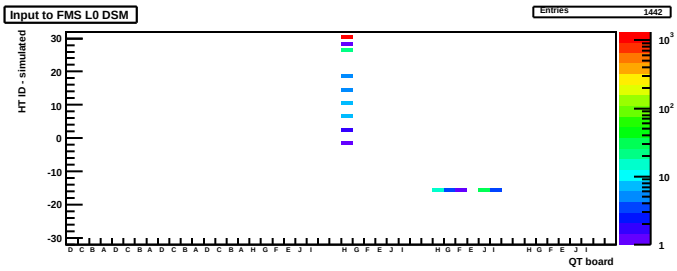

Input to FMS L0 DSM
Entries 66576
QT8(3) sum
QT board

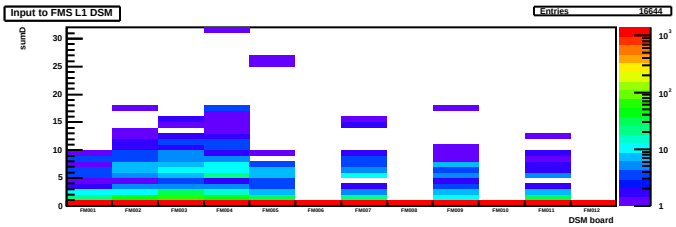

Input to FMS L0 DSM
Entries 1315
QT8(3) sum - simulated
QT board

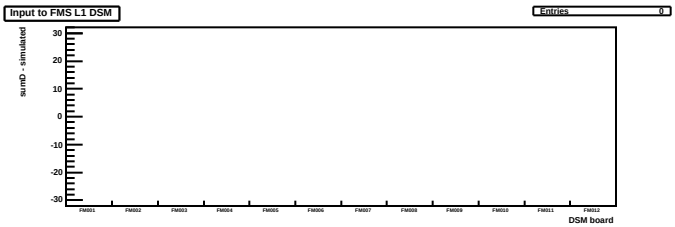

Input to FMS L0 DSM
Entries 66576
HT ADC
QT board

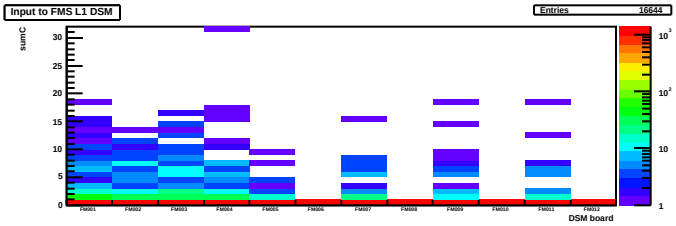

Input to FMS L0 DSM
Entries 1
HT ADC - simulated
QT board

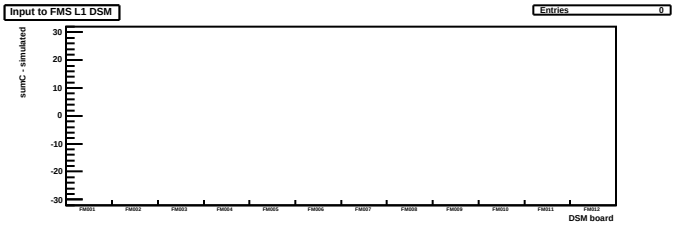

Input to FMS L0 DSM
Entries 66576
HT ID
QT board

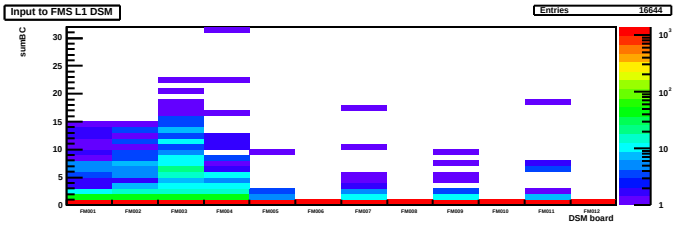

Input to FMS L0 DSM
Entries 1442
HT ID - simulated
QT board

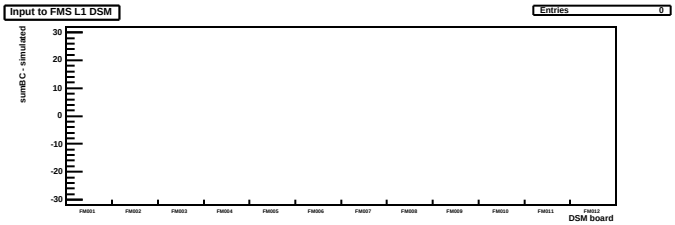

Input to FMS L1 DSM
Entries 16644
sumD
DSM board

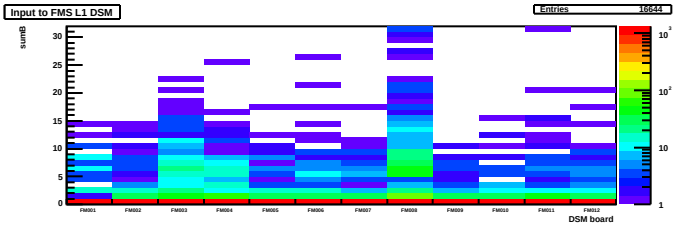

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

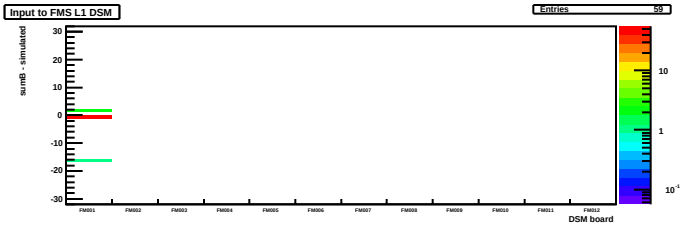

Input to FMS L1 DSM
Entries 16644
sumC
DSM board

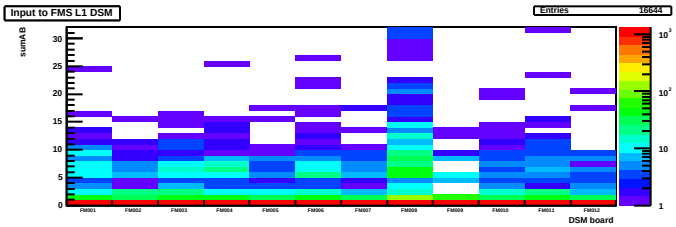

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

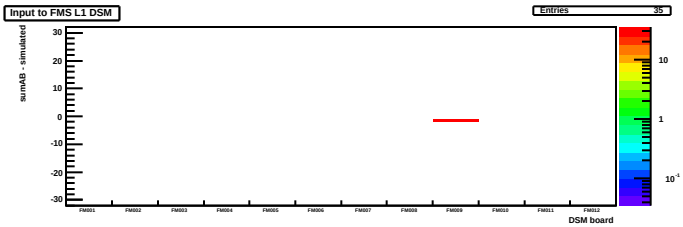

Input to FMS L1 DSM
Entries 16644
sumBC
DSM board

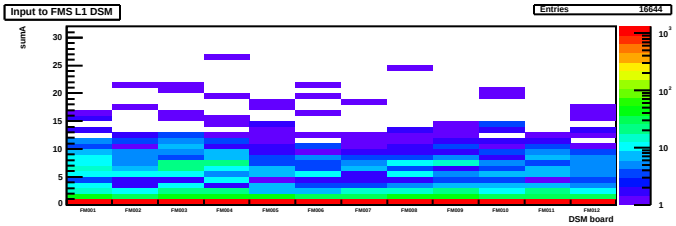

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

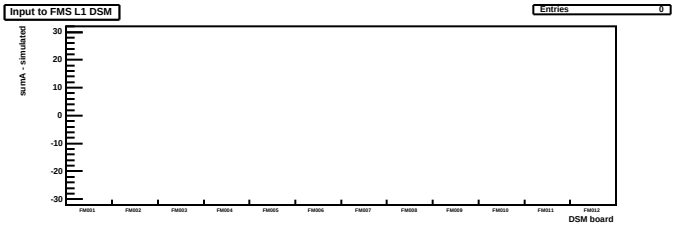

Input to FMS L1 DSM
Entries 16644
sumB
DSM board

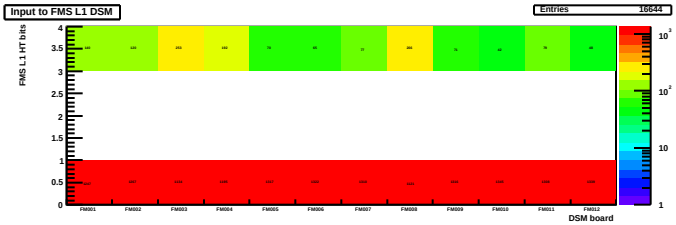

Input to FMS L1 DSM
Entries 59
sumB - simulated
DSM board

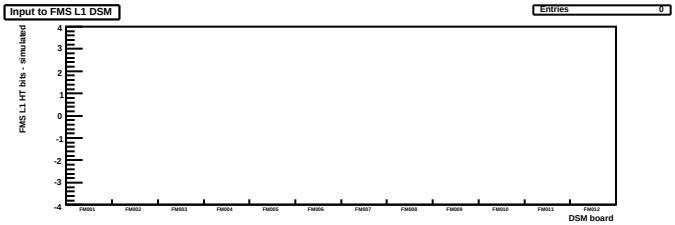

Input to FMS L1 DSM
Entries 16644
sumAB
DSM board

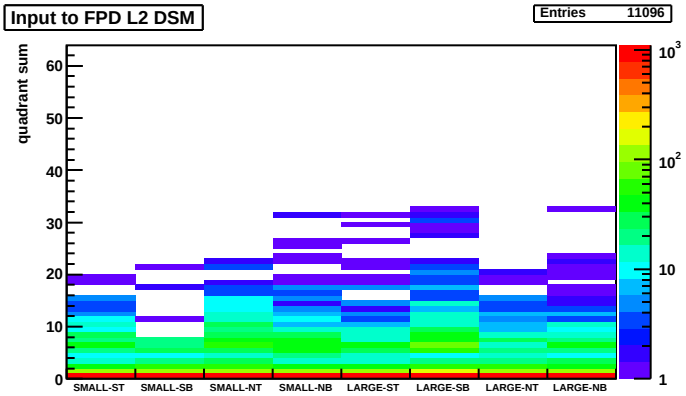

Input to FMS L1 DSM
Entries 35
sumAB - simulated
DSM board

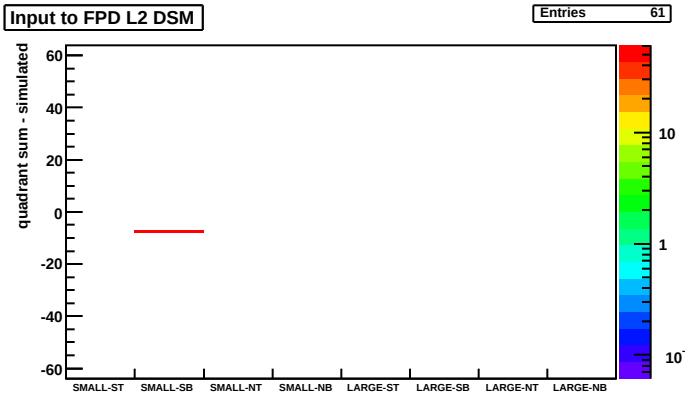

Input to FMS L1 DSM
Entries 16644
sumA
DSM board

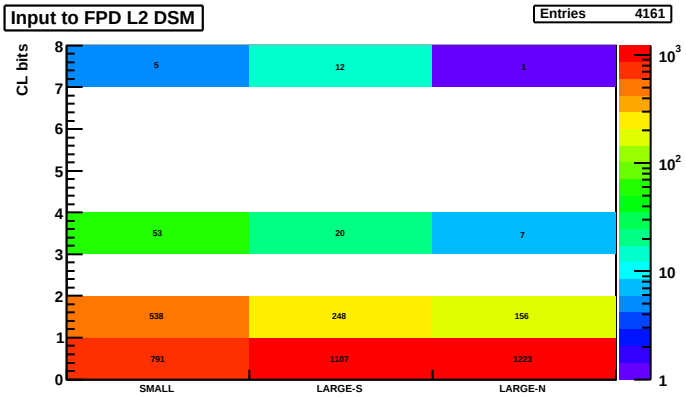

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

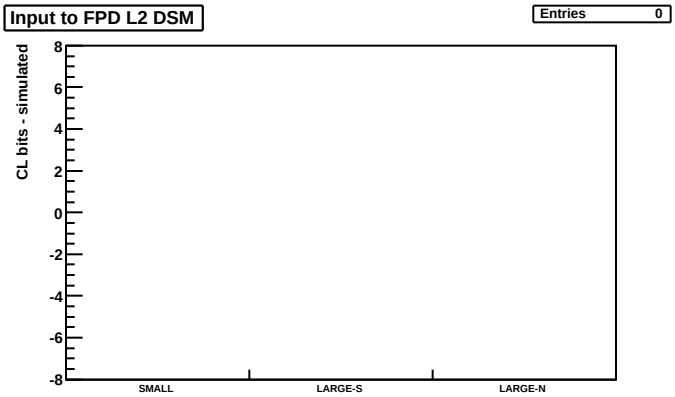

Input to FMS L1 DSM
Entries 16644
FMS L1 HT bits
DSM board

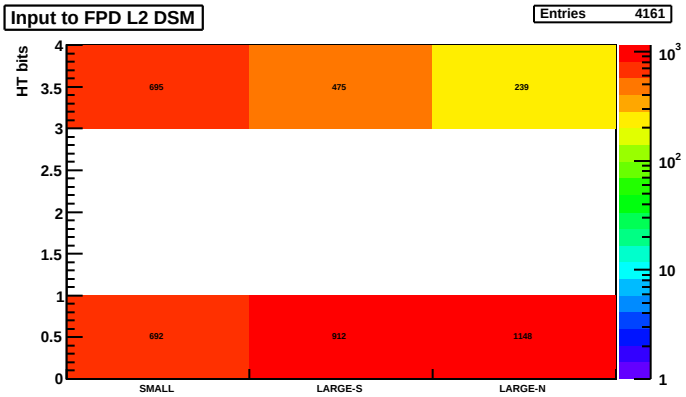

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

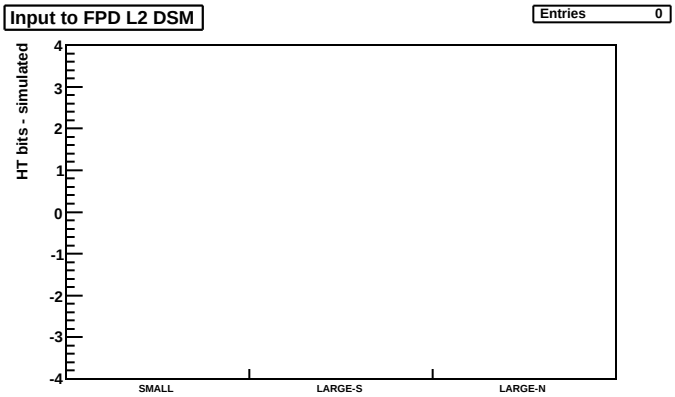

Input to FPD L2 DSM
Entries 11096
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

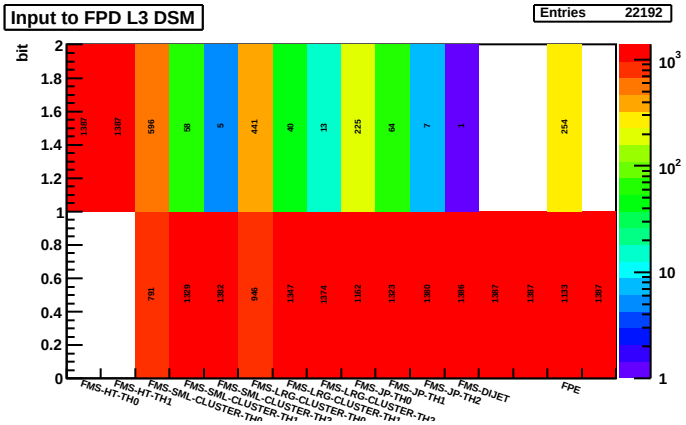

Input to FPD L2 DSM
Entries 61
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

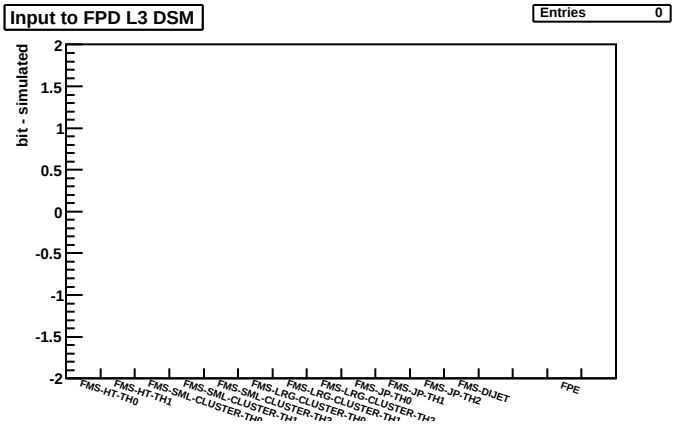

Input to FPD L2 DSM
Entries 4161
CL bits
5
12
1
53
20
7
538
248
156
791
1107
1223
SMALL
LARGE-S
LARGE-N

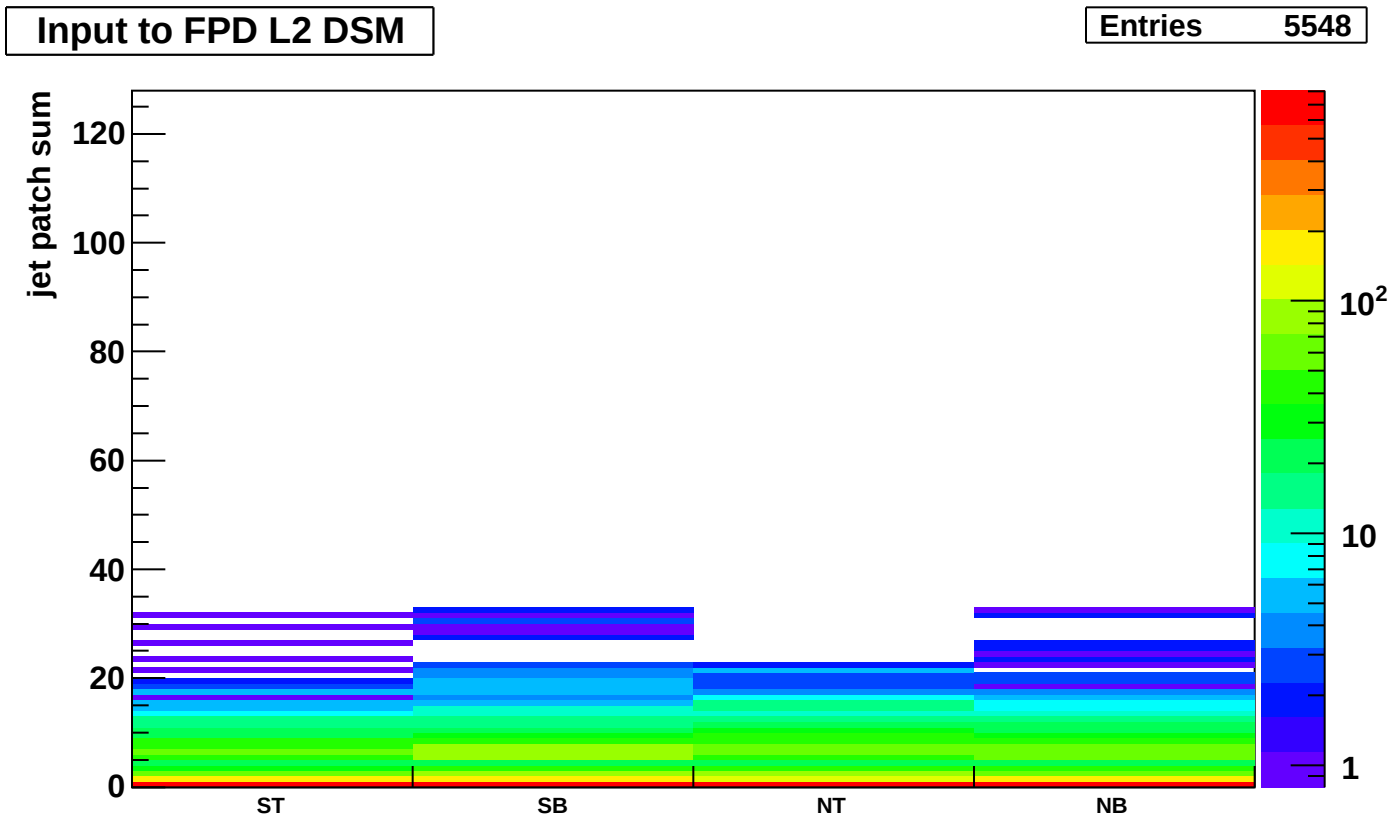

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

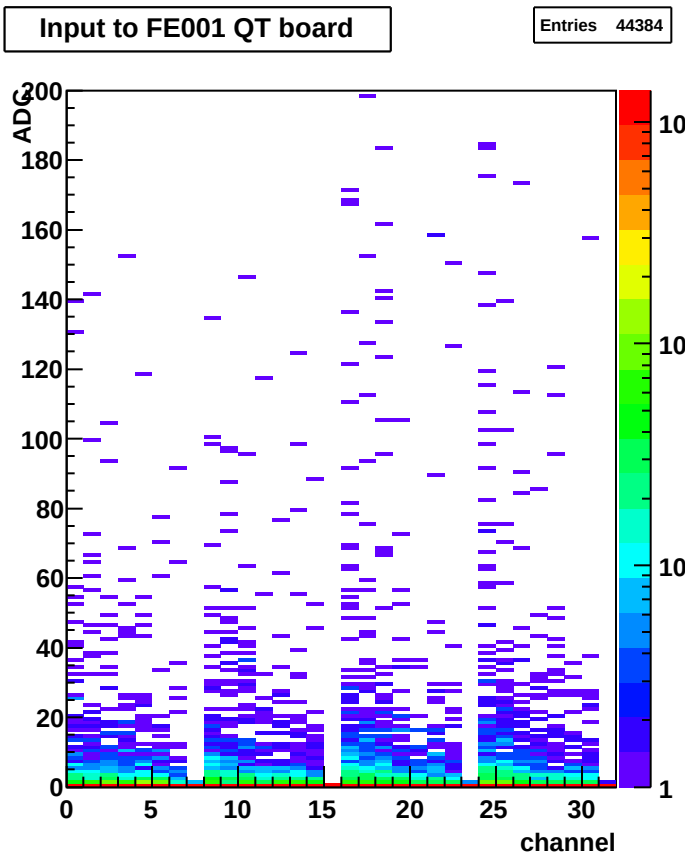

Input to FPD L2 DSM
Entries 4161
HT bits
695
475
239
692
912
1148
SMALL
LARGE-S
LARGE-N

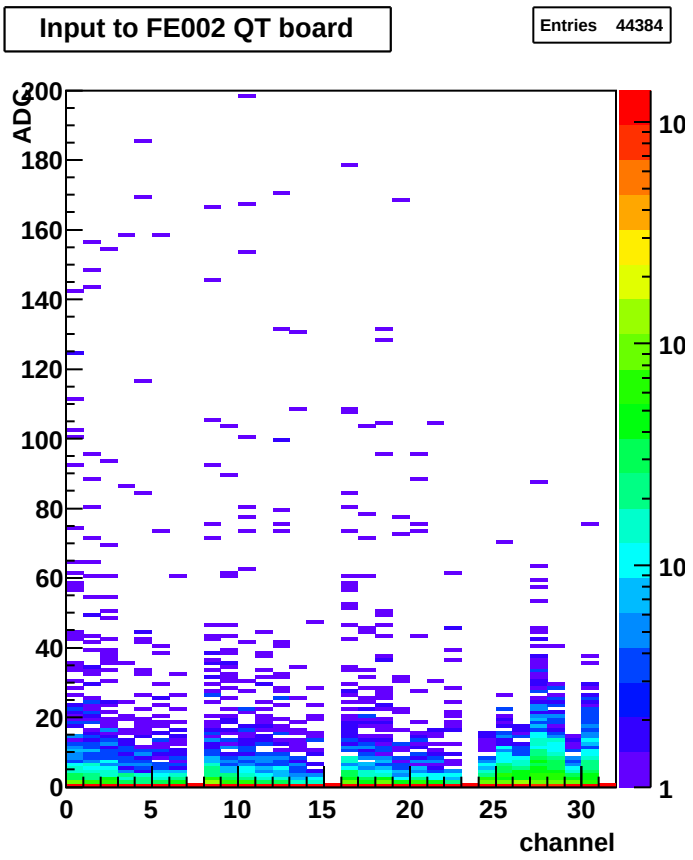

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

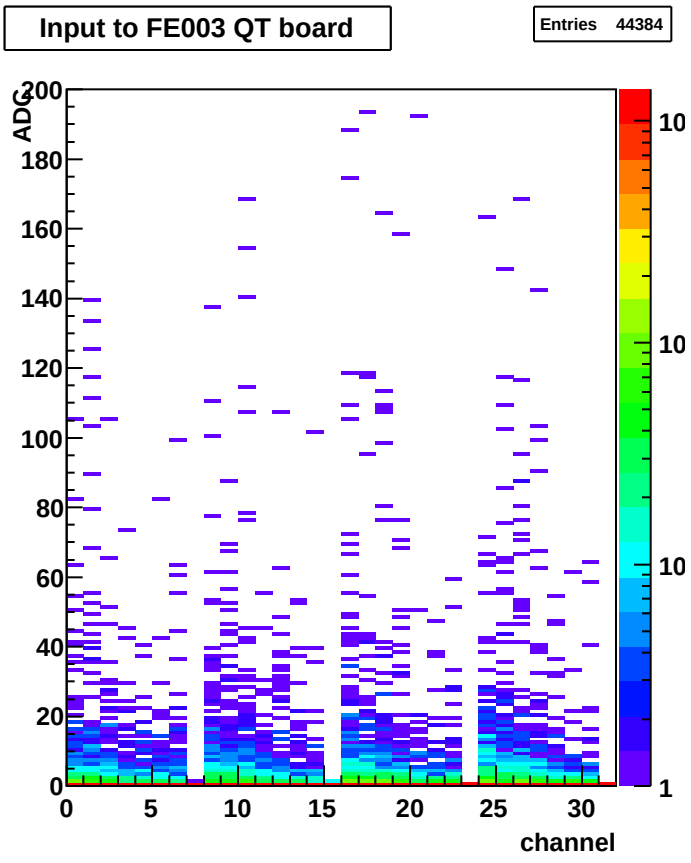

Input to FPD L3 DSM
Entries 22192
bit
1387
1387
596
58
5
441
40
13
225
64
7
1
254
791
1329
1382
946
1347
1374
1162
1323
1380
1386
1387
1387
1133
1387
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

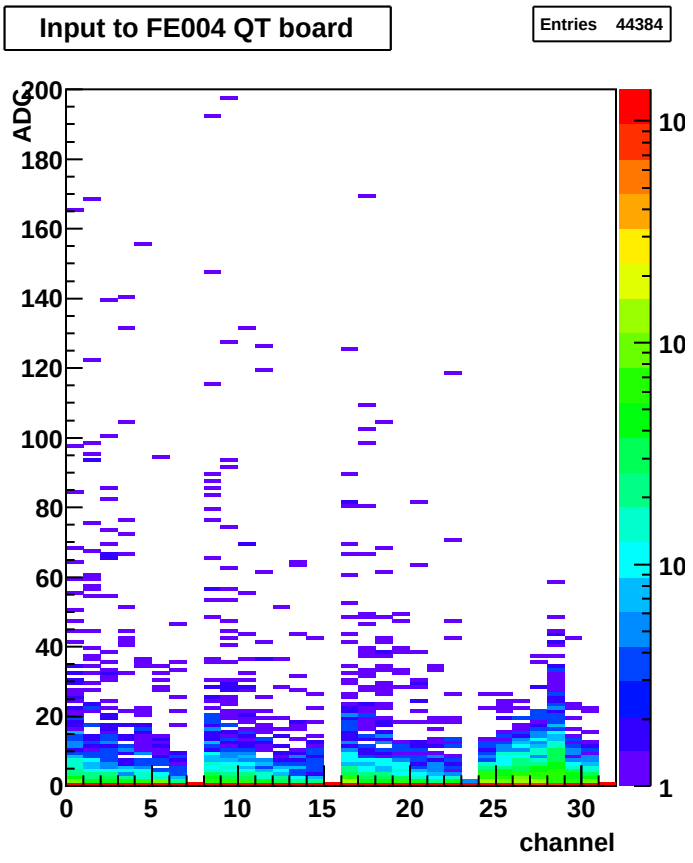

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

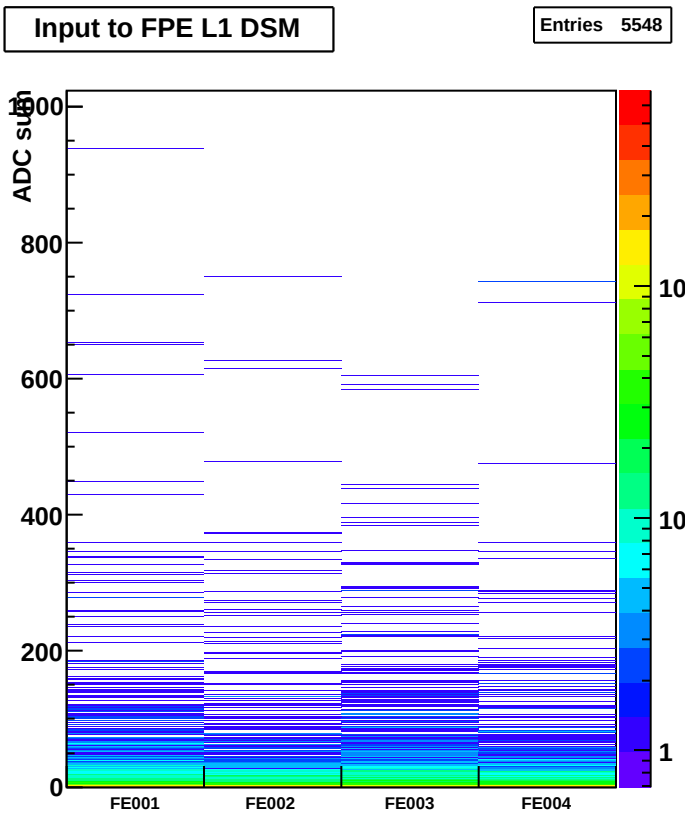

Input to FPD L2 DSM
Entries 5548
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^2
10
1

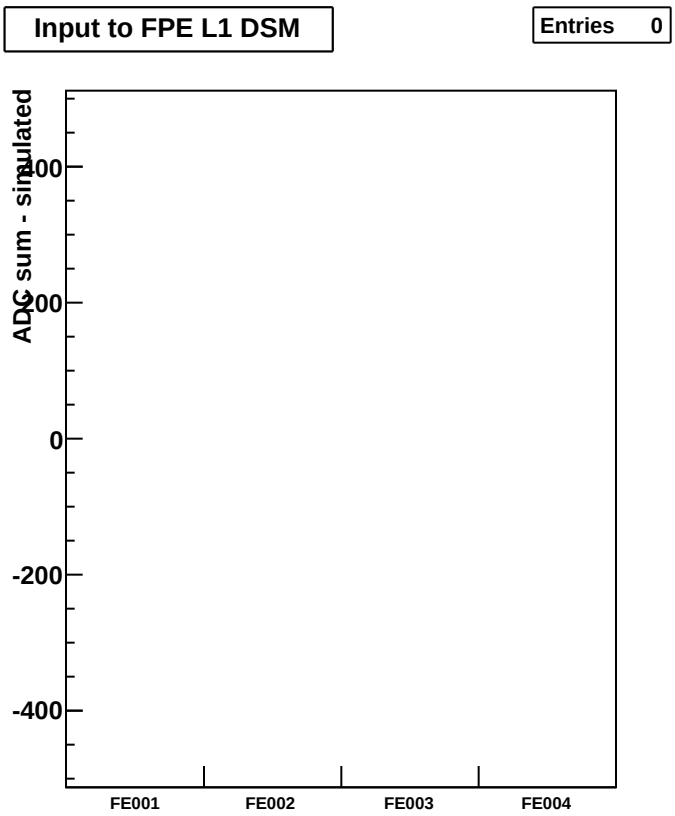

Input to FE001 QT board
Entries 44384
ADC
channel

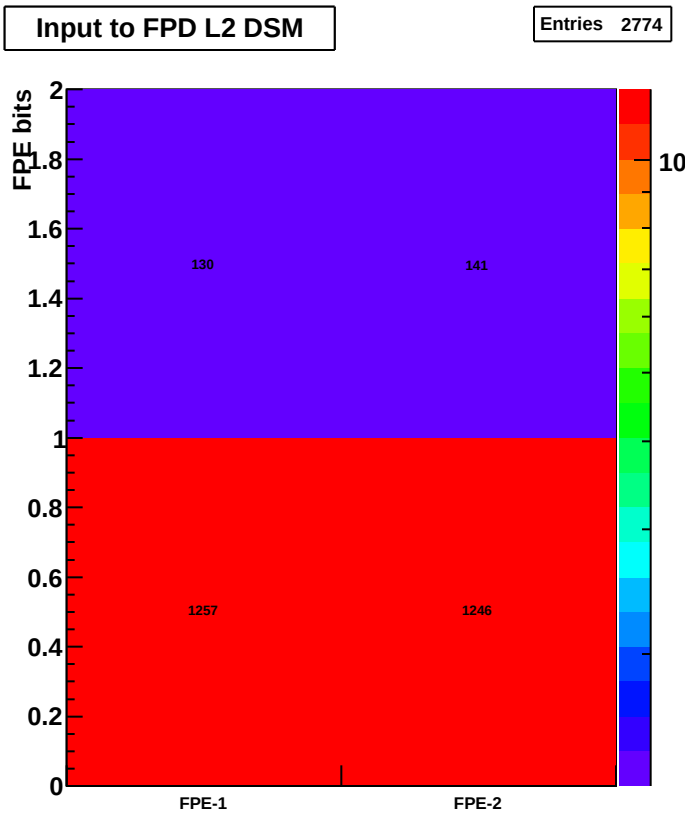

Input to FE002 QT board
Entries 44384
ADC
channel

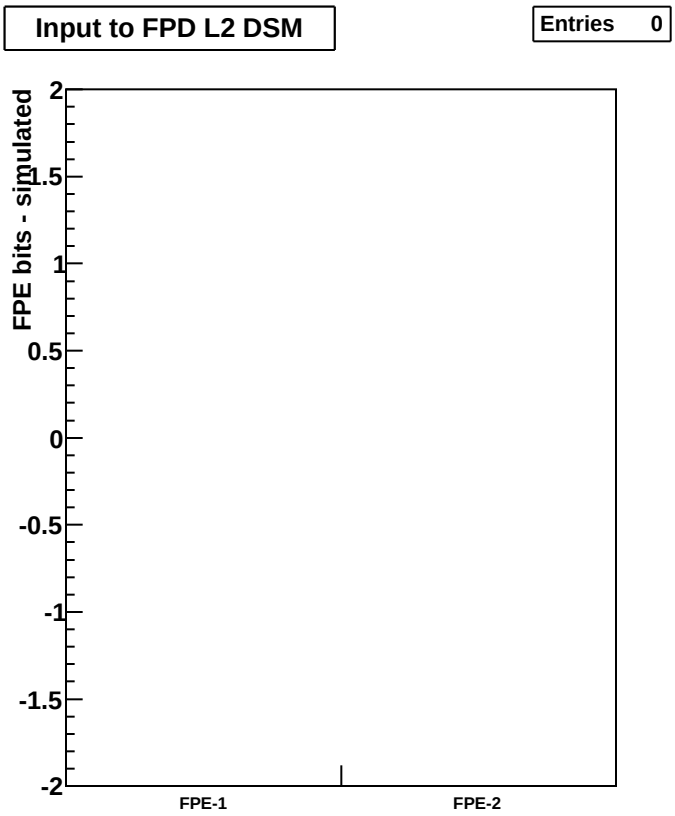

Input to FE003 QT board
Entries 44384
ADC
channel


Input to FE004 QT board
Entries 44384
ADC
channel


Input to FPE L1 DSM
Entries 5548
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 2774
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
130
141
1257
1246
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2