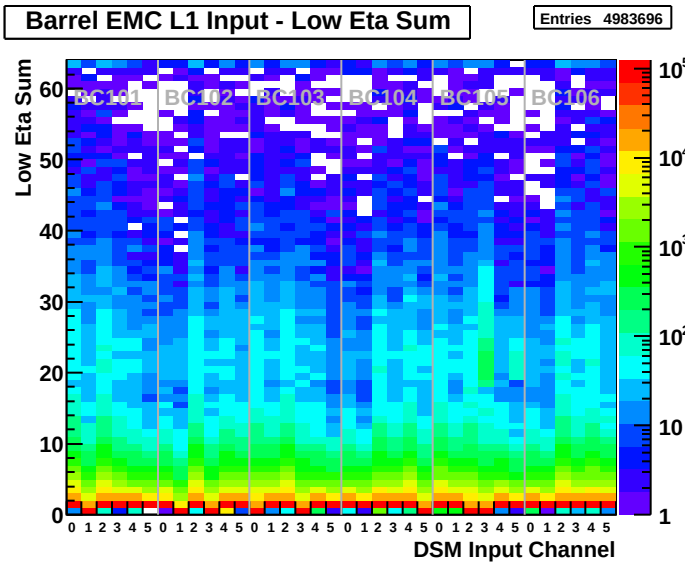
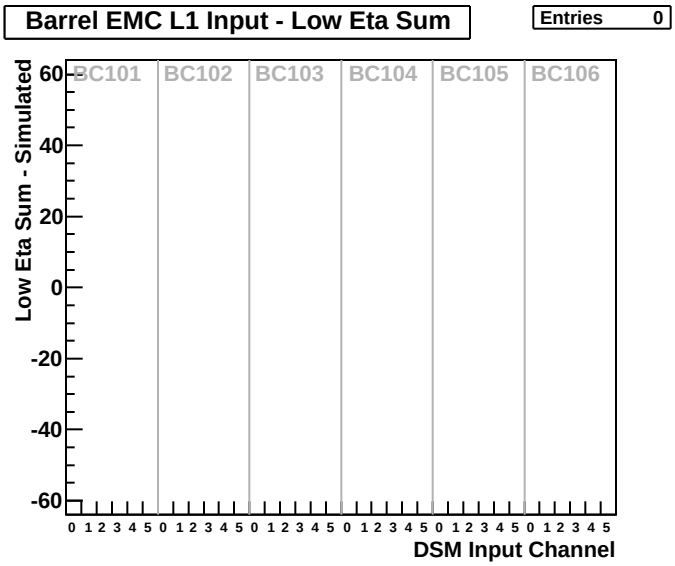
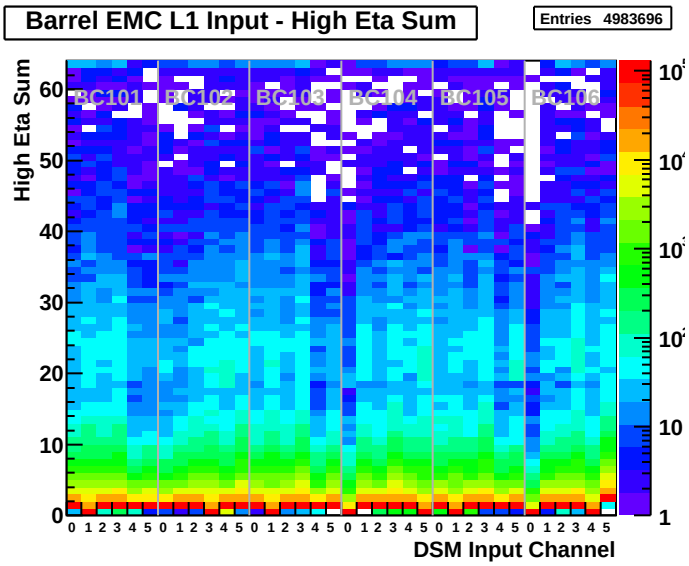
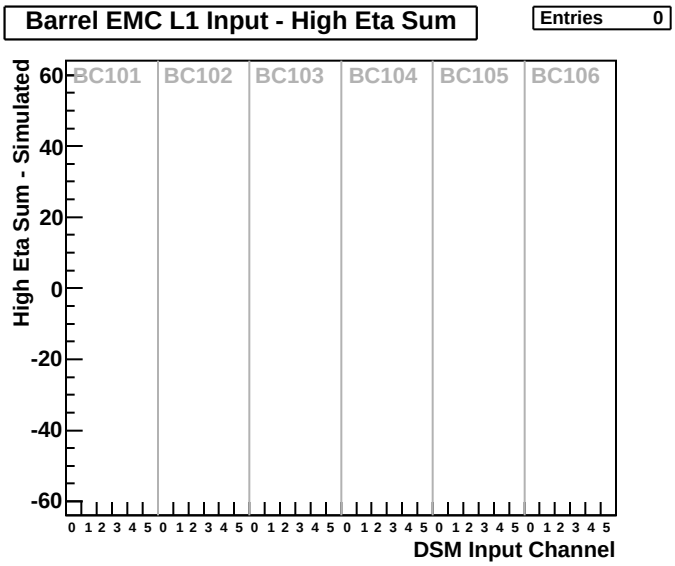
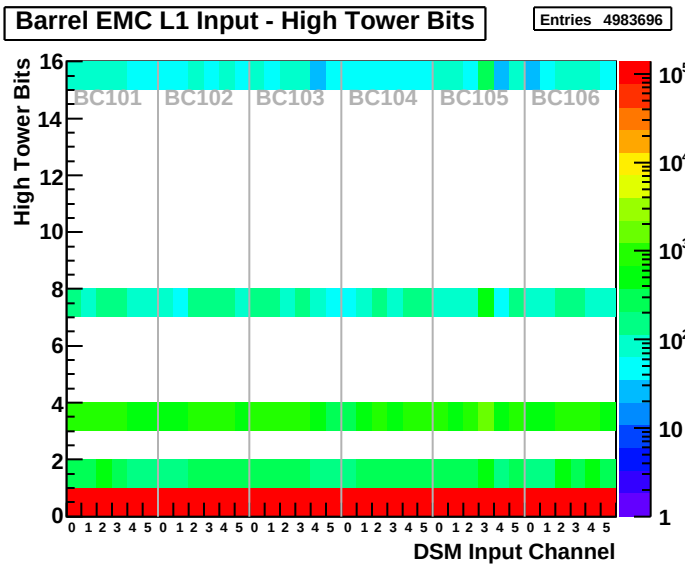
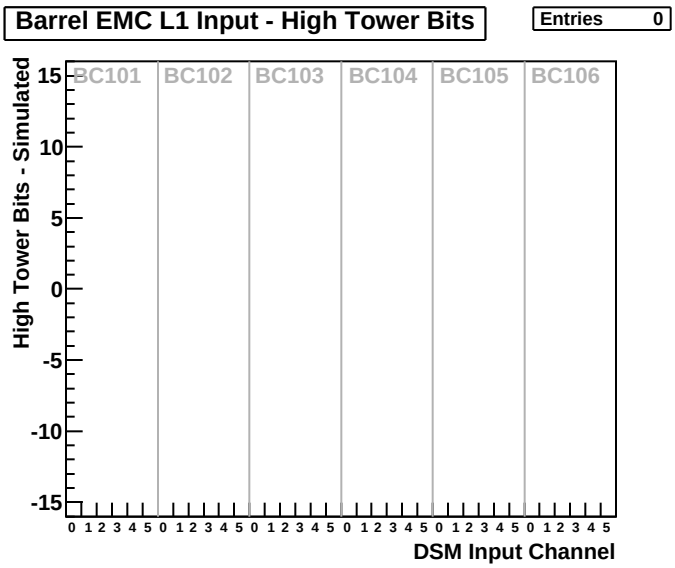

Barrel EMC L1 Input - Low Eta Sum
Entries 4983696
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

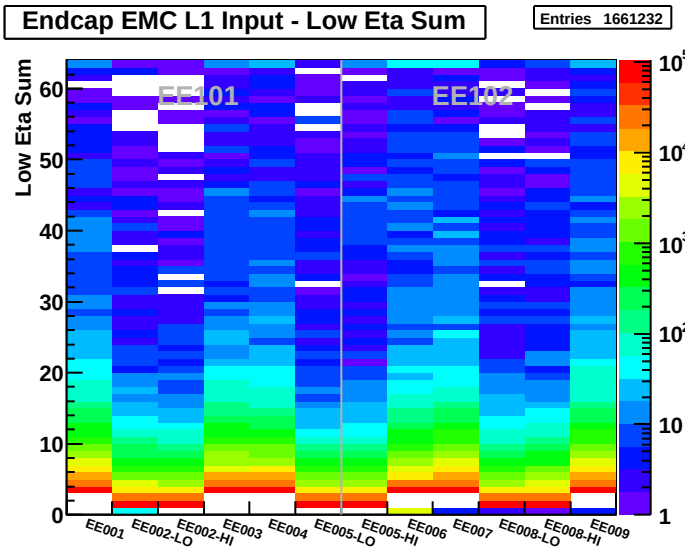

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

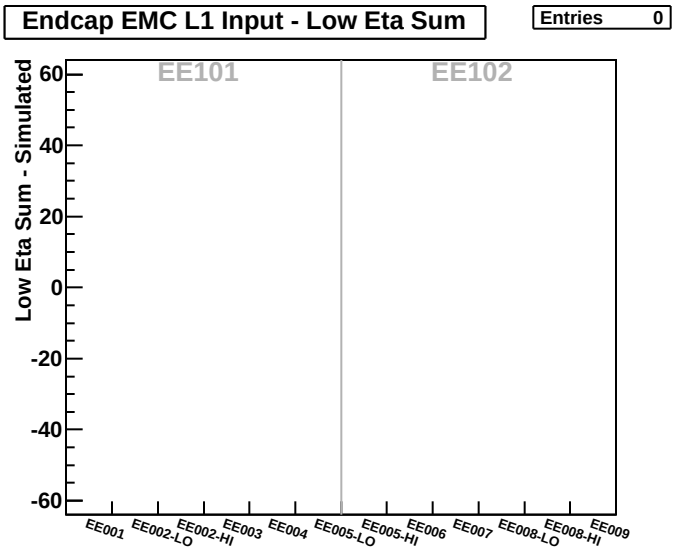

Barrel EMC L1 Input - High Eta Sum
Entries 4983696
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

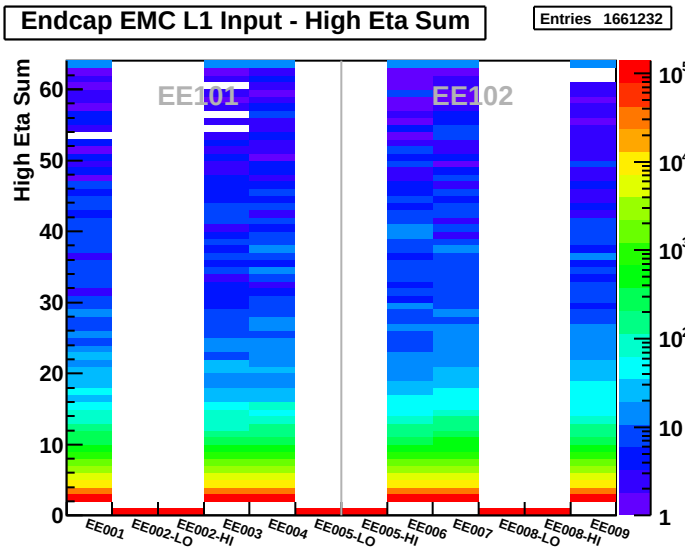

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

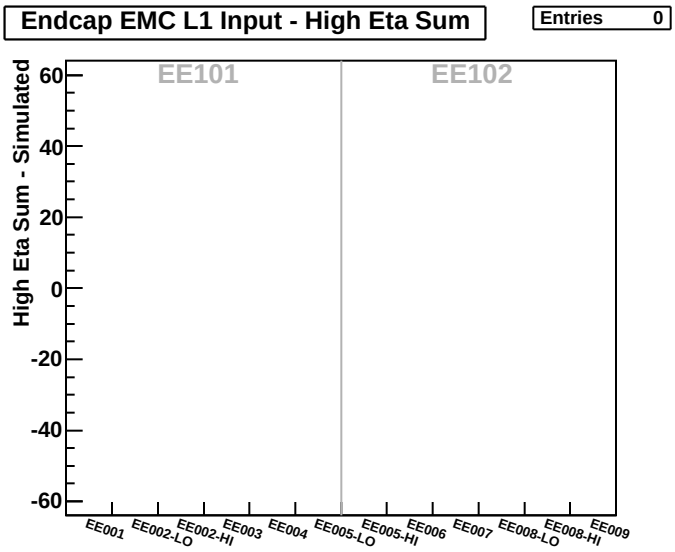

Barrel EMC L1 Input - High Tower Bits
Entries 4983696
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

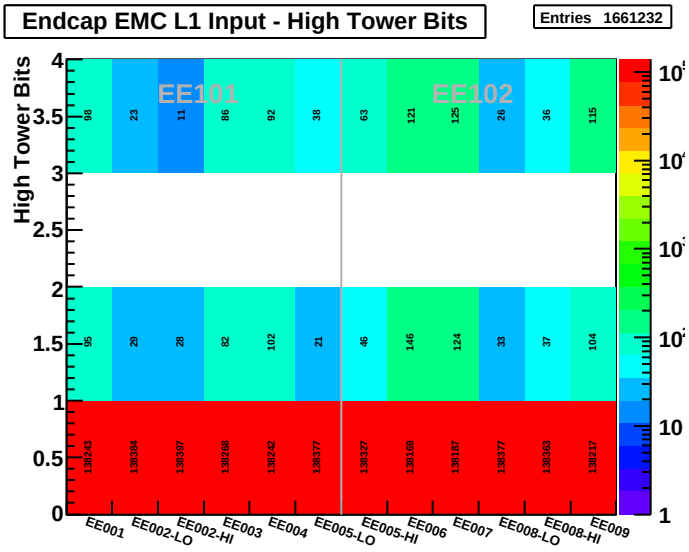

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

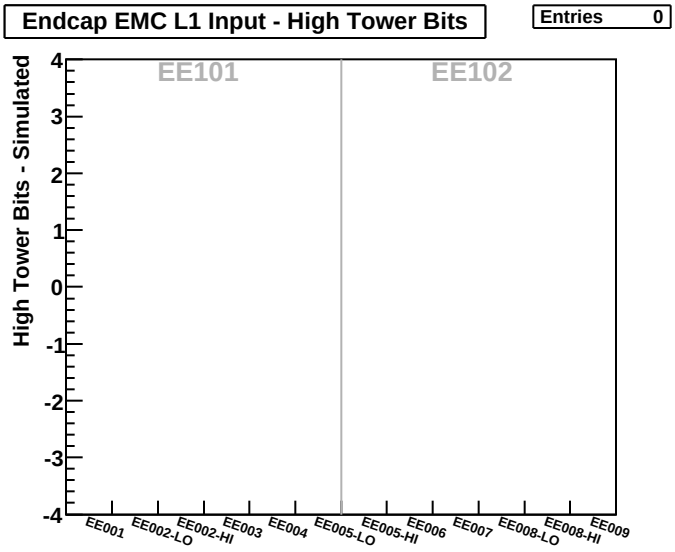

Endcap EMC L1 Input - Low Eta Sum
Entries 1661232
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

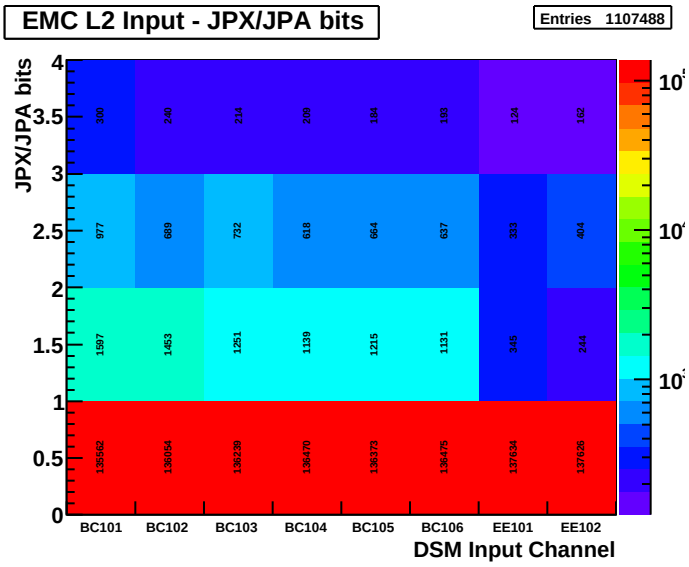

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

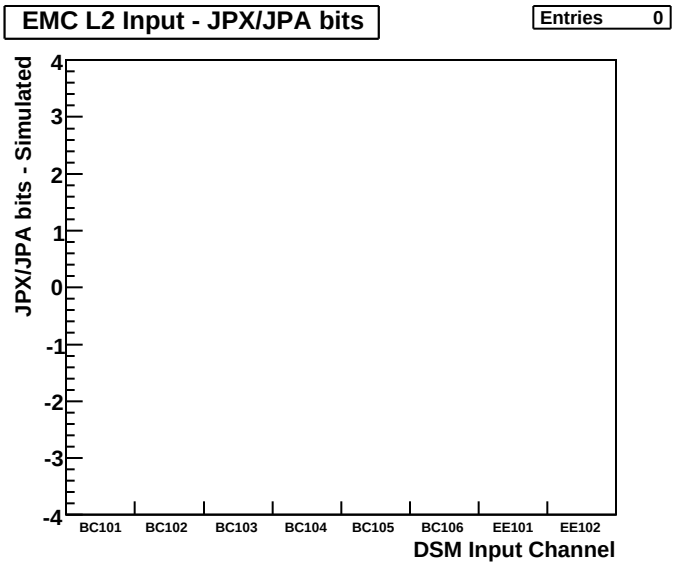

Endcap EMC L1 Input - High Eta Sum
Entries 1661232
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

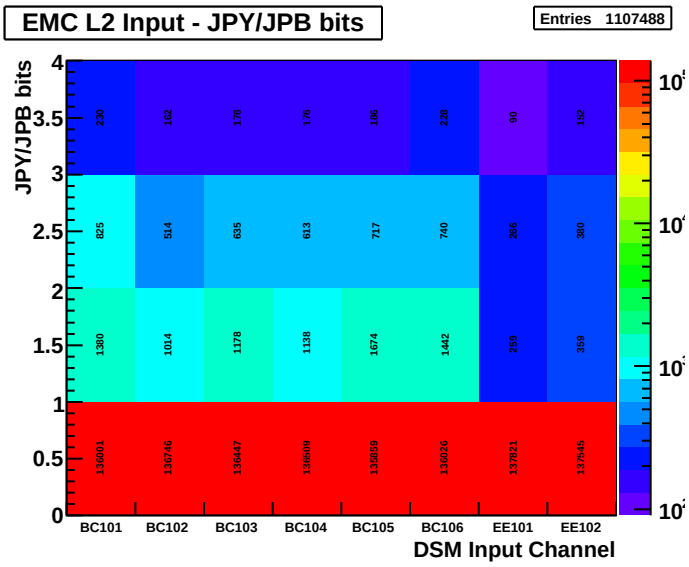

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

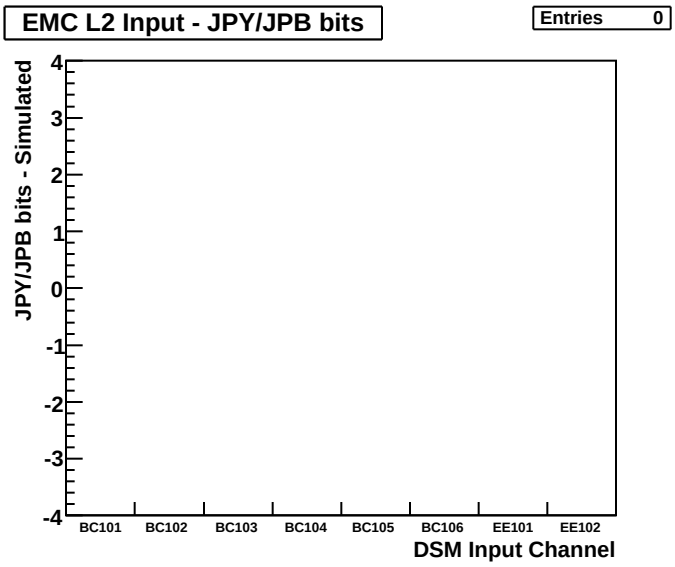

Endcap EMC L1 Input - High Tower Bits
Entries 1661232
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

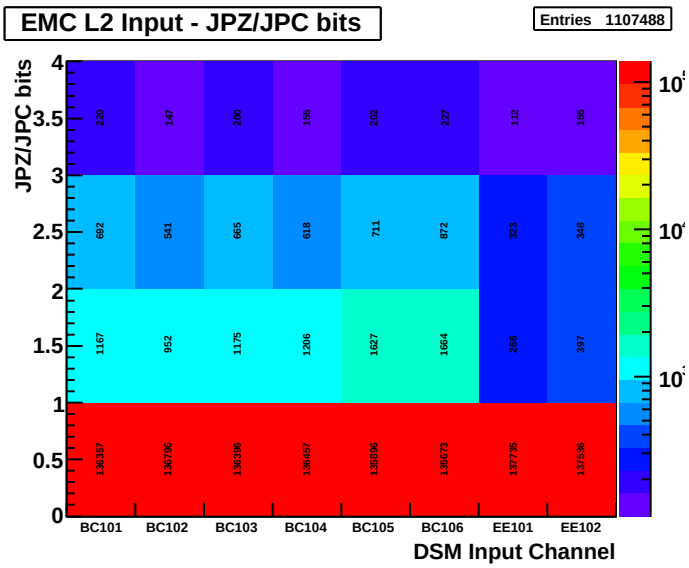

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

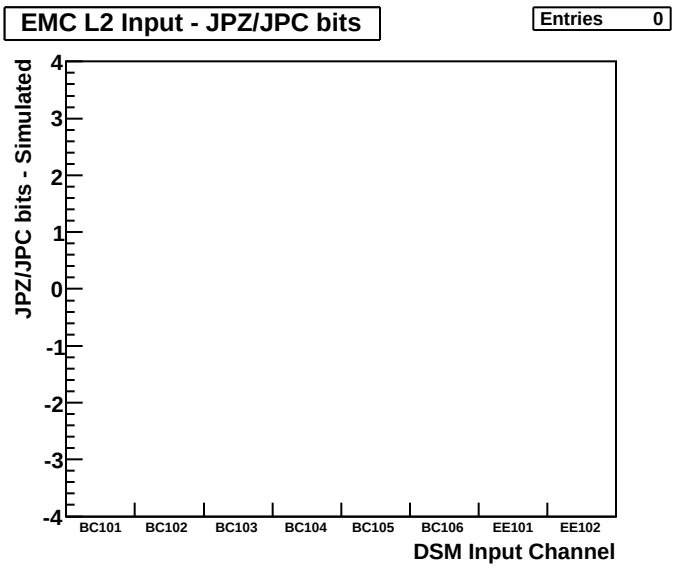

EMC L2 Input - JPX/JPA bits
Entries 1107488
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
300 240 214 209 184 193 124 162
977 689 732 618 664 637 333 404
1597 1453 1251 1139 1215 1131 345 244
135562 136054 136239 136470 136373 136475 137634 137626

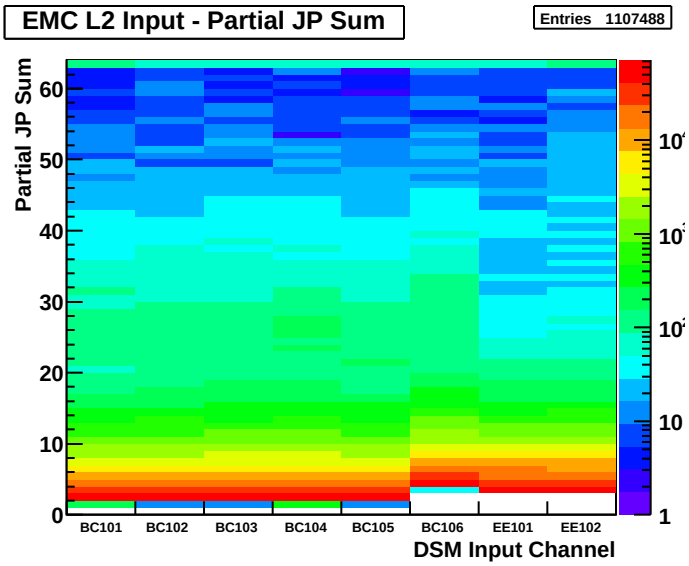

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

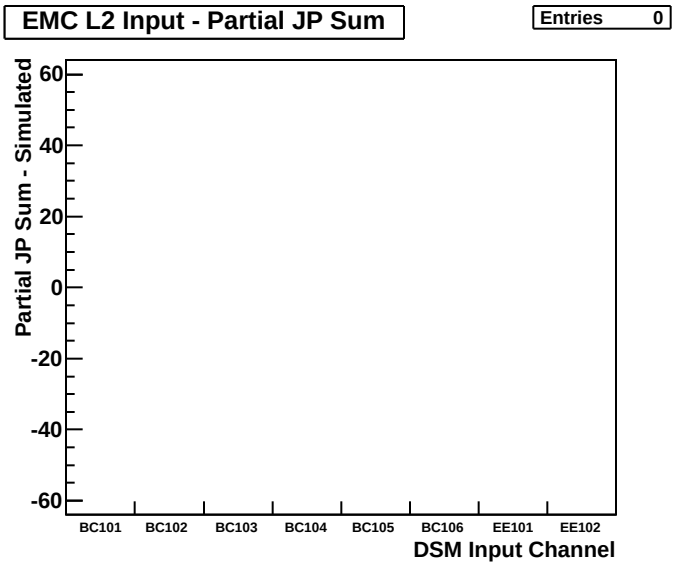

EMC L2 Input - JPY/JPB bits
Entries 1107488
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
230 162 176 176 186 228 90 152
825 514 635 613 717 740 266 380
1380 1014 1178 1138 1674 1442 259 359
136001 136746 136447 136509 135959 136026 137821 137545

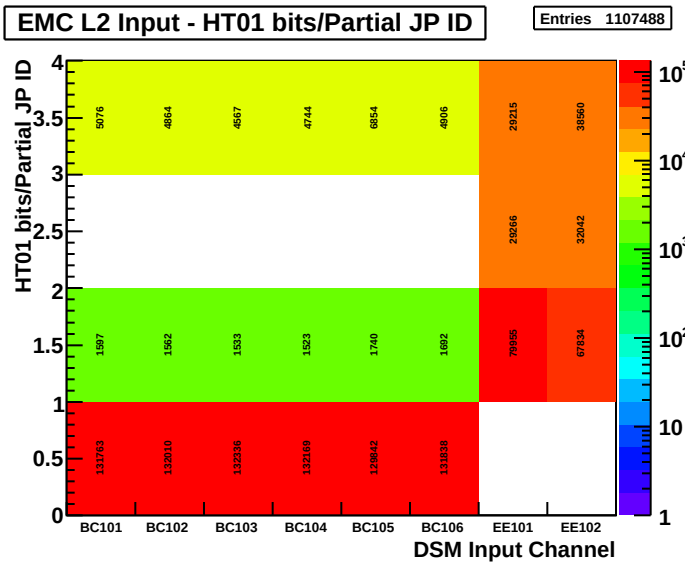

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

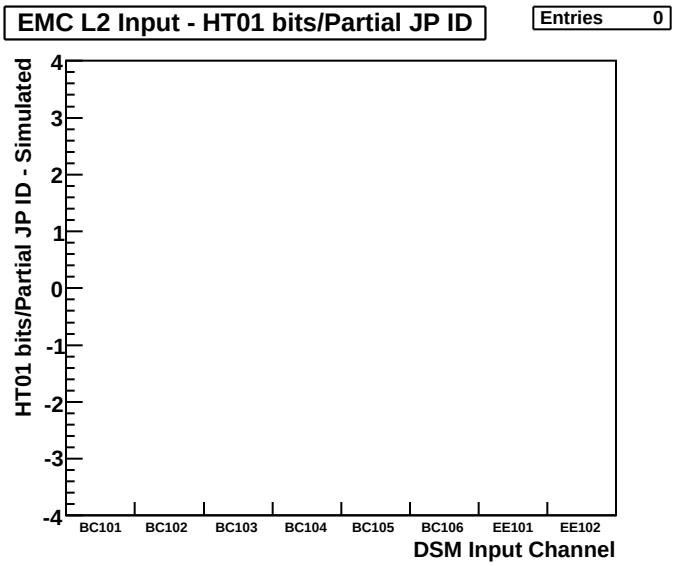

EMC L2 Input - JPZ/JPC bits
Entries 1107488
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
220 147 200 155 202 227 112 155
692 541 665 618 711 872 323 348
1167 952 1175 1206 1627 1664 266 397
136357 136796 136396 136457 135896 135673 137735 137536

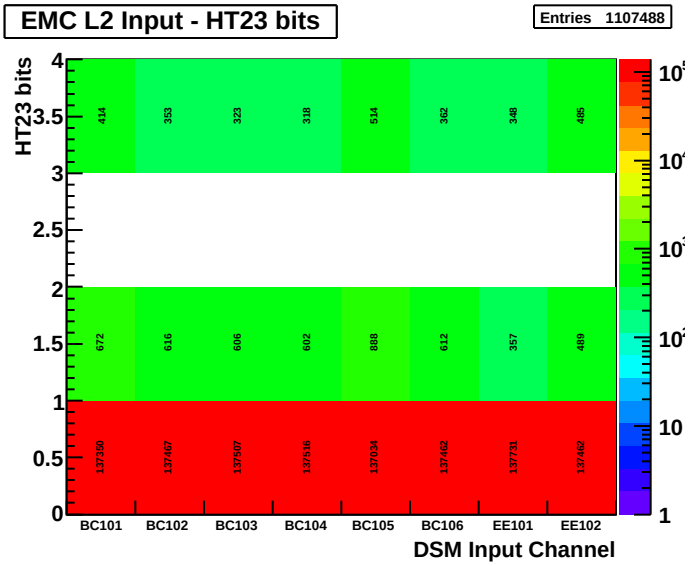

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

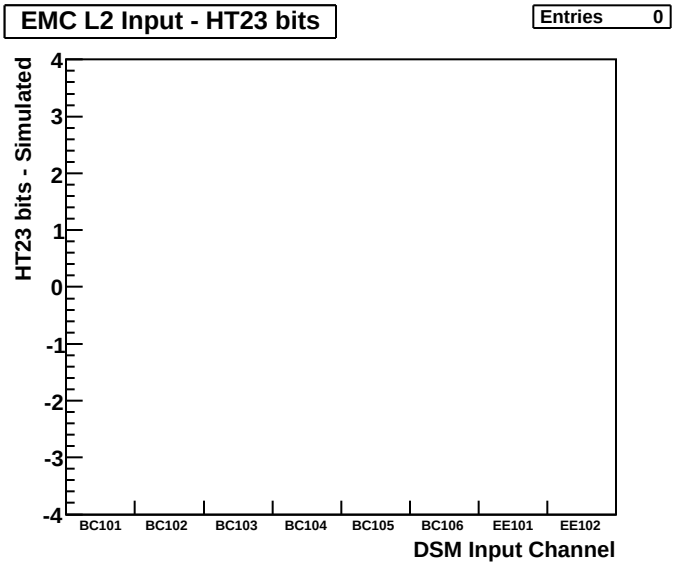

EMC L2 Input - Partial JP Sum
Entries 1107488
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

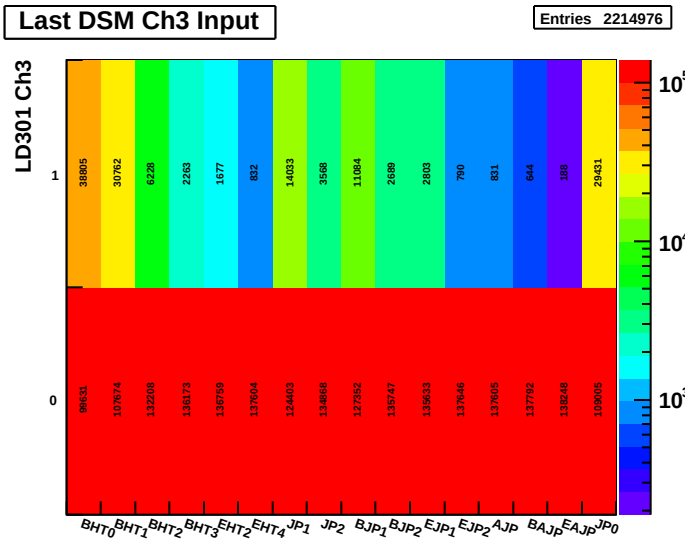

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

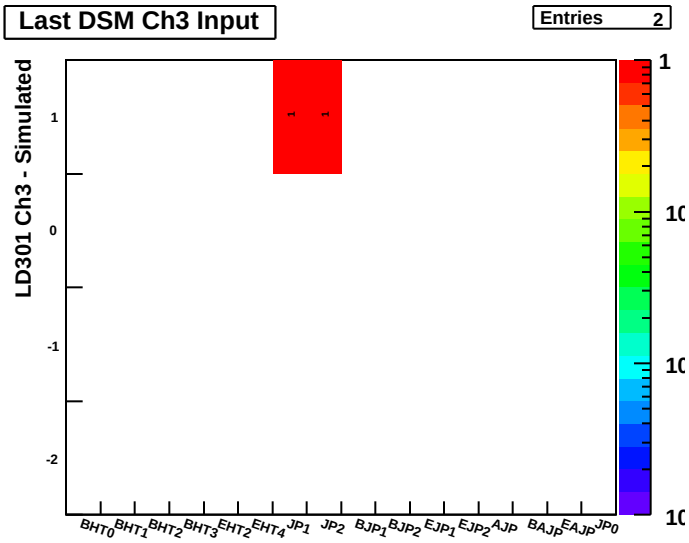

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1107488
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

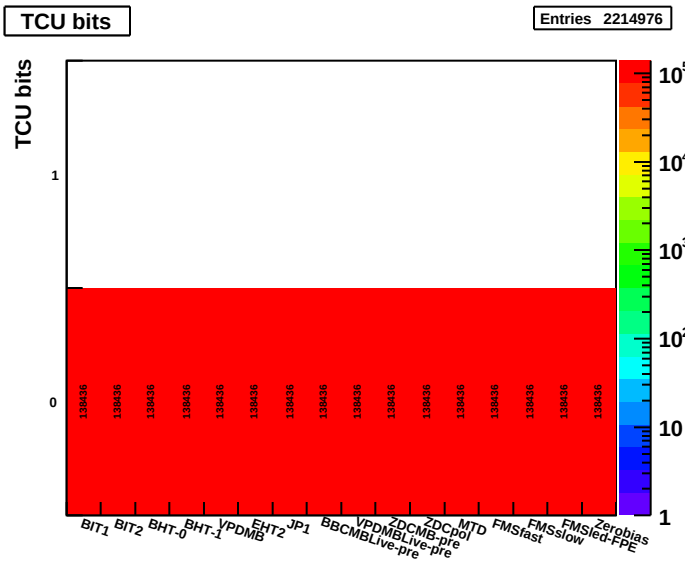

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

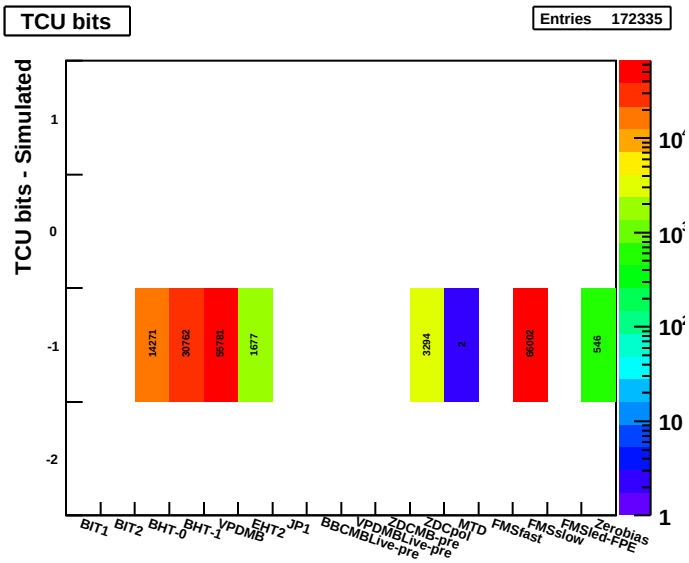

EMC L2 Input - HT23 bits
Entries 1107488
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

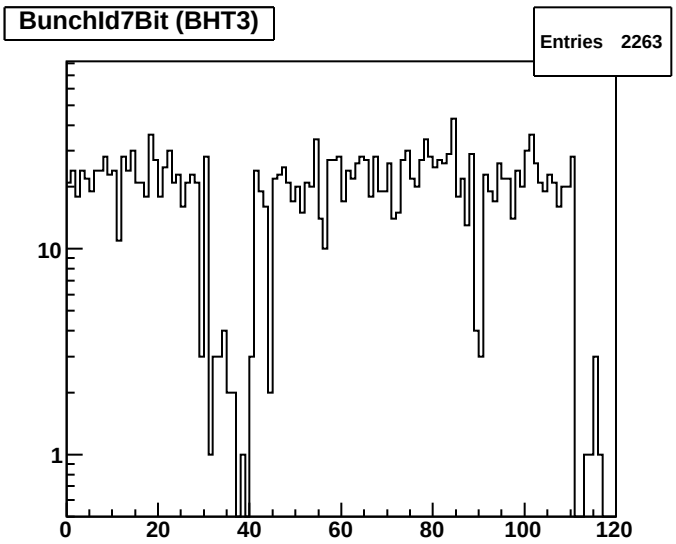

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

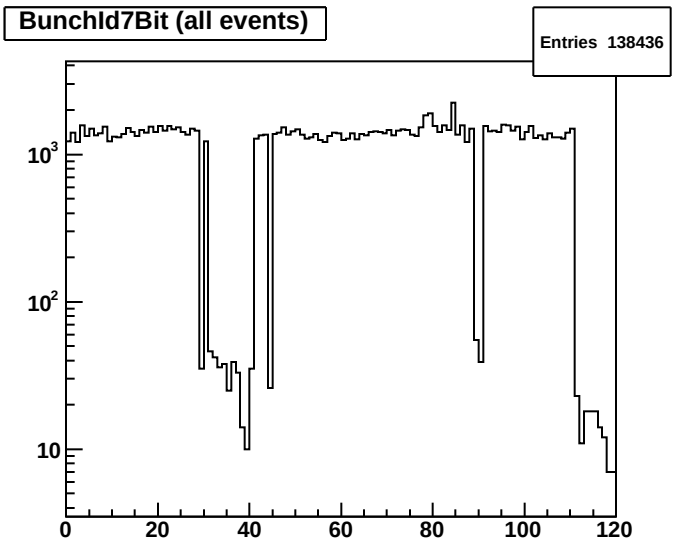

Last DSM Ch3 Input
Entries 2214976
LD301 Ch3
38805 30762 6228 2263 1677 832 14033 3568 11084 2689 2803 790 831 644 168 29431
99631 107674 132208 136173 136759 137604 124403 134868 127352 135747 135633 137646 137605 137792 138248 109005
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

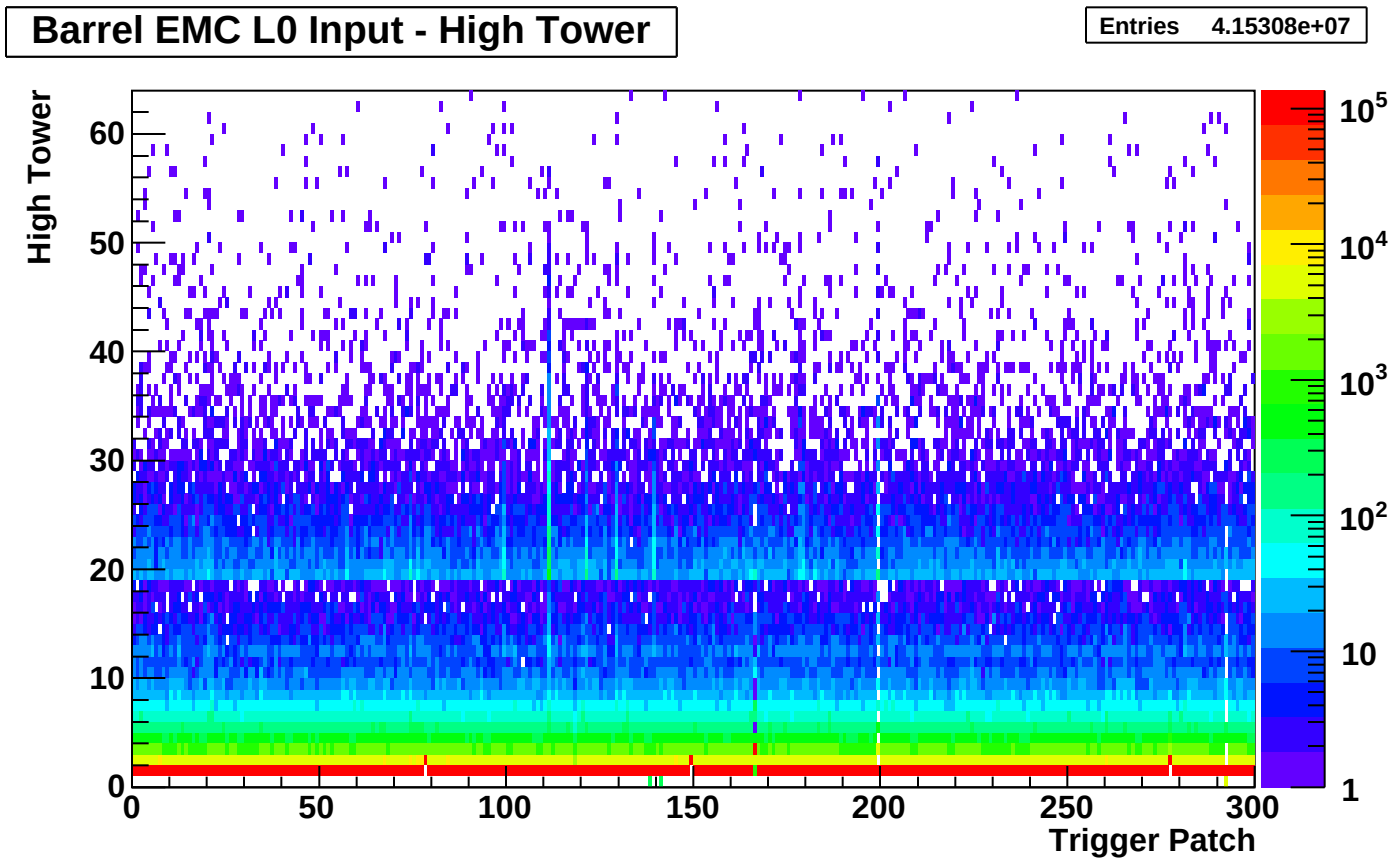

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
1 1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

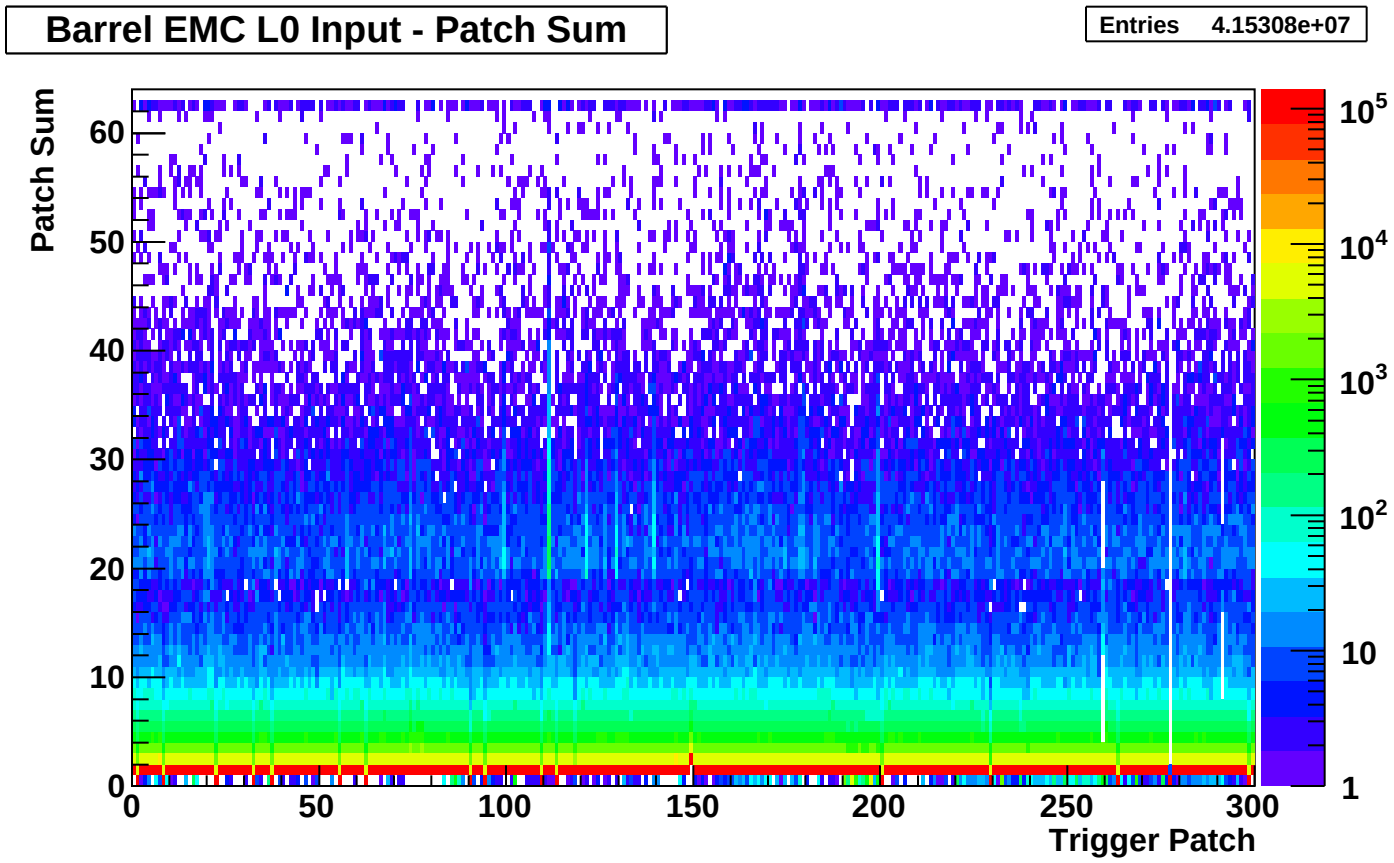

TCU bits
Entries 2214976
TCU bits
138436 138436 138436 138436 138436 138436 138436 138436 138436 138436 138436 138436 138436 138436 138436 138436
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

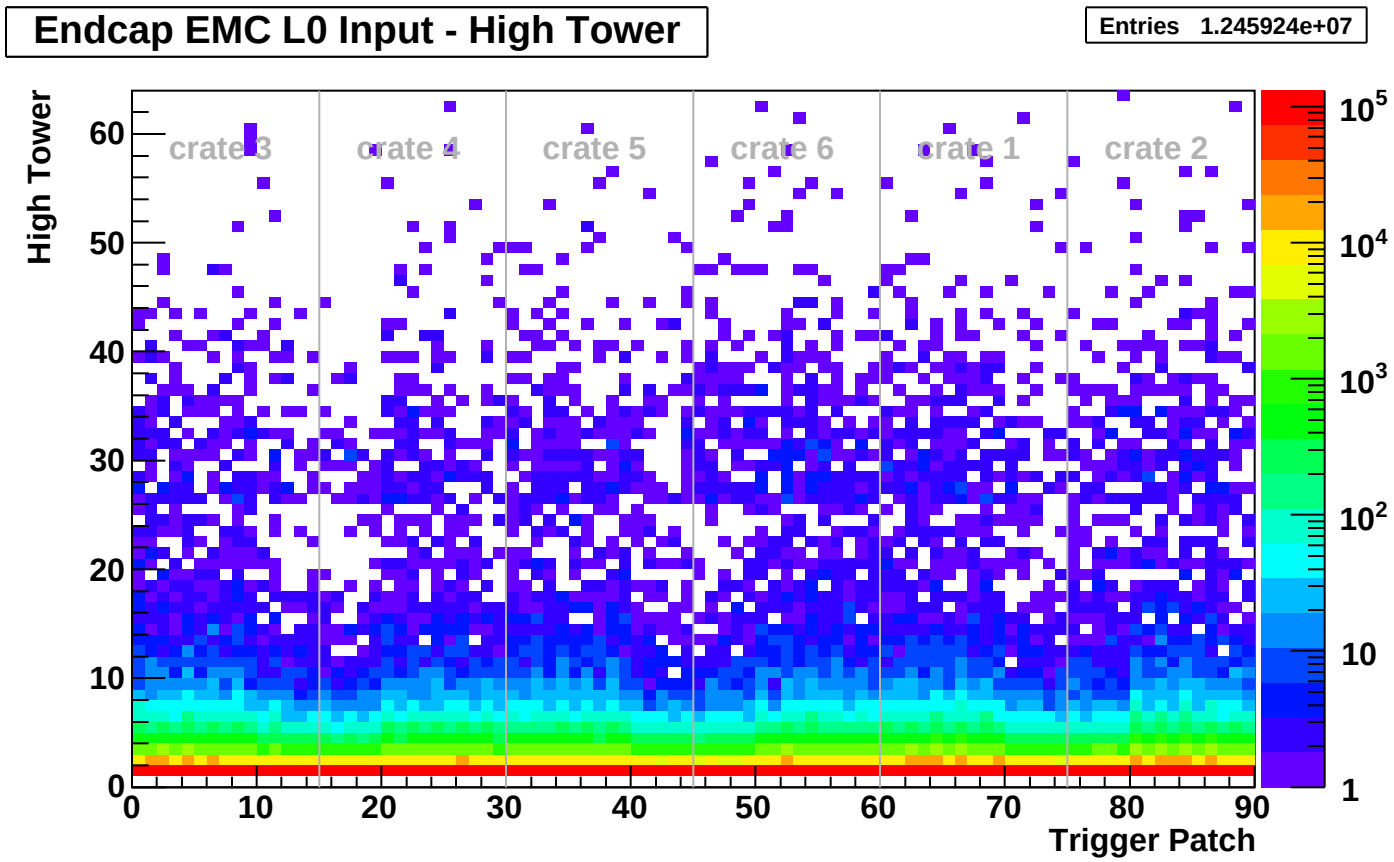

TCU bits
Entries 172335
TCU bits - Simulated
14271 30762 55781 1677 3294 2 66002 546
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

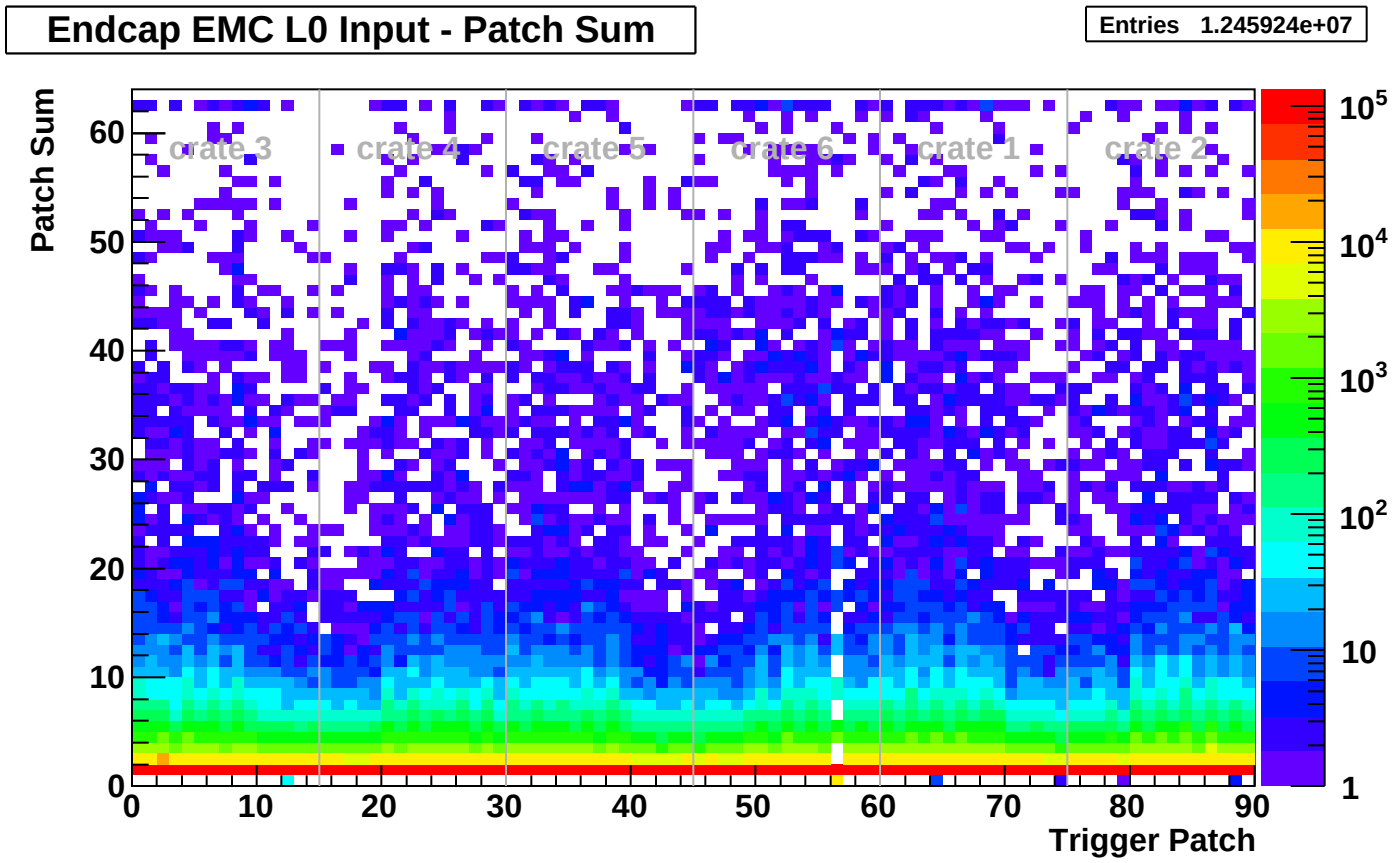

BunchId7Bit (BHT3)
Entries 2263

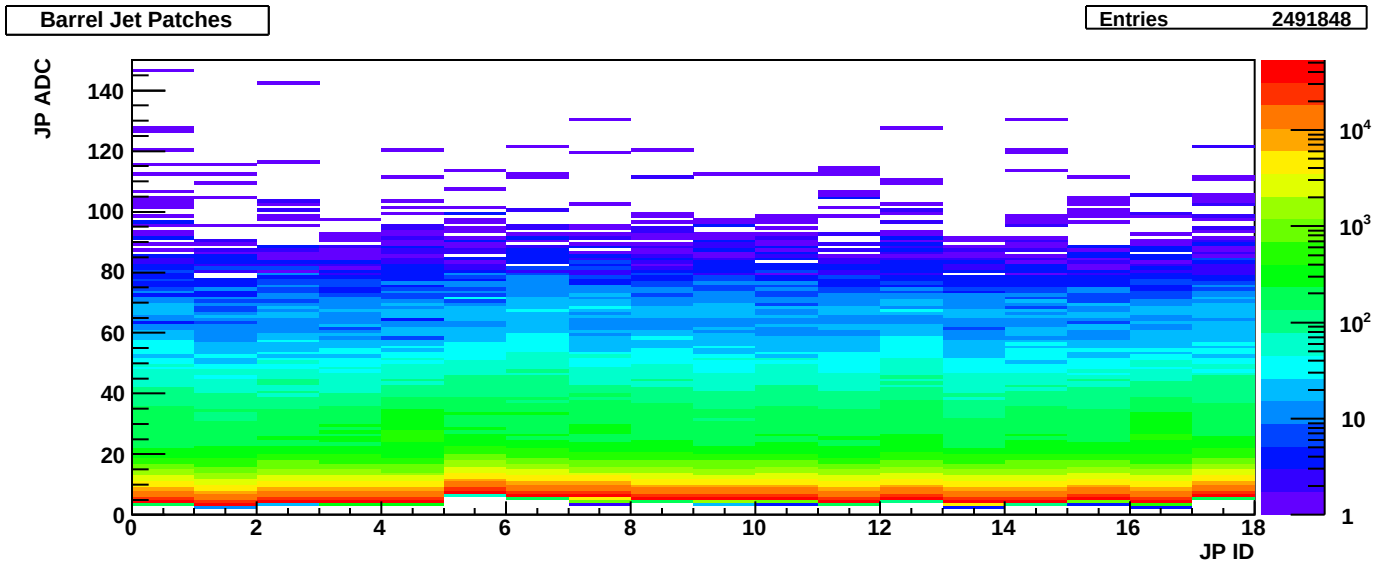

BunchId7Bit (all events)
Entries 138436

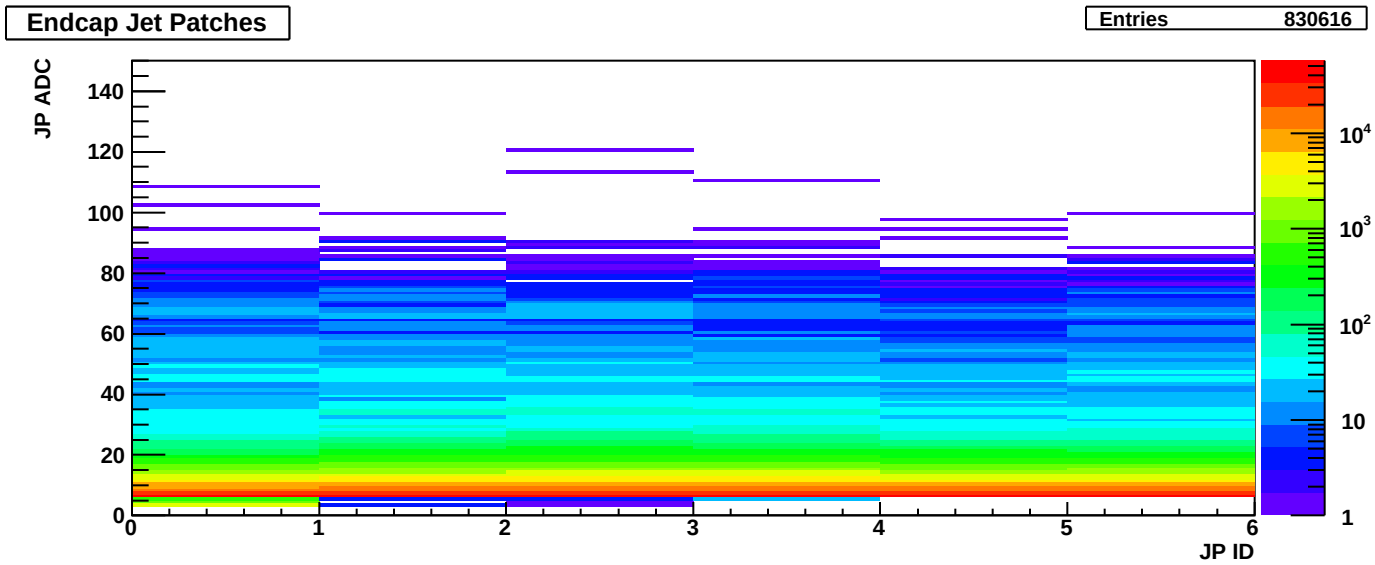

Barrel EMC L0 Input - High Tower
Entries 4.15308e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

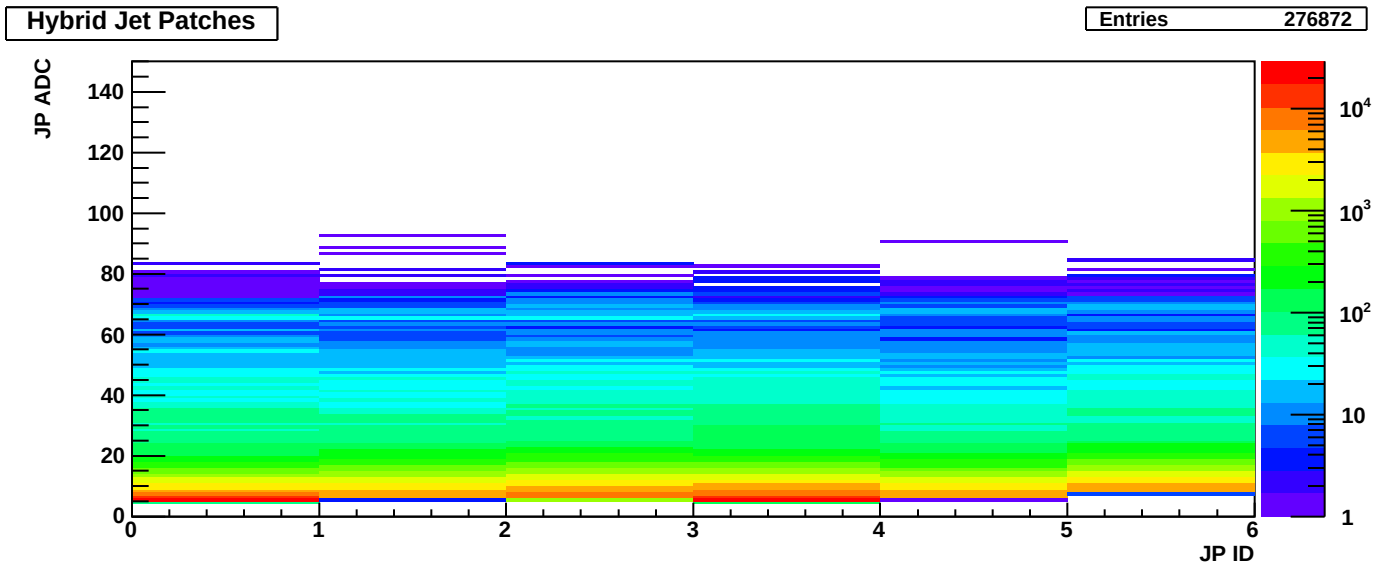

Barrel EMC L0 Input - Patch Sum
Entries 4.15308e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

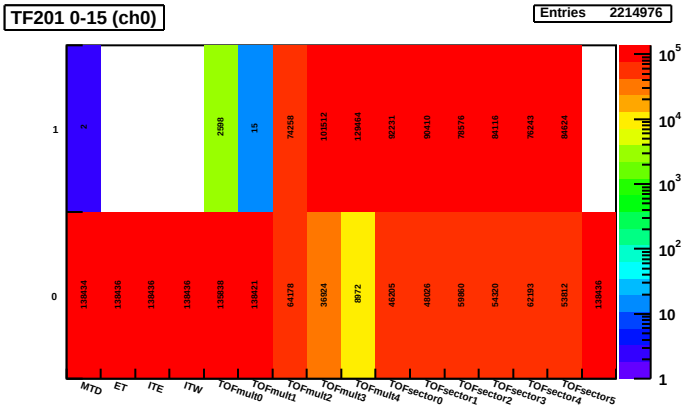

Endcap EMC L0 Input - High Tower
Entries 1.245924e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

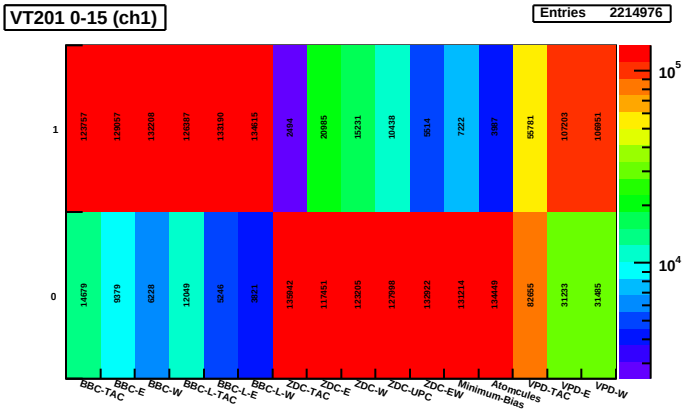

Endcap EMC L0 Input - Patch Sum
Entries 1.245924e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

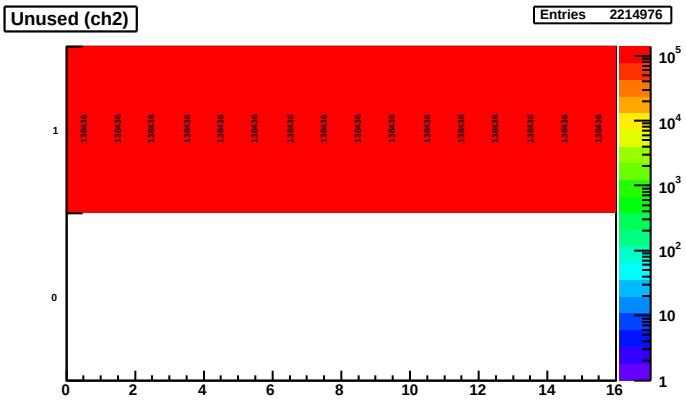

Barrel Jet Patches
Entries 2491848
JP ADC
JP ID

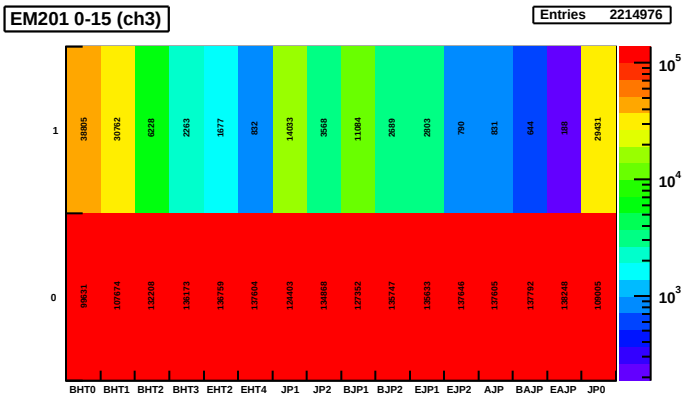

Endcap Jet Patches
Entries 830616
JP ADC
JP ID

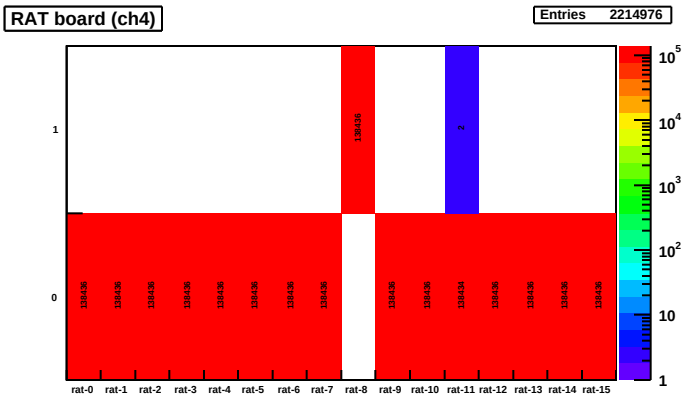

Hybrid Jet Patches
Entries 276872
JP ADC
JP ID

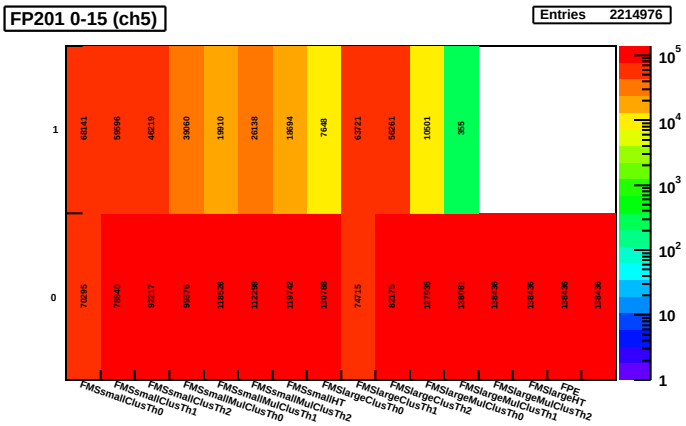

TF201 0-15 (ch0)
Entries 2214976
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

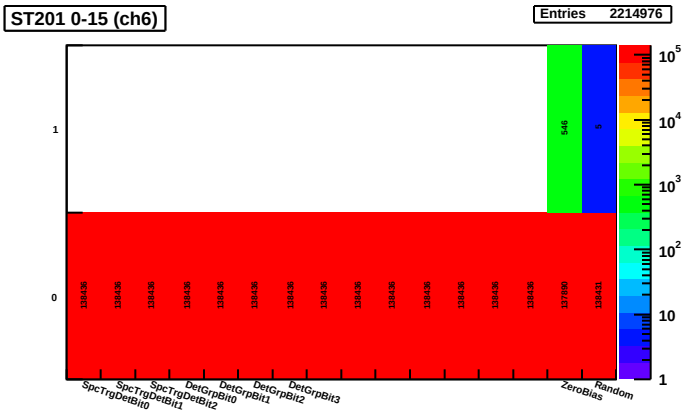

VT201 0-15 (ch1)
Entries 2214976
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

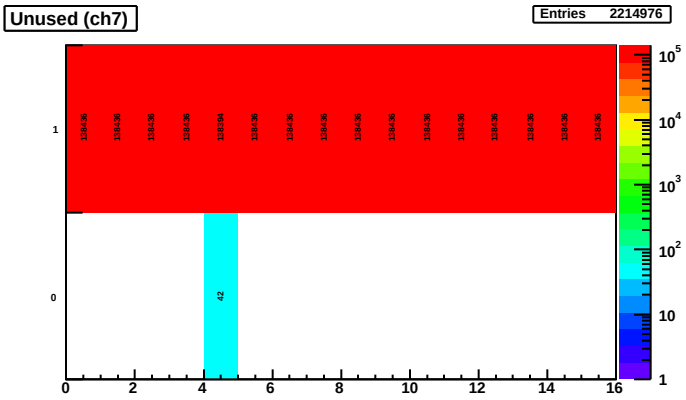

Unused (ch2)
Entries 2214976

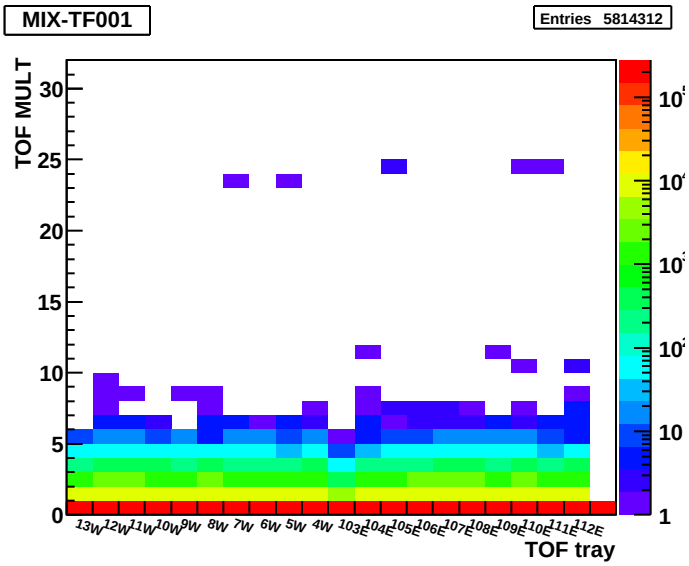

EM201 0-15 (ch3)
Entries 2214976
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

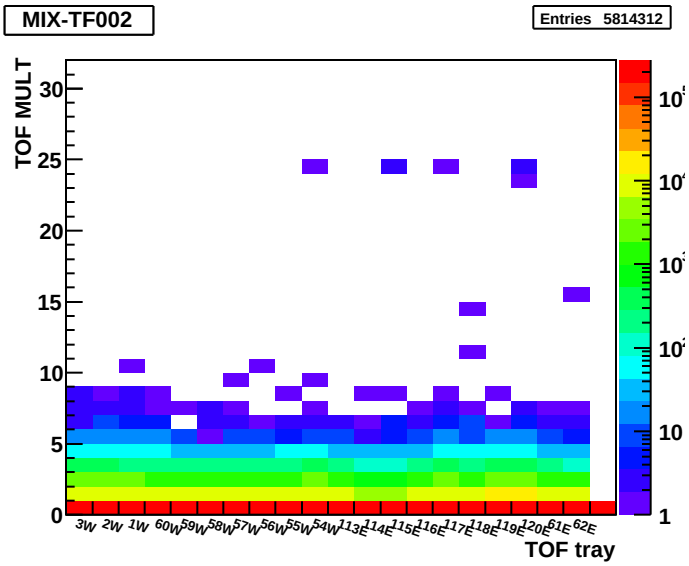

RAT board (ch4)
Entries 2214976
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

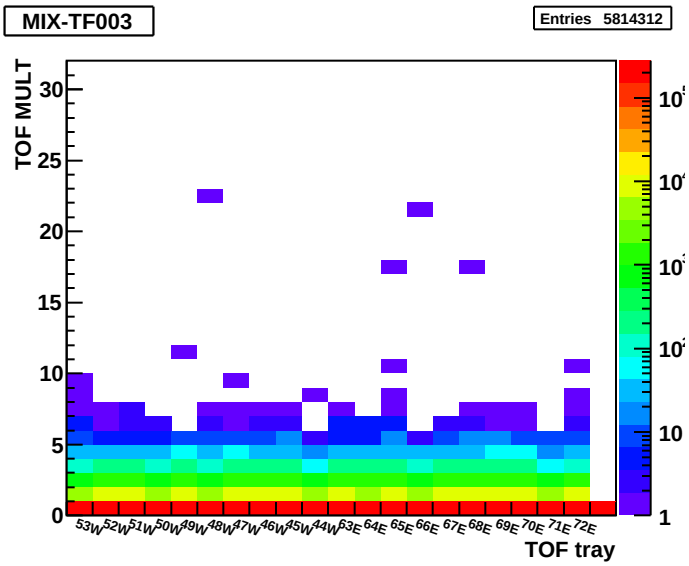

FP201 0-15 (ch5)
Entries 2214976
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

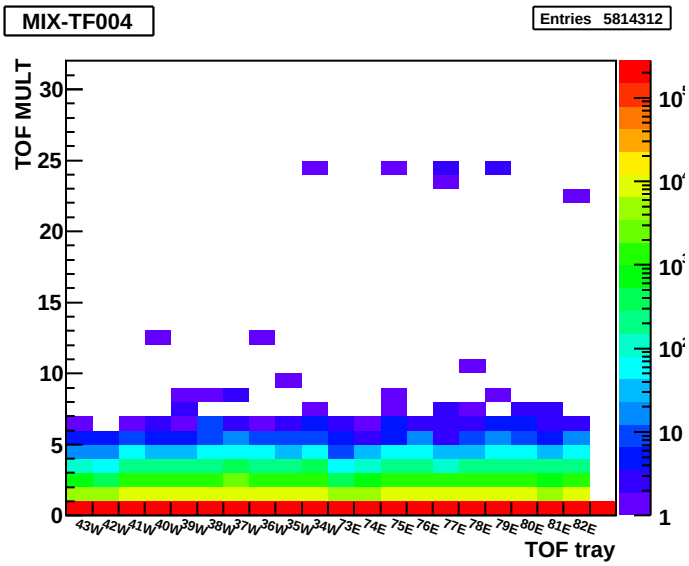

ST201 0-15 (ch6)
Entries 2214976
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

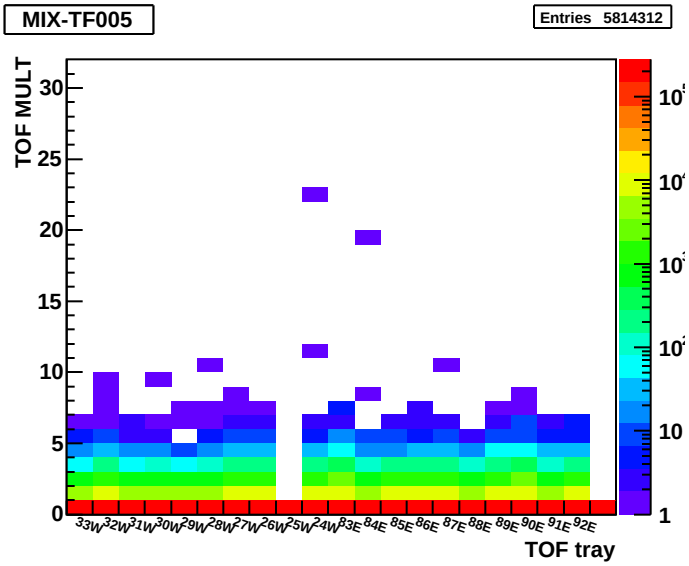

Unused (ch7)
Entries 2214976

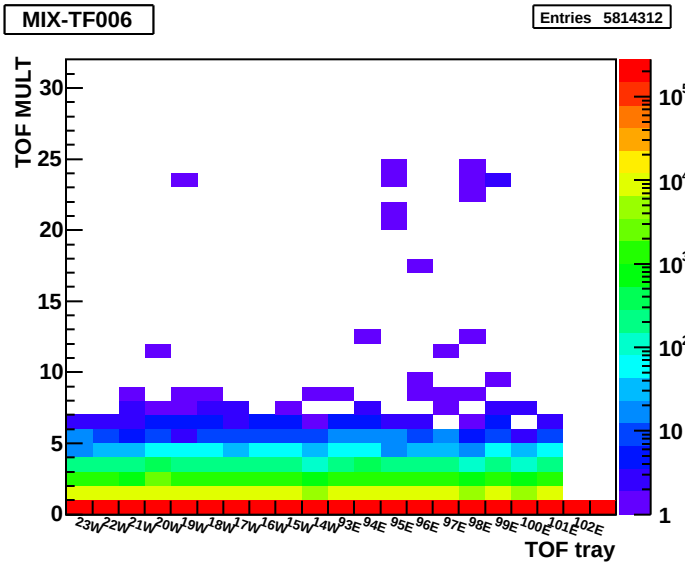

MIX-TF001
Entries 5814312
TOF MULT
TOF tray

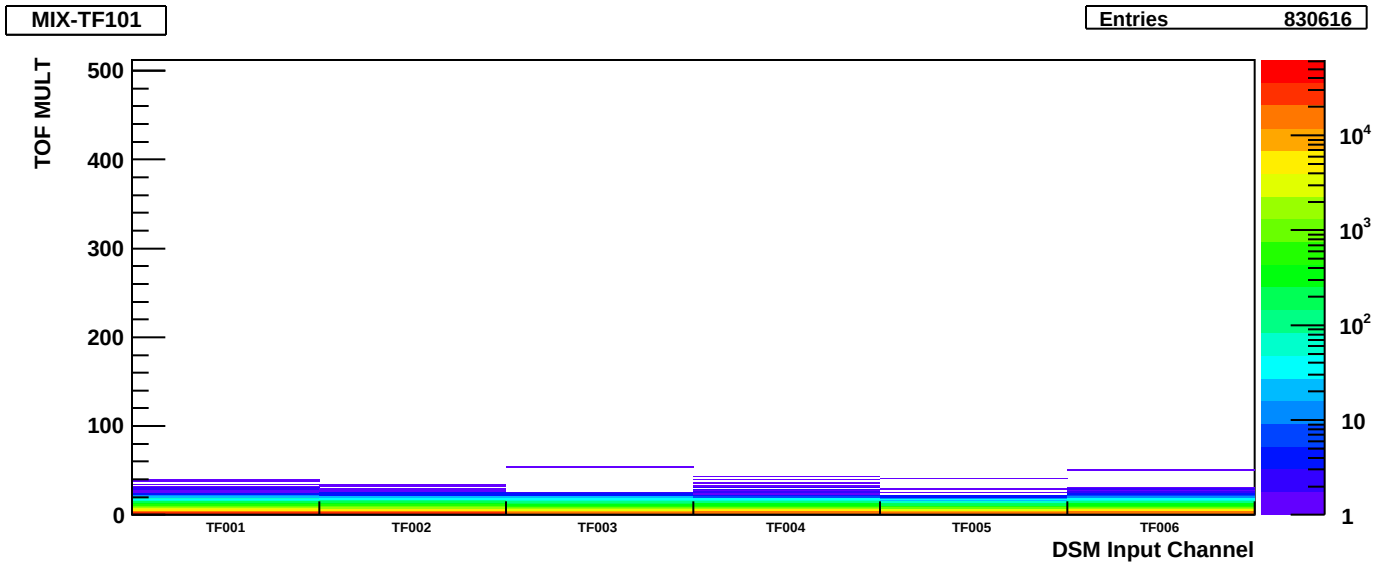

MIX-TF002
Entries 5814312
TOF MULT
TOF tray

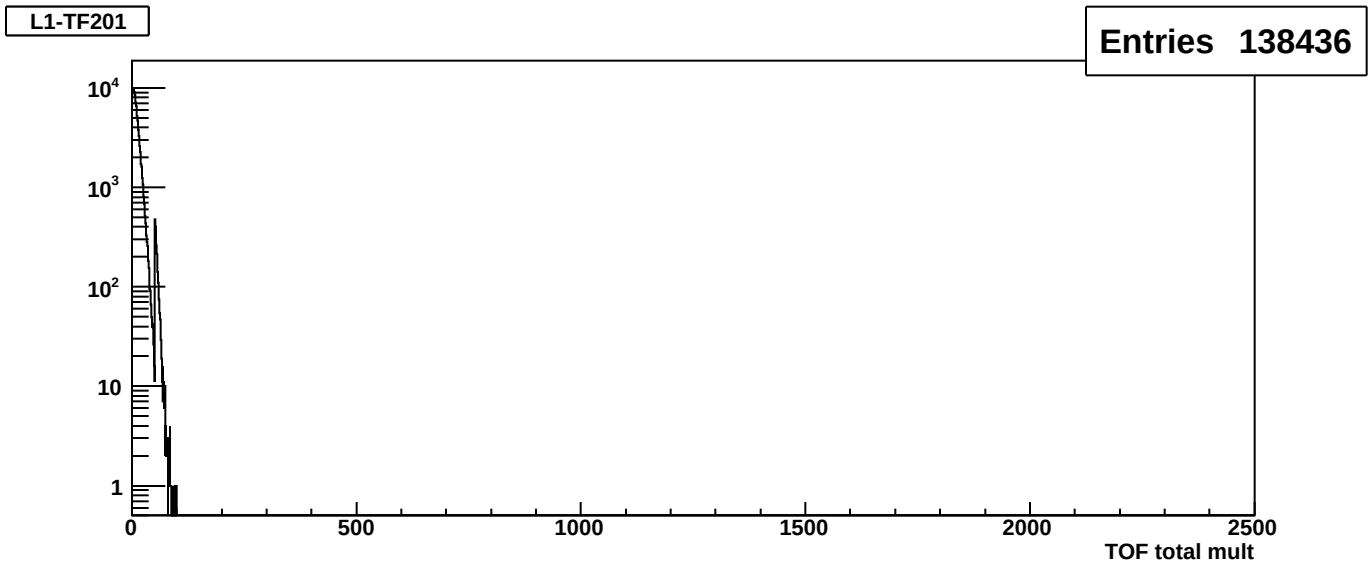

MIX-TF003
Entries 5814312
TOF MULT
TOF tray

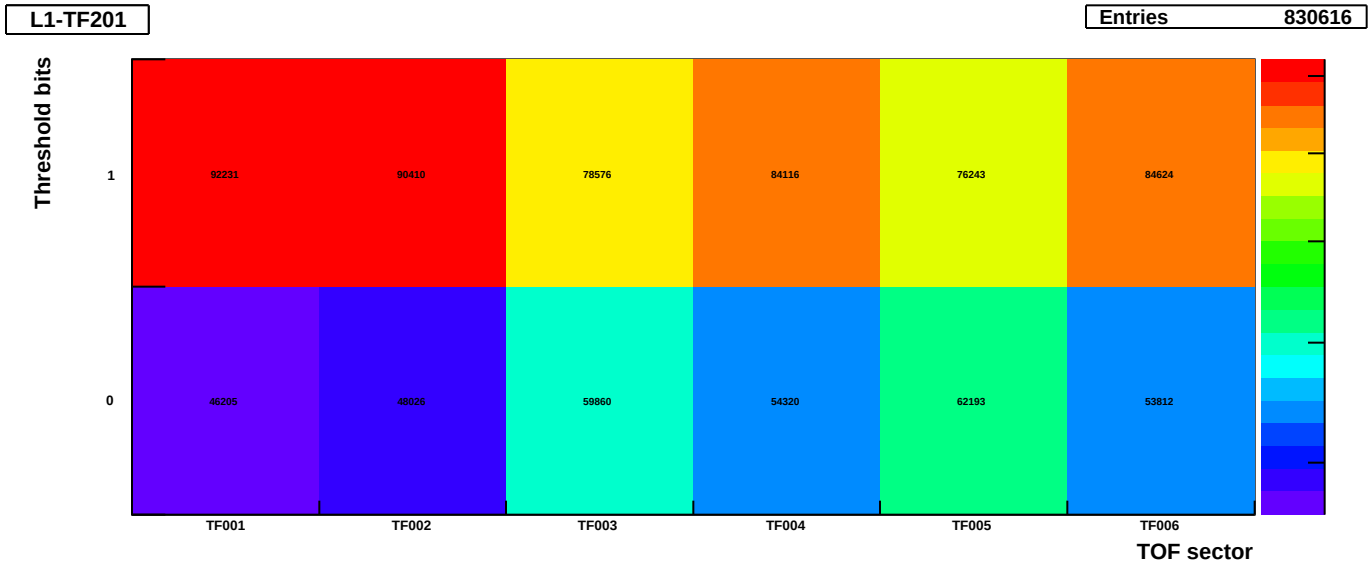

MIX-TF004
Entries 5814312
TOF MULT
TOF tray

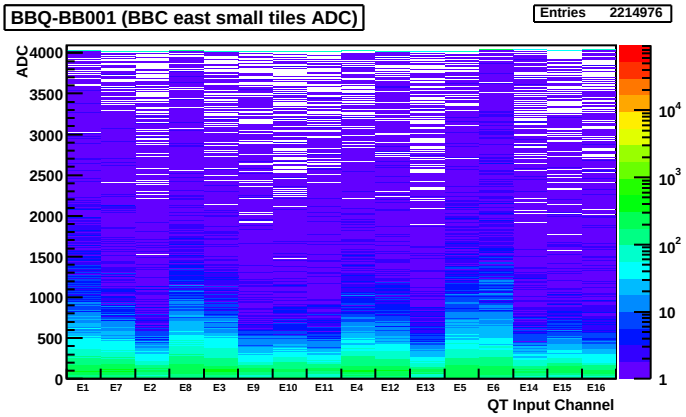

MIX-TF005
Entries 5814312
TOF MULT
TOF tray

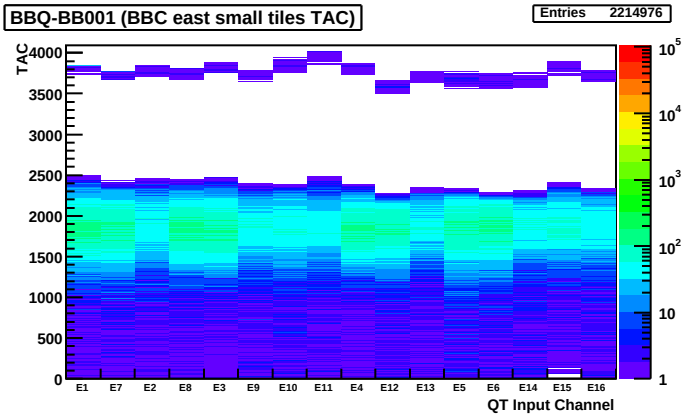

MIX-TF006
Entries 5814312
TOF MULT
TOF tray

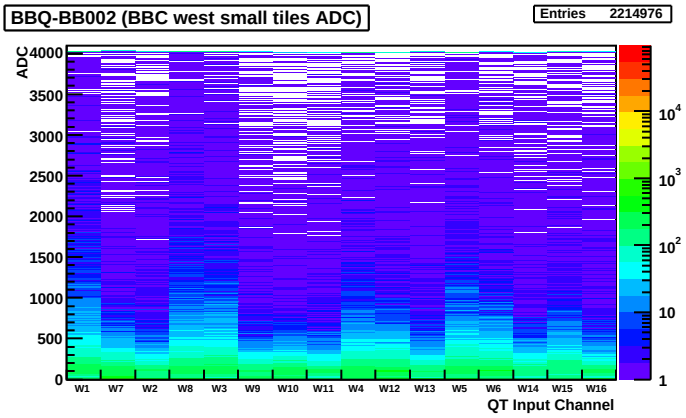

MIX-TF101
Entries 830616
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

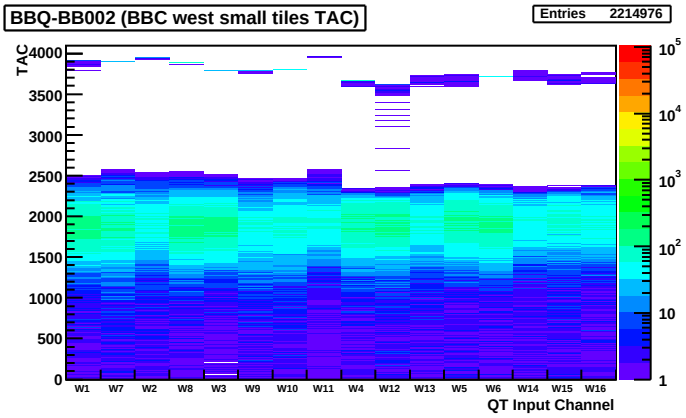

L1-TF201
Entries 138436
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

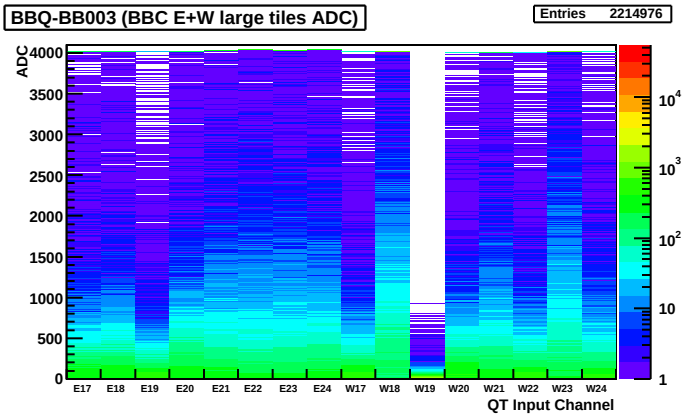

L1-TF201
Entries 830616
Threshold bits
1
0
92231
90410
78576
84116
76243
84624
46205
48026
59860
54320
62193
53812
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

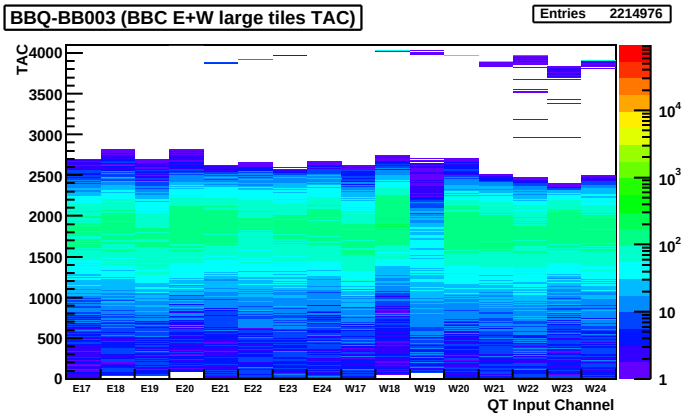

BBQ-BB001 (BBC east small tiles ADC)
Entries 2214976
ADC
QT Input Channel

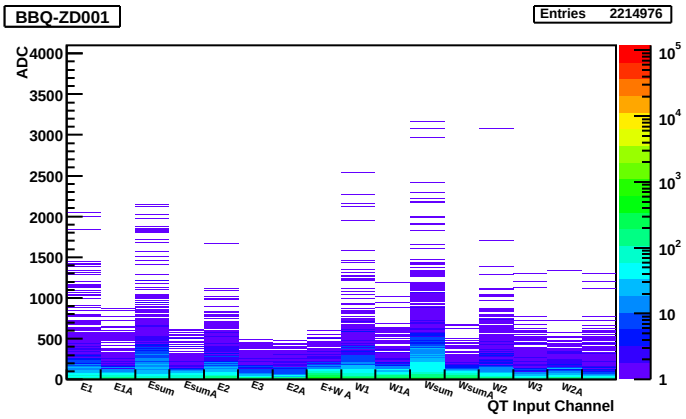

BBQ-BB001 (BBC east small tiles TAC)
Entries 2214976
TAC
QT Input Channel

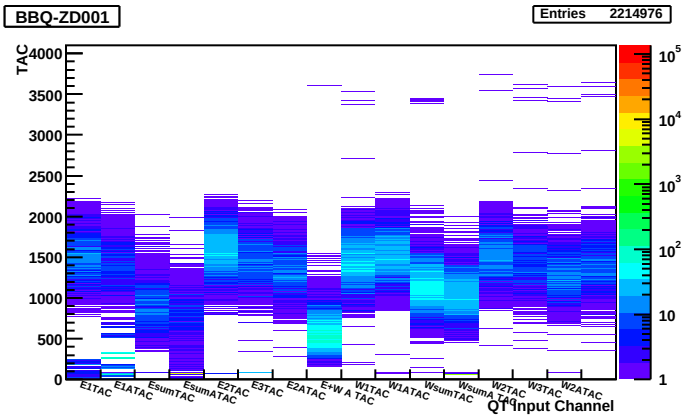

BBQ-BB002 (BBC west small tiles ADC)
Entries 2214976
ADC
QT Input Channel

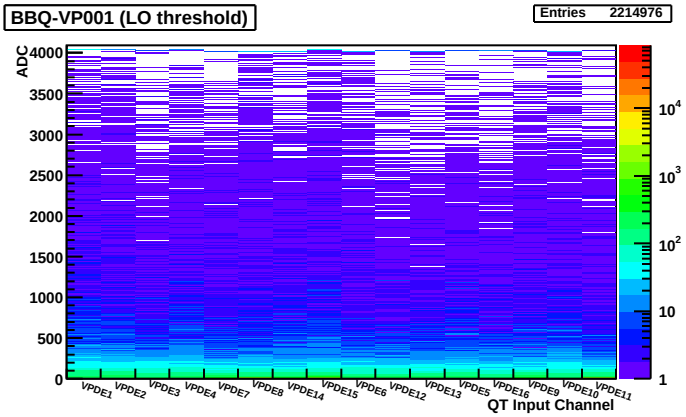

BBQ-BB002 (BBC west small tiles TAC)
Entries 2214976
TAC
QT Input Channel

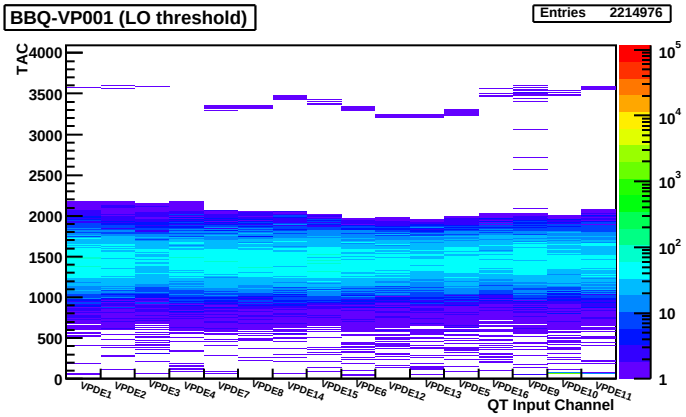

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 2214976
ADC
QT Input Channel

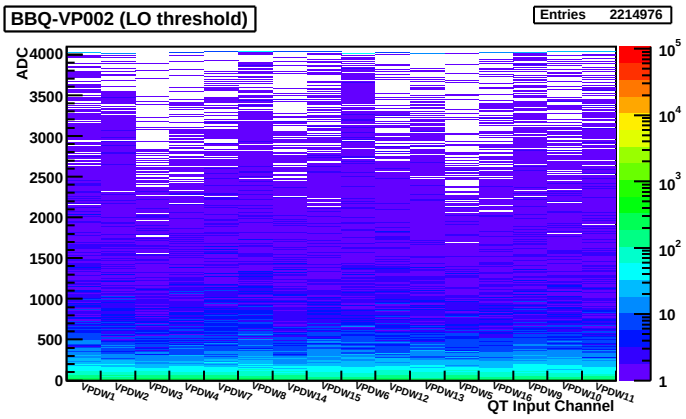

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 2214976
TAC
QT Input Channel

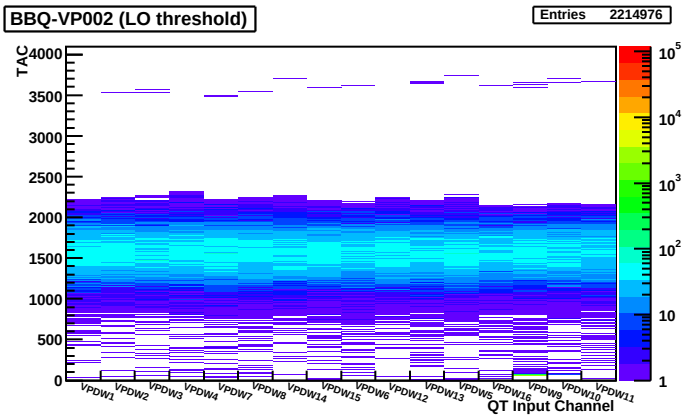

BBQ-ZD001
Entries 2214976
ADC
QT Input Channel

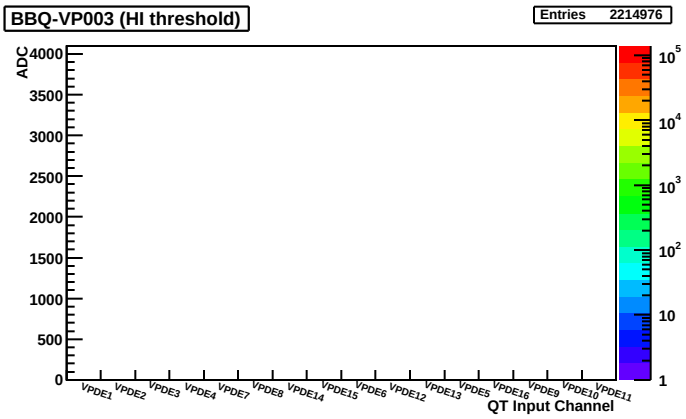

BBQ-ZD001
Entries 2214976
TAC
QT Input Channel

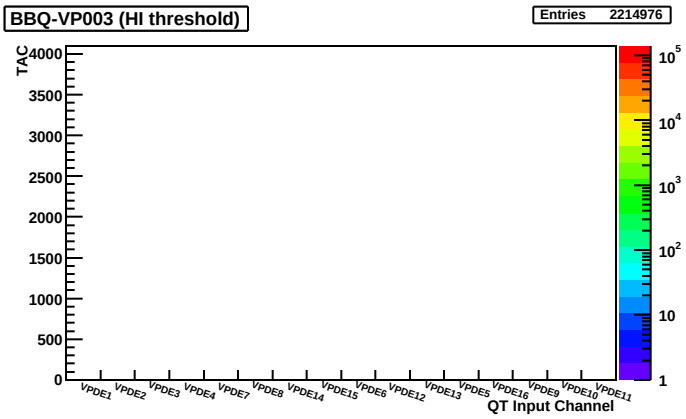

BBQ-VP001 (LO threshold)
Entries 2214976
ADC
QT Input Channel

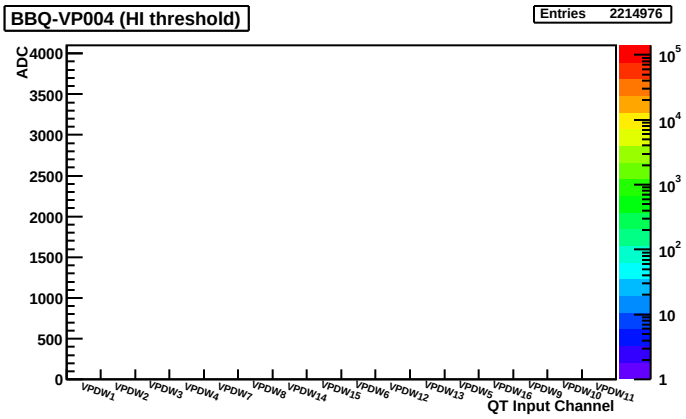

BBQ-VP001 (LO threshold)
Entries 2214976
TAC
QT Input Channel

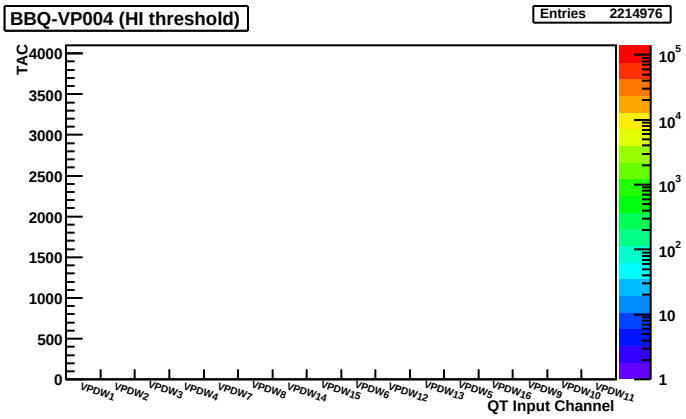

BBQ-VP002 (LO threshold)
Entries 2214976
ADC
QT Input Channel

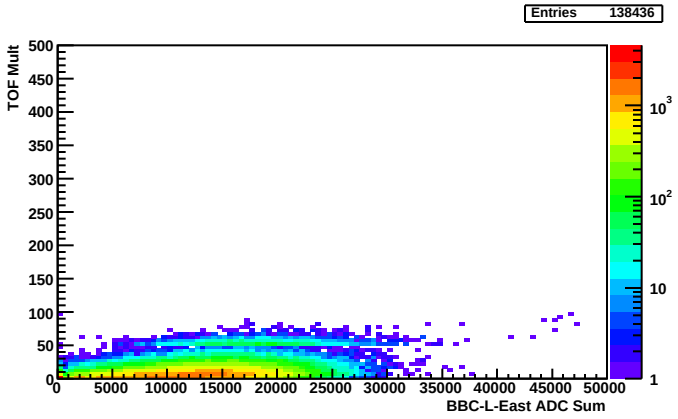

BBQ-VP002 (LO threshold)
Entries 2214976
TAC
QT Input Channel

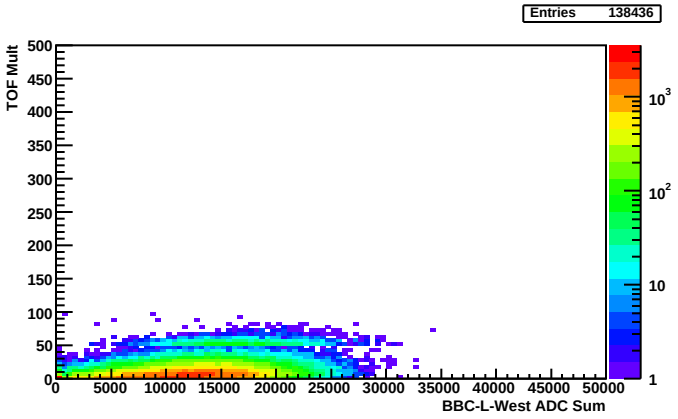

BBQ-VP003 (HI threshold)
Entries 2214976
ADC
QT Input Channel

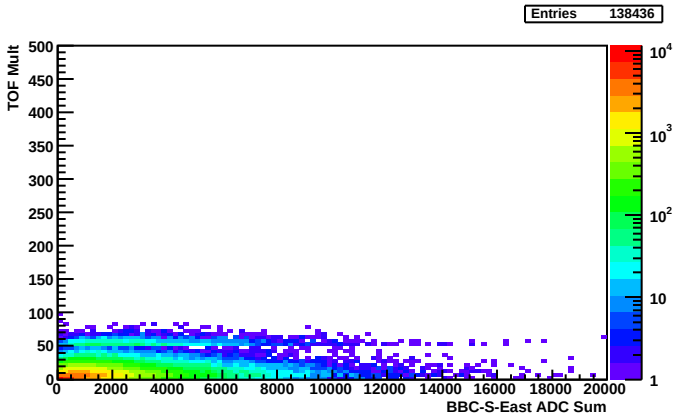

BBQ-VP003 (HI threshold)
Entries 2214976
TAC
QT Input Channel

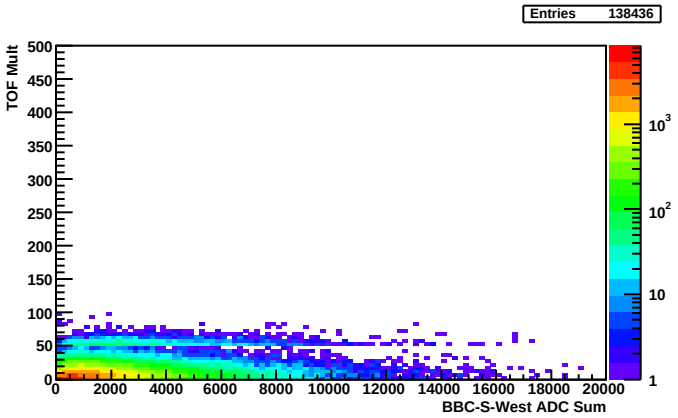

BBQ-VP004 (HI threshold)
Entries 2214976
ADC
QT Input Channel

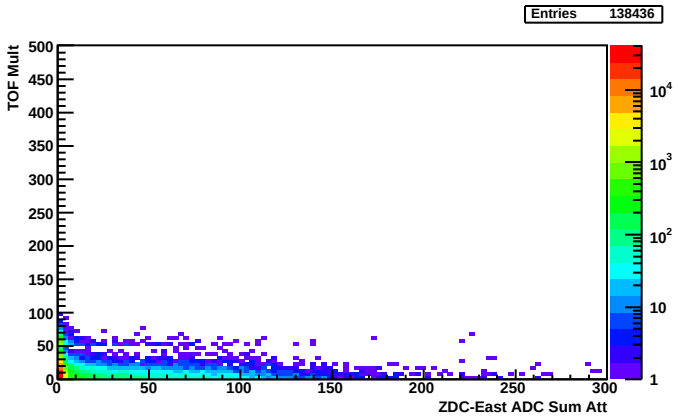

BBQ-VP004 (HI threshold)
Entries 2214976
TAC
QT Input Channel

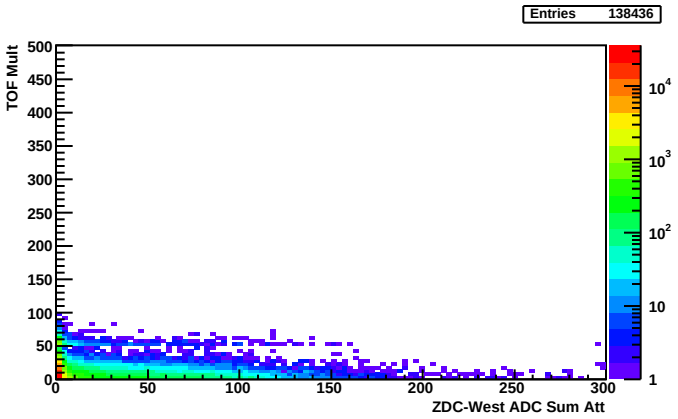

Entries 138436
TOF Mult
BBC-L-East ADC Sum

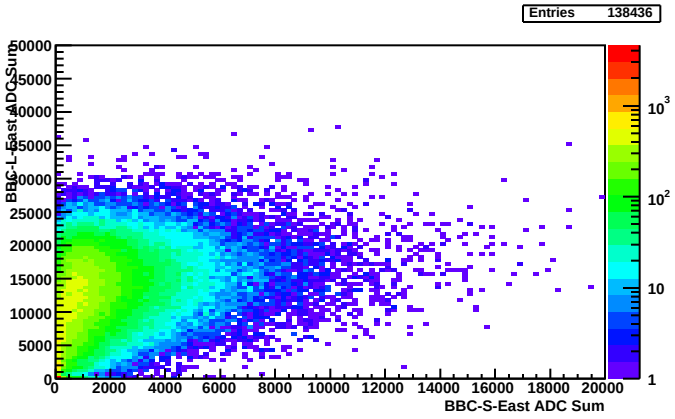

Entries 138436
TOF Mult
BBC-L-West ADC Sum

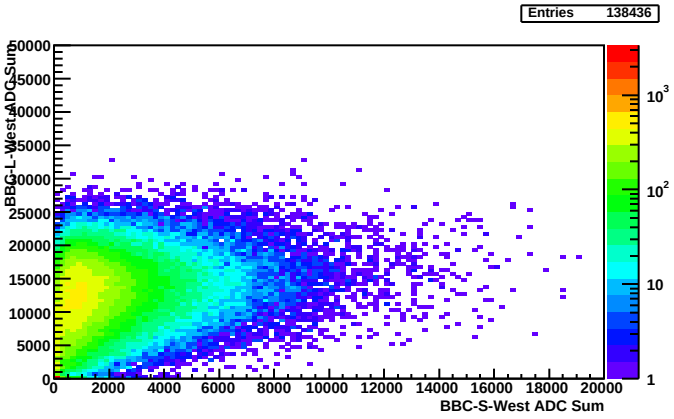

Entries 138436
TOF Mult
BBC-S-East ADC Sum

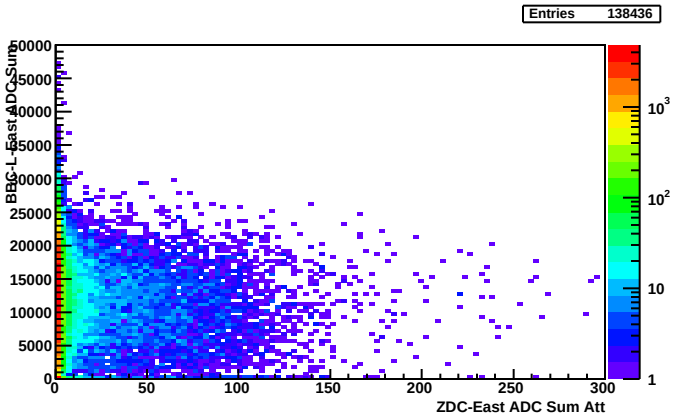

Entries 138436
TOF Mult
BBC-S-West ADC Sum

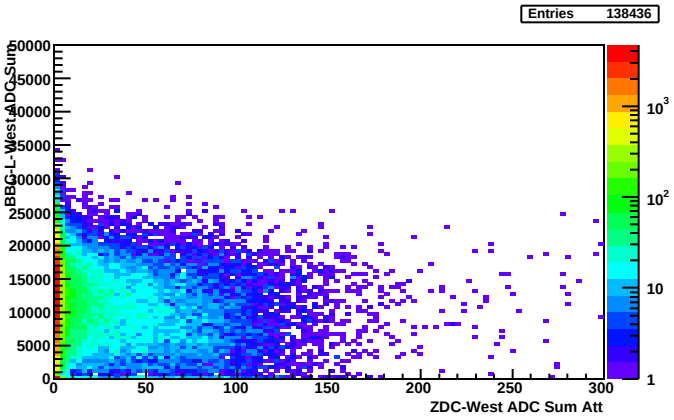

Entries 138436
TOF Mult
ZDC-East ADC Sum Att

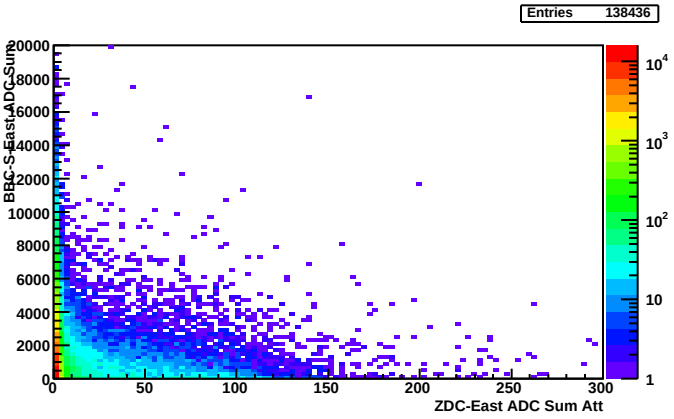

Entries 138436
TOF Mult
ZDC-West ADC Sum Att

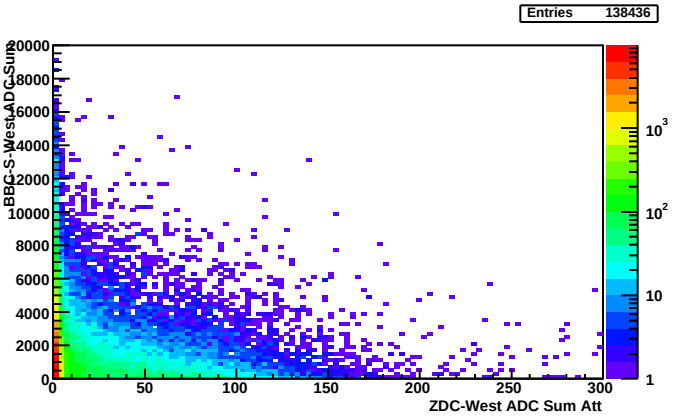

Entries 138436
BBC-L-East ADC Sum
BBC-S-East ADC Sum

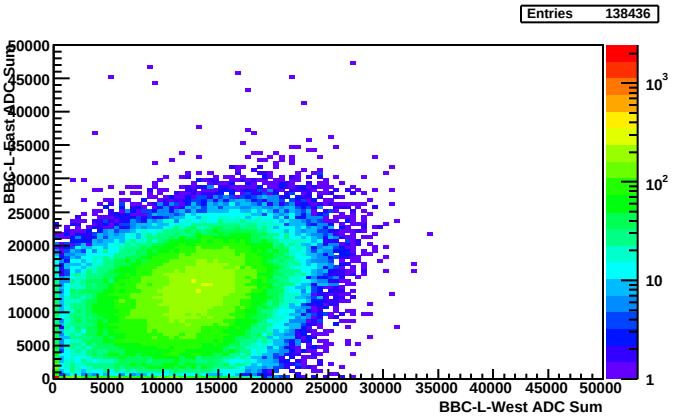

Entries 138436
BBC-L-West ADC Sum
BBC-S-West ADC Sum

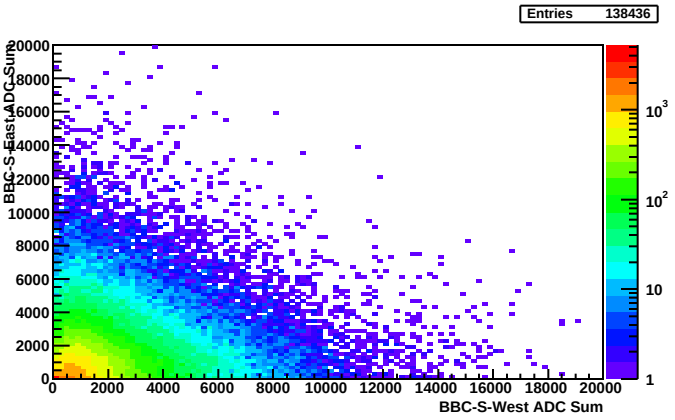

Entries 138436
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

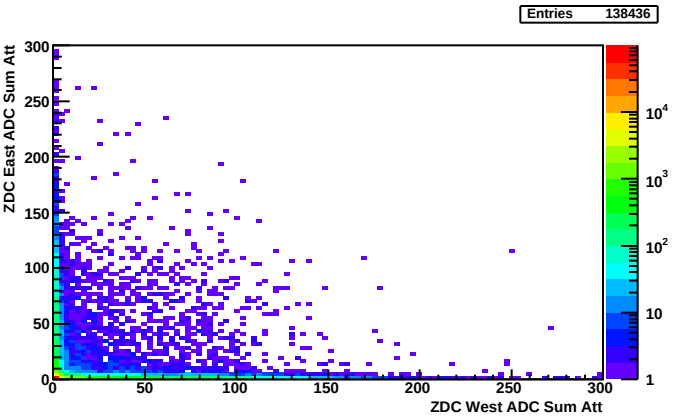

Entries 138436
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

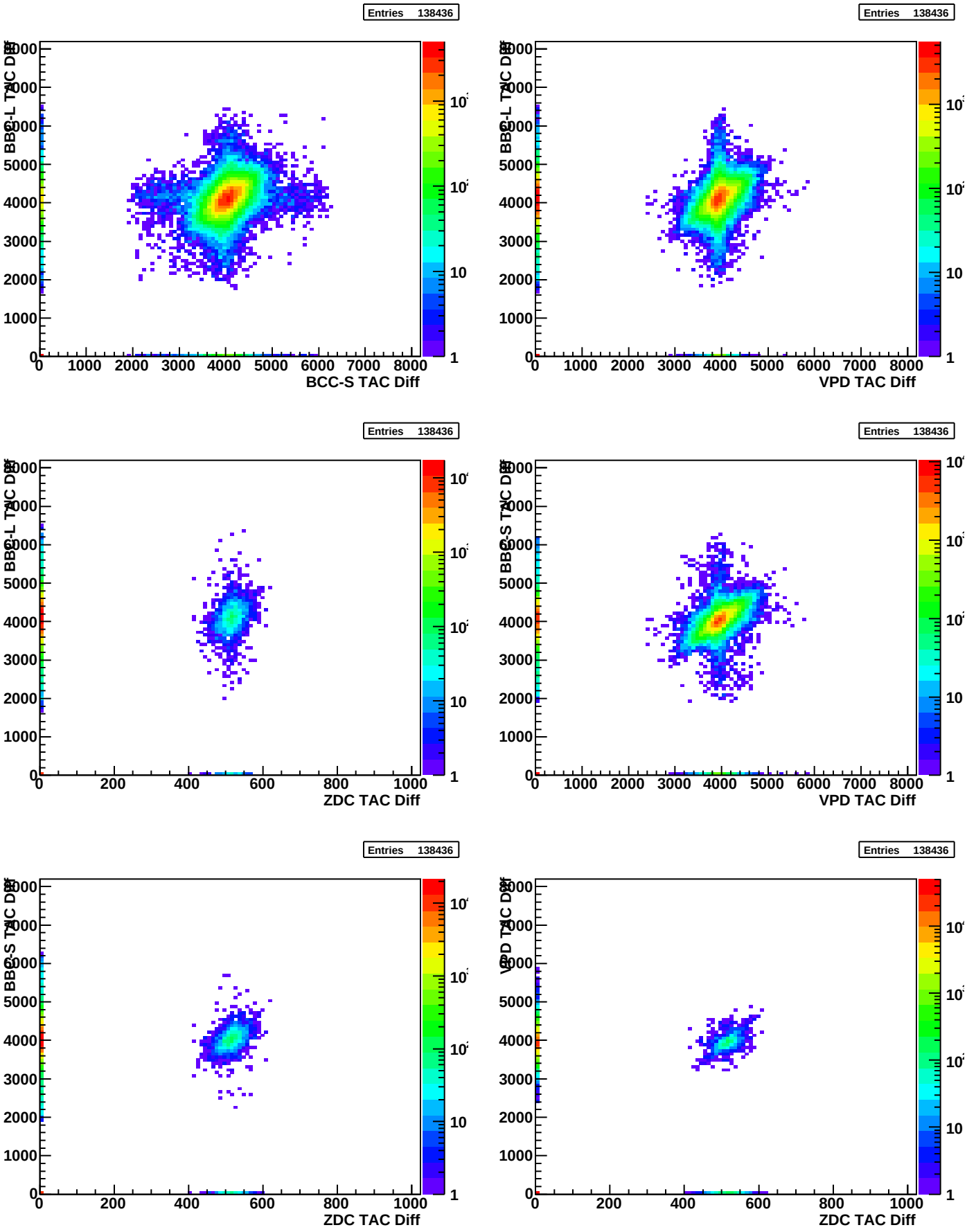

Entries 138436
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

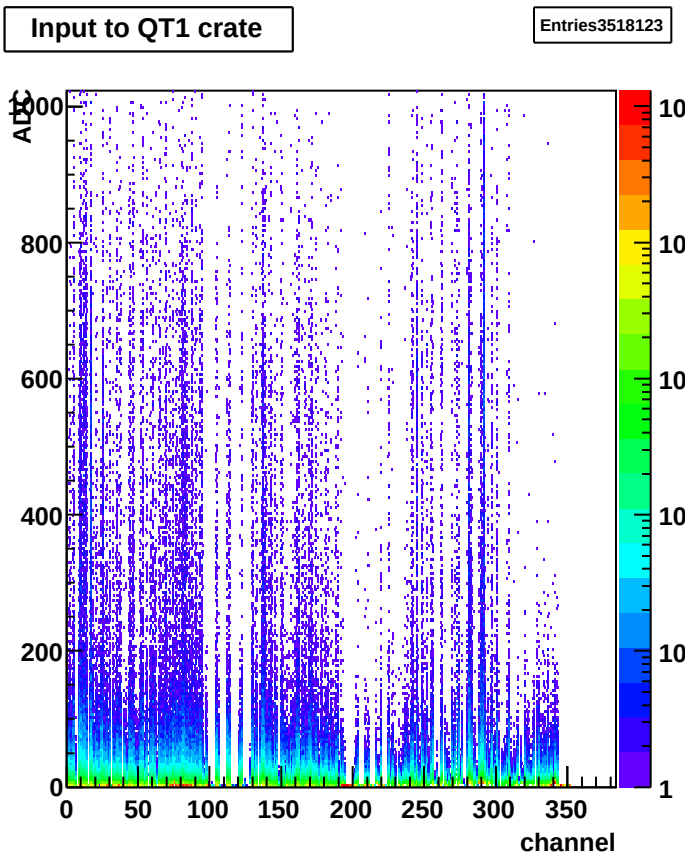

Entries 138436
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

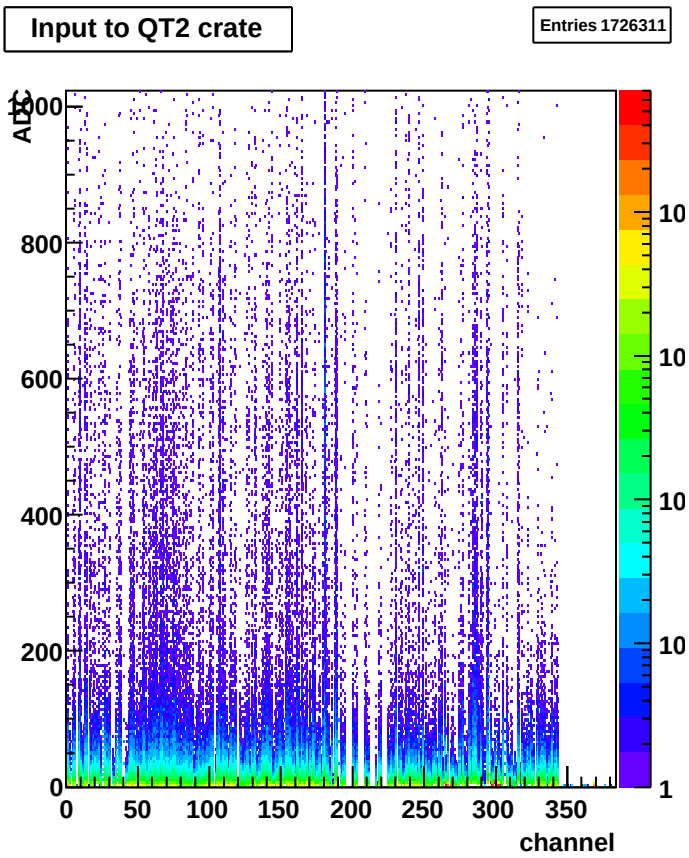

Entries 138436
BBC-L-East ADC Sum
BBC-L-West ADC Sum

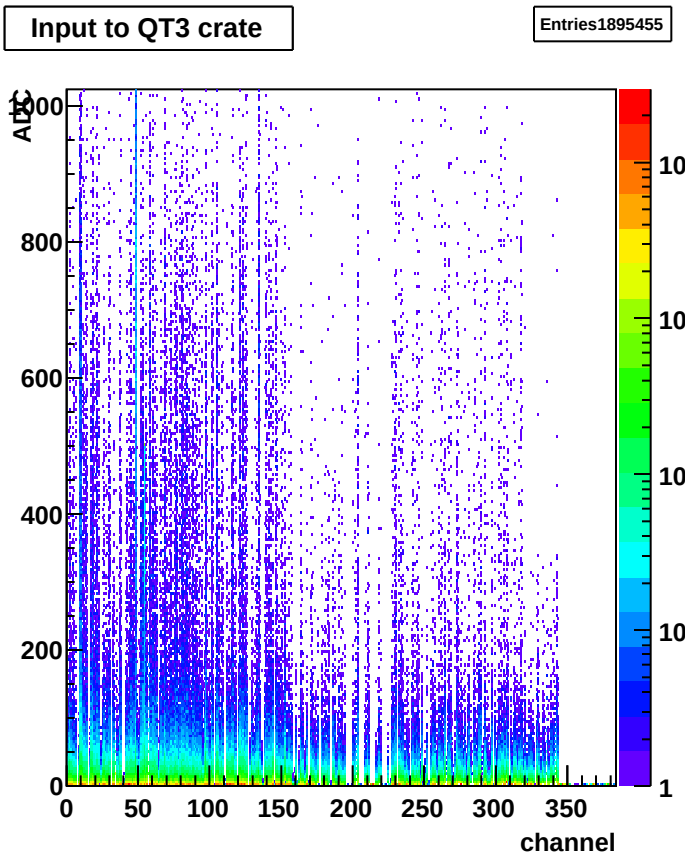

Entries 138436
BBC-S-East ADC Sum
BBC-S-West ADC Sum

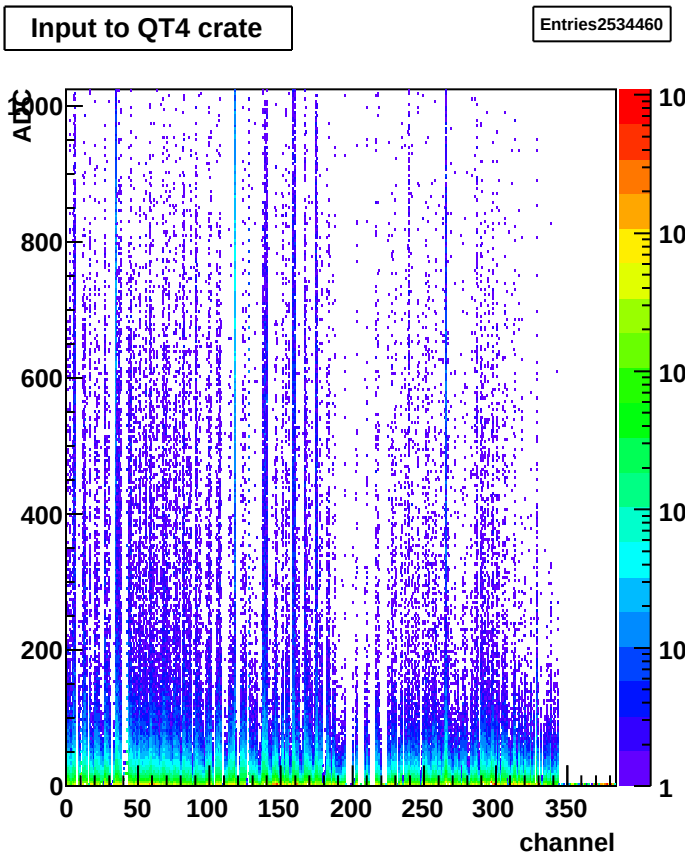

Entries 138436
ZDC East ADC Sum Att
ZDC West ADC Sum Att

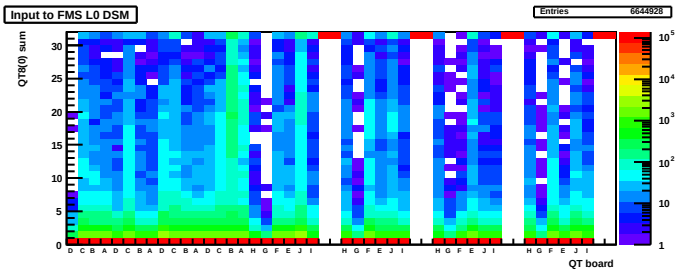

Entries 138436
BBC-L TAC Diff
BCC-S TAC Diff
Entries 138436
BBC-L TAC Diff
VPD TAC Diff
Entries 138436
BBC-L TAC Diff
ZDC TAC Diff
Entries 138436
BBC-S TAC Diff
VPD TAC Diff
Entries 138436
BBC-S TAC Diff
ZDC TAC Diff
Entries 138436
VPD TAC Diff
ZDC TAC Diff

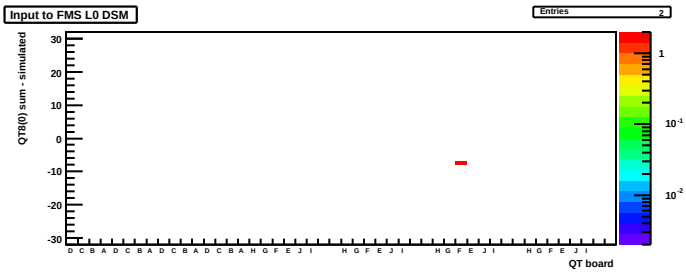

Input to QT1 crate
Entries3518123
ADC
channel

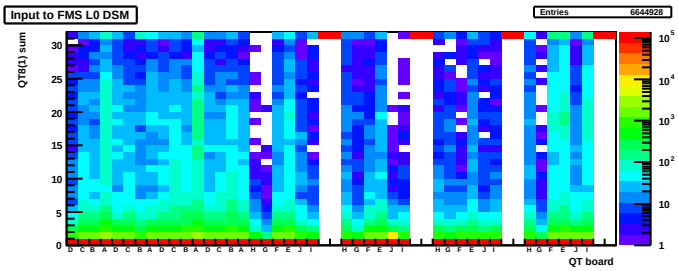

Input to QT2 crate
Entries 1726311
ADC
channel

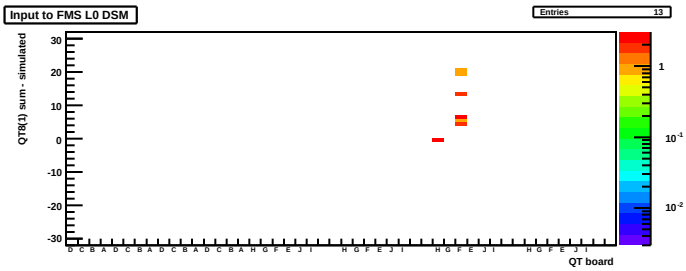

Input to QT3 crate
Entries1895455
ADC
channel

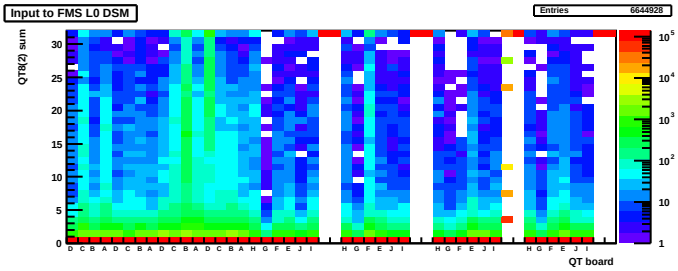

Input to QT4 crate
Entries2534460
ADC
channel

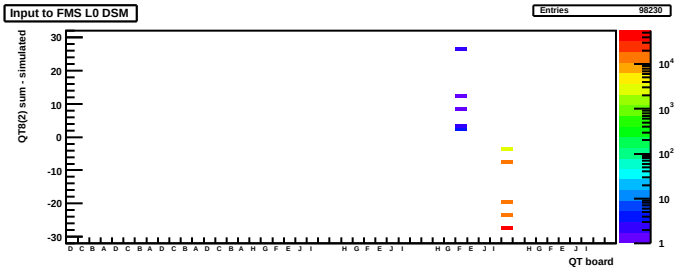

Input to FMS L0 DSM
Entries 6644928
QT8(0) sum
QT board

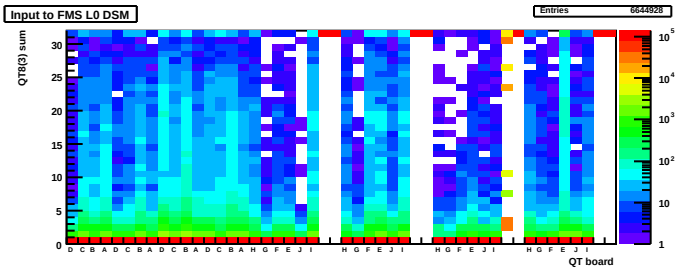

Input to FMS L0 DSM
Entries 2
QT8(0) sum - simulated
QT board

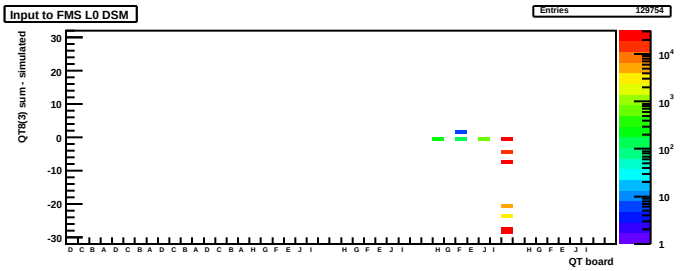

Input to FMS L0 DSM
Entries 6644928
QT8(1) sum
QT board

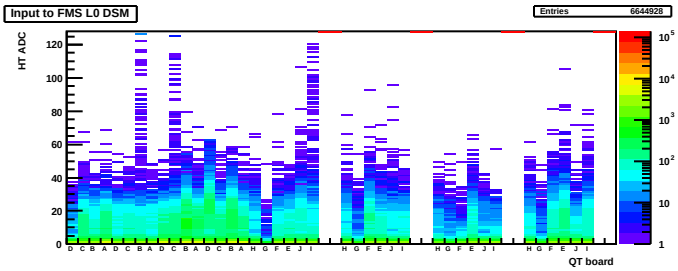

Input to FMS L0 DSM
Entries 13
QT8(1) sum - simulated
QT board

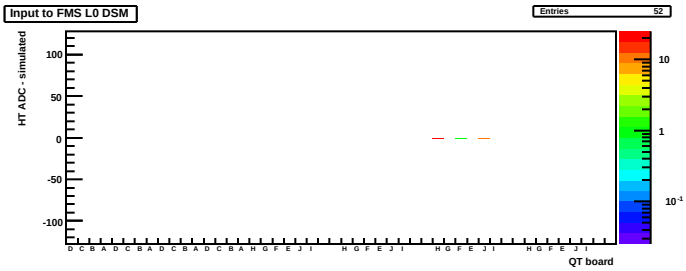

Input to FMS L0 DSM
Entries 6644928
QT8(2) sum
QT board

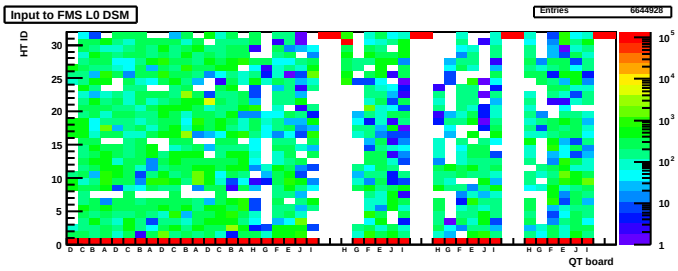

Input to FMS L0 DSM
Entries 98230
QT8(2) sum - simulated
QT board

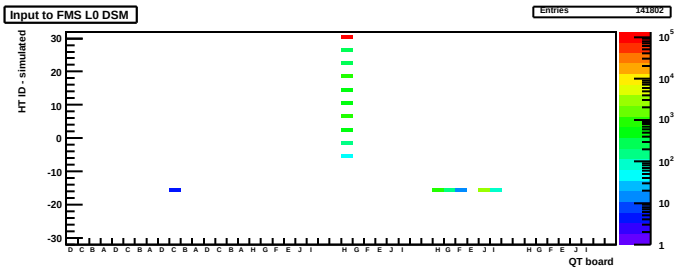

Input to FMS L0 DSM
Entries 6644928
QT8(3) sum
QT board

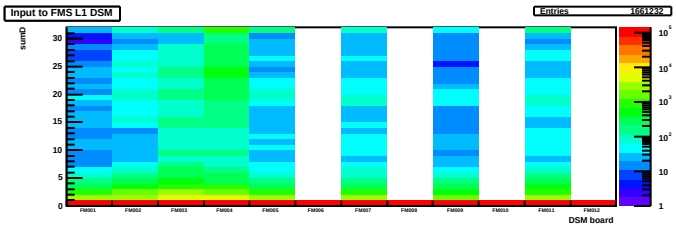

Input to FMS L0 DSM
Entries 129754
QT8(3) sum - simulated
QT board

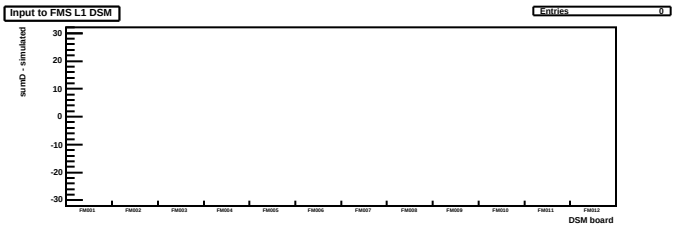

Input to FMS L0 DSM
Entries 6644928
HT ADC
QT board

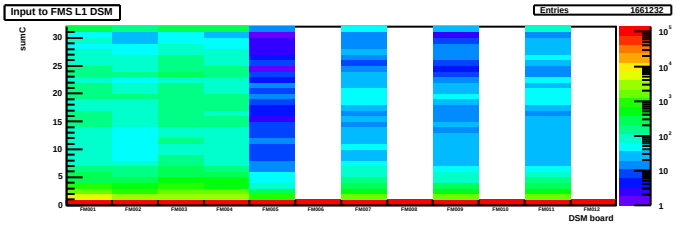

Input to FMS L0 DSM
Entries 52
HT ADC - simulated
QT board

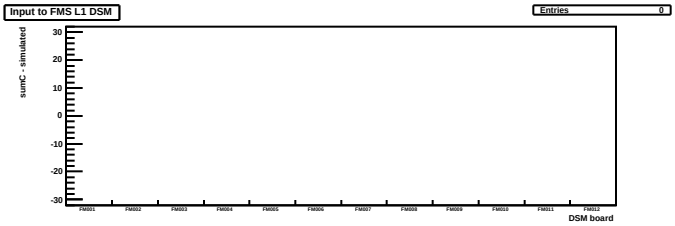

Input to FMS L0 DSM
Entries 6644928
HT ID
QT board

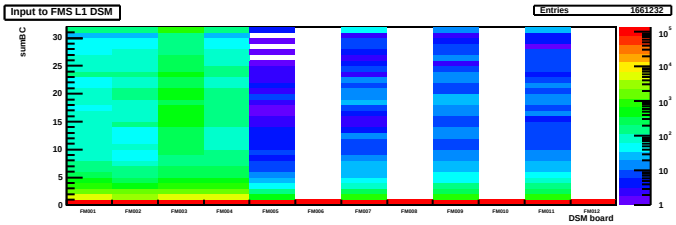

Input to FMS L0 DSM
Entries 141802
HT ID - simulated
QT board

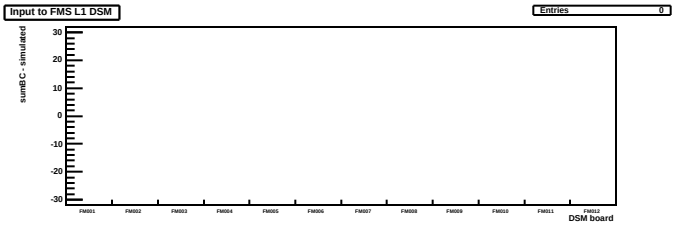

Input to FMS L1 DSM
Entries 1661232
sumD
DSM board

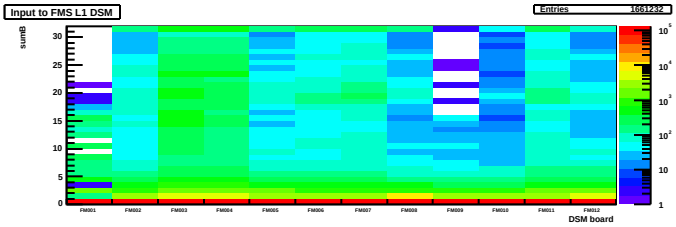

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

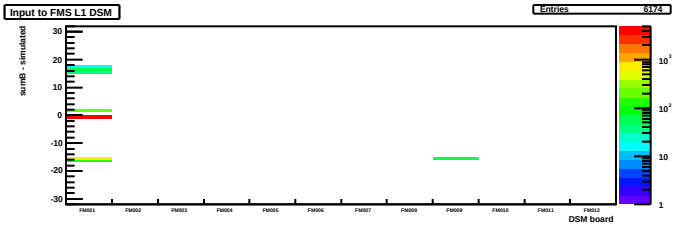

Input to FMS L1 DSM
Entries 1661232
sumC
DSM board

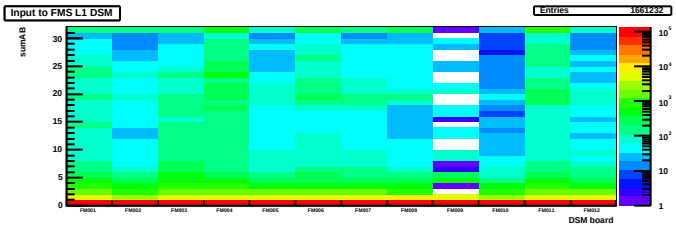

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

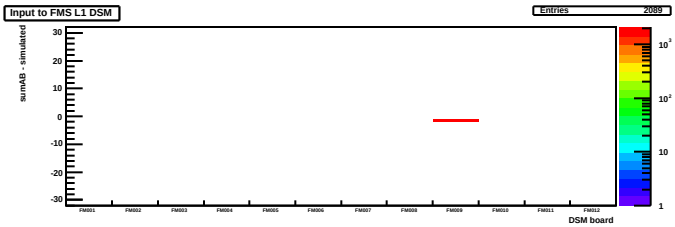

Input to FMS L1 DSM
Entries 1661232
sumBC
DSM board

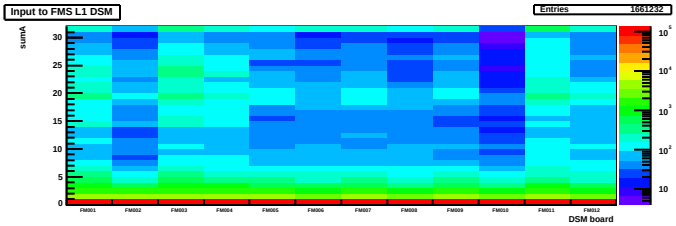

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

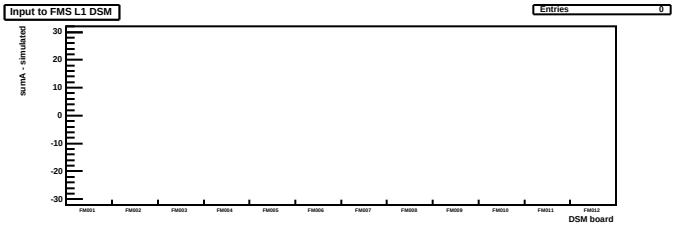

Input to FMS L1 DSM
Entries 1661232
sumB
DSM board

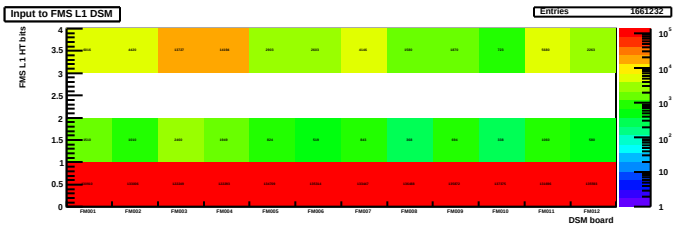

Input to FMS L1 DSM
Entries 6174
sumB - simulated
DSM board

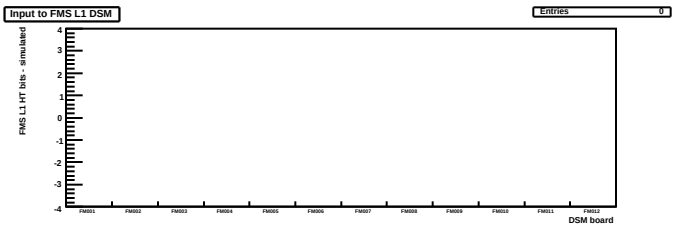

Input to FMS L1 DSM
Entries 1661232
sumAB
DSM board

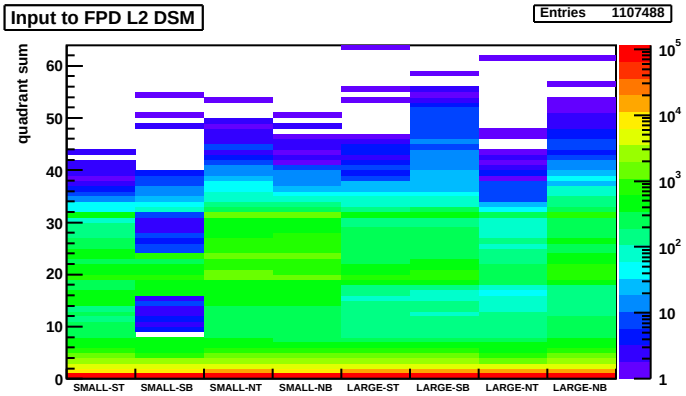

Input to FMS L1 DSM
Entries 2089
sumAB - simulated
DSM board

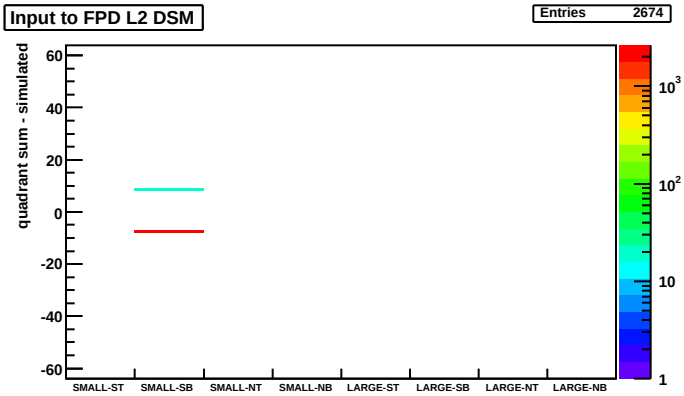

Input to FMS L1 DSM
Entries 1661232
sumA
DSM board

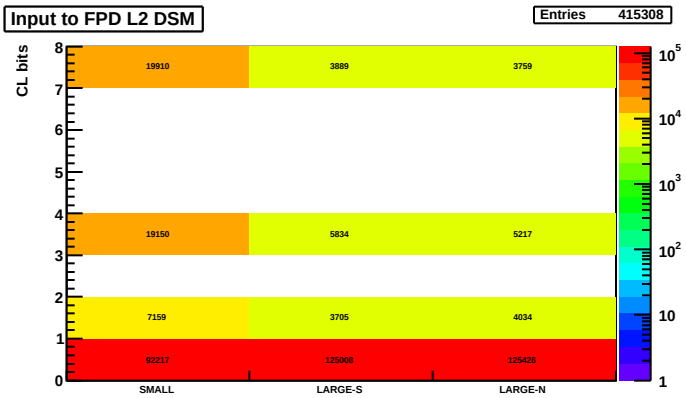

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

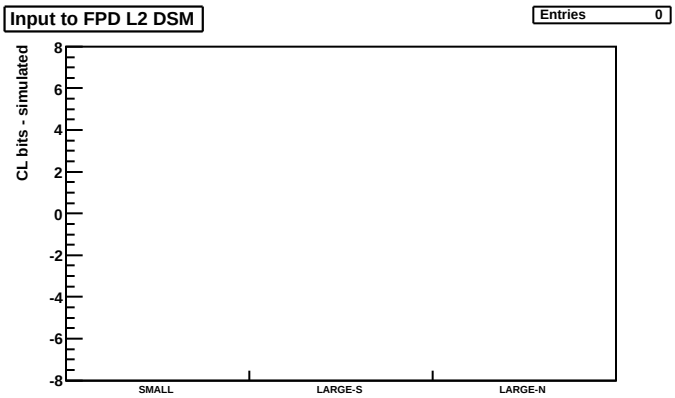

Input to FMS L1 DSM
Entries 1661232
FMS L1 HT bits
DSM board

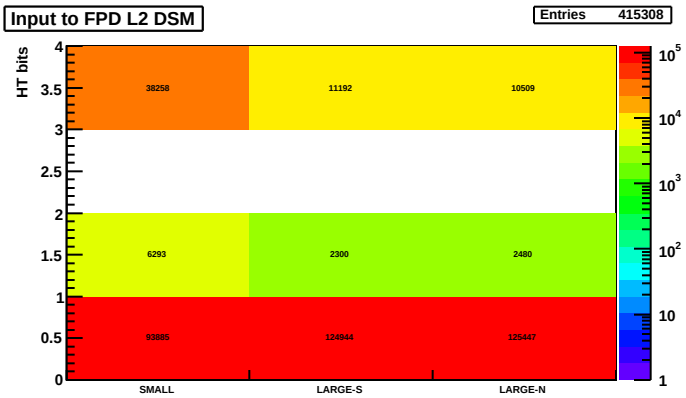

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

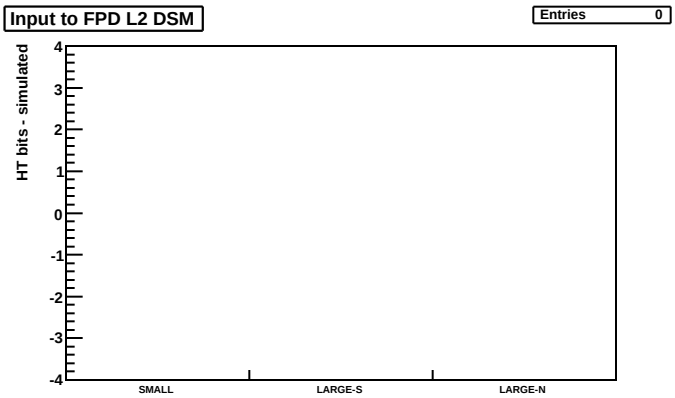

Input to FPD L2 DSM
Entries 1107488
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

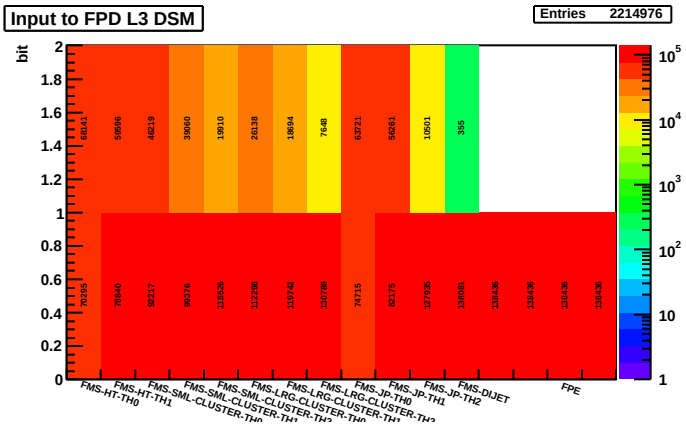

Input to FPD L2 DSM
Entries 2674
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

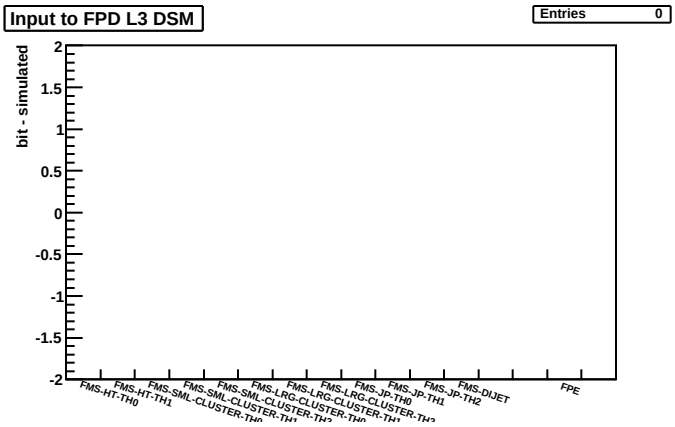

Input to FPD L2 DSM
Entries 415308
CL bits
19910
3889
3759
19150
5834
5217
7159
3705
4034
92217
125008
125426
SMALL
LARGE-S
LARGE-N

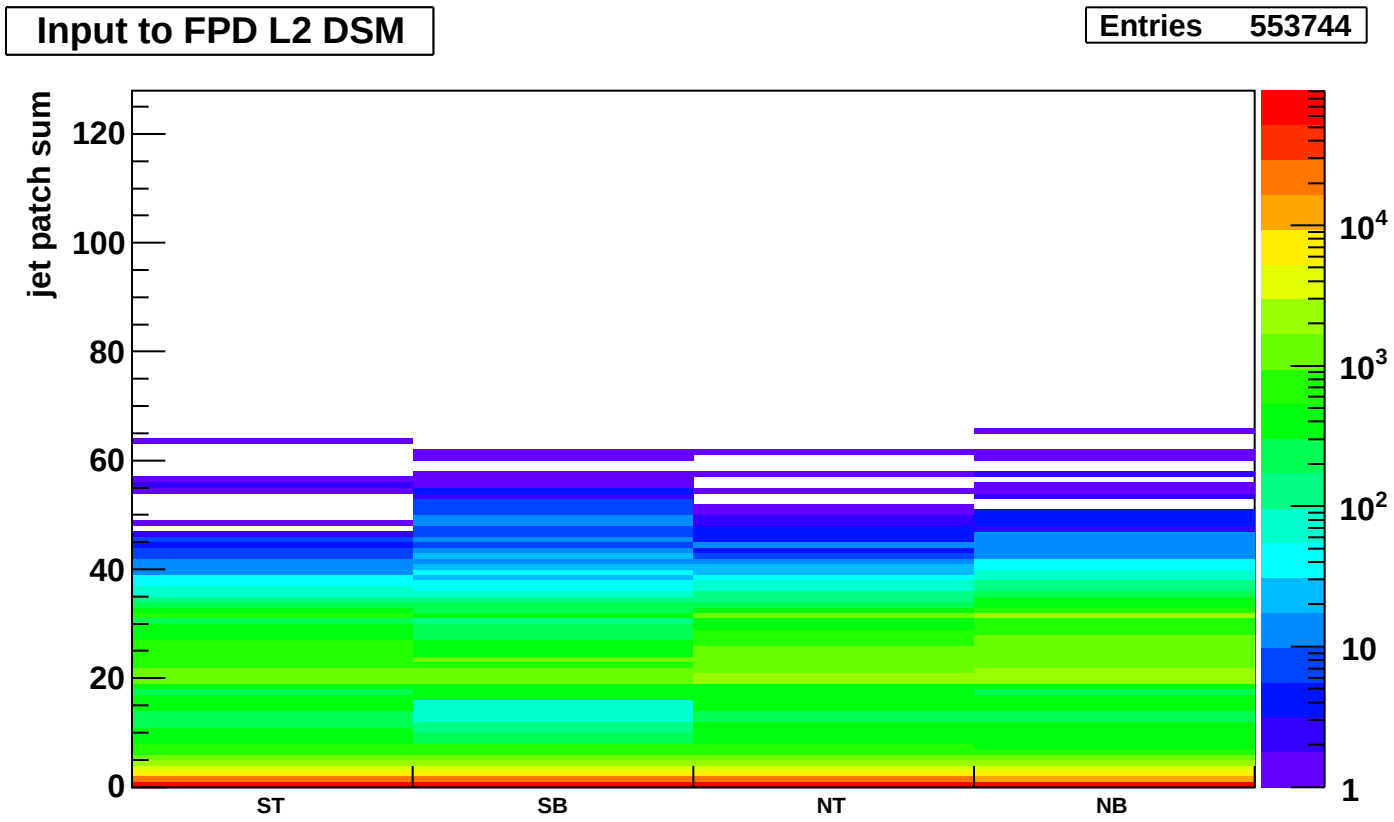

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

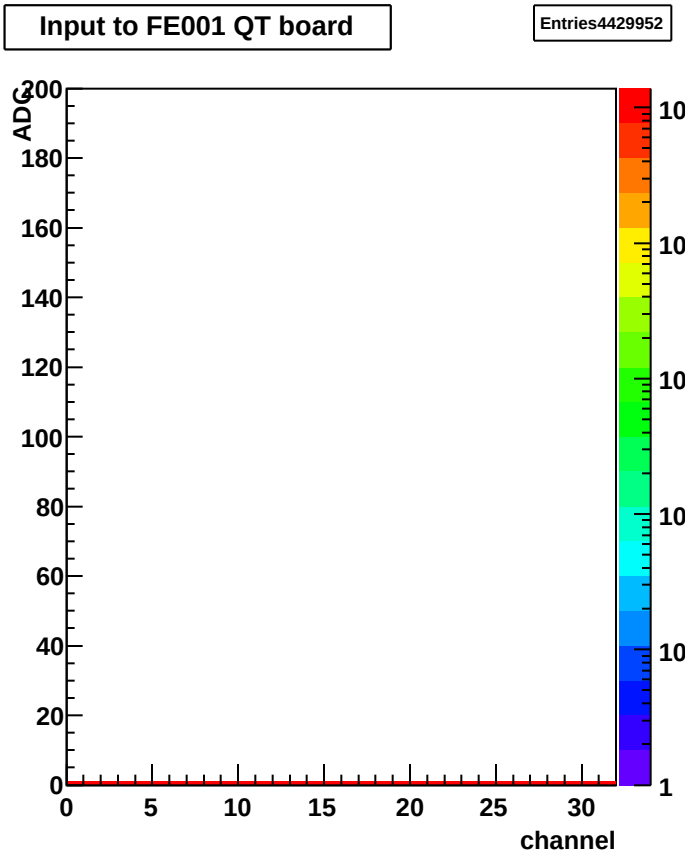

Input to FPD L2 DSM
Entries 415308
HT bits
38258
11192
10509
6293
2300
2480
93885
124944
125447
SMALL
LARGE-S
LARGE-N

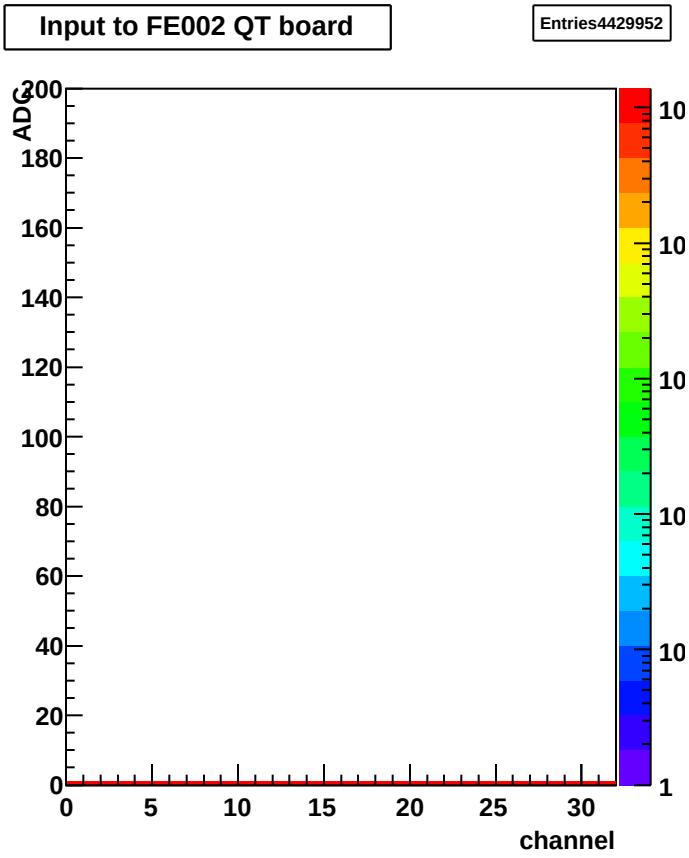

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

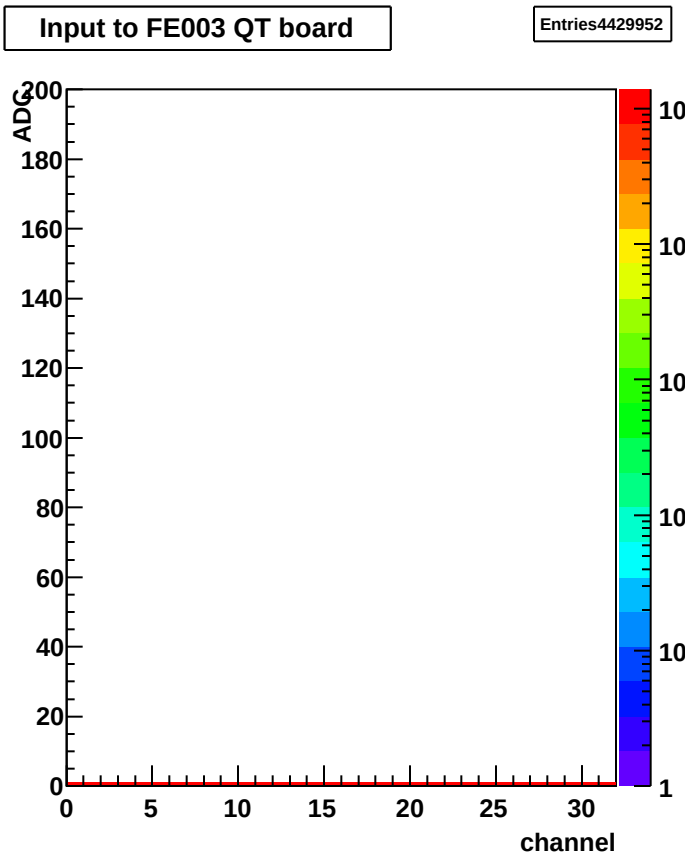

Input to FPD L3 DSM
Entries 2214976
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

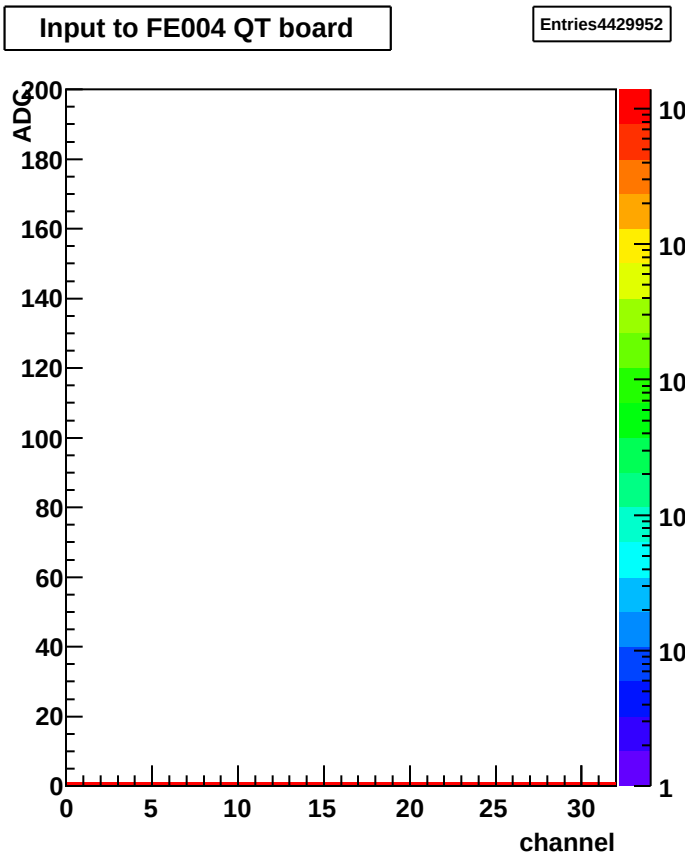

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

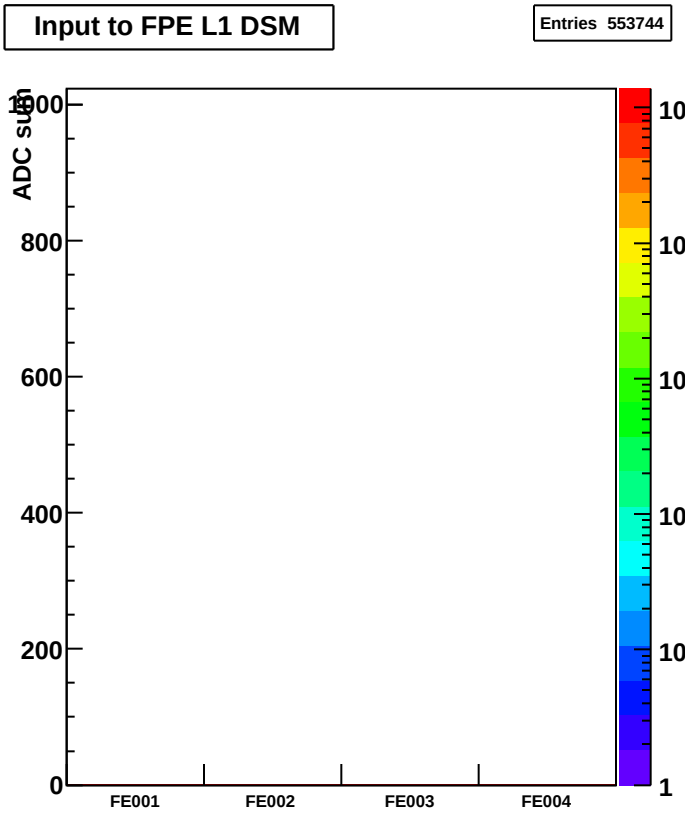

Input to FPD L2 DSM
Entries 553744
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

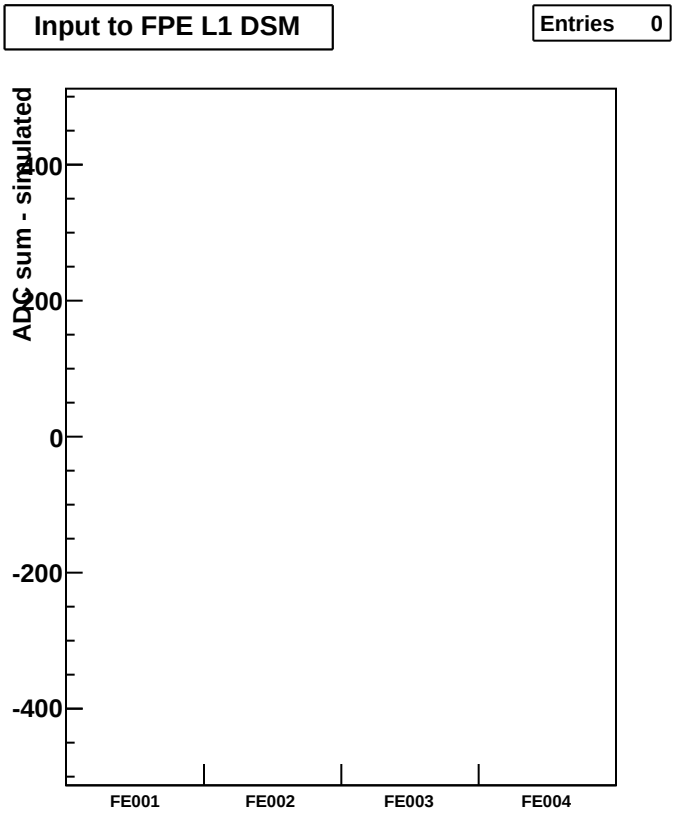

Input to FE001 QT board
Entries4429952
ADC
channel

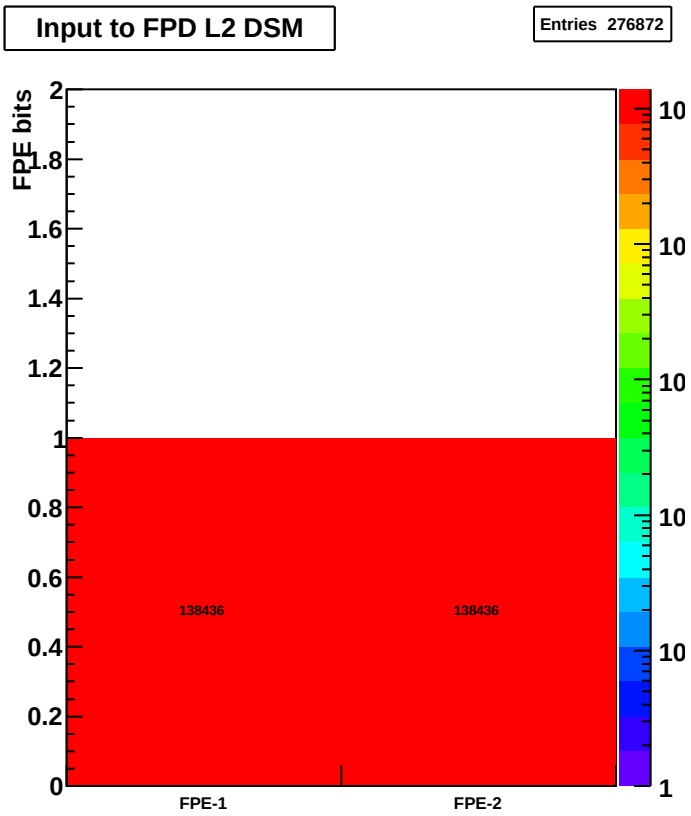

Input to FE002 QT board
Entries4429952
ADC
channel

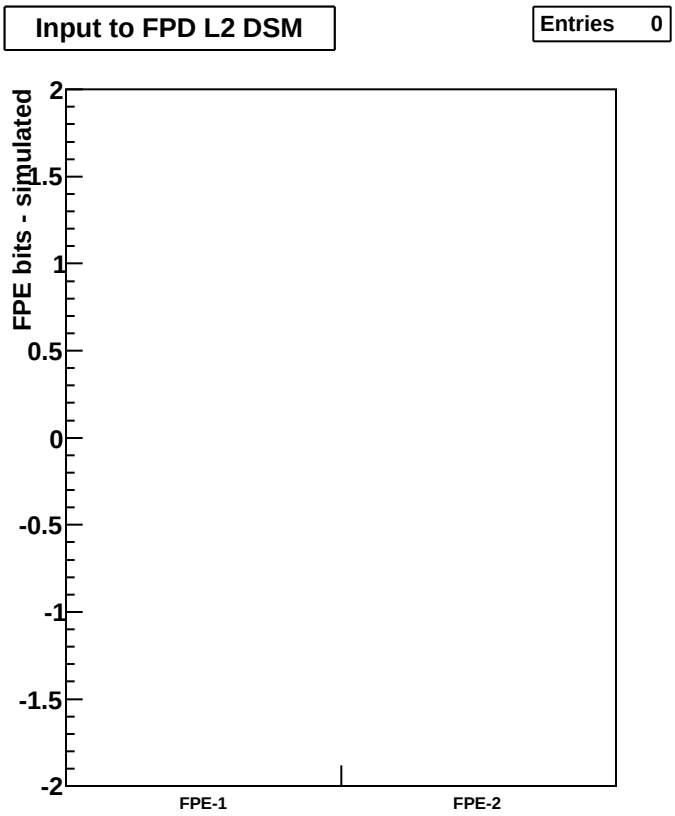

Input to FE003 QT board
Entries4429952
ADC
channel


Input to FE004 QT board
Entries4429952
ADC
channel


Input to FPE L1 DSM
Entries 553744
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 276872
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
138436
138436
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2