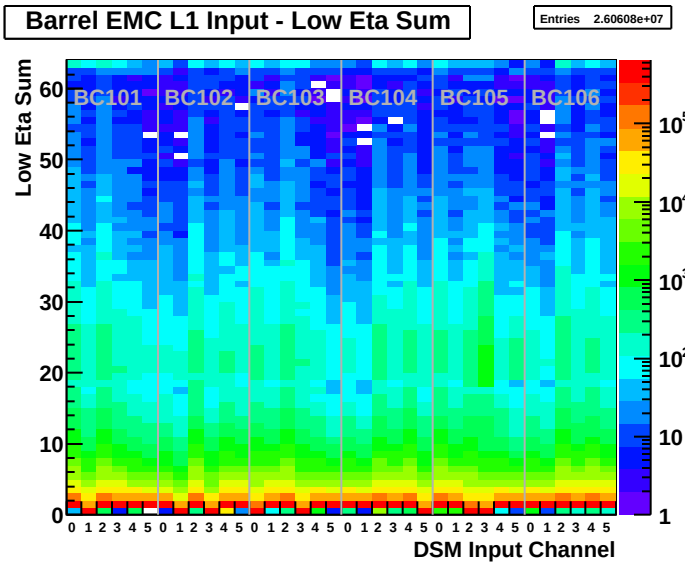
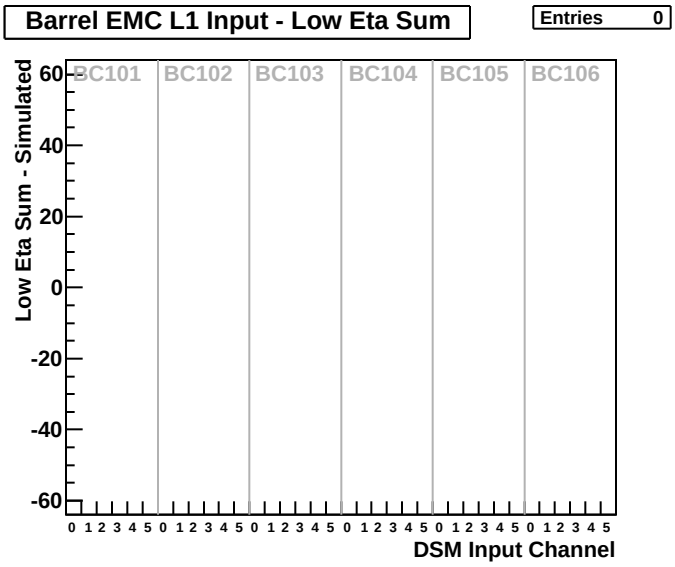
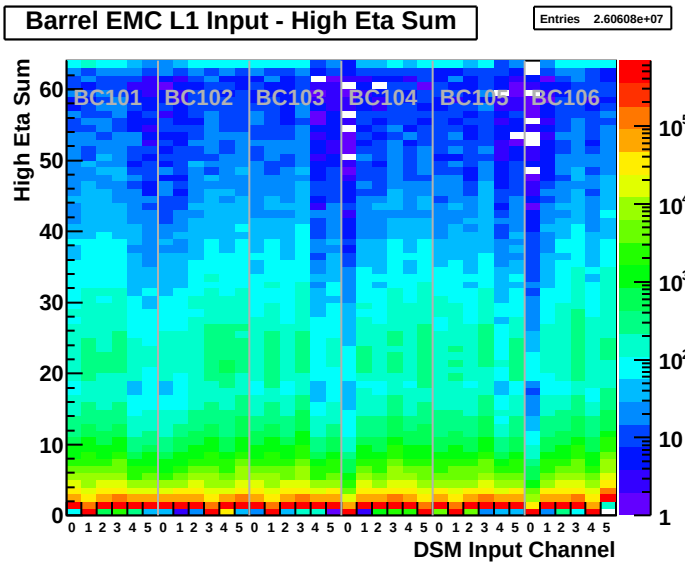
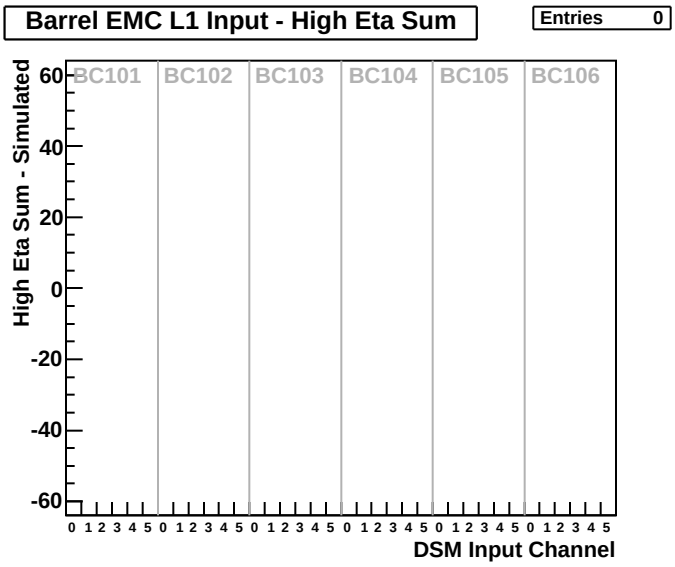
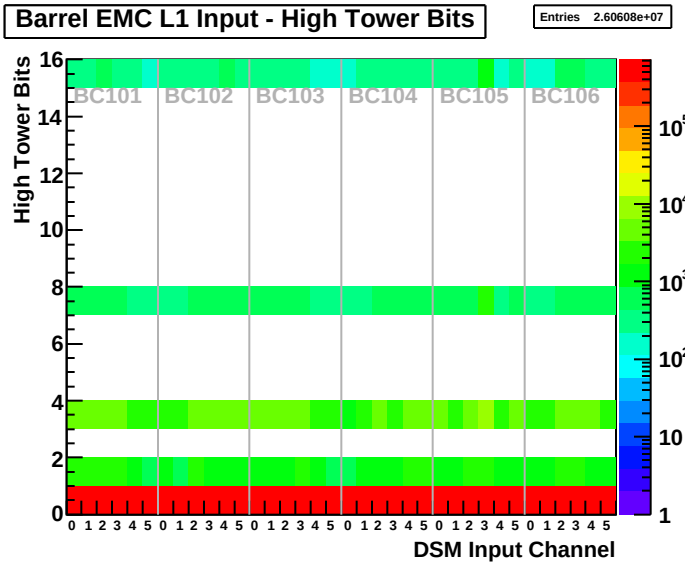
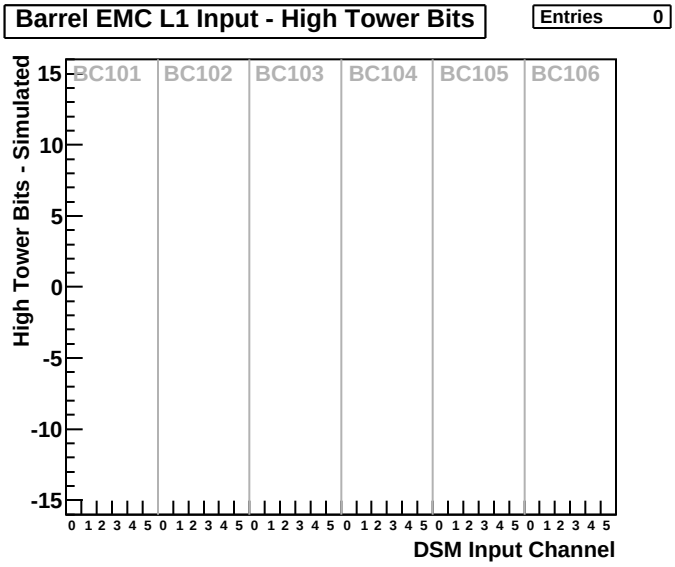

Barrel EMC L1 Input - Low Eta Sum
Entries 2.60608e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

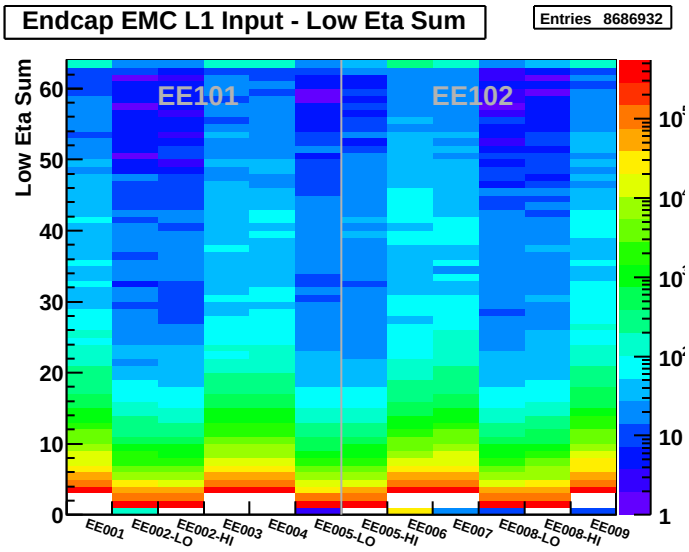

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

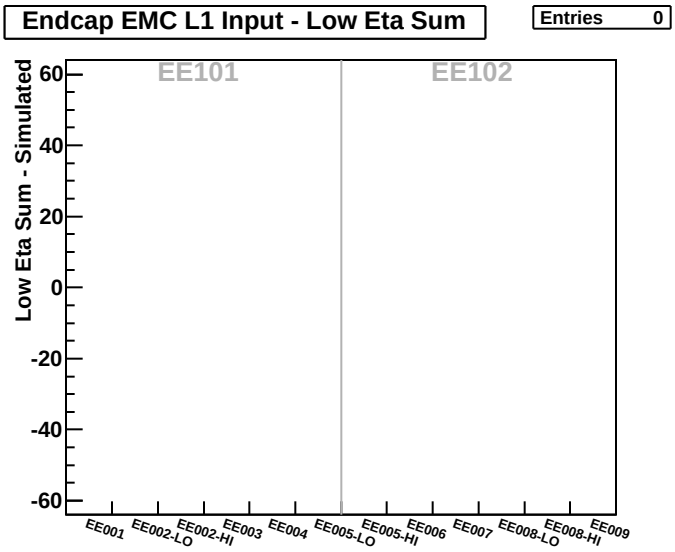

Barrel EMC L1 Input - High Eta Sum
Entries 2.60608e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

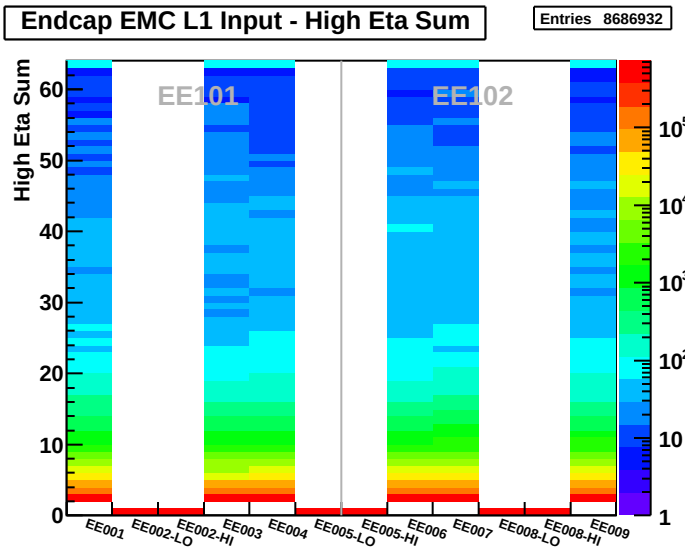

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

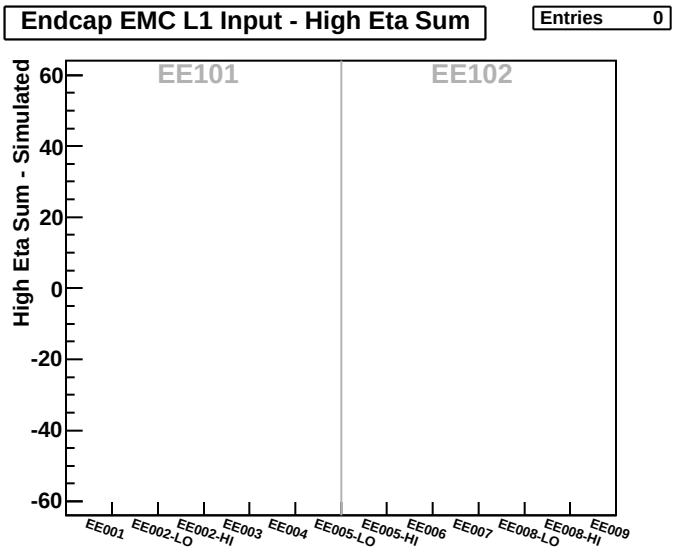

Barrel EMC L1 Input - High Tower Bits
Entries 2.60608e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

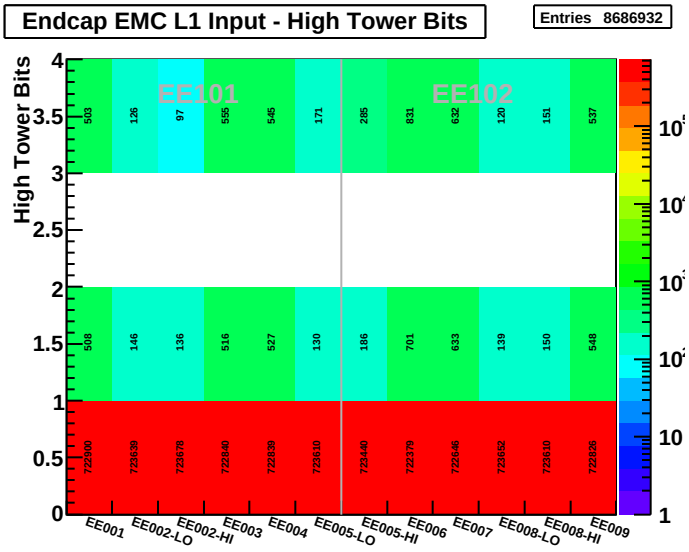

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

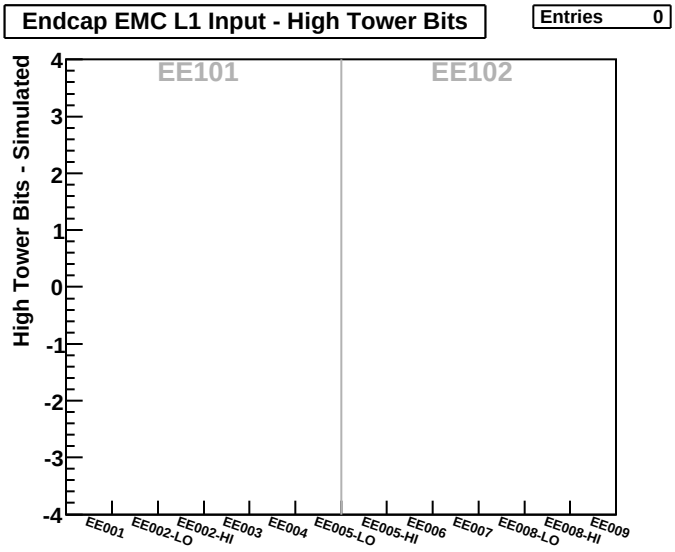

Endcap EMC L1 Input - Low Eta Sum
Entries 8686932
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

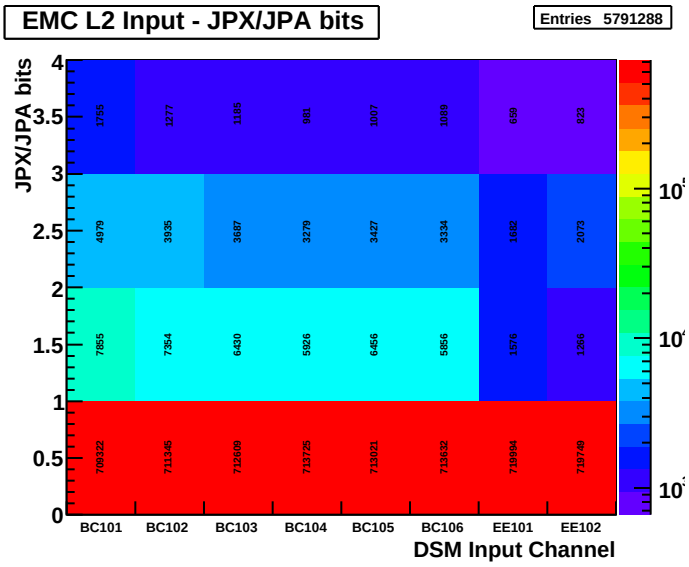

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

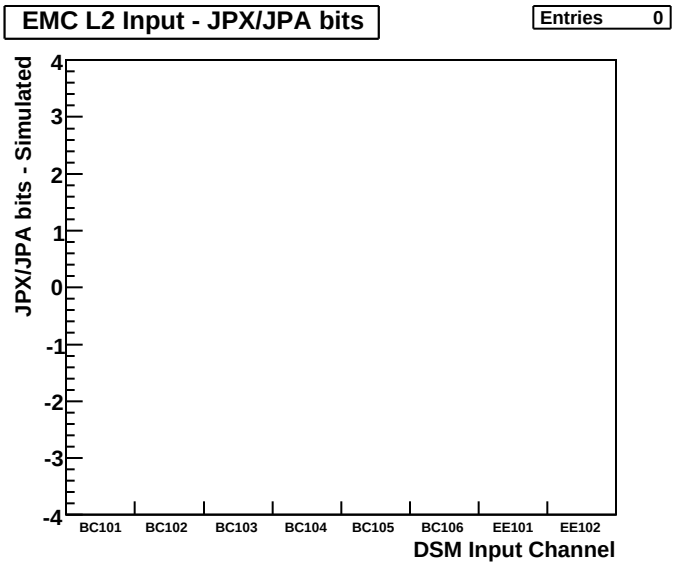

Endcap EMC L1 Input - High Eta Sum
Entries 8686932
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

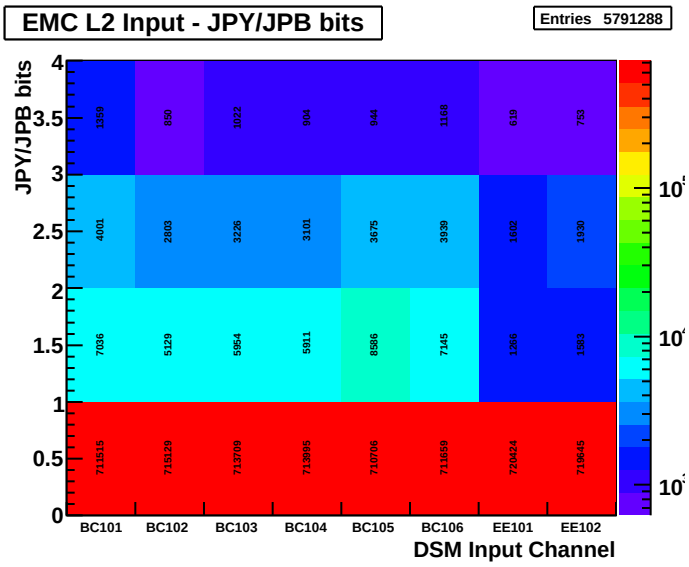

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

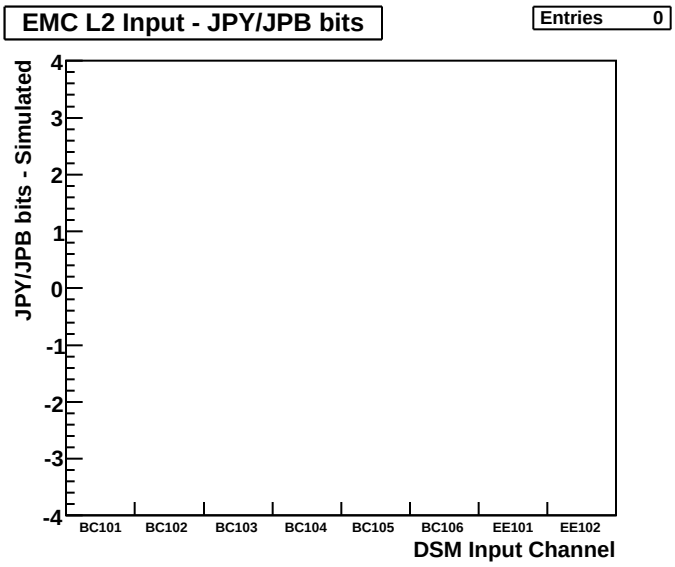

Endcap EMC L1 Input - High Tower Bits
Entries 8686932
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

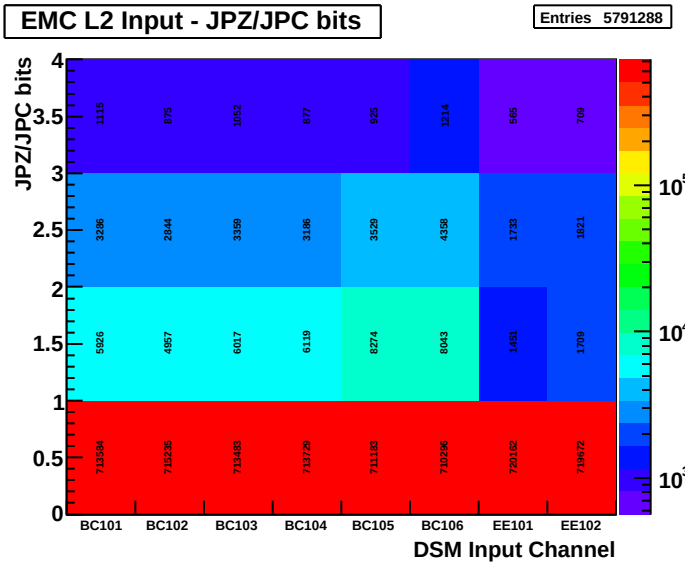

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

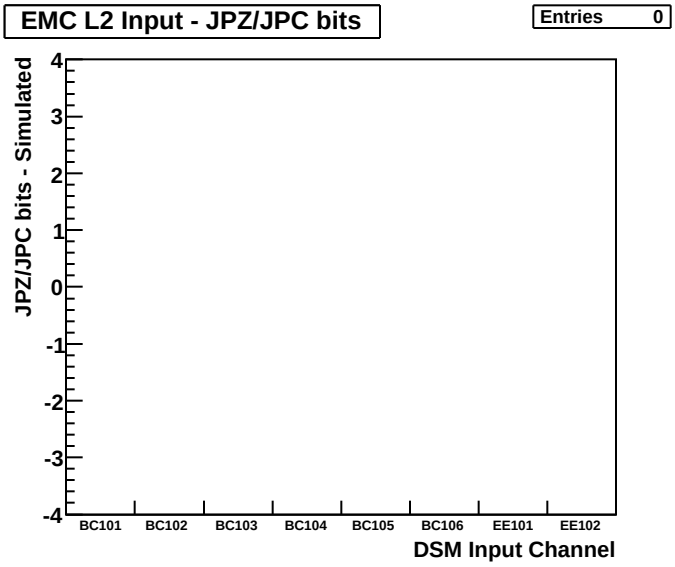

EMC L2 Input - JPX/JPA bits
Entries 5791288
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1755 1277 1185 981 1007 1089 659 823
4979 3935 3687 3279 3427 3334 1682 2073
7855 7354 6430 5926 6456 5856 1576 1266
708322 711345 712609 713725 713021 713632 719994 719749

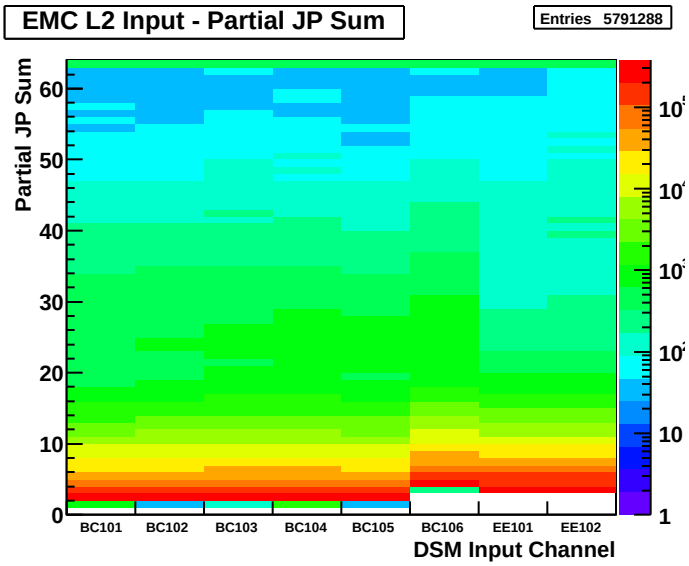

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

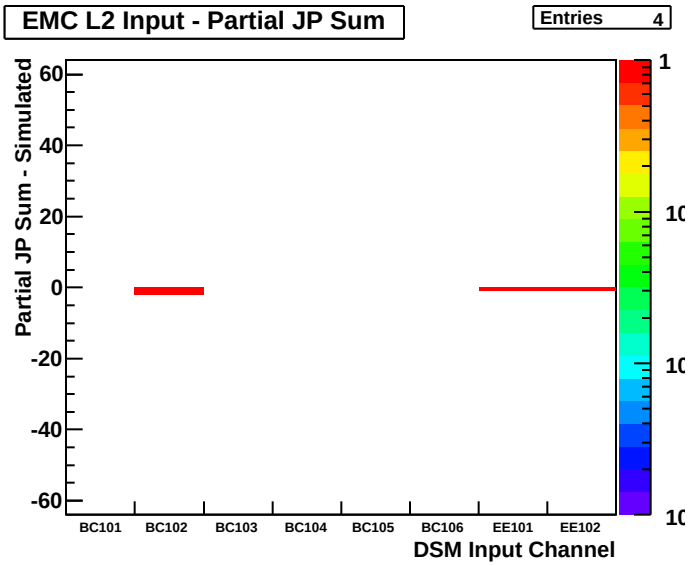

EMC L2 Input - JPY/JPB bits
Entries 5791288
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1359 850 1022 904 944 1168 619 753
4001 2803 3226 3101 3675 3939 1602 1930
7036 5129 5954 5911 8586 7145 1266 1533
711515 715129 712709 713995 710706 711659 720424 719645

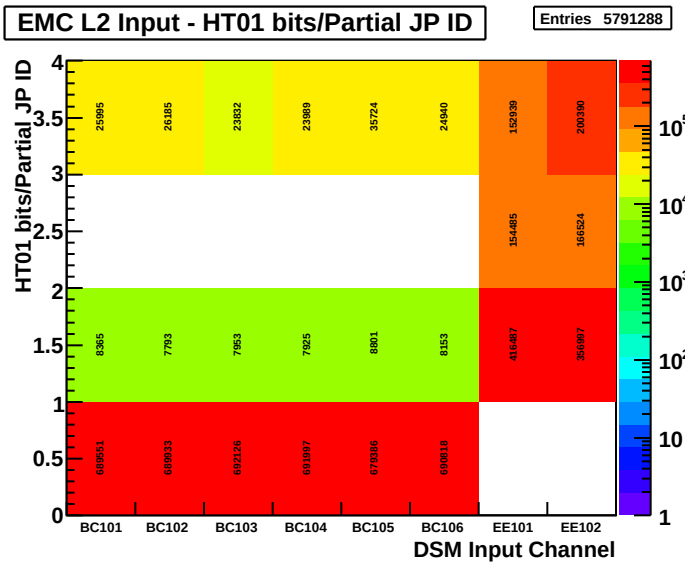

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

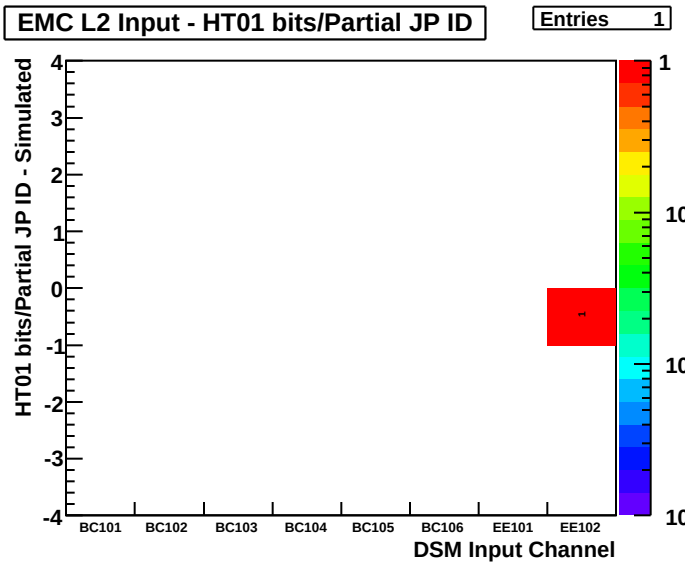

EMC L2 Input - JPZ/JPC bits
Entries 5791288
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1115 875 1052 877 925 1214 565 709
3286 2844 3359 3186 3529 4358 1733 1821
5926 4957 6017 6119 8274 8043 1451 1709
713584 715235 713483 713729 711183 710296 720162 719672

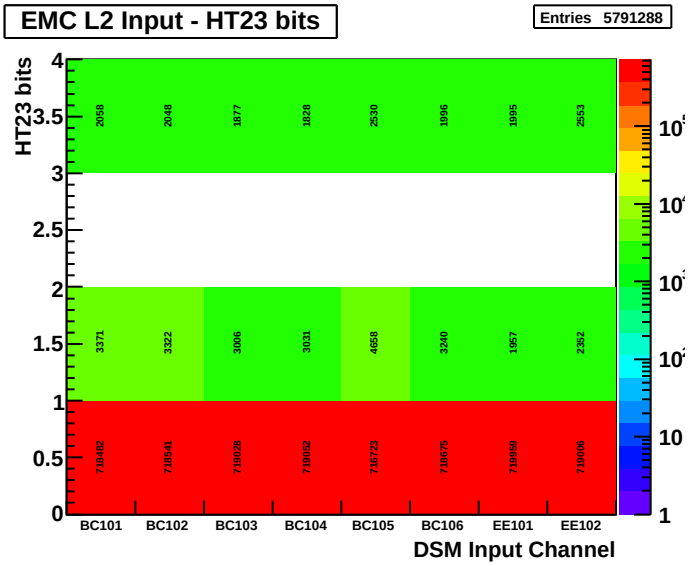

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

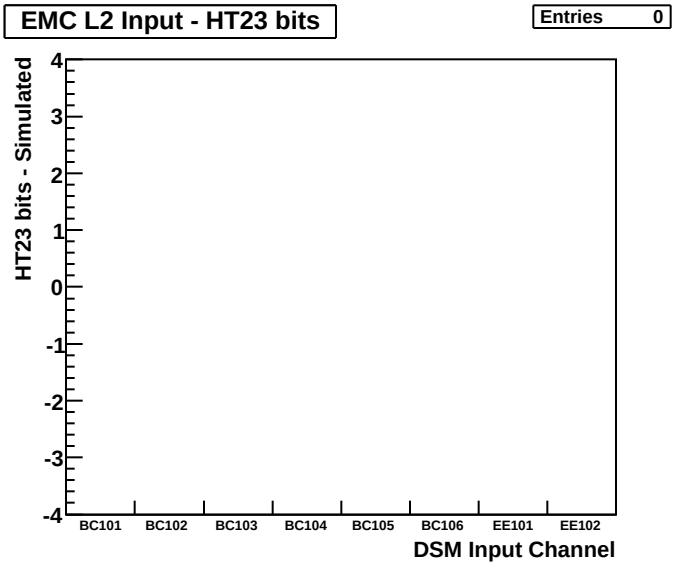

EMC L2 Input - Partial JP Sum
Entries 5791288
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

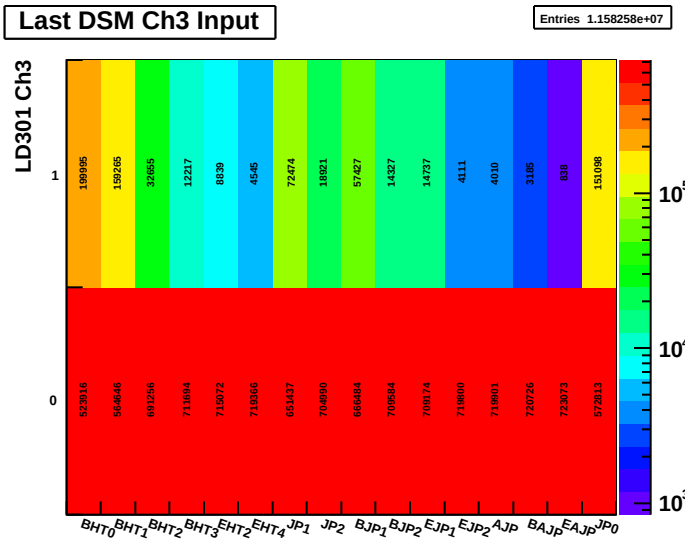

EMC L2 Input - Partial JP Sum
Entries 4
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

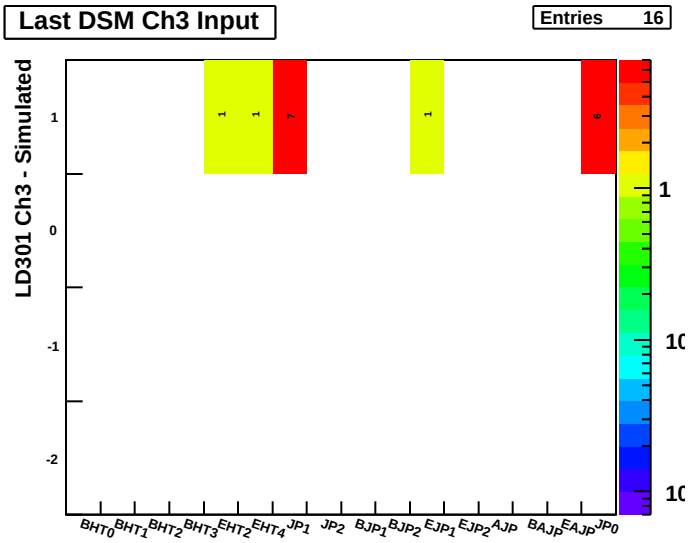

EMC L2 Input - HT01 bits/Partial JP ID
Entries 5791288
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

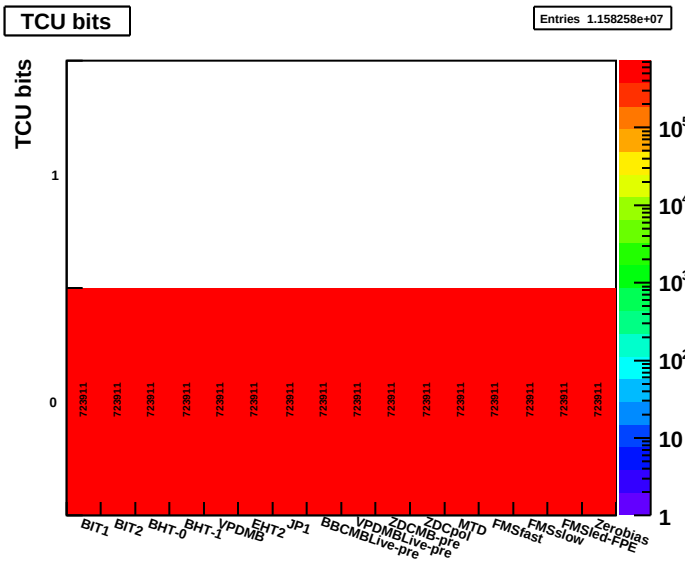

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

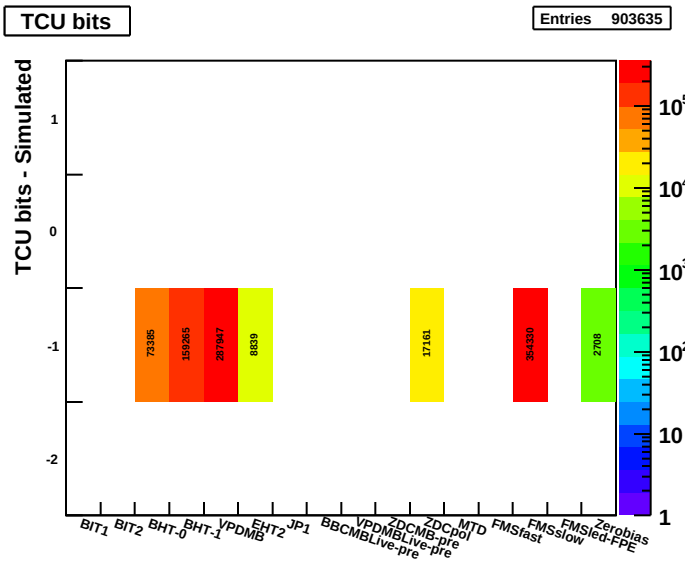

EMC L2 Input - HT23 bits
Entries 5791288
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

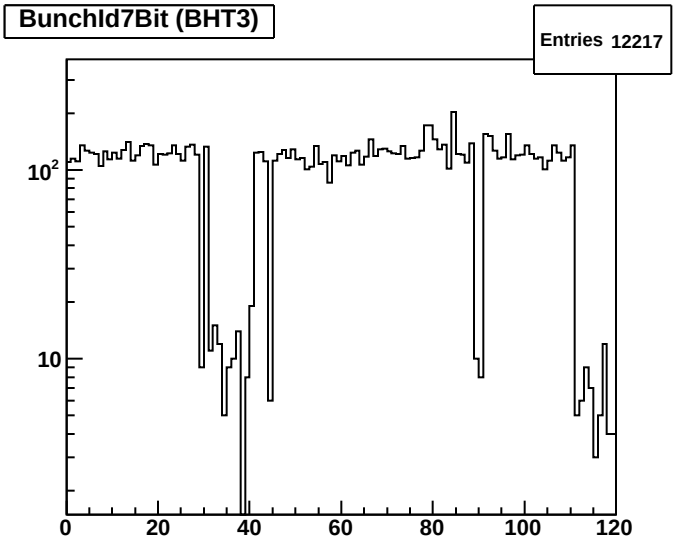

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

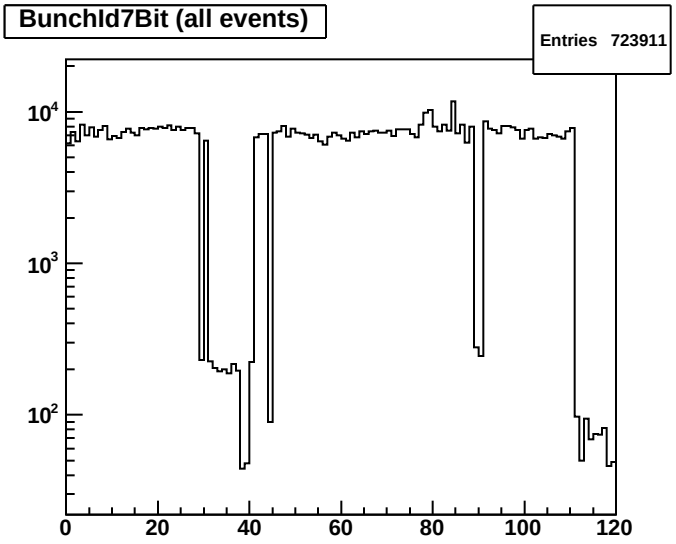

Last DSM Ch3 Input
Entries 1.158258e+07
LD301 Ch3
199995 159265 32655 12217 8839 4545 72474 18921 57427 14327 14737 4111 4010 3185 838 151098
523916 564646 691256 711694 715072 719366 651437 704990 666484 709584 709174 719800 719901 720726 723073 572813
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

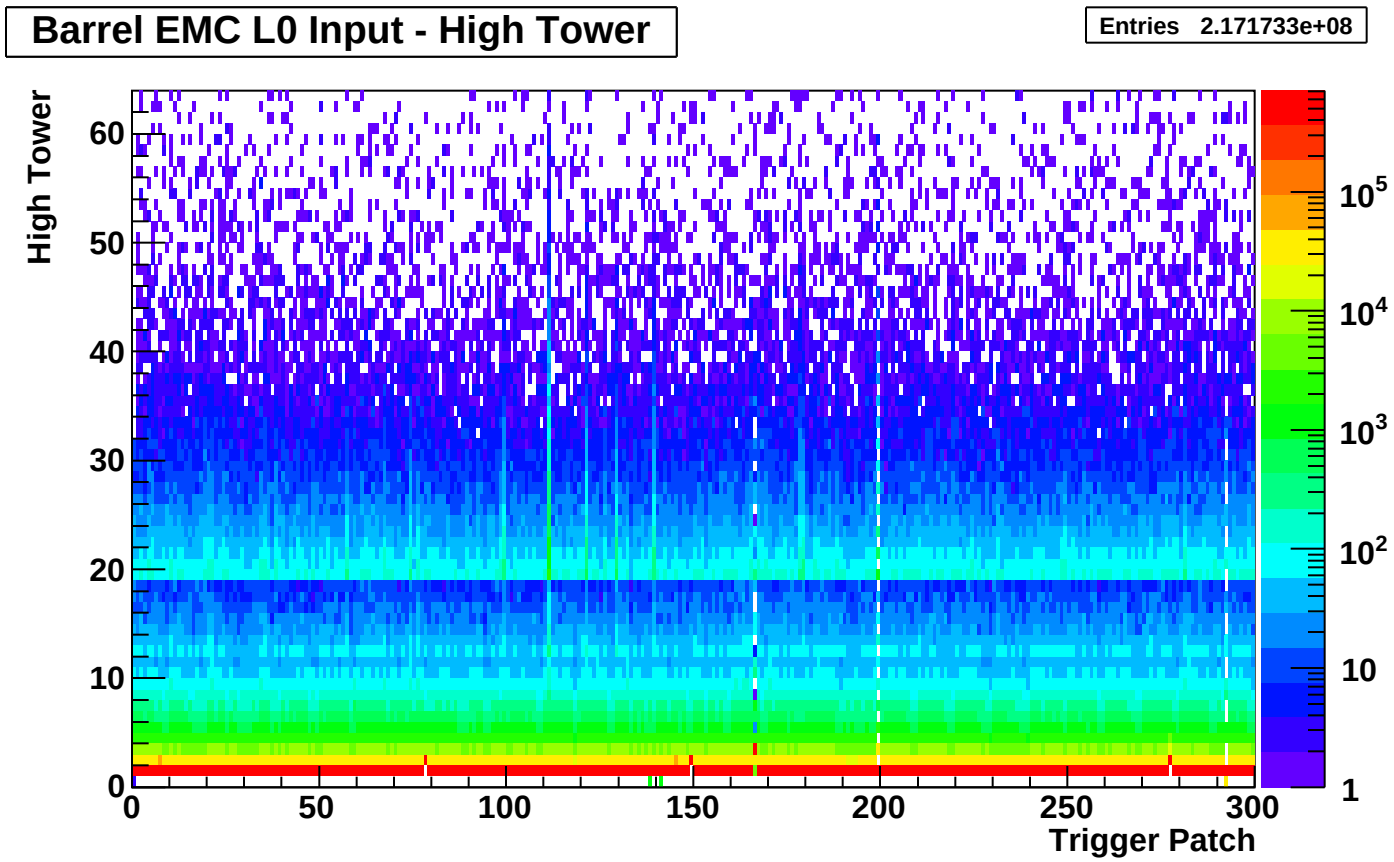

Last DSM Ch3 Input
Entries 16
LD301 Ch3 - Simulated
1 1 7 1 6
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

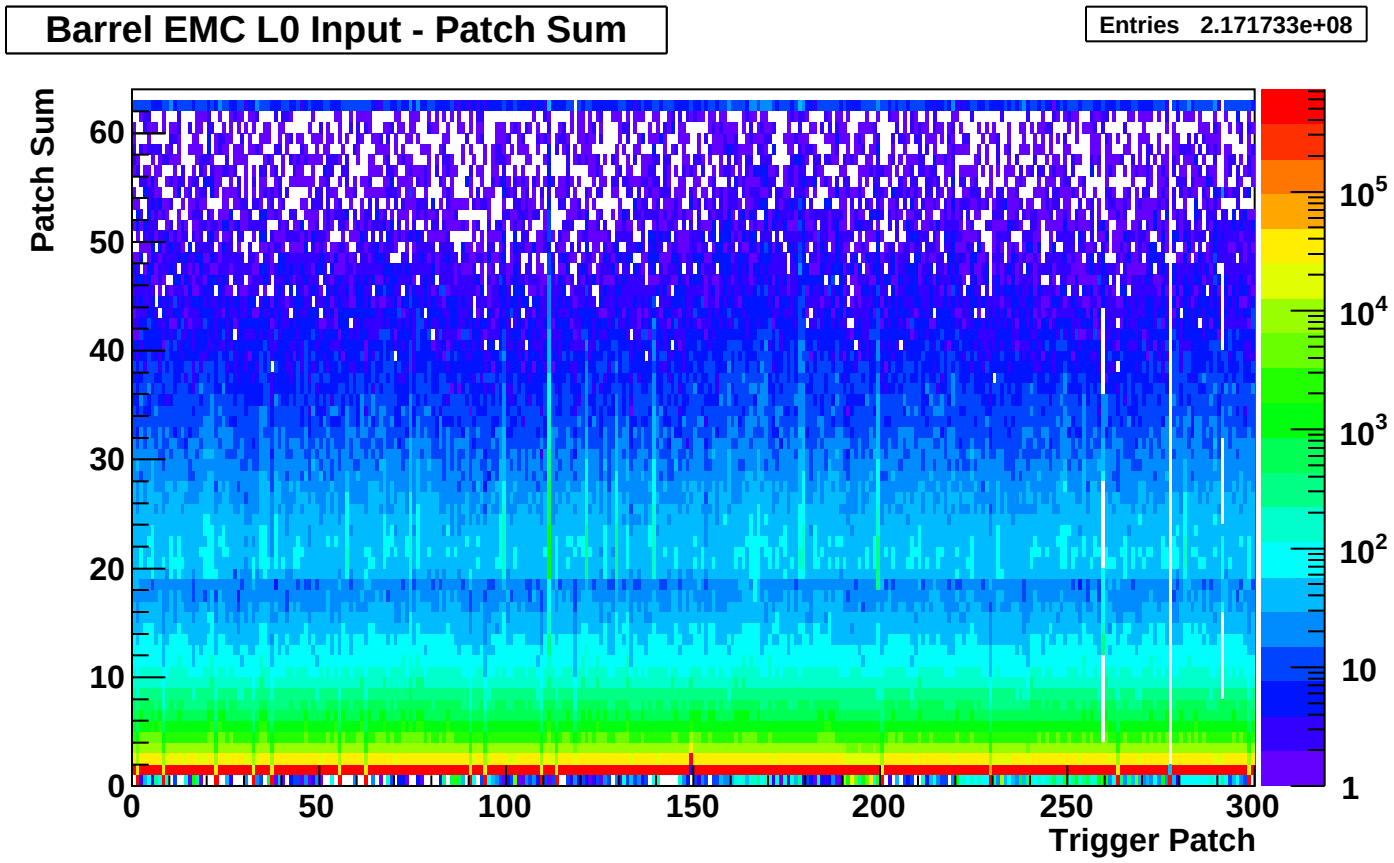

TCU bits
Entries 1.158258e+07
TCU bits
723911 723911 723911 723911 723911 723911 723911 723911 723911 723911 723911 723911 723911 723911 723911 723911
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

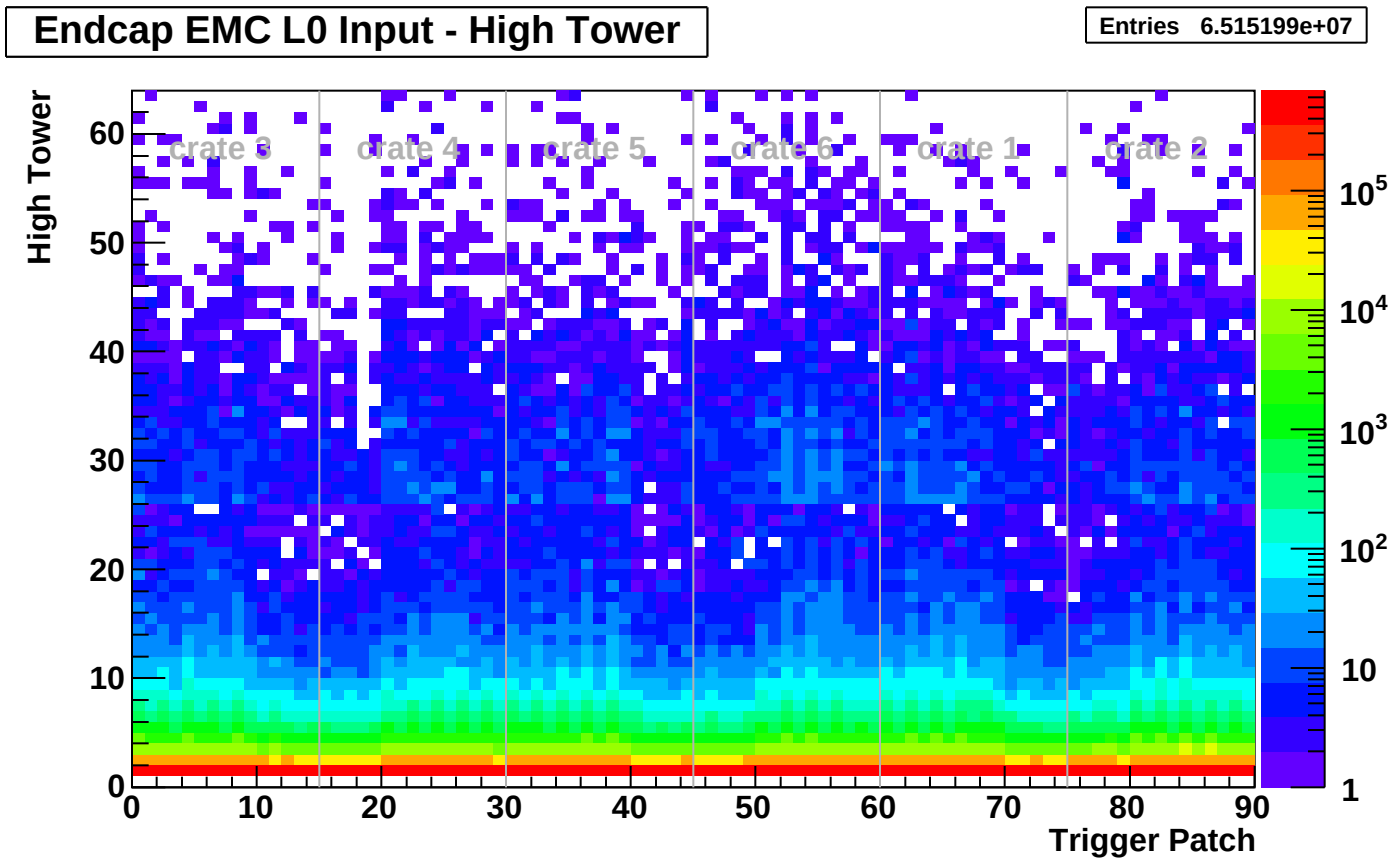

TCU bits
Entries 903635
TCU bits - Simulated
73385 159265 287947 8839 17161 354330 2708
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

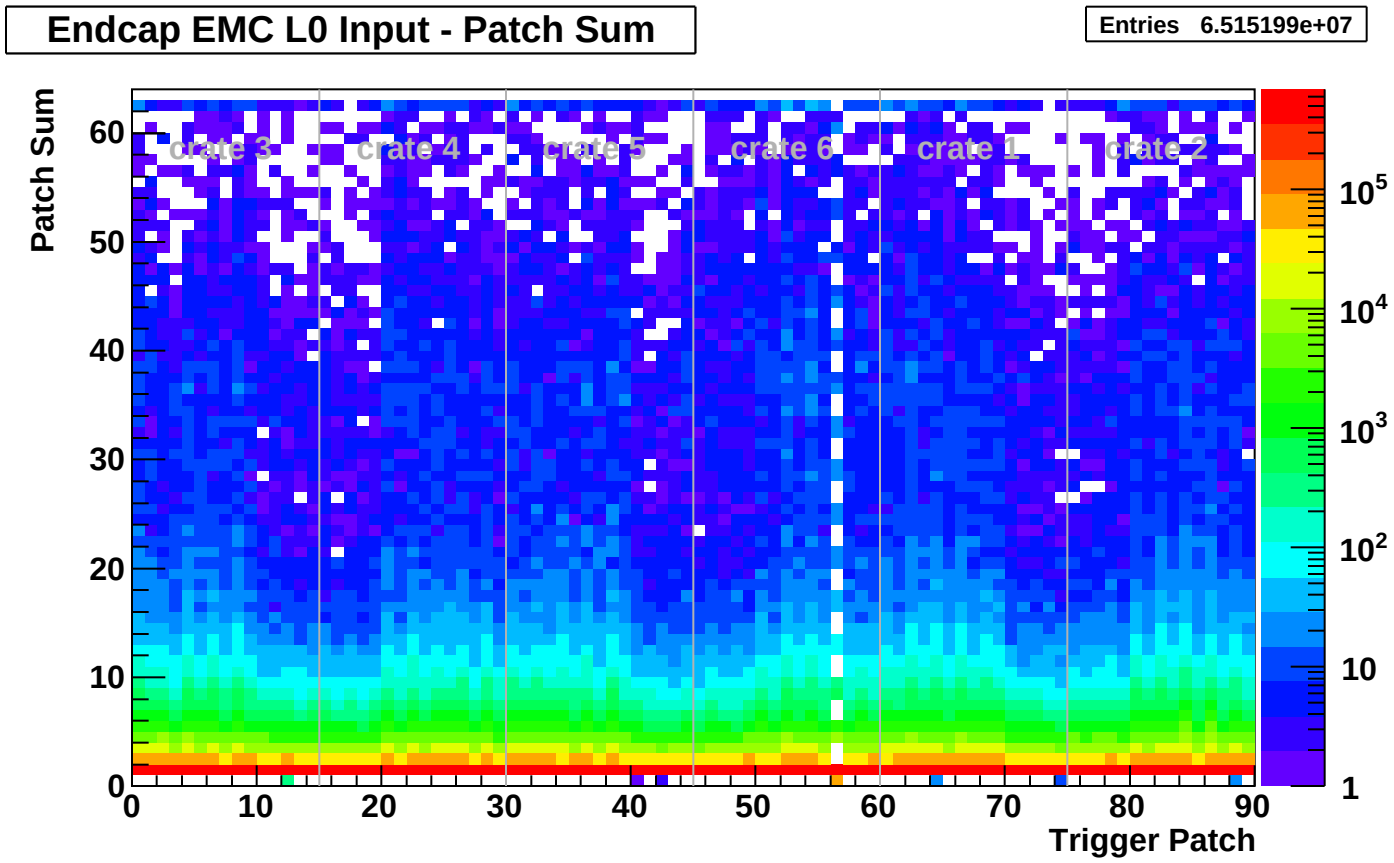

BunchId7Bit (BHT3)
Entries 12217

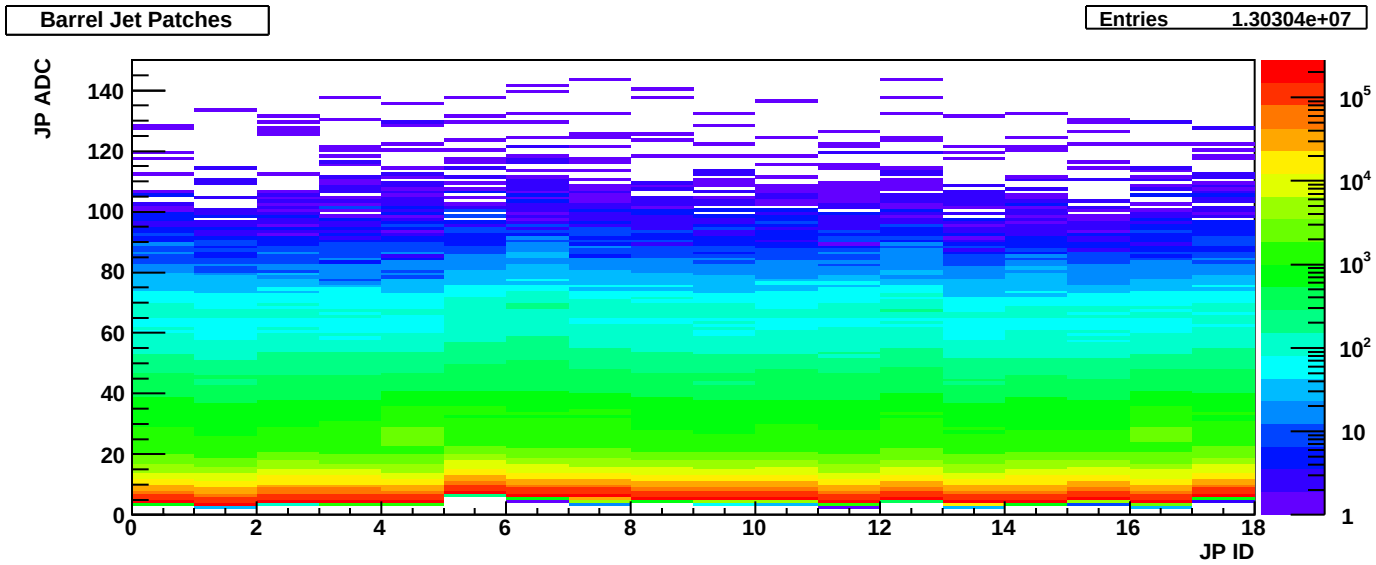

BunchId7Bit (all events)
Entries 723911

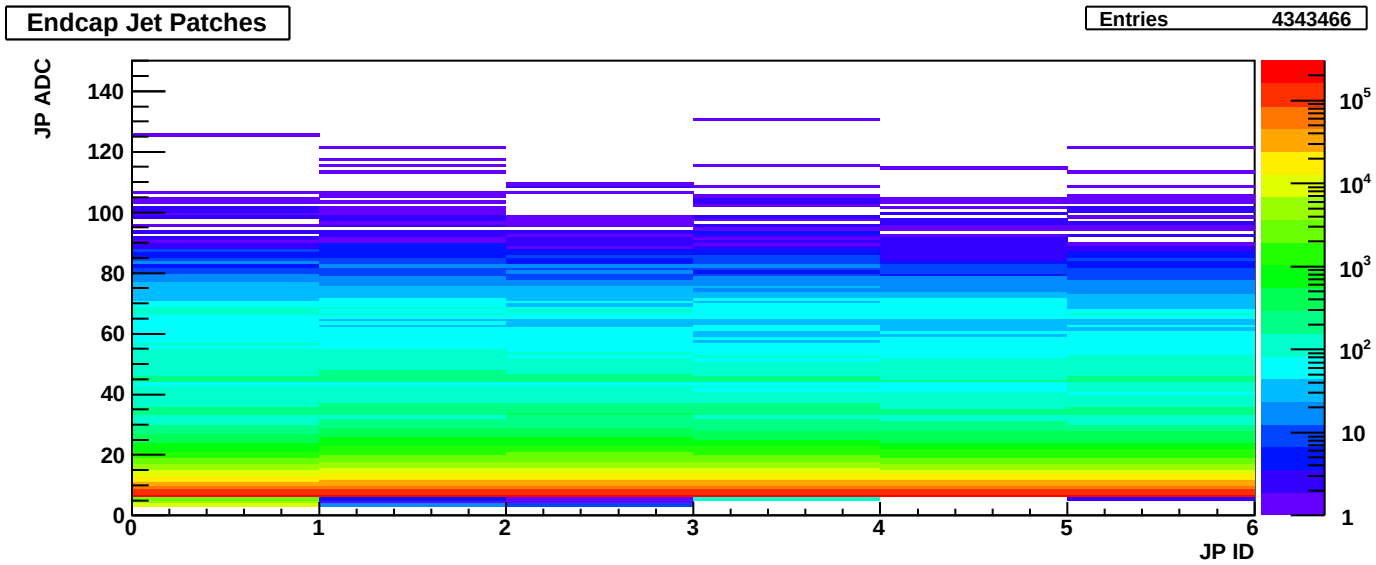

Barrel EMC L0 Input - High Tower
Entries 2.171733e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

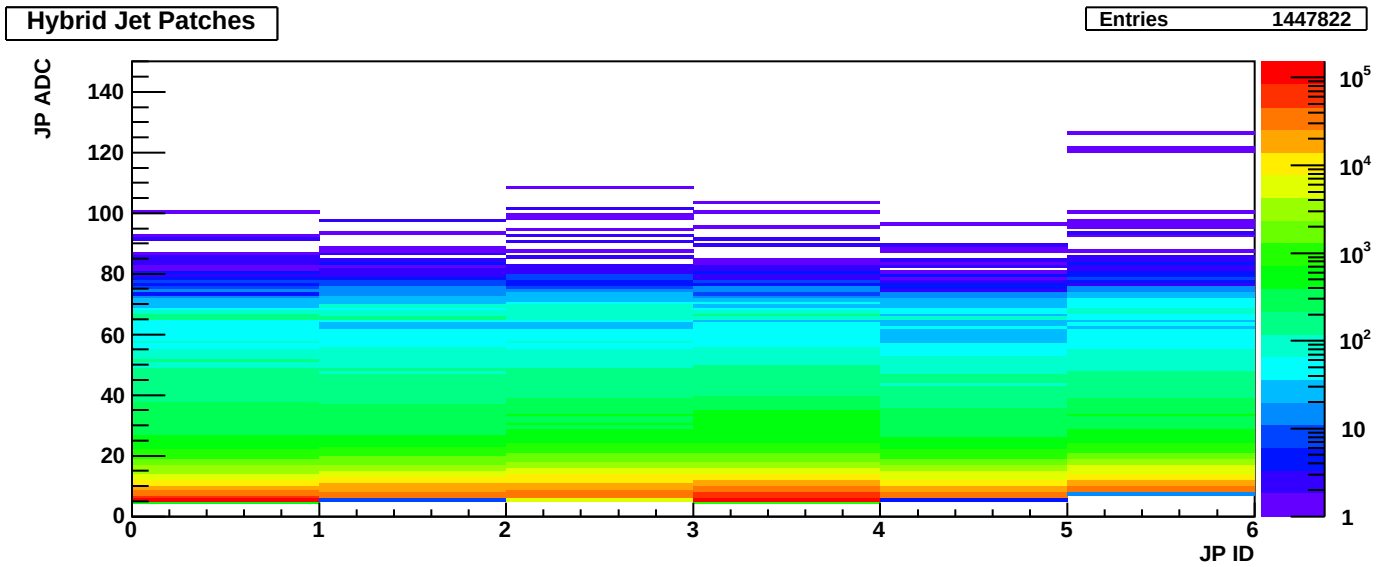

Barrel EMC L0 Input - Patch Sum
Entries 2.171733e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

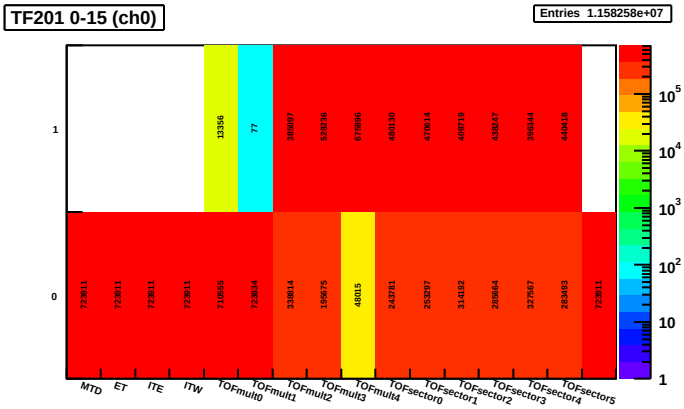

Endcap EMC L0 Input - High Tower
Entries 6.515199e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

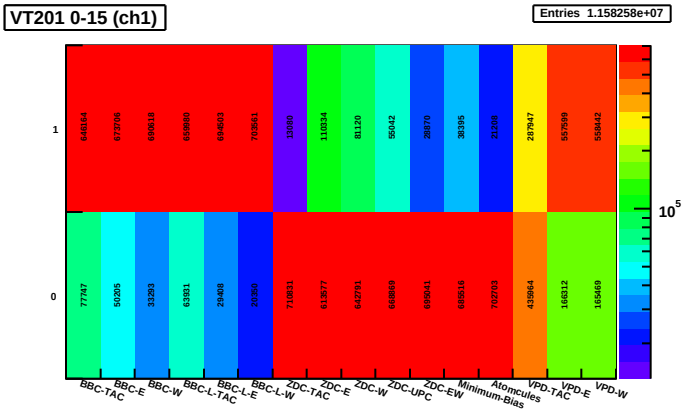

Endcap EMC L0 Input - Patch Sum
Entries 6.515199e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

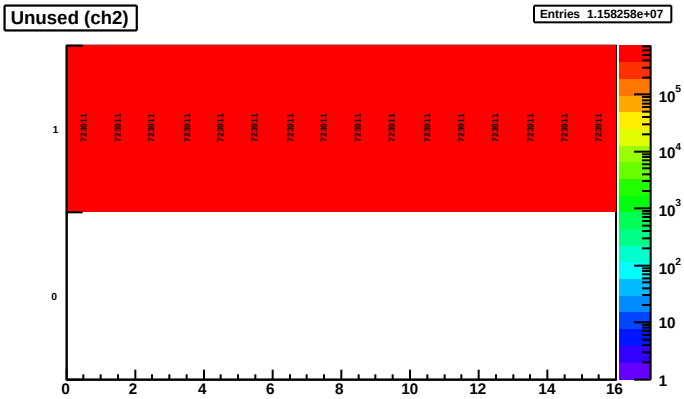

Barrel Jet Patches
Entries 1.30304e+07
JP ADC
JP ID

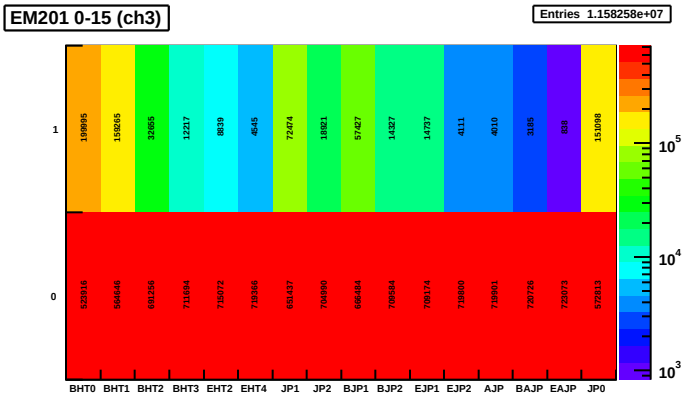

Endcap Jet Patches
Entries 4343466
JP ADC
JP ID

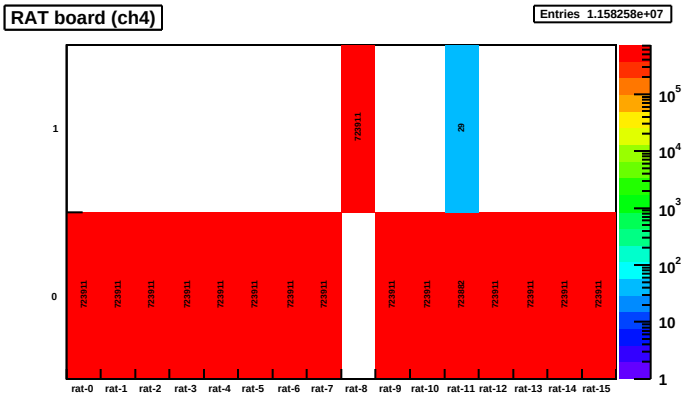

Hybrid Jet Patches
Entries 1447822
JP ADC
JP ID

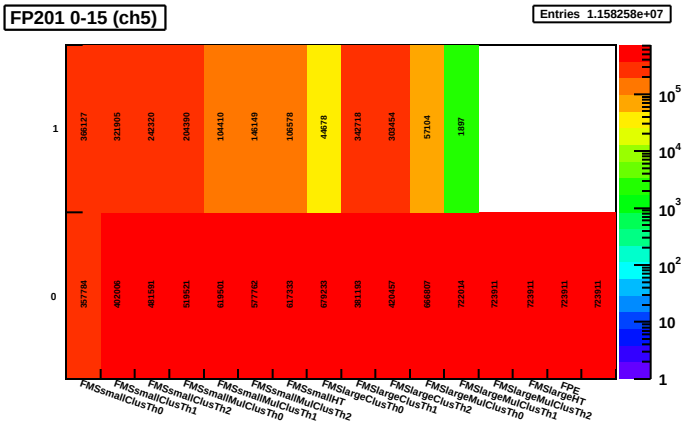

TF201 0-15 (ch0)
Entries 1.158258e+07
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

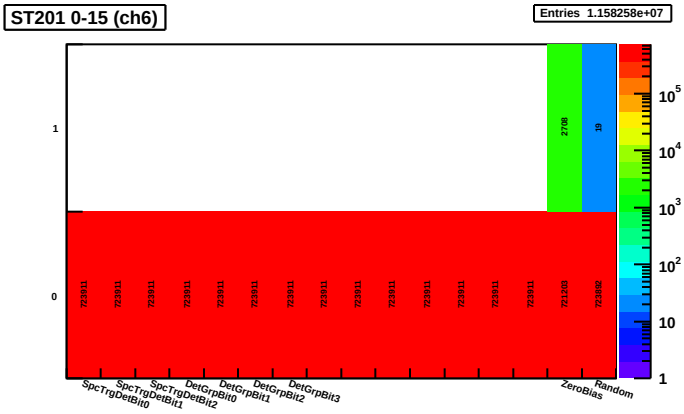

VT201 0-15 (ch1)
Entries 1.158258e+07
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

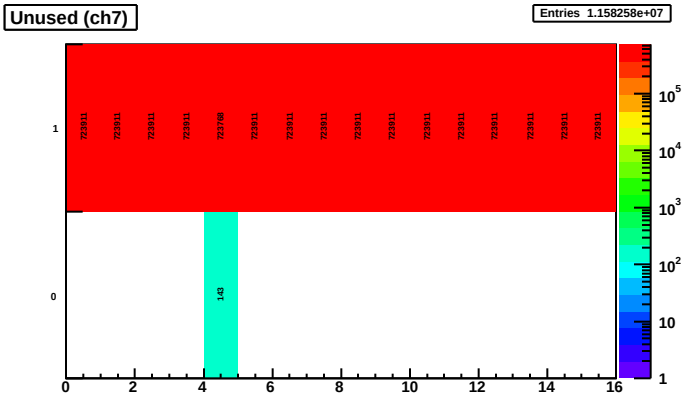

Unused (ch2)
Entries 1.158258e+07

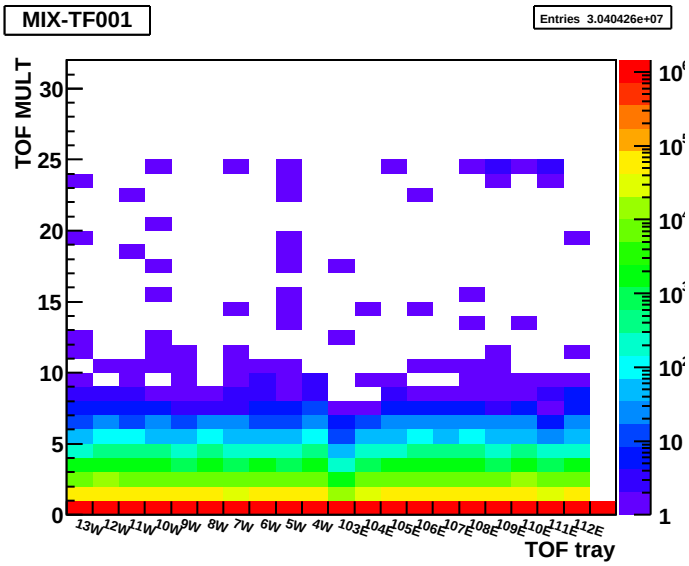

EM201 0-15 (ch3)
Entries 1.158258e+07
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

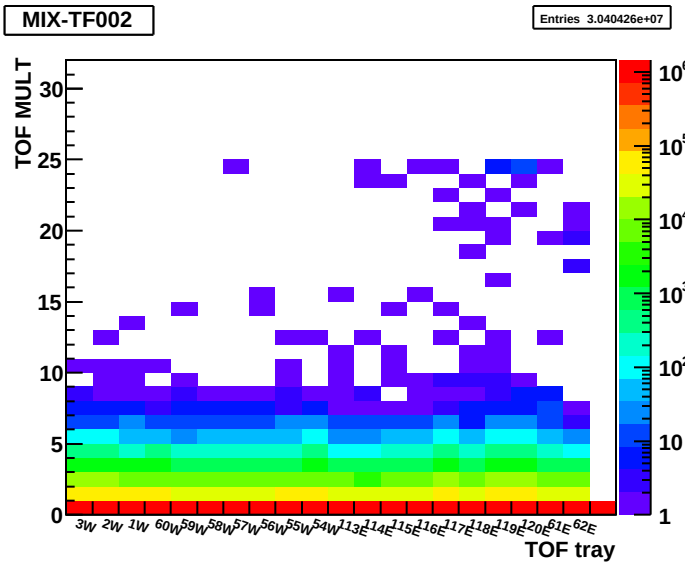

RAT board (ch4)
Entries 1.158258e+07
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

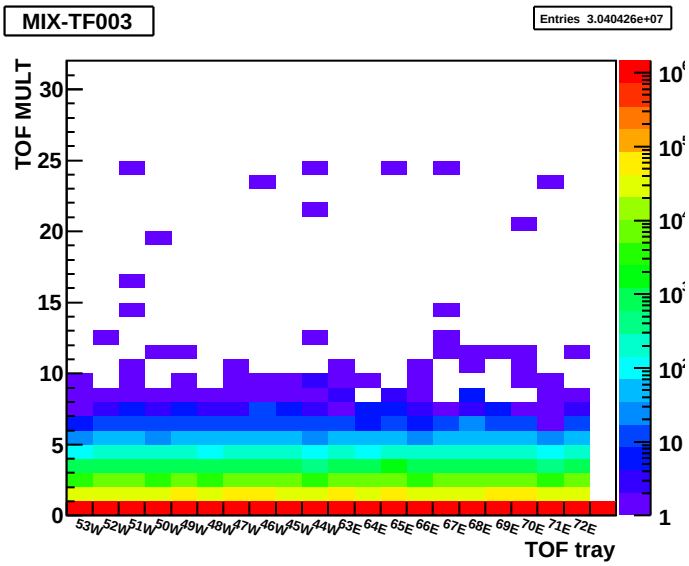

FP201 0-15 (ch5)
Entries 1.158258e+07
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

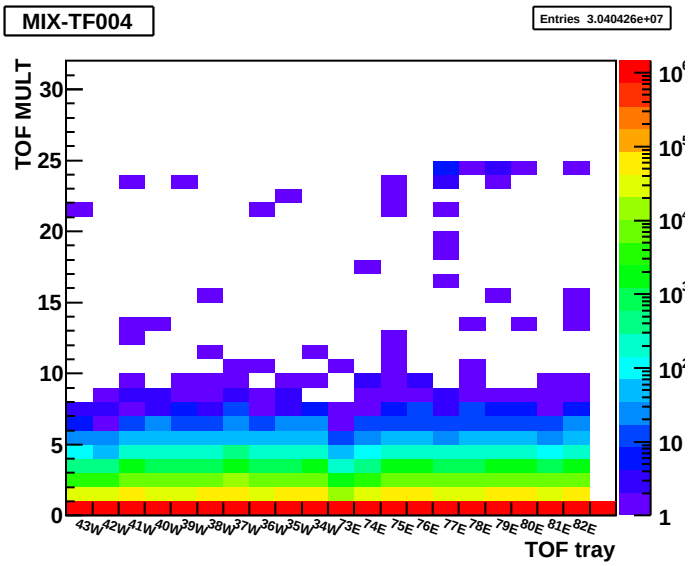

ST201 0-15 (ch6)
Entries 1.158258e+07
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

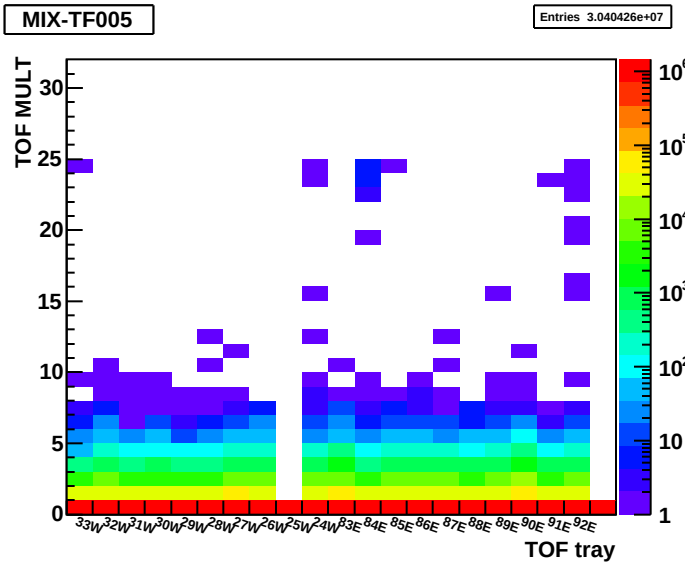

Unused (ch7)
Entries 1.158258e+07

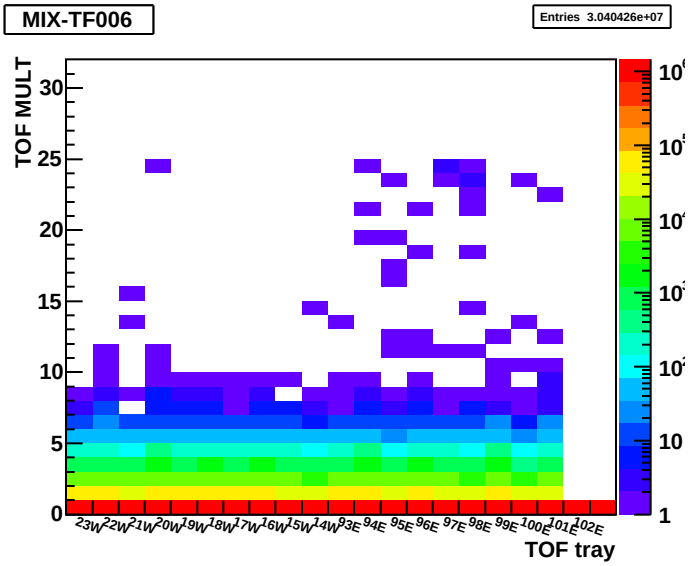

MIX-TF001
Entries 3.040426e+07
TOF MULT
TOF tray

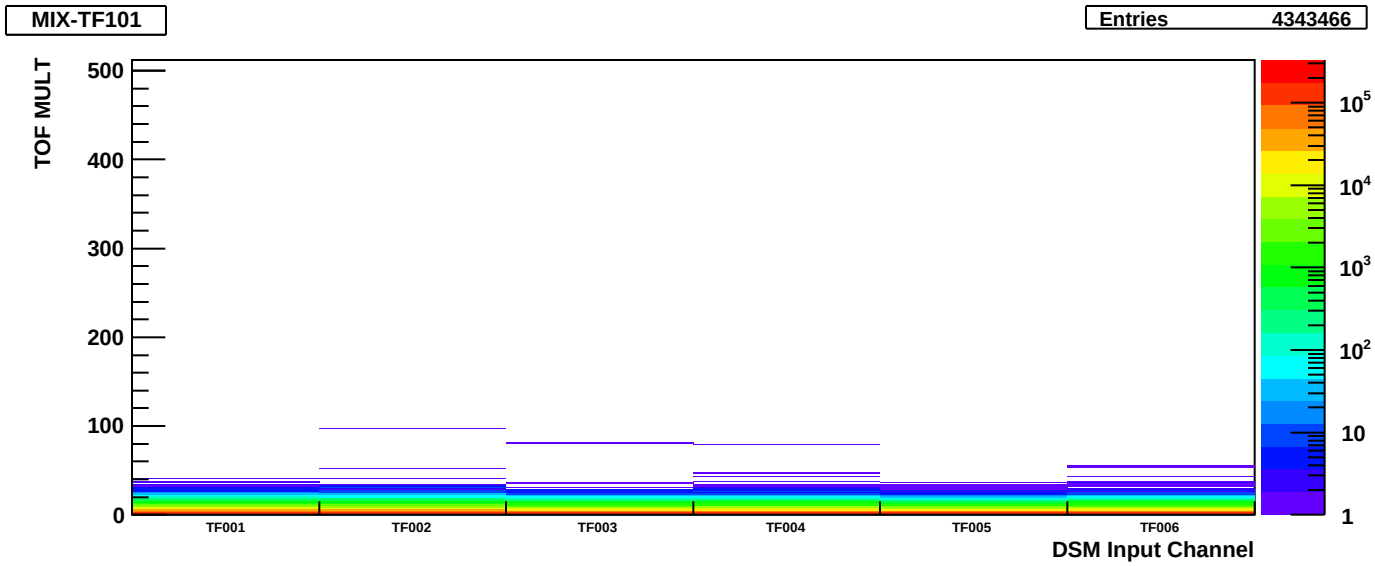

MIX-TF002
Entries 3.040426e+07
TOF MULT
TOF tray

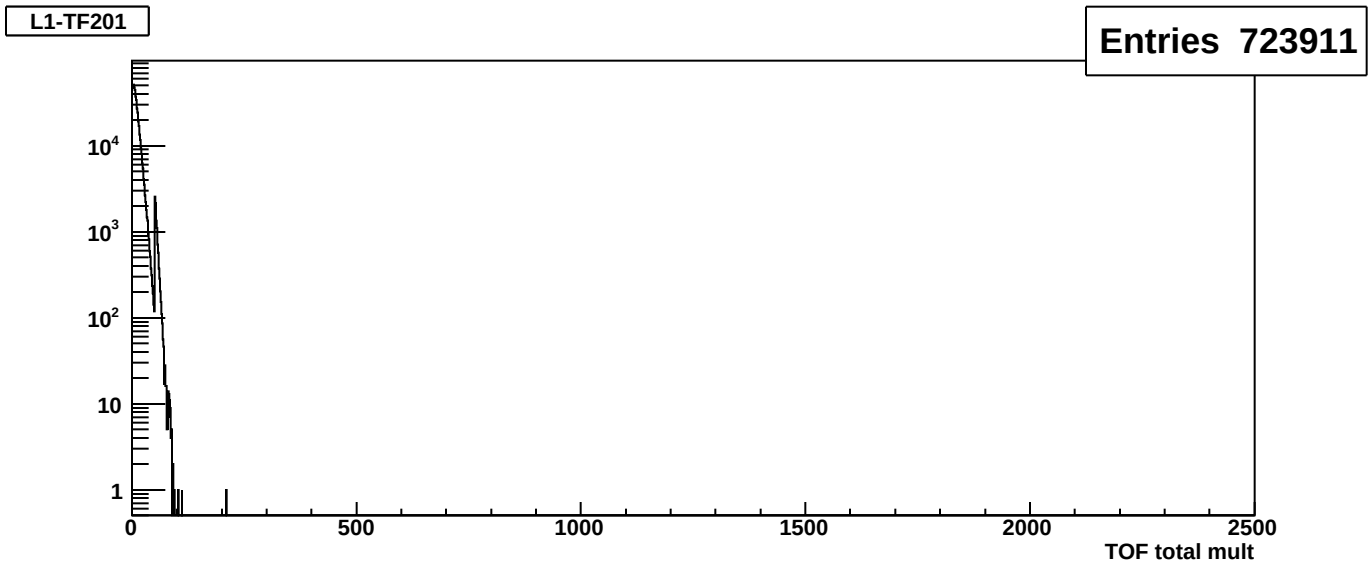

MIX-TF003
Entries 3.040426e+07
TOF MULT
TOF tray

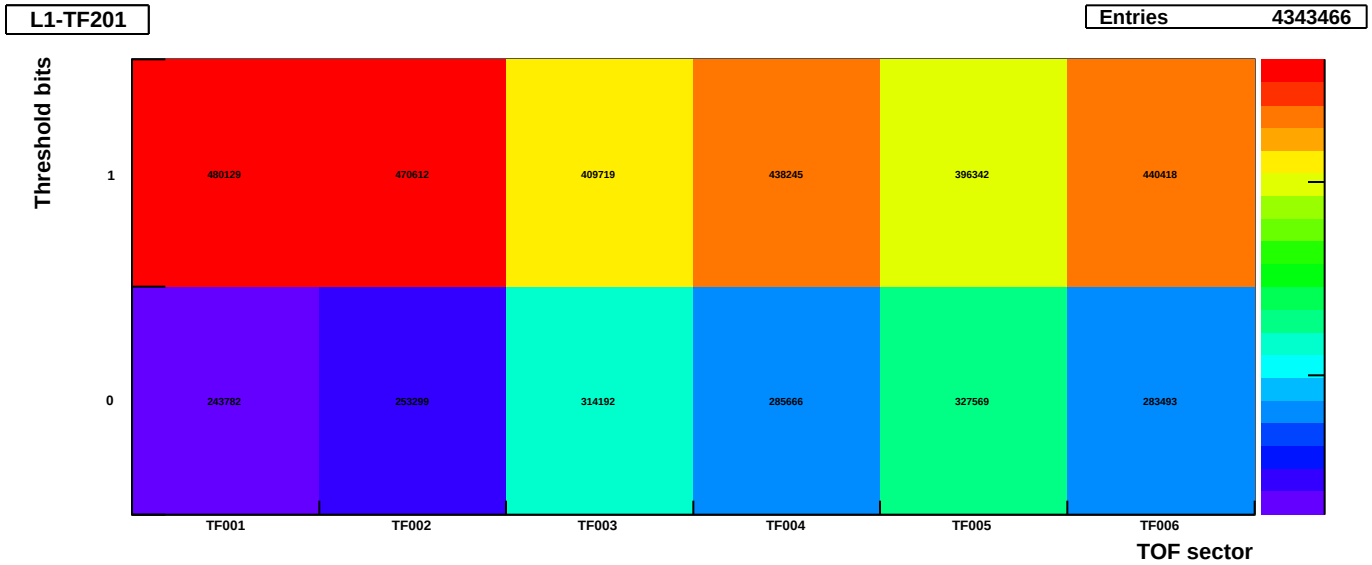

MIX-TF004
Entries 3.040426e+07
TOF MULT
TOF tray

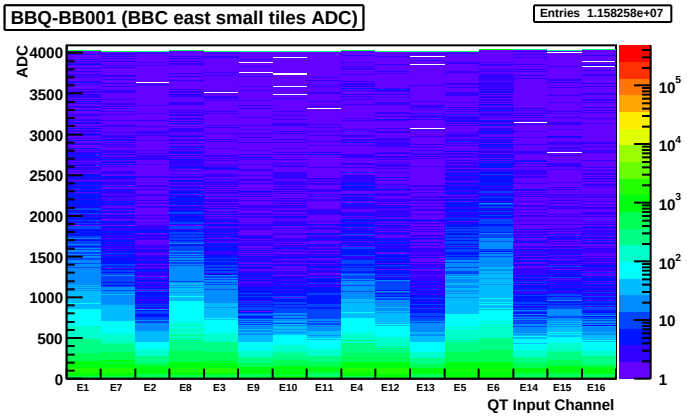

MIX-TF005
Entries 3.040426e+07
TOF MULT
TOF tray

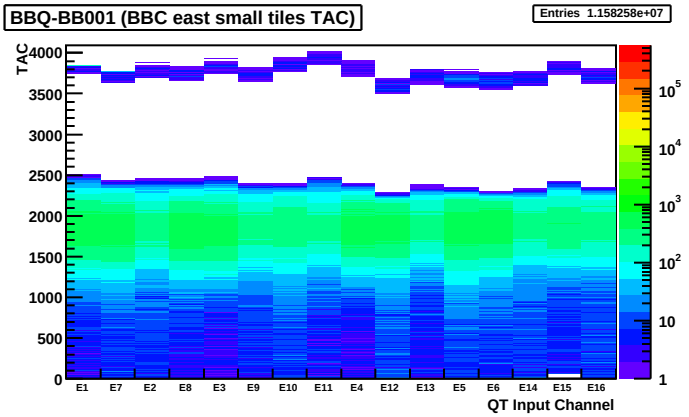

MIX-TF006
Entries 3.040426e+07
TOF MULT
TOF tray

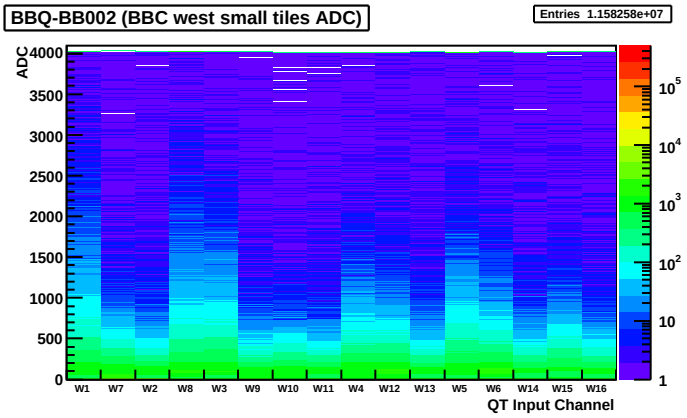

MIX-TF101
Entries 4343466
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

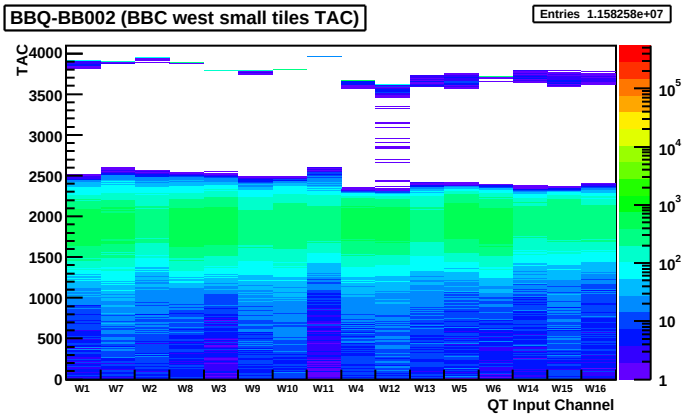

L1-TF201
Entries 723911
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

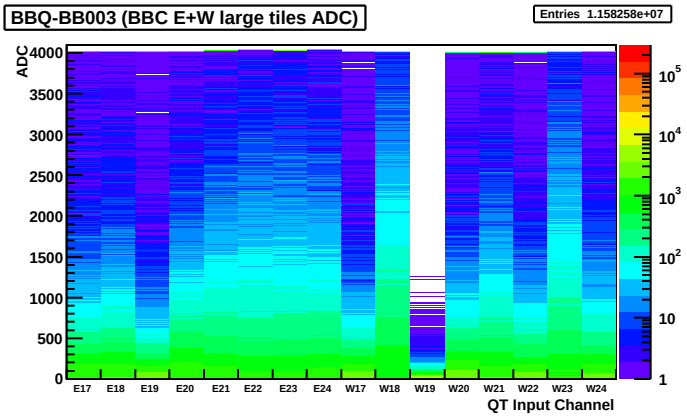

L1-TF201
Entries 4343466
Threshold bits
1
0
480129
470612
409719
438245
396342
440418
243782
253299
314192
285666
327569
283493
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

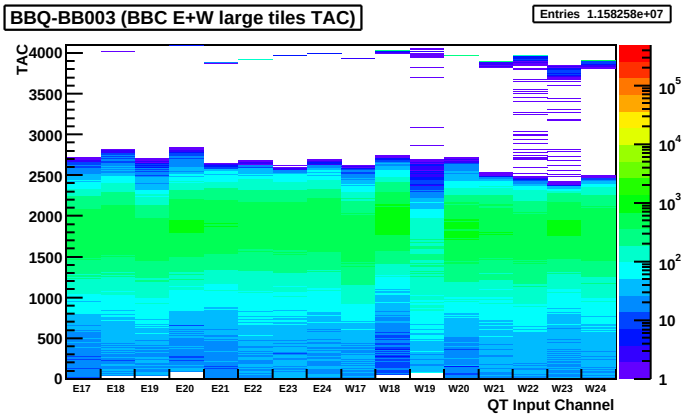

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.158258e+07
ADC
QT Input Channel

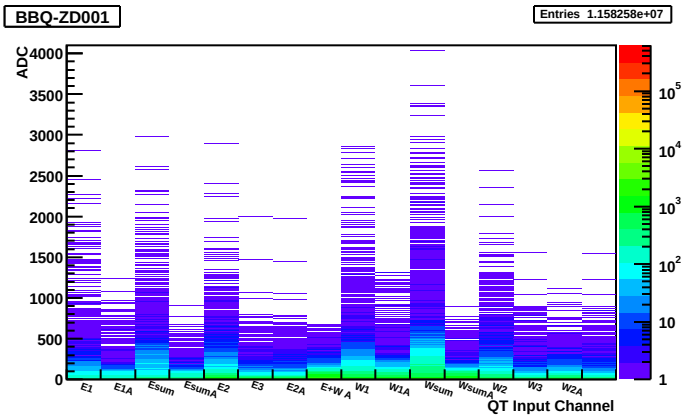

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.158258e+07
TAC
QT Input Channel

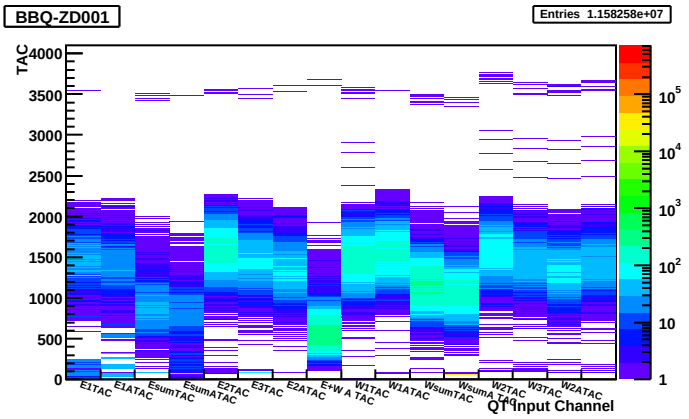

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.158258e+07
ADC
QT Input Channel

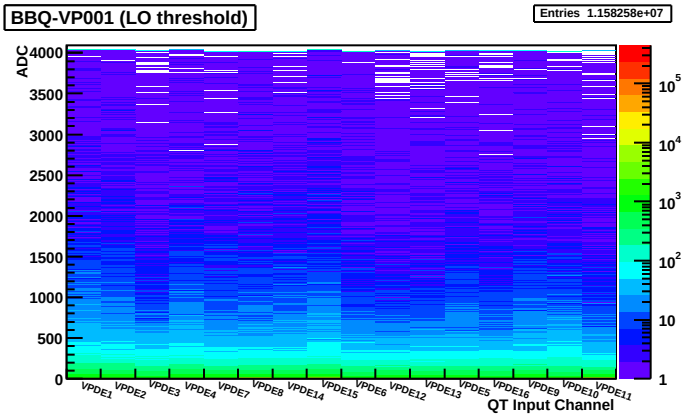

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.158258e+07
TAC
QT Input Channel

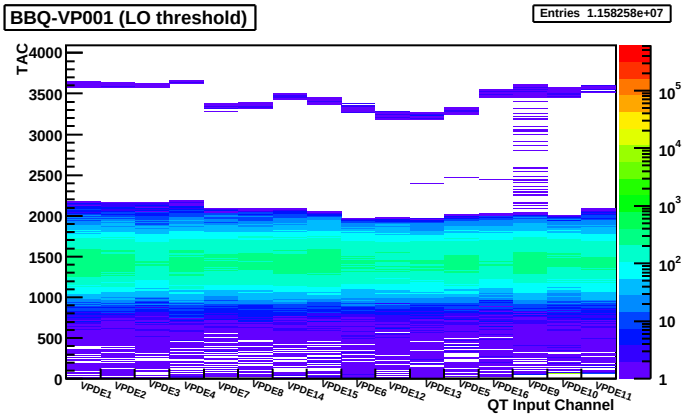

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.158258e+07
ADC
QT Input Channel

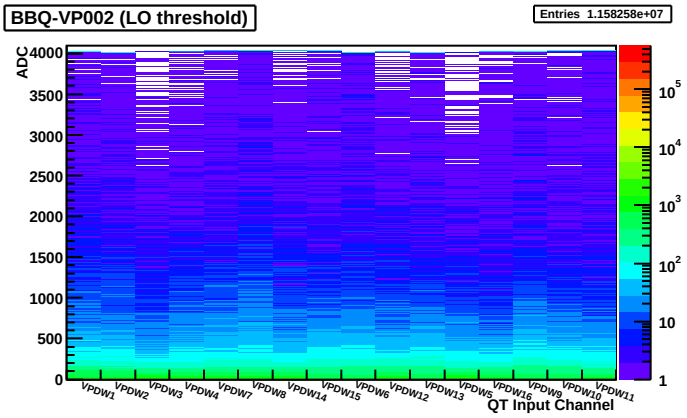

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.158258e+07
TAC
QT Input Channel

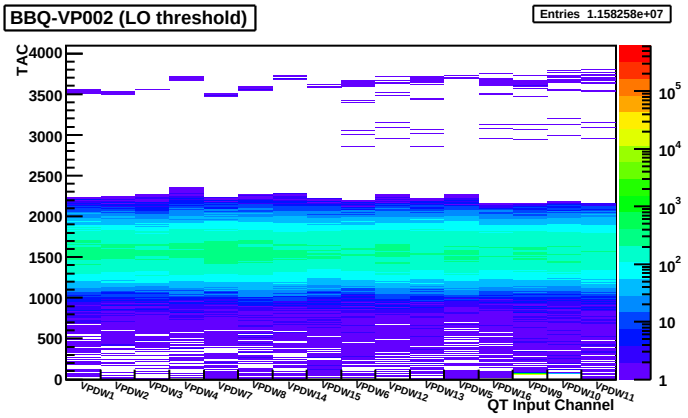

BBQ-ZD001
Entries 1.158258e+07
ADC
QT Input Channel

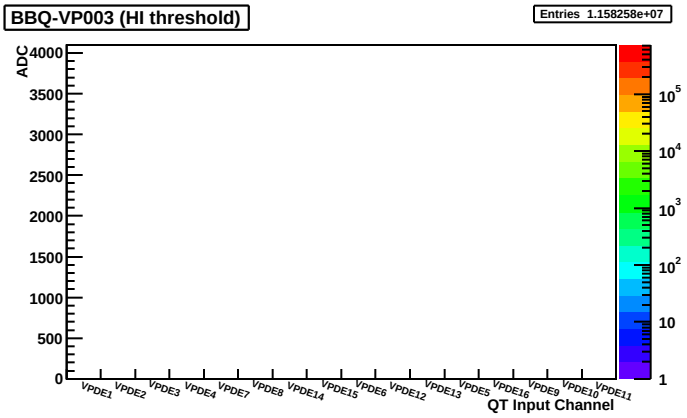

BBQ-ZD001
Entries 1.158258e+07
TAC
QT Input Channel

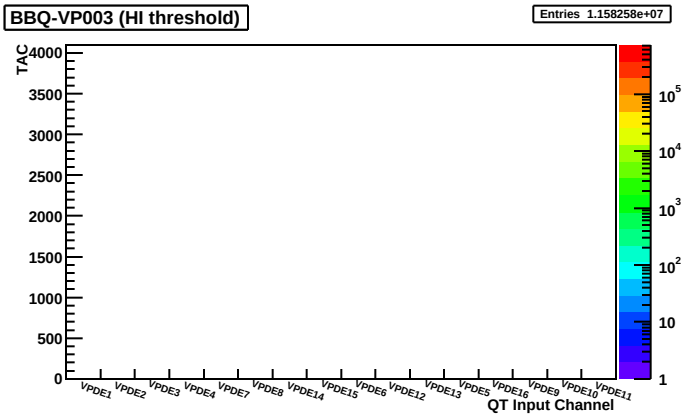

BBQ-VP001 (LO threshold)
Entries 1.158258e+07
ADC
QT Input Channel

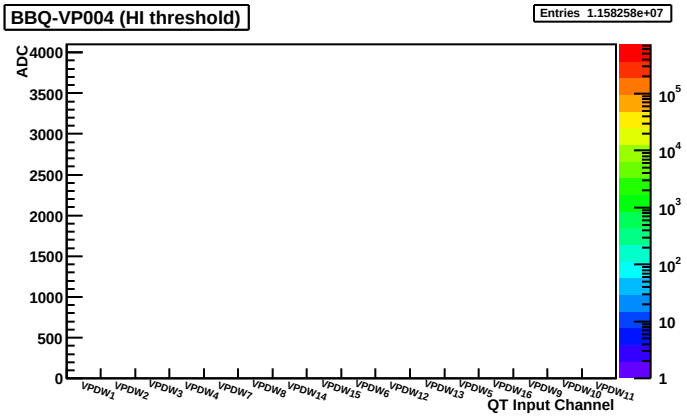

BBQ-VP001 (LO threshold)
Entries 1.158258e+07
TAC
QT Input Channel

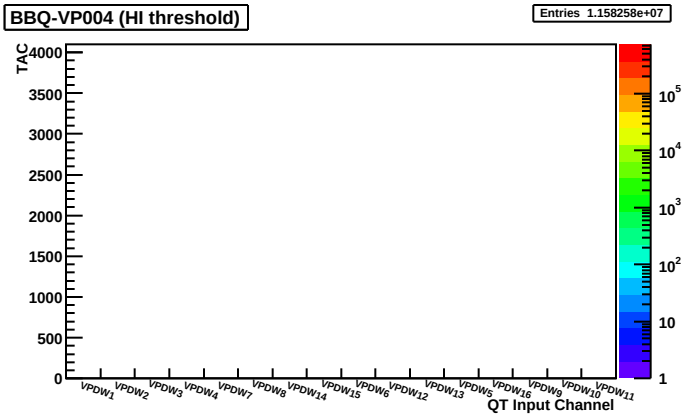

BBQ-VP002 (LO threshold)
Entries 1.158258e+07
ADC
QT Input Channel

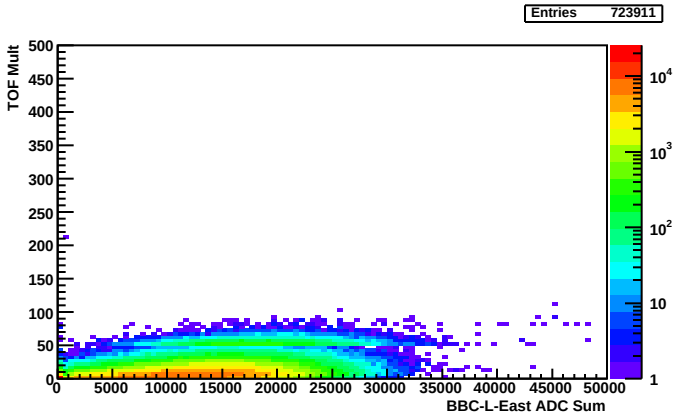

BBQ-VP002 (LO threshold)
Entries 1.158258e+07
TAC
QT Input Channel

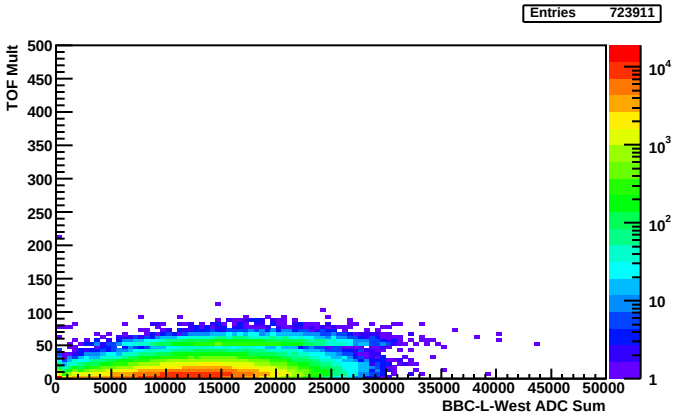

BBQ-VP003 (HI threshold)
Entries 1.158258e+07
ADC
QT Input Channel

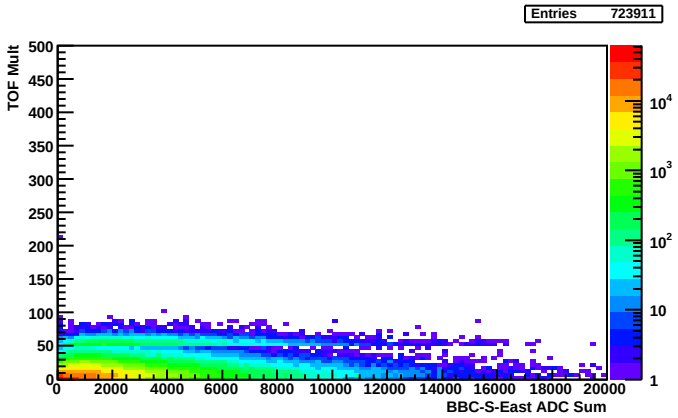

BBQ-VP003 (HI threshold)
Entries 1.158258e+07
TAC
QT Input Channel

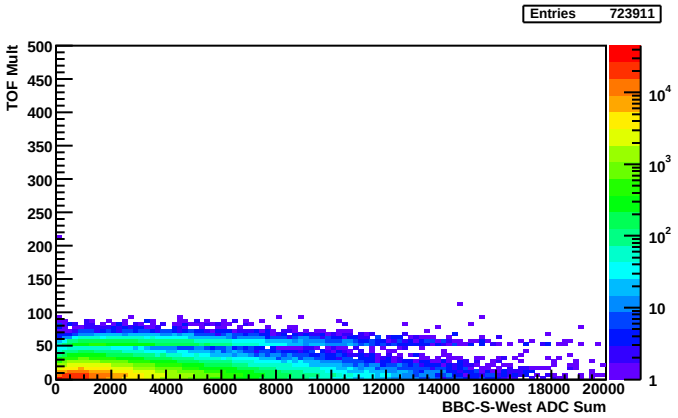

BBQ-VP004 (HI threshold)
Entries 1.158258e+07
ADC
QT Input Channel

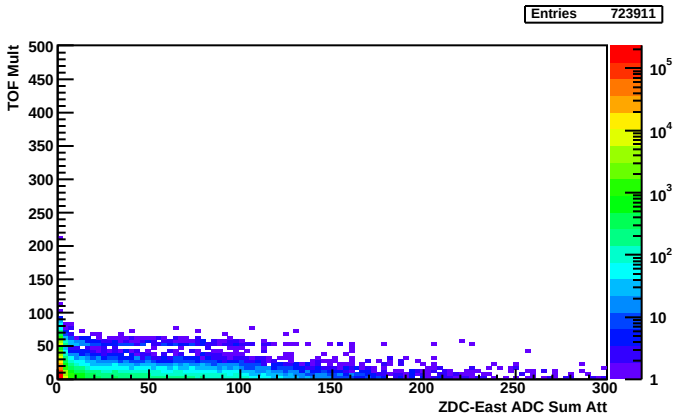

BBQ-VP004 (HI threshold)
Entries 1.158258e+07
TAC
QT Input Channel

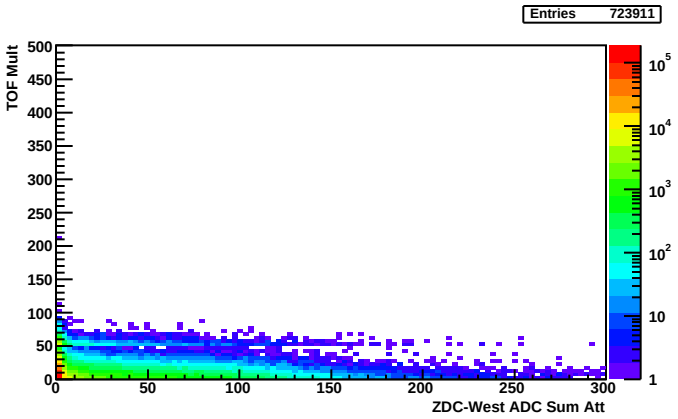

Entries 723911
TOF Mult
BBC-L-East ADC Sum

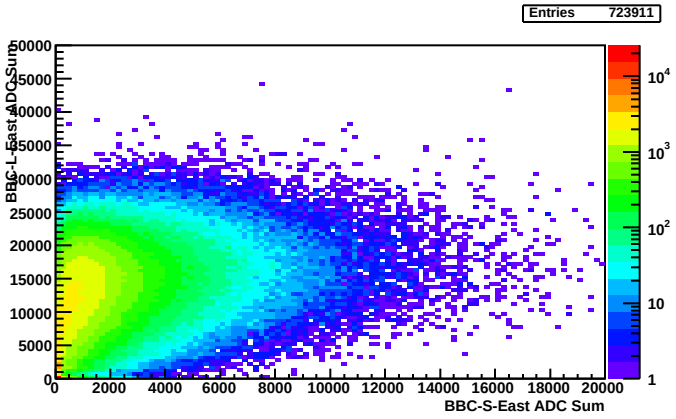

Entries 723911
TOF Mult
BBC-L-West ADC Sum

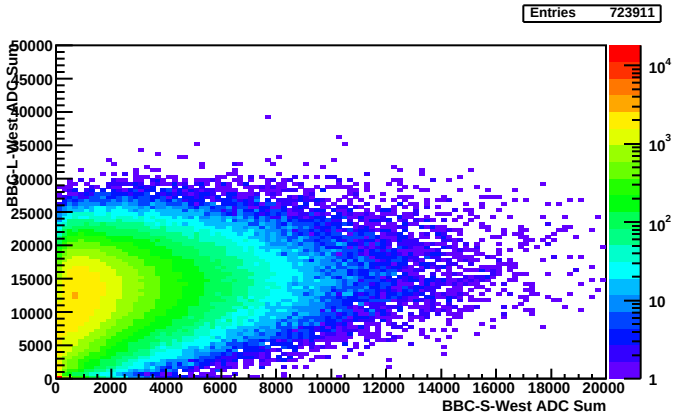

Entries 723911
TOF Mult
BBC-S-East ADC Sum

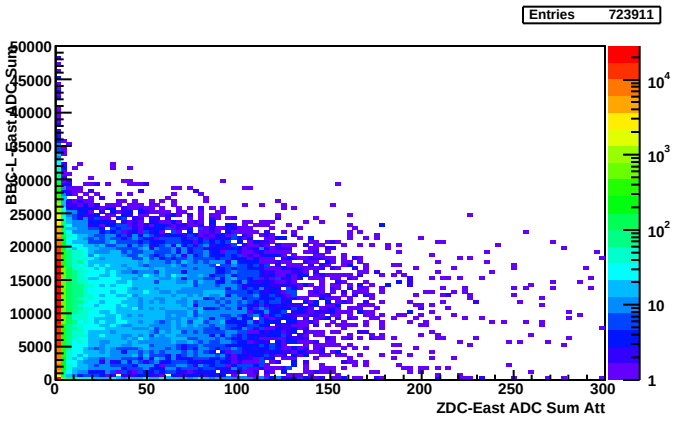

Entries 723911
TOF Mult
BBC-S-West ADC Sum

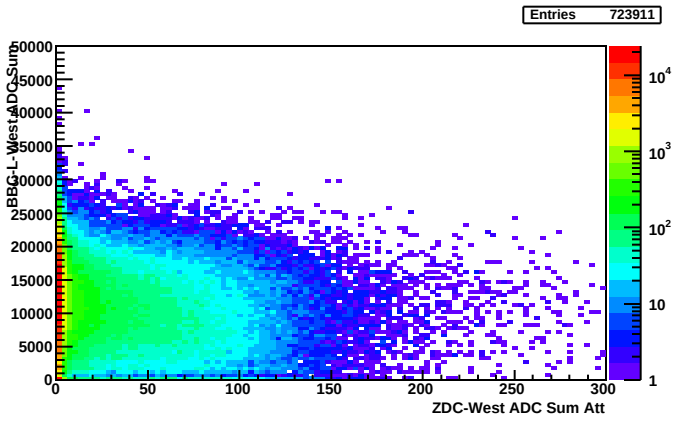

Entries 723911
TOF Mult
ZDC-East ADC Sum Att

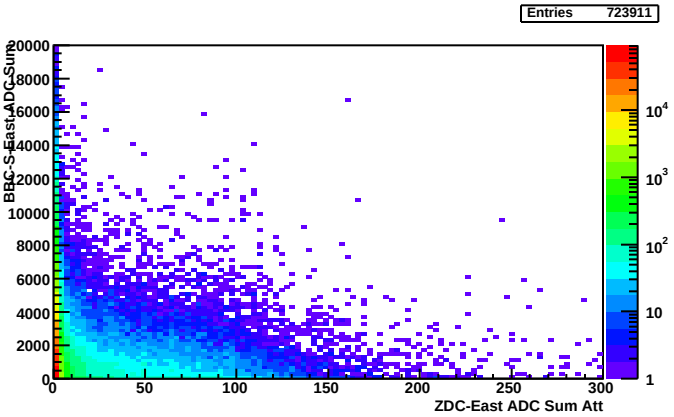

Entries 723911
TOF Mult
ZDC-West ADC Sum Att

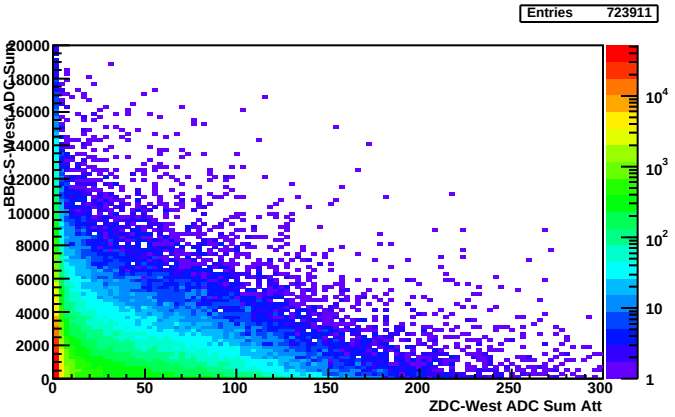

Entries 723911
BBC-L-East ADC Sum
BBC-S-East ADC Sum

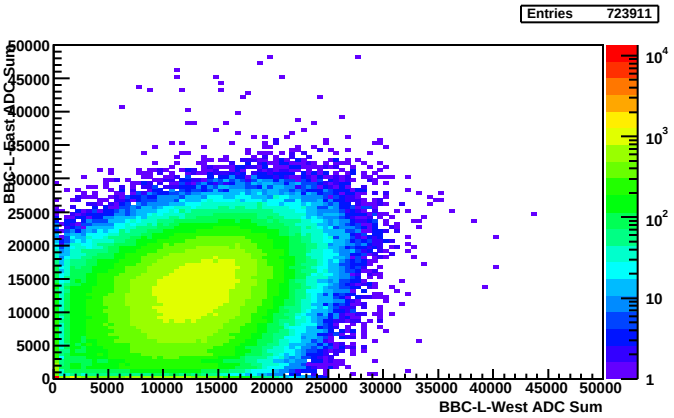

Entries 723911
BBC-L-West ADC Sum
BBC-S-West ADC Sum

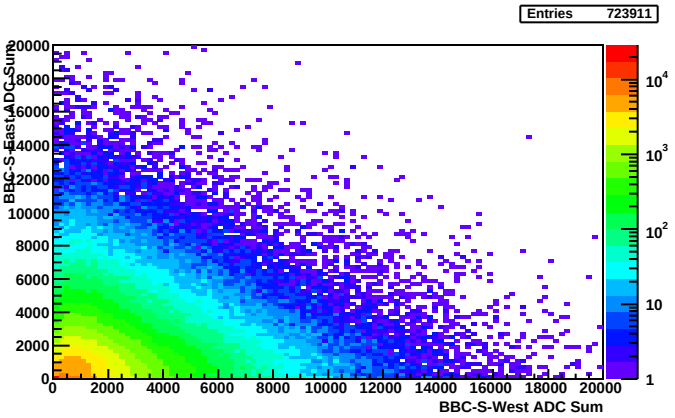

Entries 723911
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

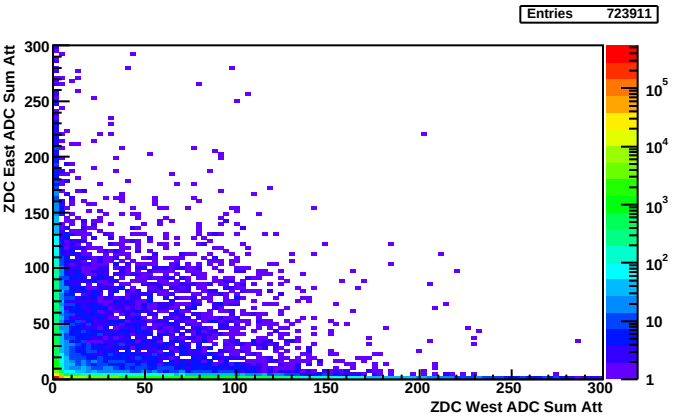

Entries 723911
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

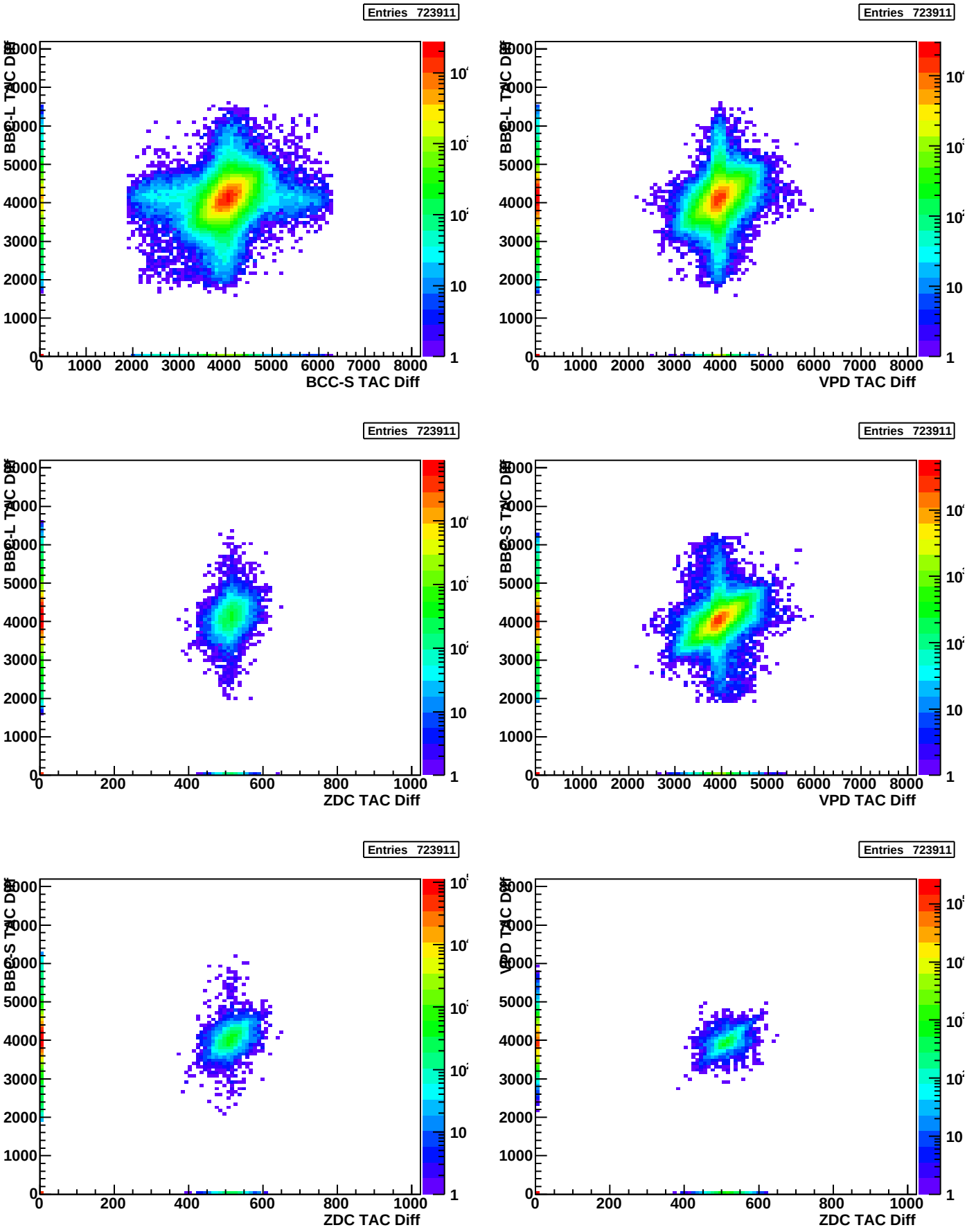

Entries 723911
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

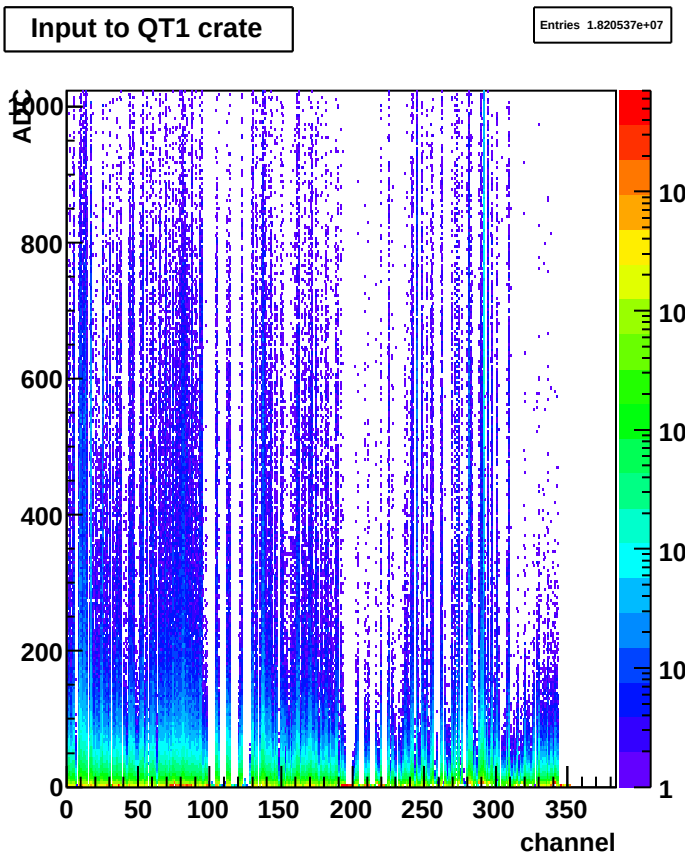

Entries 723911
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

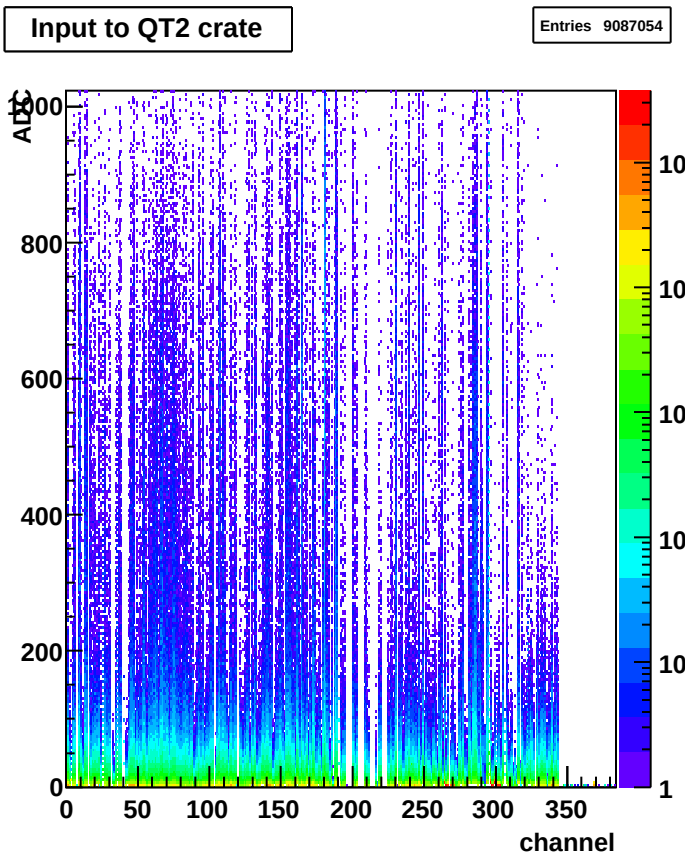

Entries 723911
BBC-L-East ADC Sum
BBC-L-West ADC Sum

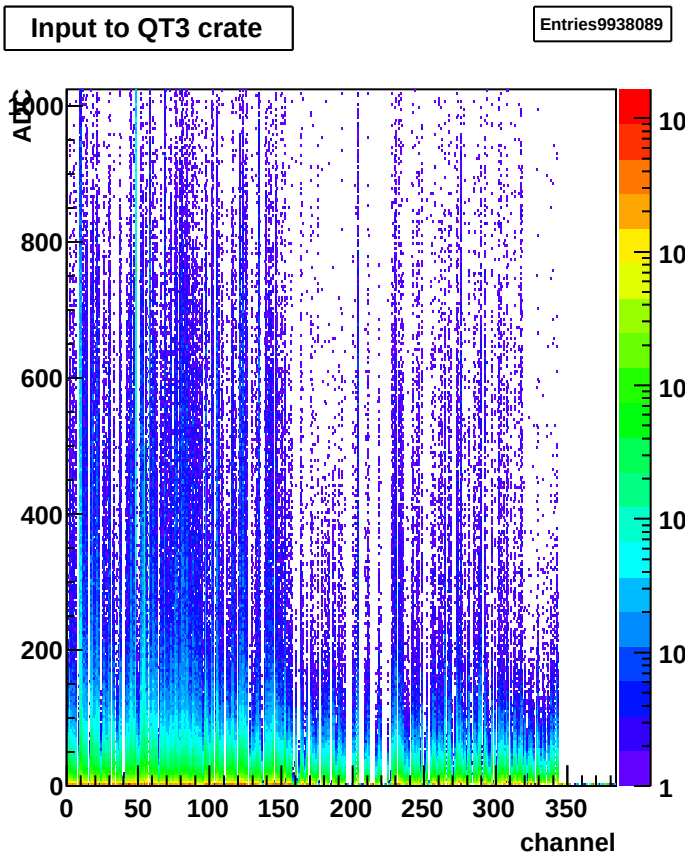

Entries 723911
BBC-S-East ADC Sum
BBC-S-West ADC Sum

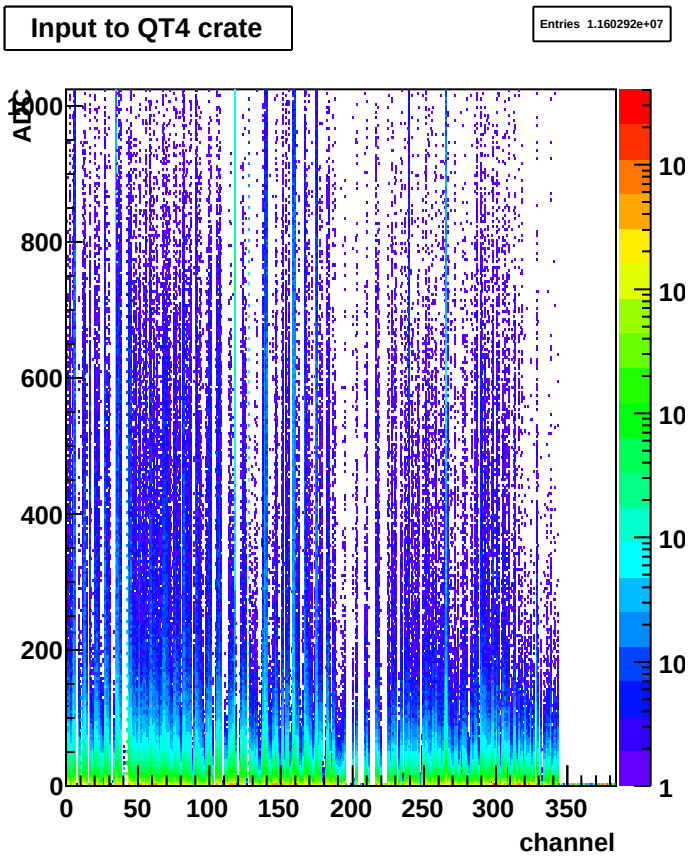

Entries 723911
ZDC East ADC Sum Att
ZDC West ADC Sum Att

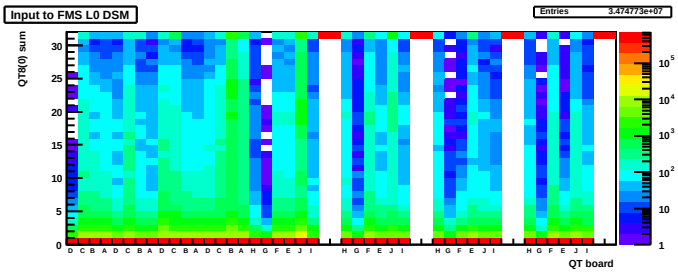

Entries 723911
BBC-L TAC Diff
BCC-S TAC Diff
Entries 723911
BBC-L TAC Diff
VPD TAC Diff
Entries 723911
BBC-L TAC Diff
ZDC TAC Diff
Entries 723911
BBC-S TAC Diff
VPD TAC Diff
Entries 723911
BBC-S TAC Diff
ZDC TAC Diff
Entries 723911
VPD TAC Diff
ZDC TAC Diff

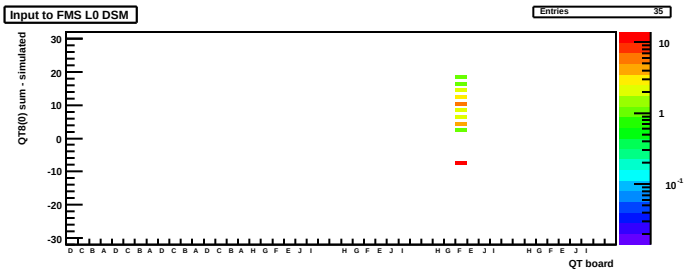

Input to QT1 crate
Entries 1.820537e+07
ADC
channel

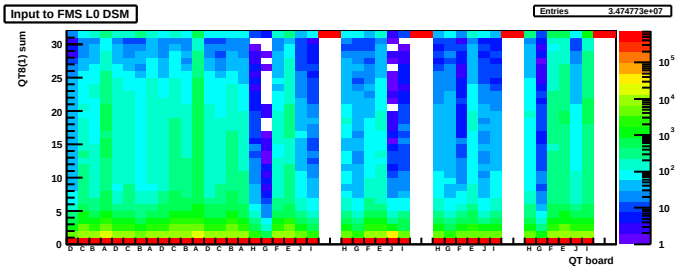

Input to QT2 crate
Entries 9087054
ADC
channel

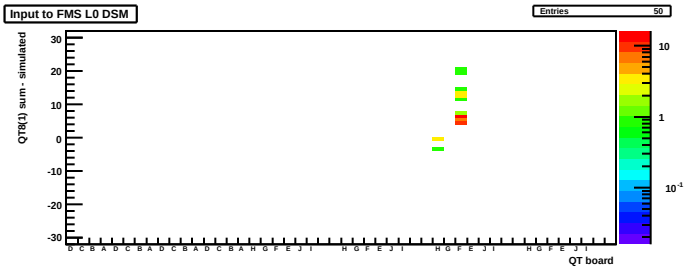

Input to QT3 crate
Entries9938089
ADC
channel

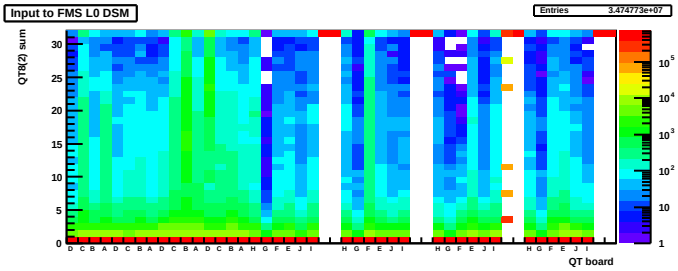

Input to QT4 crate
Entries 1.160292e+07
ADC
channel

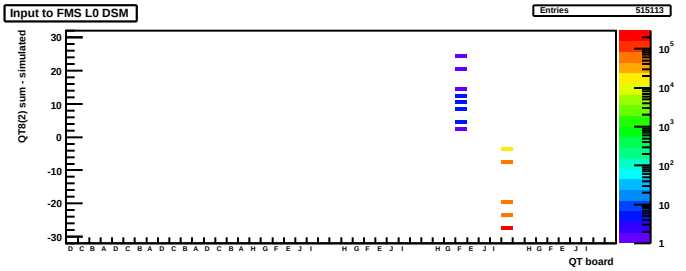

Input to FMS L0 DSM
Entries 3.474773e+07
QT8(0) sum
QT board

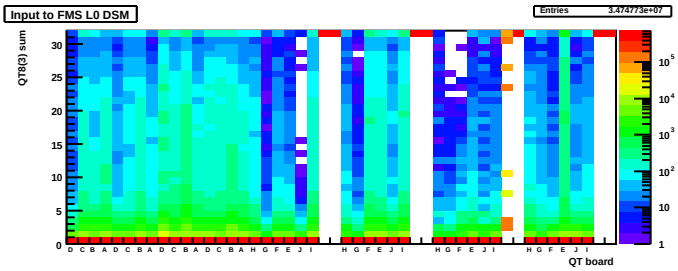

Input to FMS L0 DSM
Entries 35
QT8(0) sum - simulated
QT board

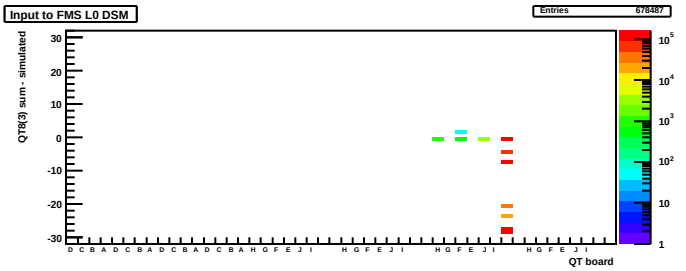

Input to FMS L0 DSM
Entries 3.474773e+07
QT8(1) sum
QT board

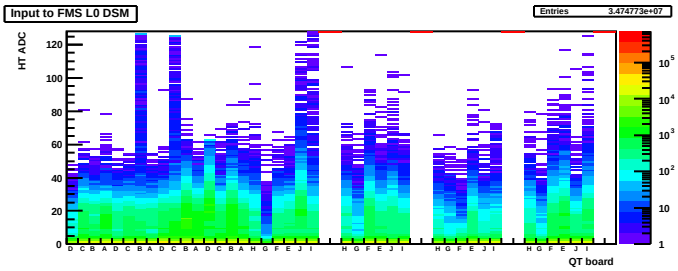

Input to FMS L0 DSM
Entries 50
QT8(1) sum - simulated
QT board

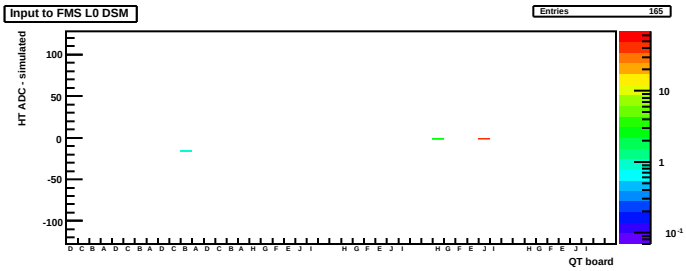

Input to FMS L0 DSM
Entries 3.474773e+07
QT8(2) sum
QT board

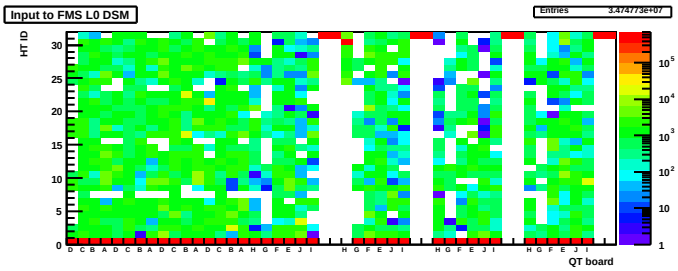

Input to FMS L0 DSM
Entries 515113
QT8(2) sum - simulated
QT board

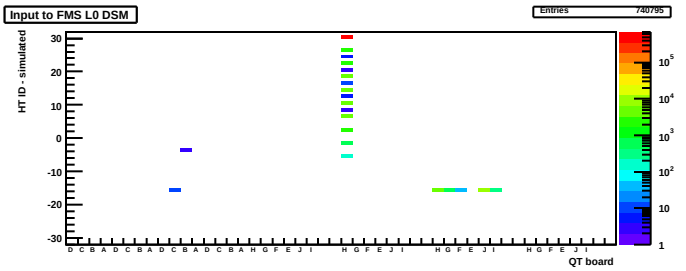

Input to FMS L0 DSM
Entries 3.474773e+07
QT8(3) sum
QT board

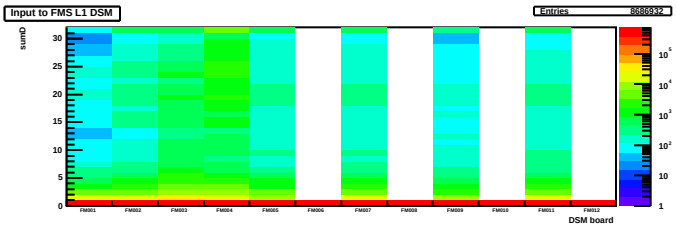

Input to FMS L0 DSM
Entries 678487
QT8(3) sum - simulated
QT board

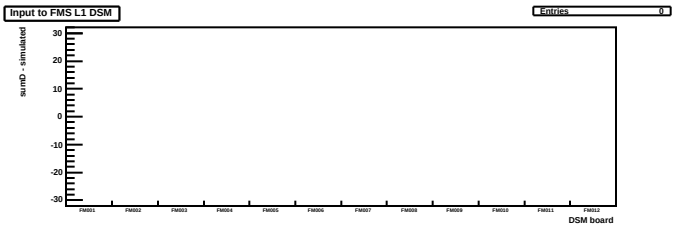

Input to FMS L0 DSM
Entries 3.474773e+07
HT ADC
QT board

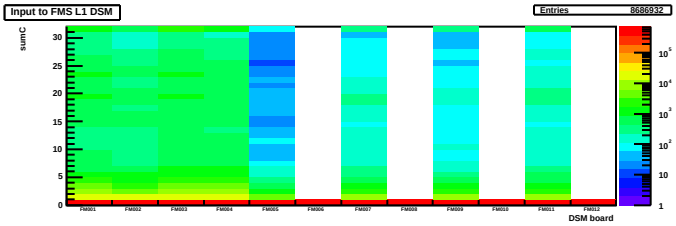

Input to FMS L0 DSM
Entries 165
HT ADC - simulated
QT board

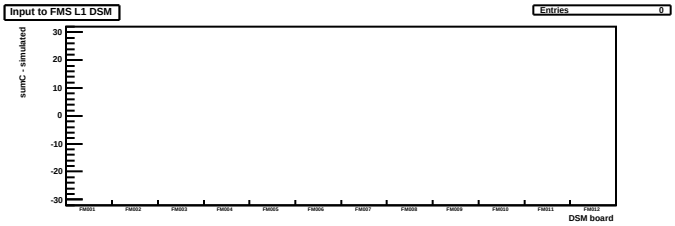

Input to FMS L0 DSM
Entries 3.474773e+07
HT ID
QT board

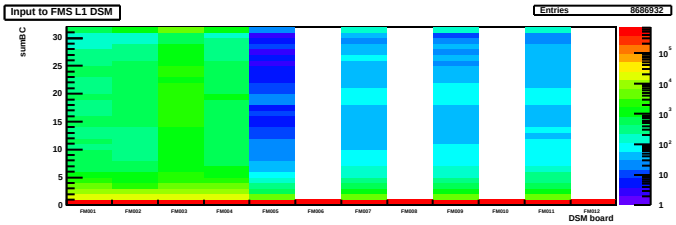

Input to FMS L0 DSM
Entries 740795
HT ID - simulated
QT board

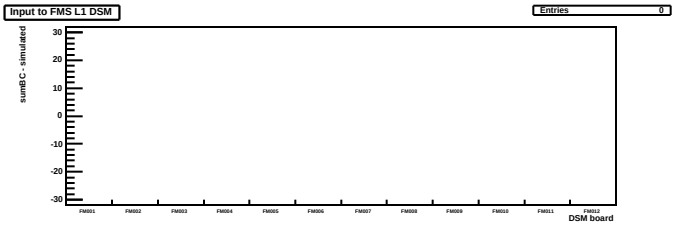

Input to FMS L1 DSM
Entries 8686932
sumD
DSM board

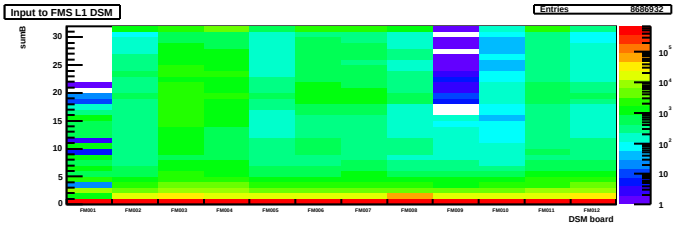

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

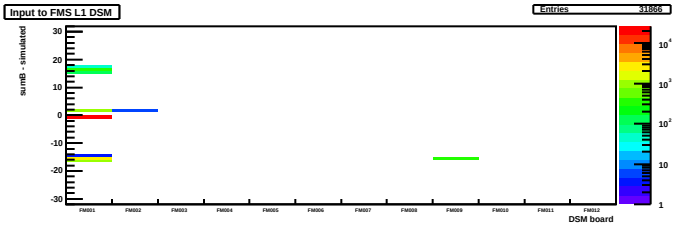

Input to FMS L1 DSM
Entries 8686932
sumC
DSM board

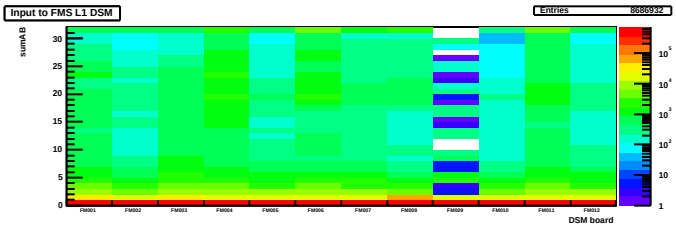

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

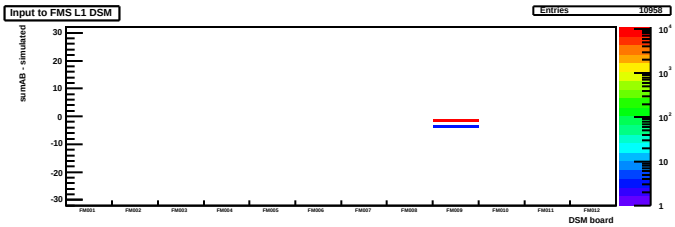

Input to FMS L1 DSM
Entries 8686932
sumBC
DSM board

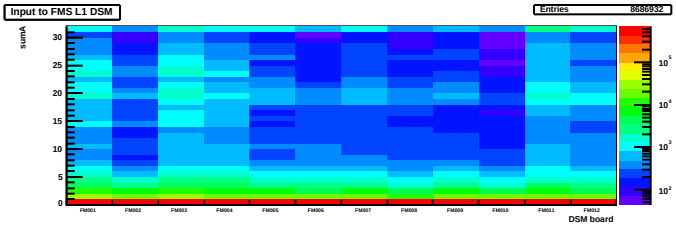

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

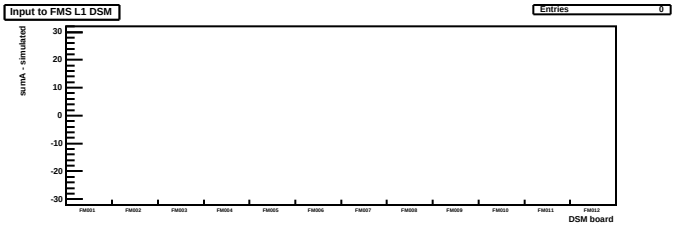

Input to FMS L1 DSM
Entries 8686932
sumB
DSM board

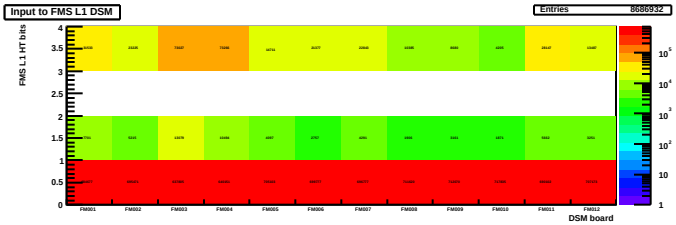

Input to FMS L1 DSM
Entries 31866
sumB - simulated
DSM board

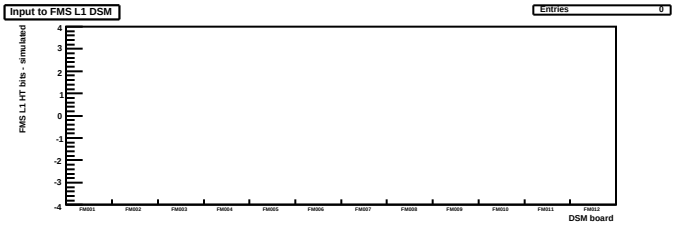

Input to FMS L1 DSM
Entries 8686932
sumAB
DSM board

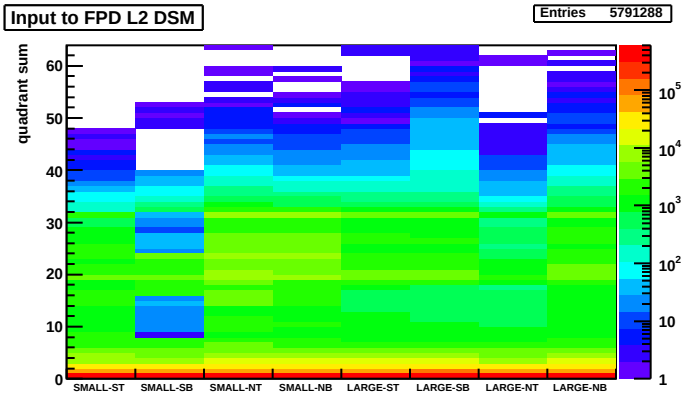

Input to FMS L1 DSM
Entries 10958
sumAB - simulated
DSM board

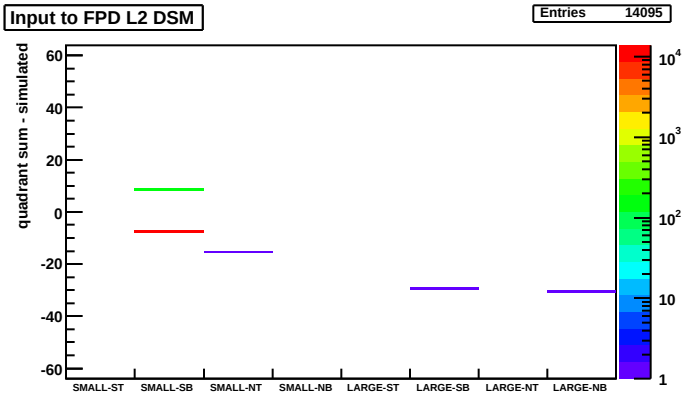

Input to FMS L1 DSM
Entries 8686932
sumA
DSM board

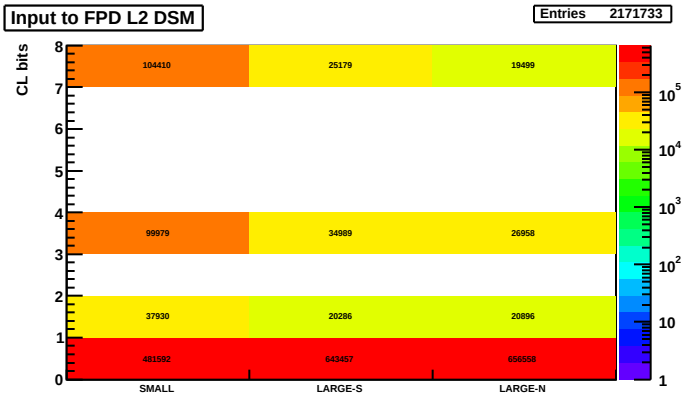

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

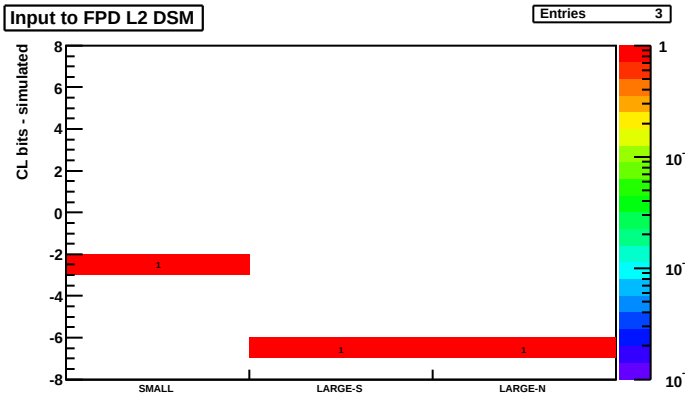

Input to FMS L1 DSM
Entries 8686932
FMS L1 HT bits
DSM board

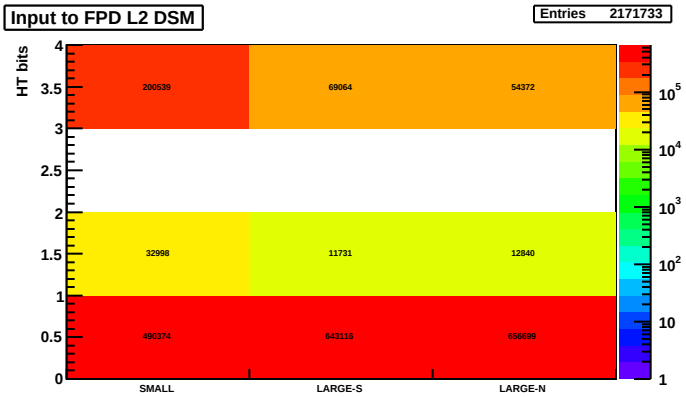

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

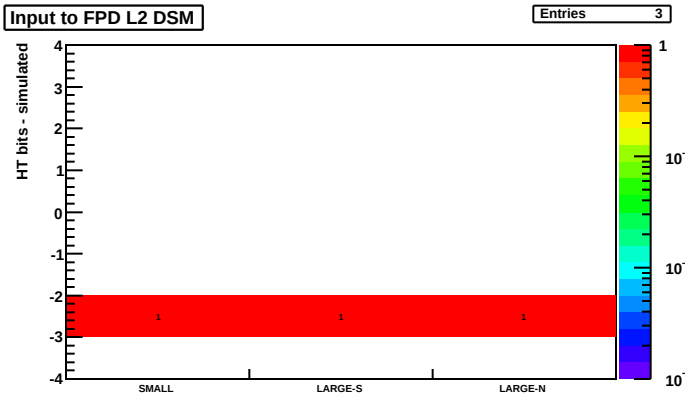

Input to FPD L2 DSM
Entries 5791288
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

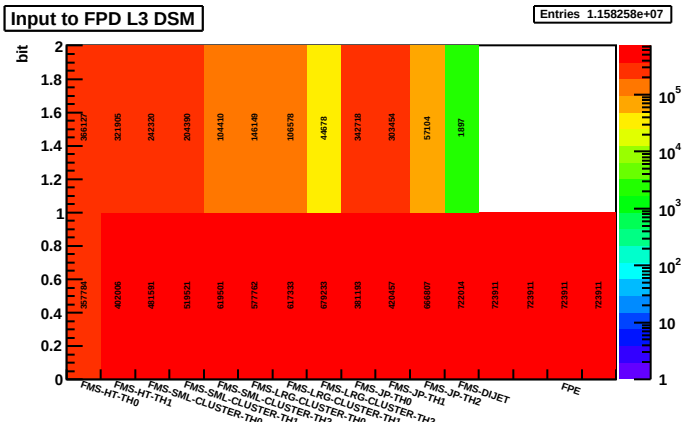

Input to FPD L2 DSM
Entries 14095
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

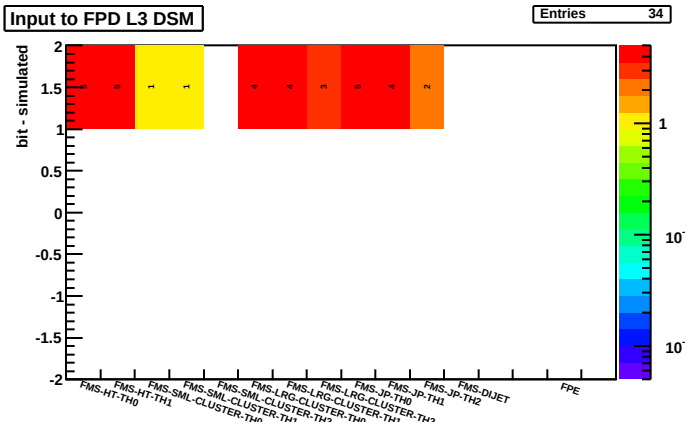

Input to FPD L2 DSM
Entries 2171733
CL bits
104410
25179
19499
99979
34989
26958
37930
20286
20896
481592
643457
656558
SMALL
LARGE-S
LARGE-N

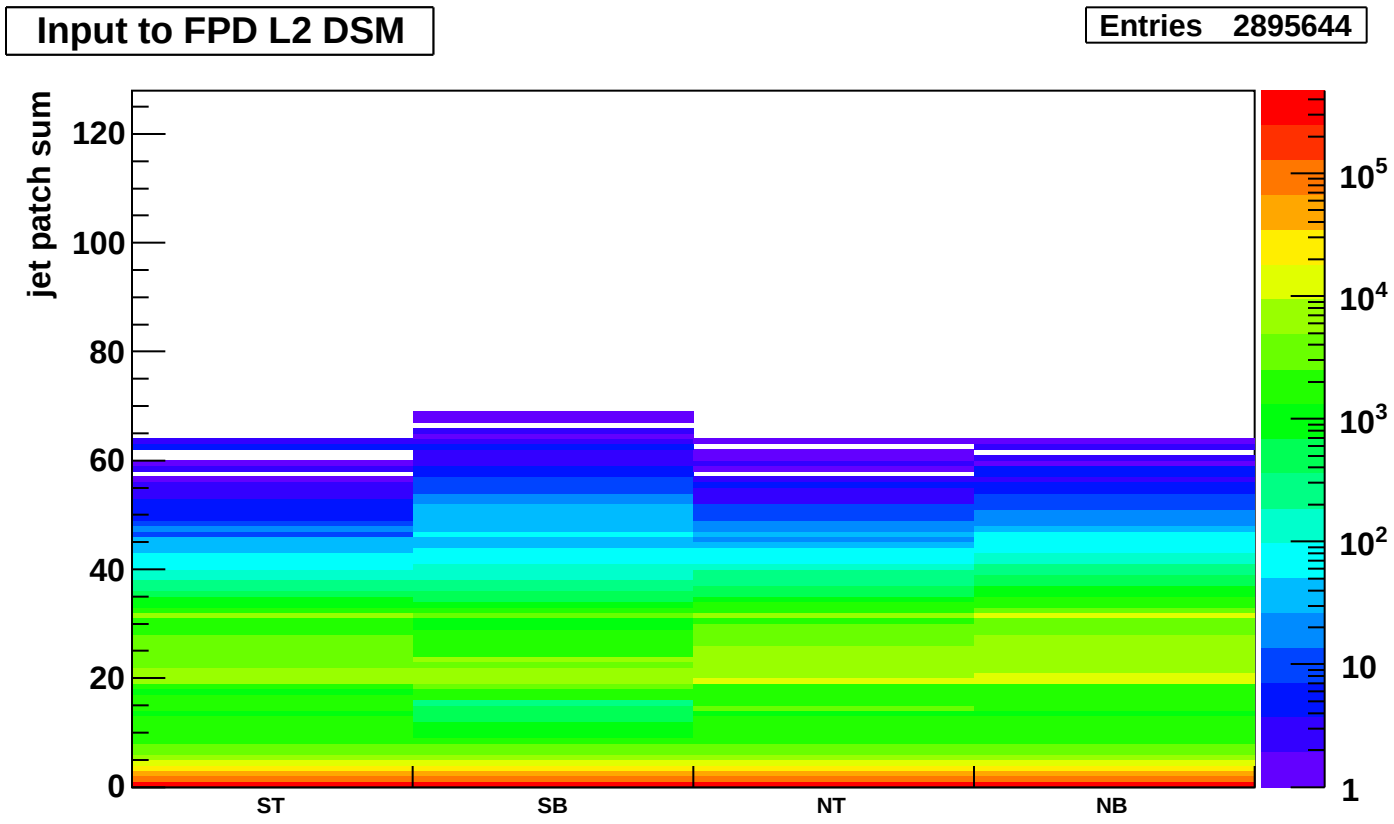

Input to FPD L2 DSM
Entries 3
CL bits - simulated
1
1
1
SMALL
LARGE-S
LARGE-N

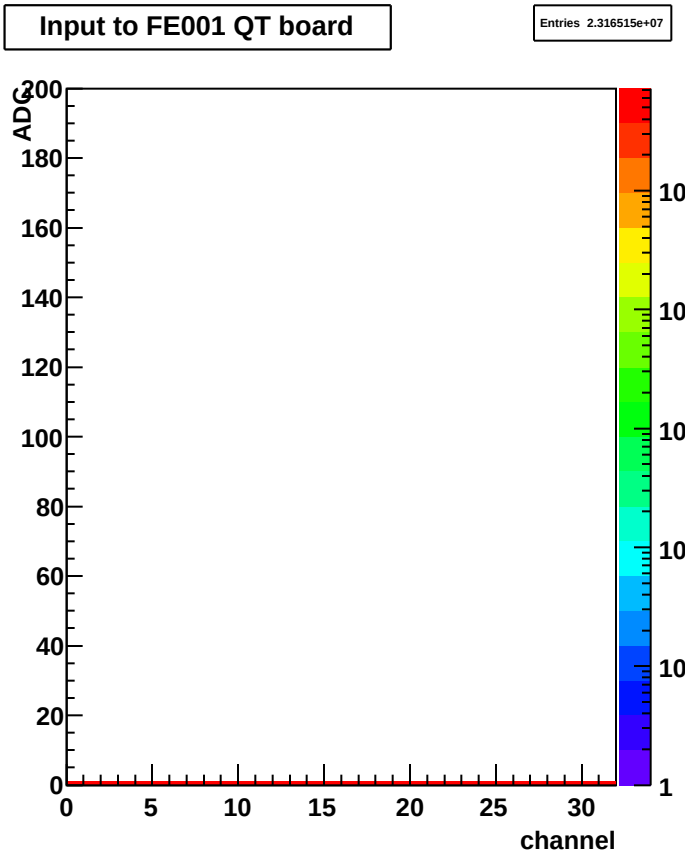

Input to FPD L2 DSM
Entries 2171733
HT bits
200539
69064
54372
32998
11731
12840
490374
643116
656699
SMALL
LARGE-S
LARGE-N

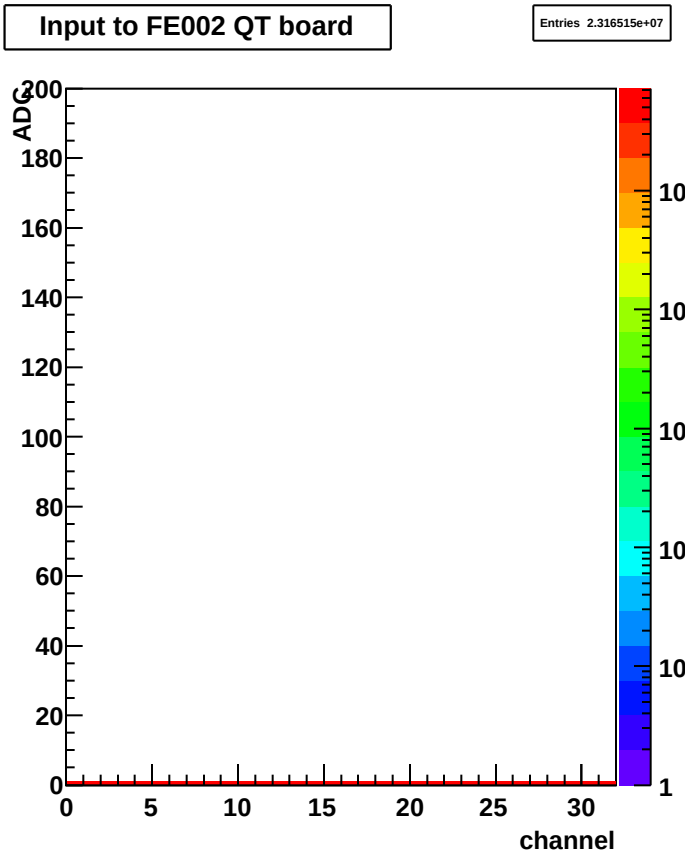

Input to FPD L2 DSM
Entries 3
HT bits - simulated
1
1
1
SMALL
LARGE-S
LARGE-N

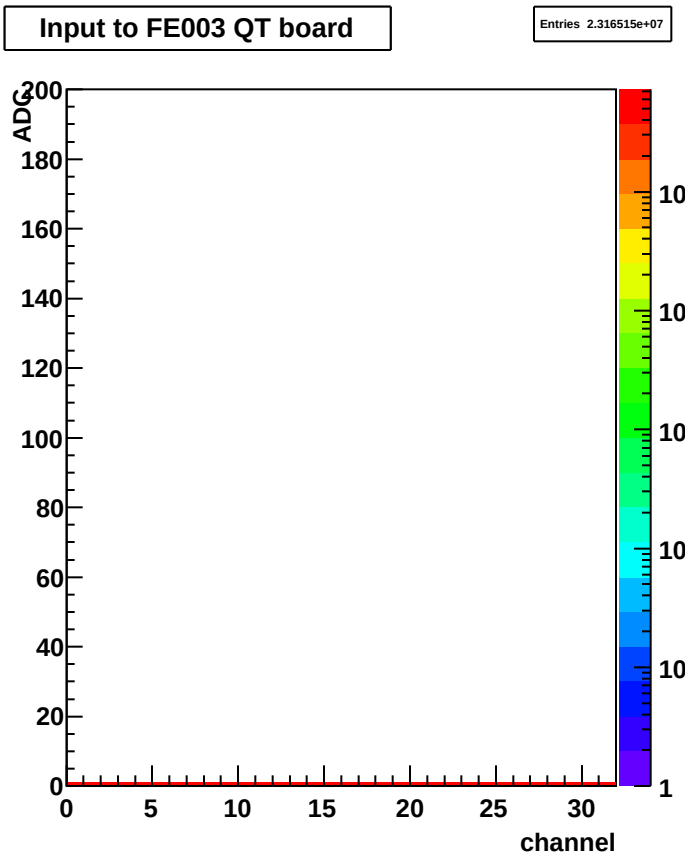

Input to FPD L3 DSM
Entries 1.158258e+07
bit
356127
321905
242320
204390
104410
146149
106578
44678
342718
303454
57104
1897
357704
402606
491591
510921
619501
577762
617333
679233
381183
420457
666807
722014
723911
723911
723911
723911
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

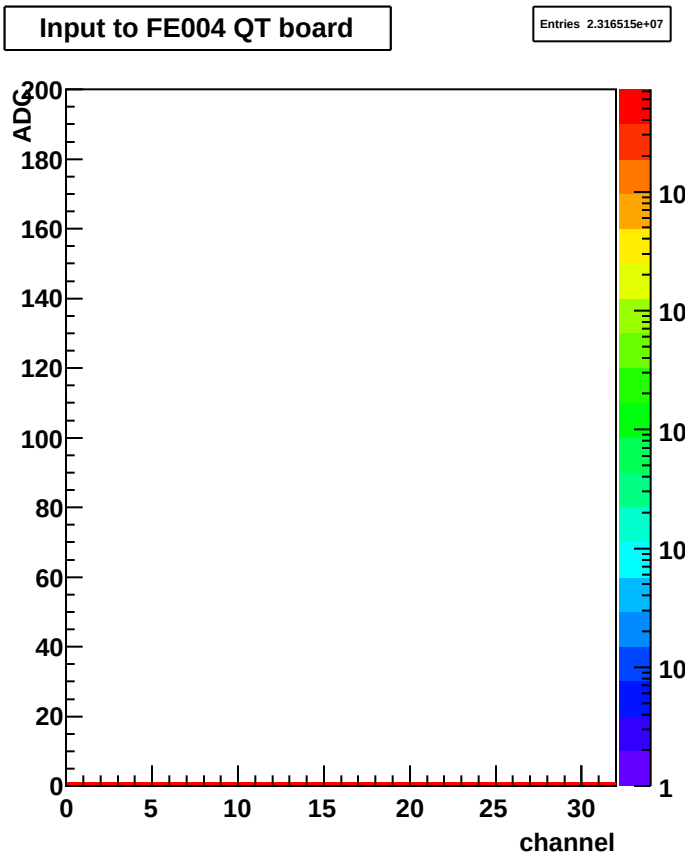

Input to FPD L3 DSM
Entries 34
bit - simulated
5
5
1
1
4
4
3
5
4
2
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

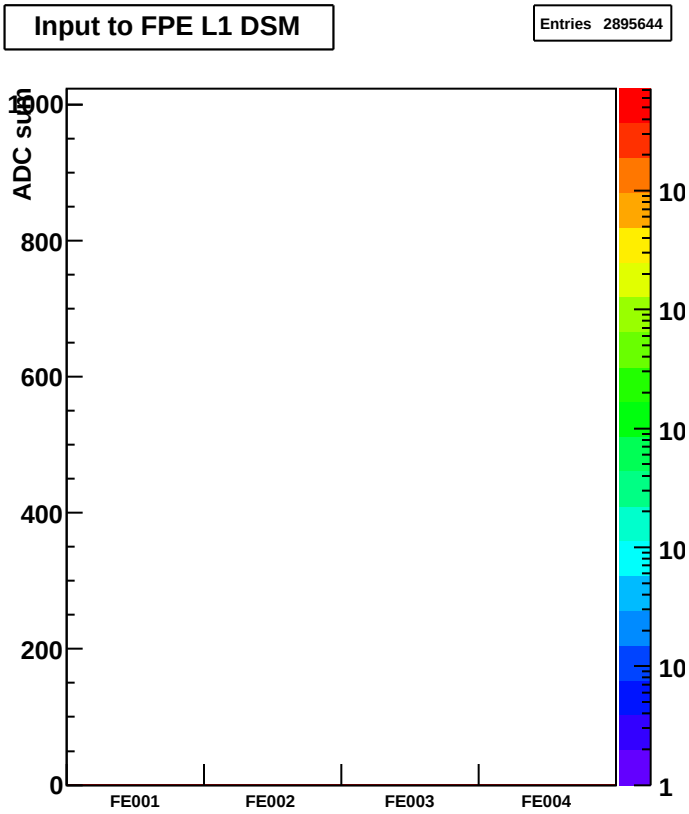

Input to FPD L2 DSM
Entries 2895644
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

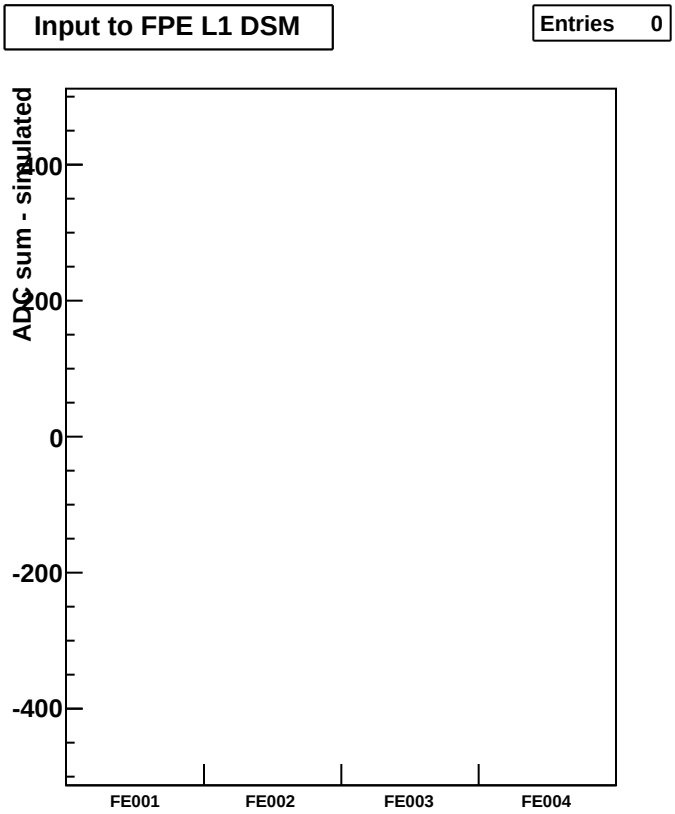

Input to FE001 QT board
Entries 2.316515e+07
ADC
channel

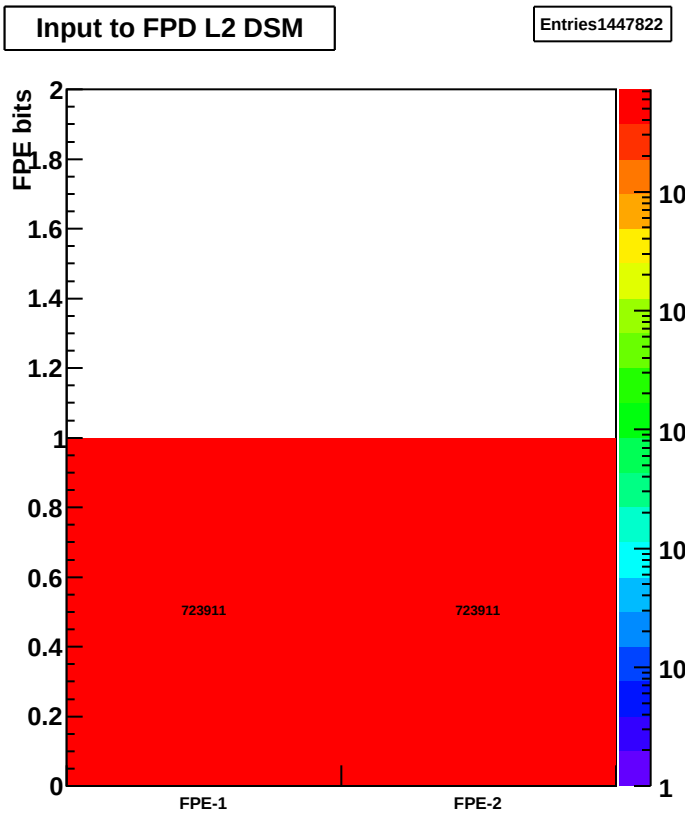

Input to FE002 QT board
Entries 2.316515e+07
ADC
channel

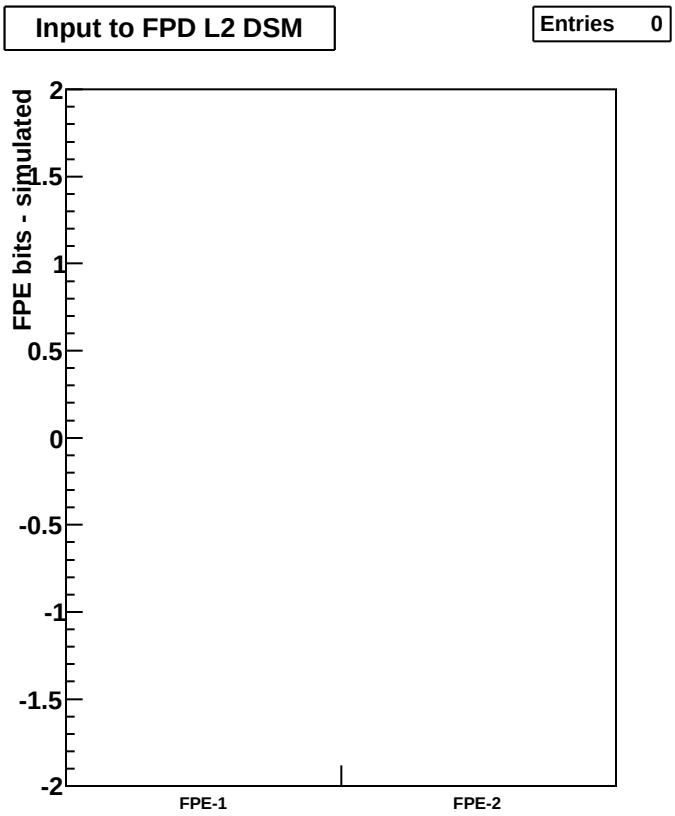

Input to FE003 QT board
Entries 2.316515e+07
ADC
channel


Input to FE004 QT board
Entries 2.316515e+07
ADC
channel


Input to FPE L1 DSM
Entries 2895644
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1447822
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
723911
723911
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2