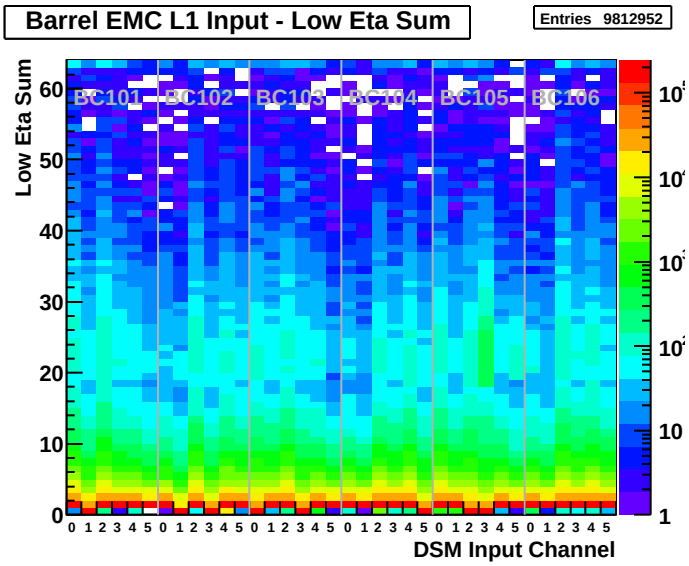
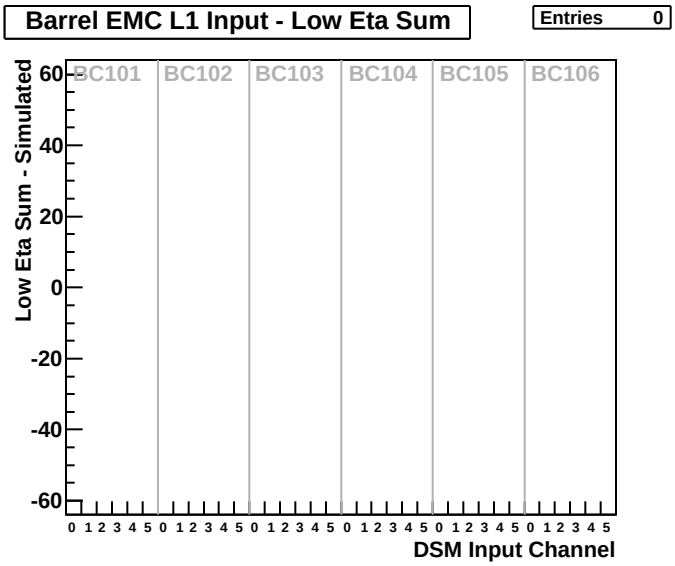
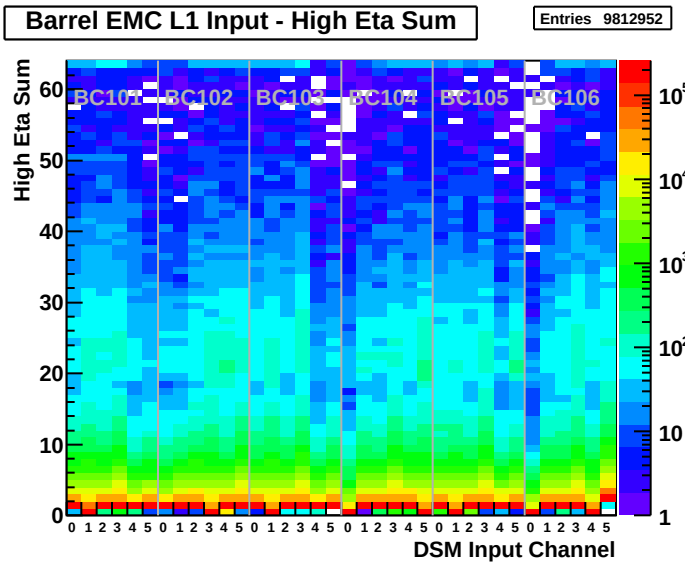
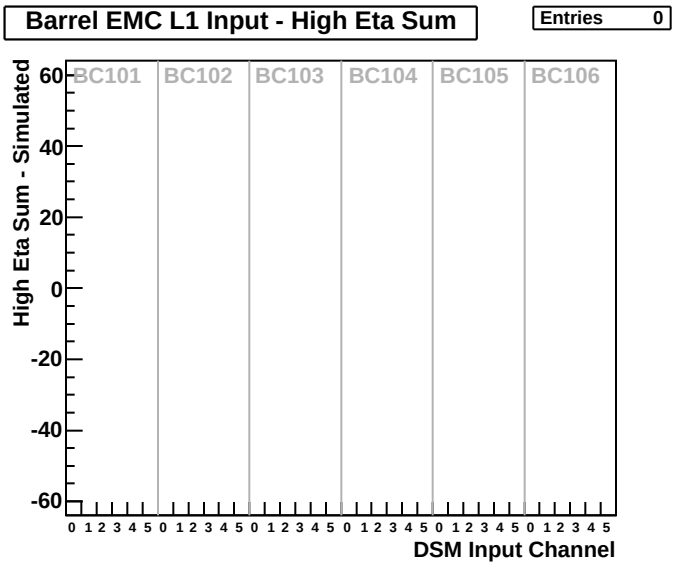
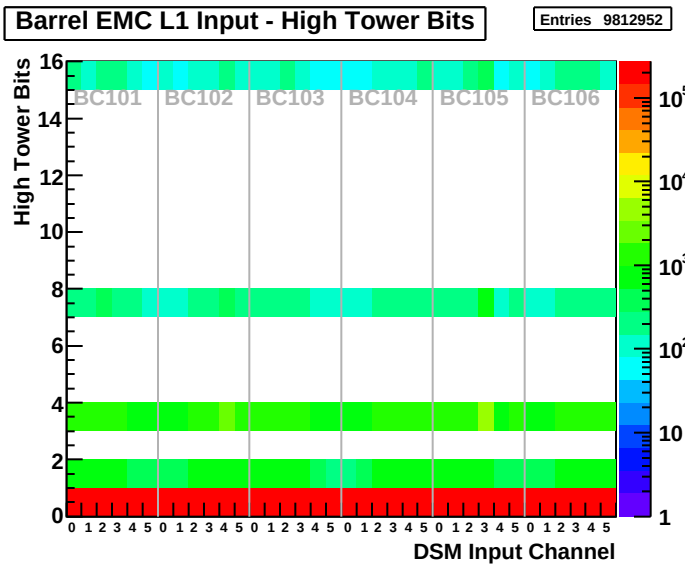
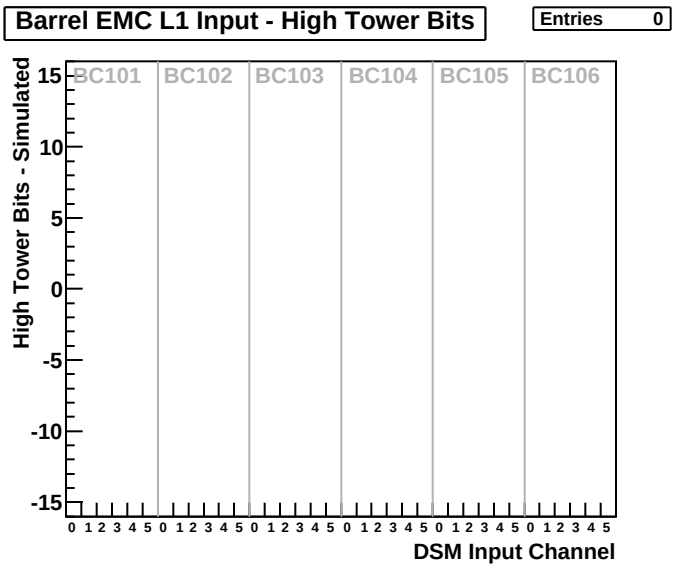

Barrel EMC L1 Input - Low Eta Sum
Entries 9812952
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

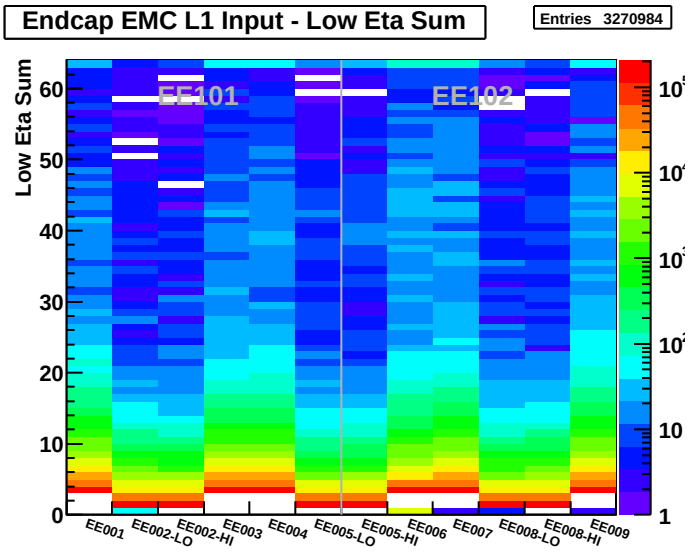

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

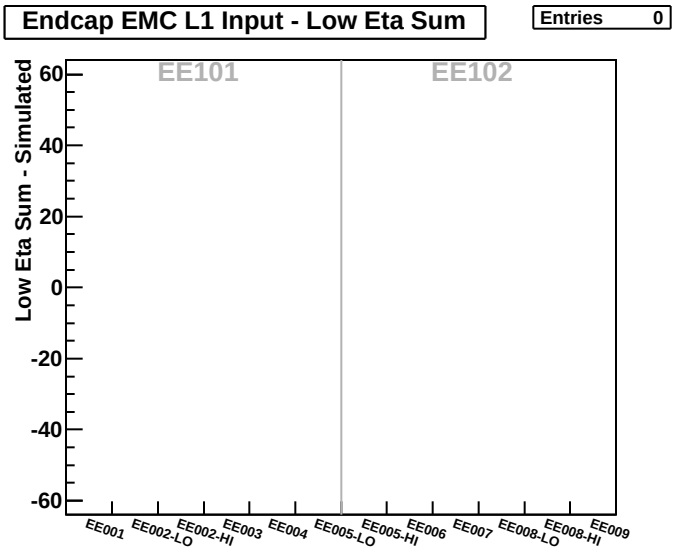

Barrel EMC L1 Input - High Eta Sum
Entries 9812952
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

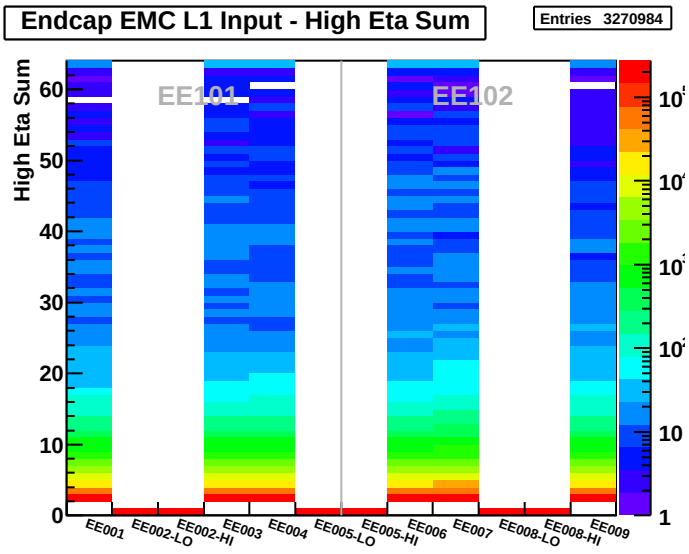

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

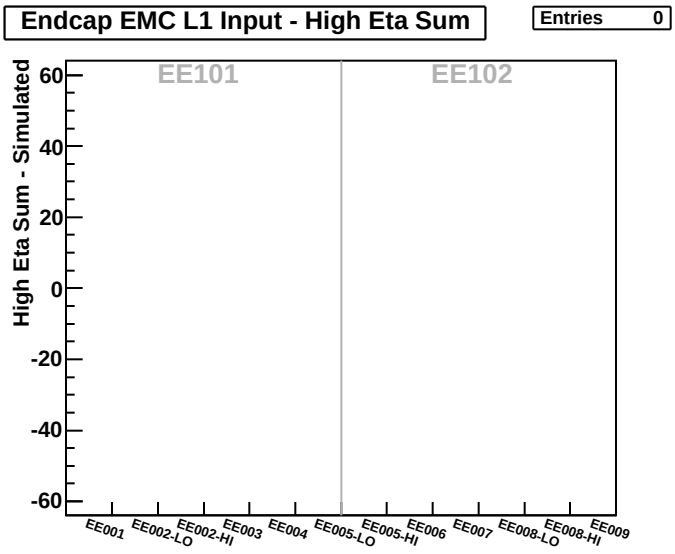

Barrel EMC L1 Input - High Tower Bits
Entries 9812952
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

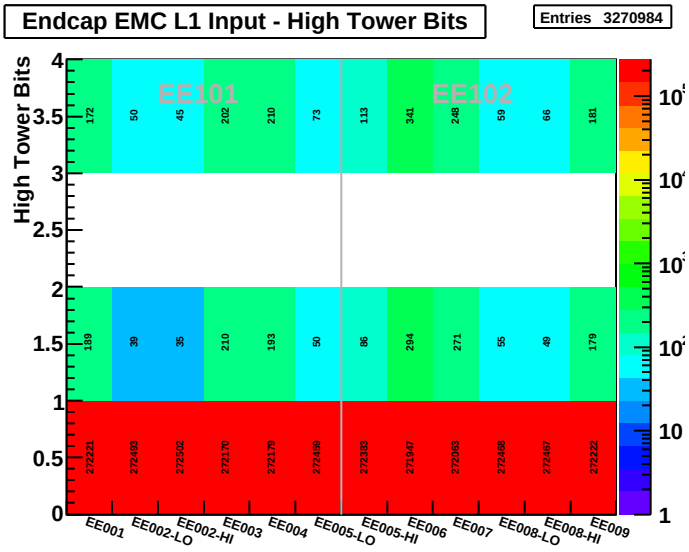

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

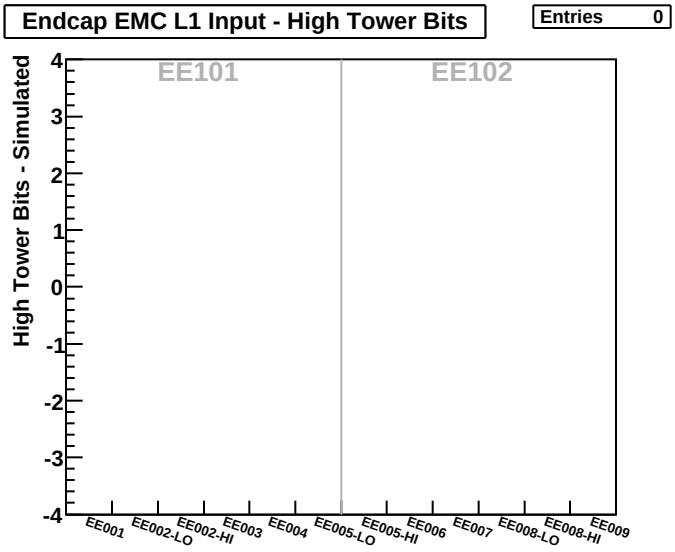

Endcap EMC L1 Input - Low Eta Sum
Entries 3270984
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

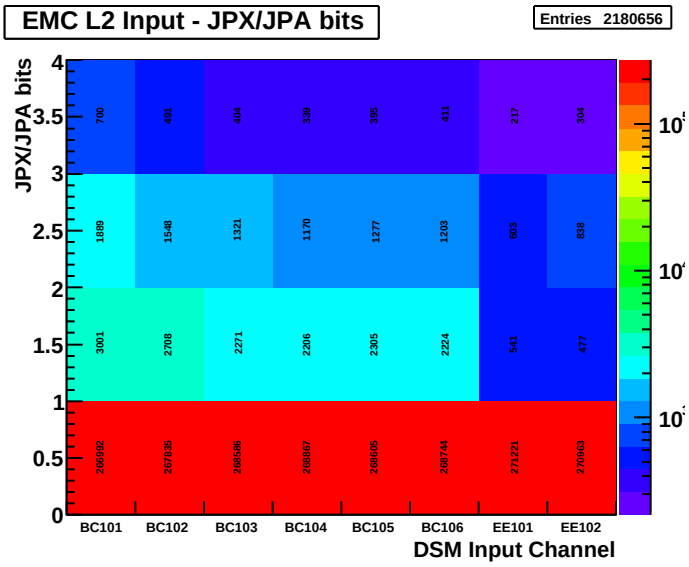

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

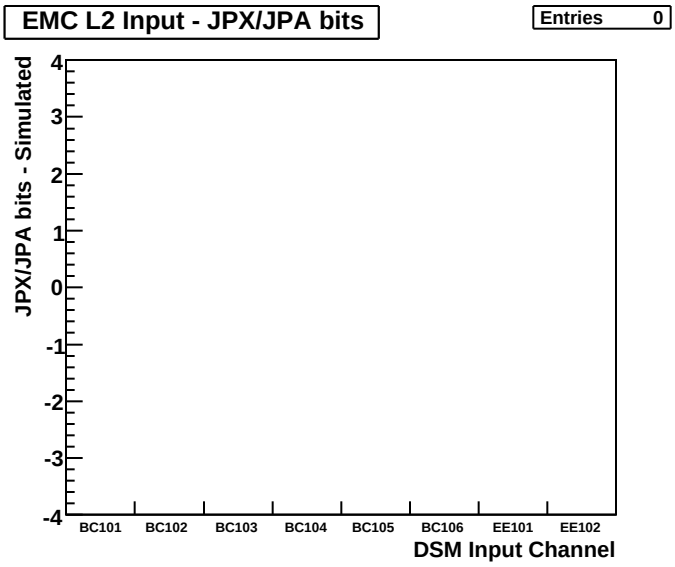

Endcap EMC L1 Input - High Eta Sum
Entries 3270984
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

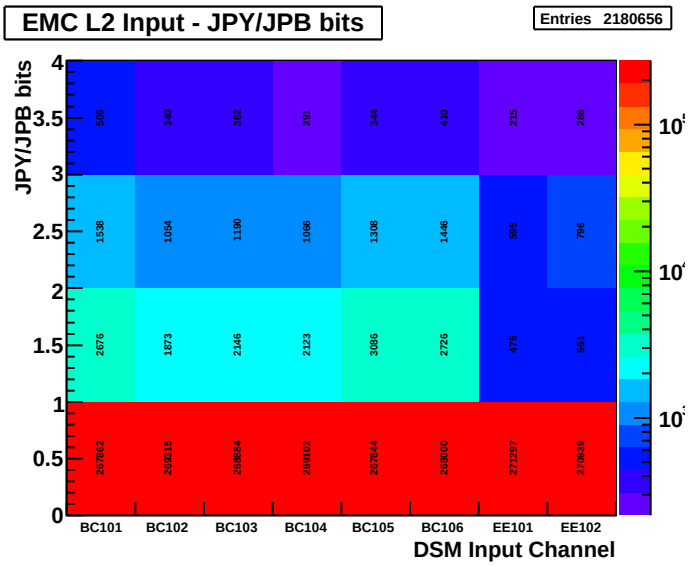

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

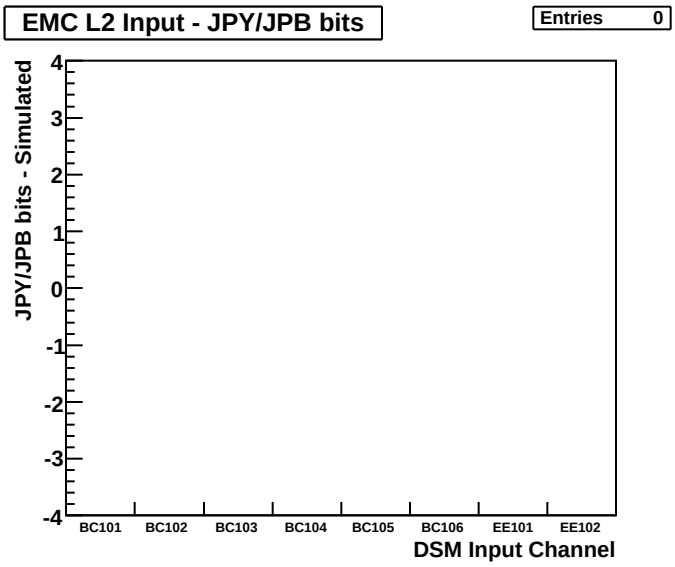

Endcap EMC L1 Input - High Tower Bits
Entries 3270984
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

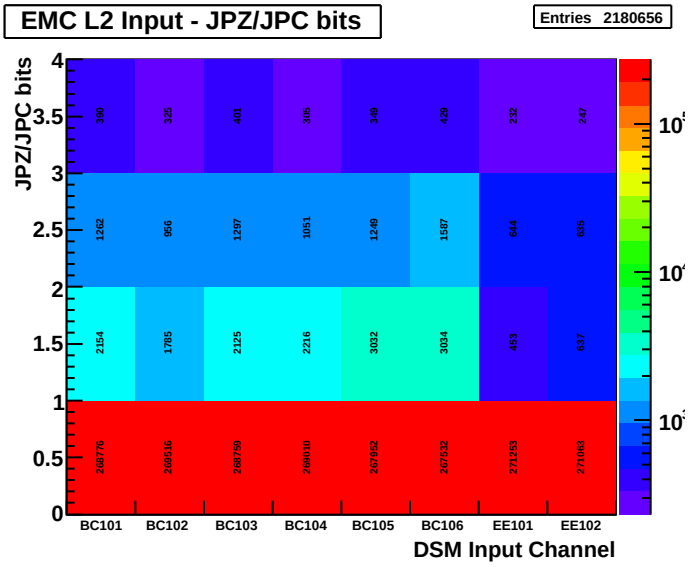

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

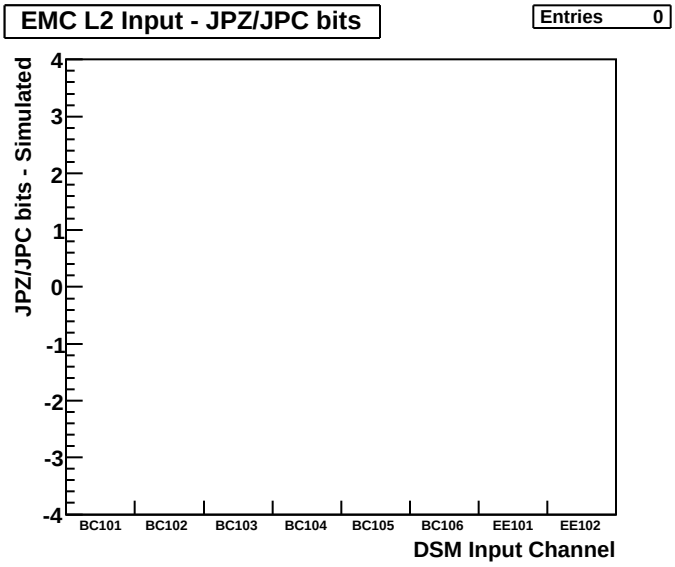

EMC L2 Input - JPX/JPA bits
Entries 2180656
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
700 491 404 339 395 411 217 304
1889 1548 1321 1170 1277 1203 603 838
3001 2708 2271 2206 2305 2224 541 477
266992 267835 268586 268867 268605 268744 271221 270963

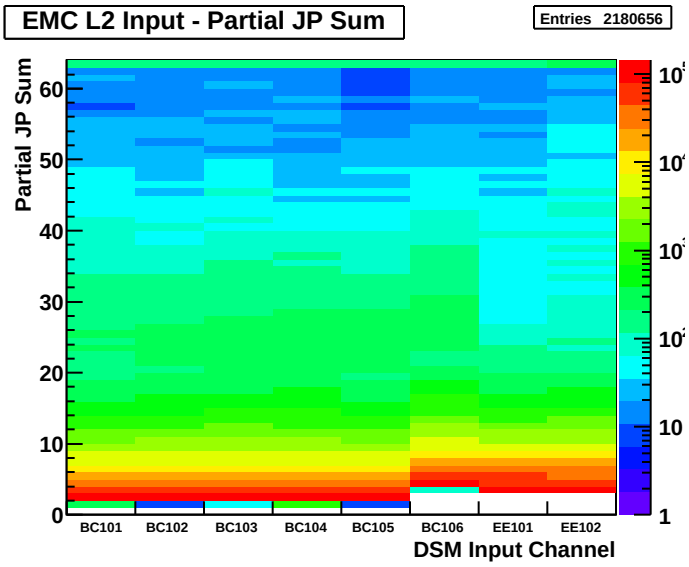

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

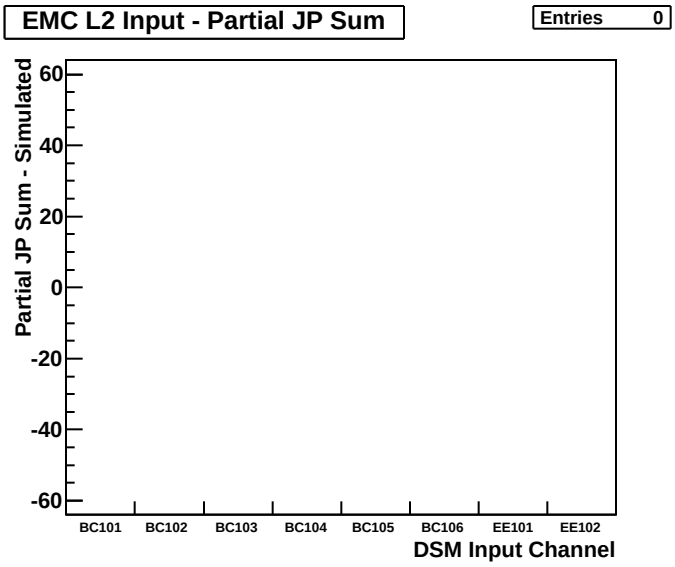

EMC L2 Input - JPY/JPB bits
Entries 2180656
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
506 340 362 291 344 410 215 286
1538 1054 1190 1066 1308 1446 585 796
2676 1873 2146 2123 3086 2726 475 561
267862 269315 268884 269102 267844 268000 271297 270939

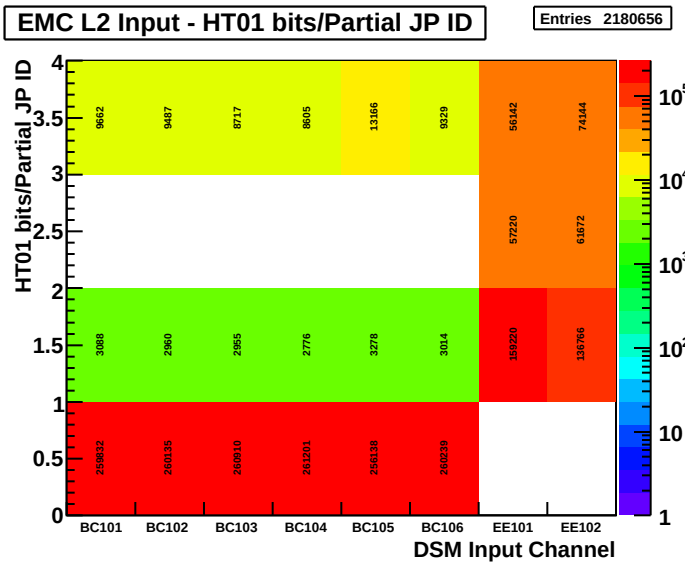

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

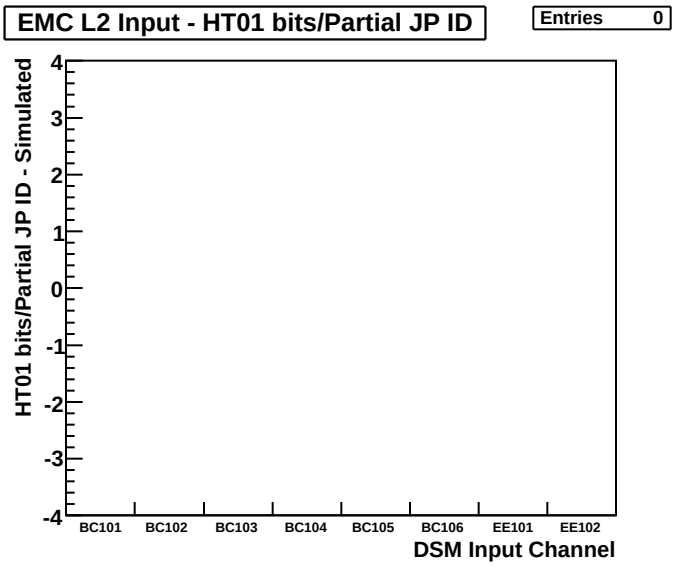

EMC L2 Input - JPZ/JPC bits
Entries 2180656
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
330 325 401 305 349 429 232 247
1262 956 1297 1051 1249 1587 644 635
2154 1785 2125 2216 3032 3034 453 637
268776 269516 268759 269010 267952 267532 271253 271063

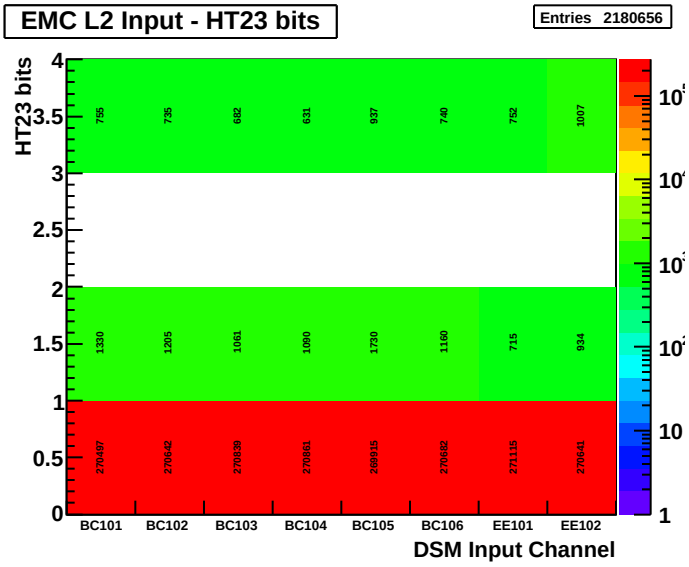

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

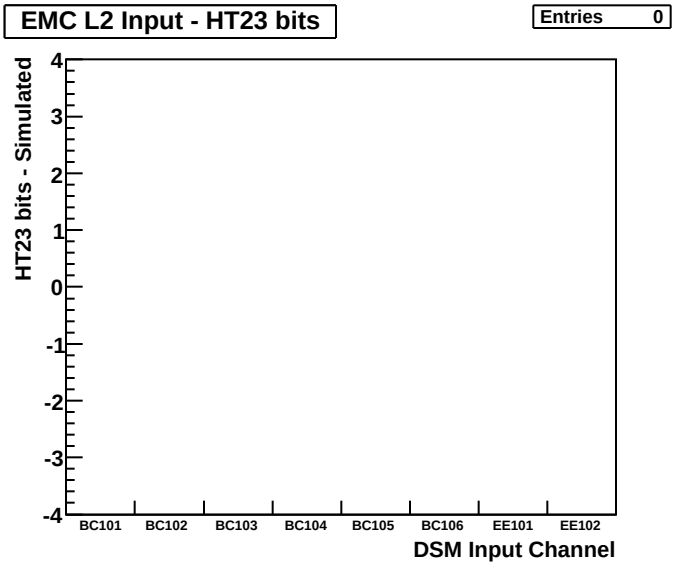

EMC L2 Input - Partial JP Sum
Entries 2180656
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

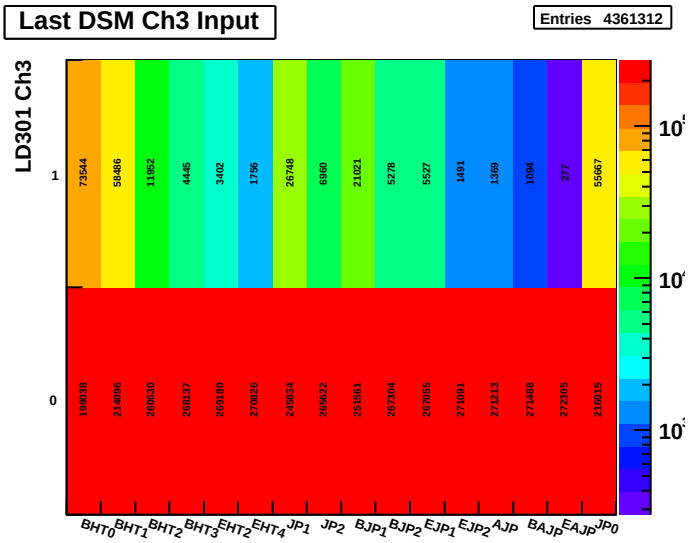

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

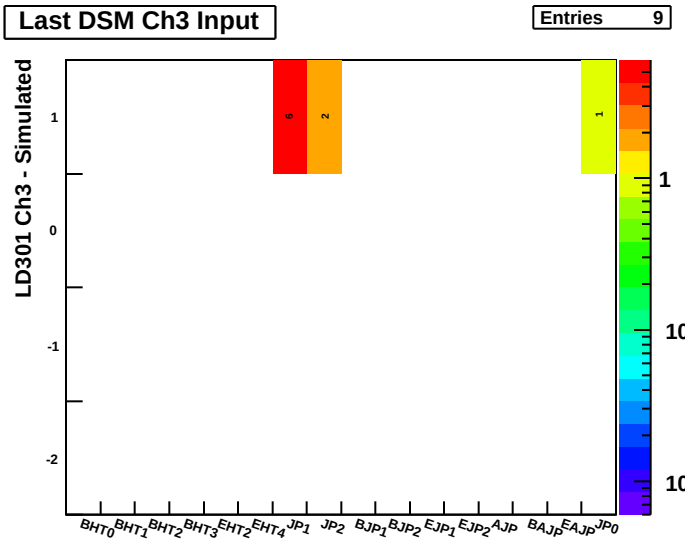

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2180656
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

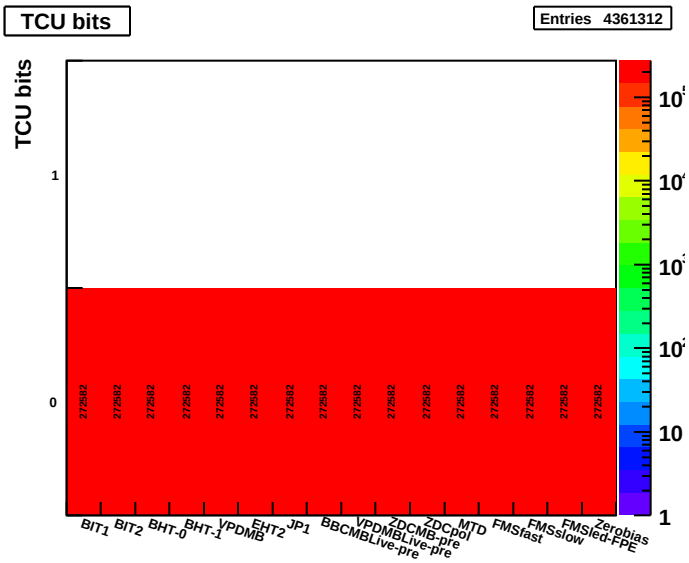

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

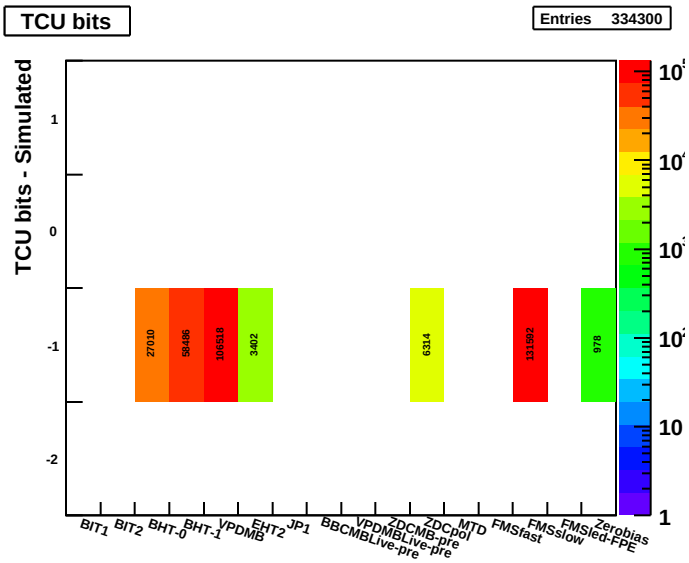

EMC L2 Input - HT23 bits
Entries 2180656
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

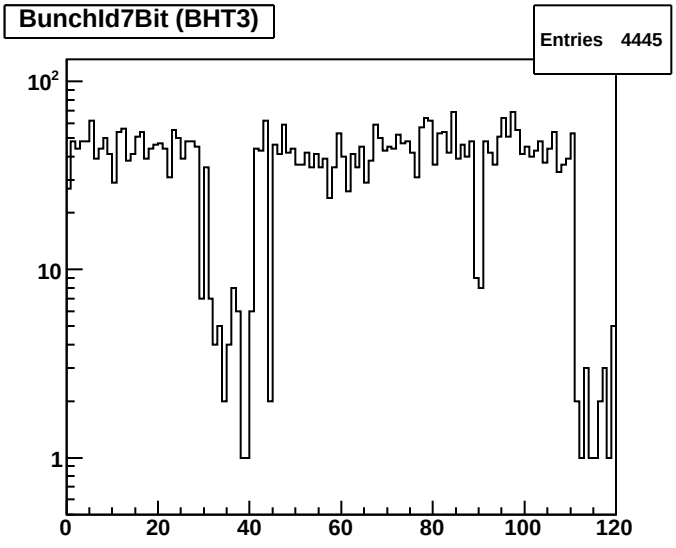

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

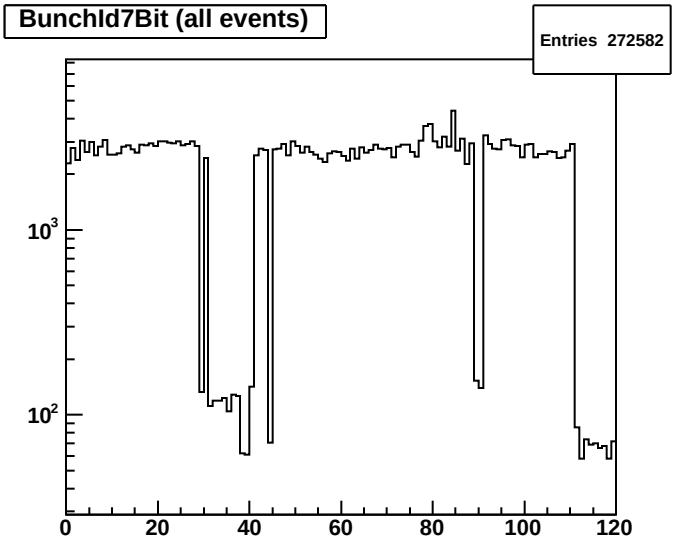

Last DSM Ch3 Input
Entries 4361312
LD301 Ch3
73544
58486
11952
4445
3402
1756
26748
6960
21021
5278
5527
1491
1369
1094
277
55667
199038
214096
260630
268137
269180
270826
245834
265622
251561
267304
267055
271091
271213
271488
272305
216915
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

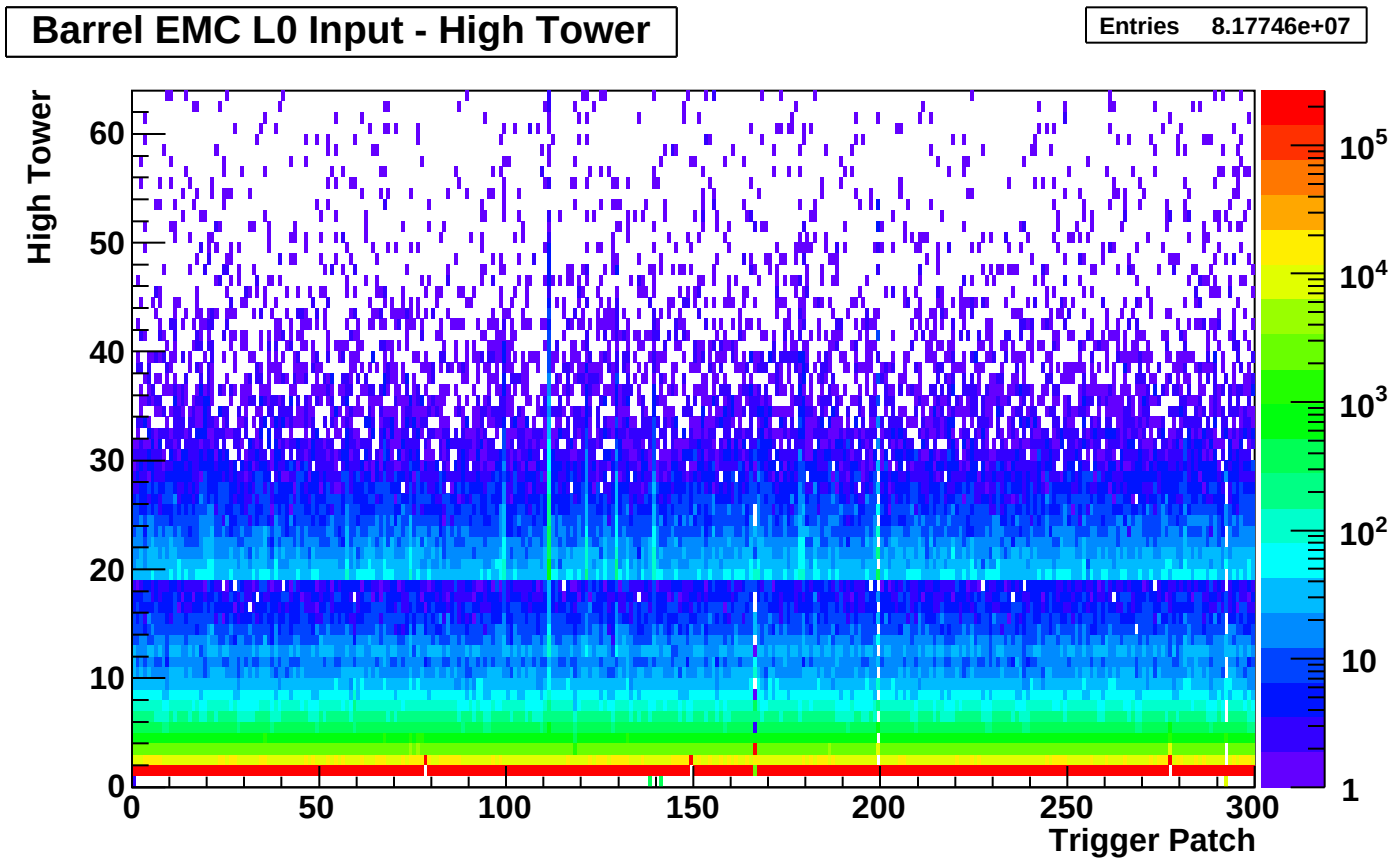

Last DSM Ch3 Input
Entries 9
LD301 Ch3 - Simulated
6
2
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

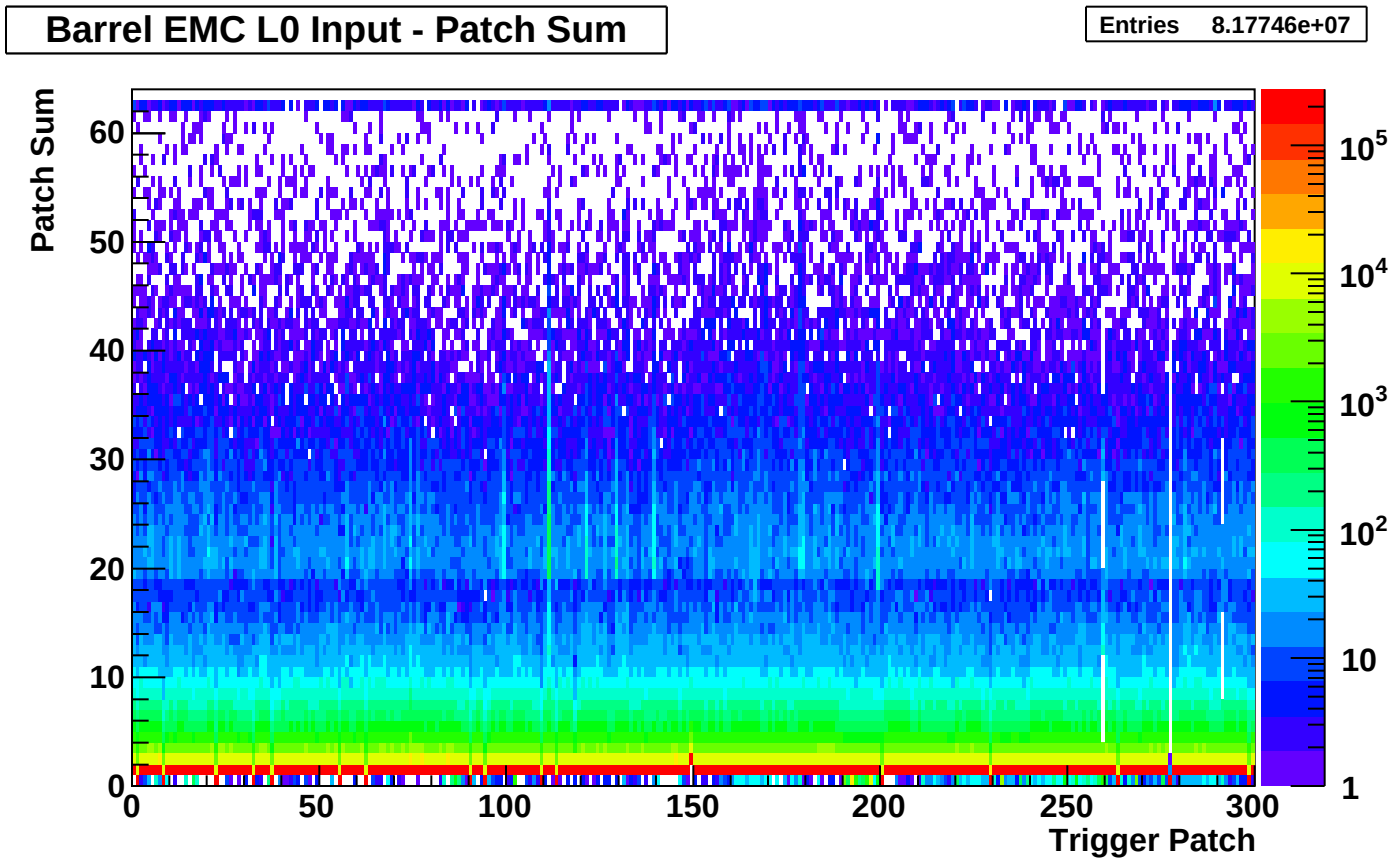

TCU bits
Entries 4361312
TCU bits
272582
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

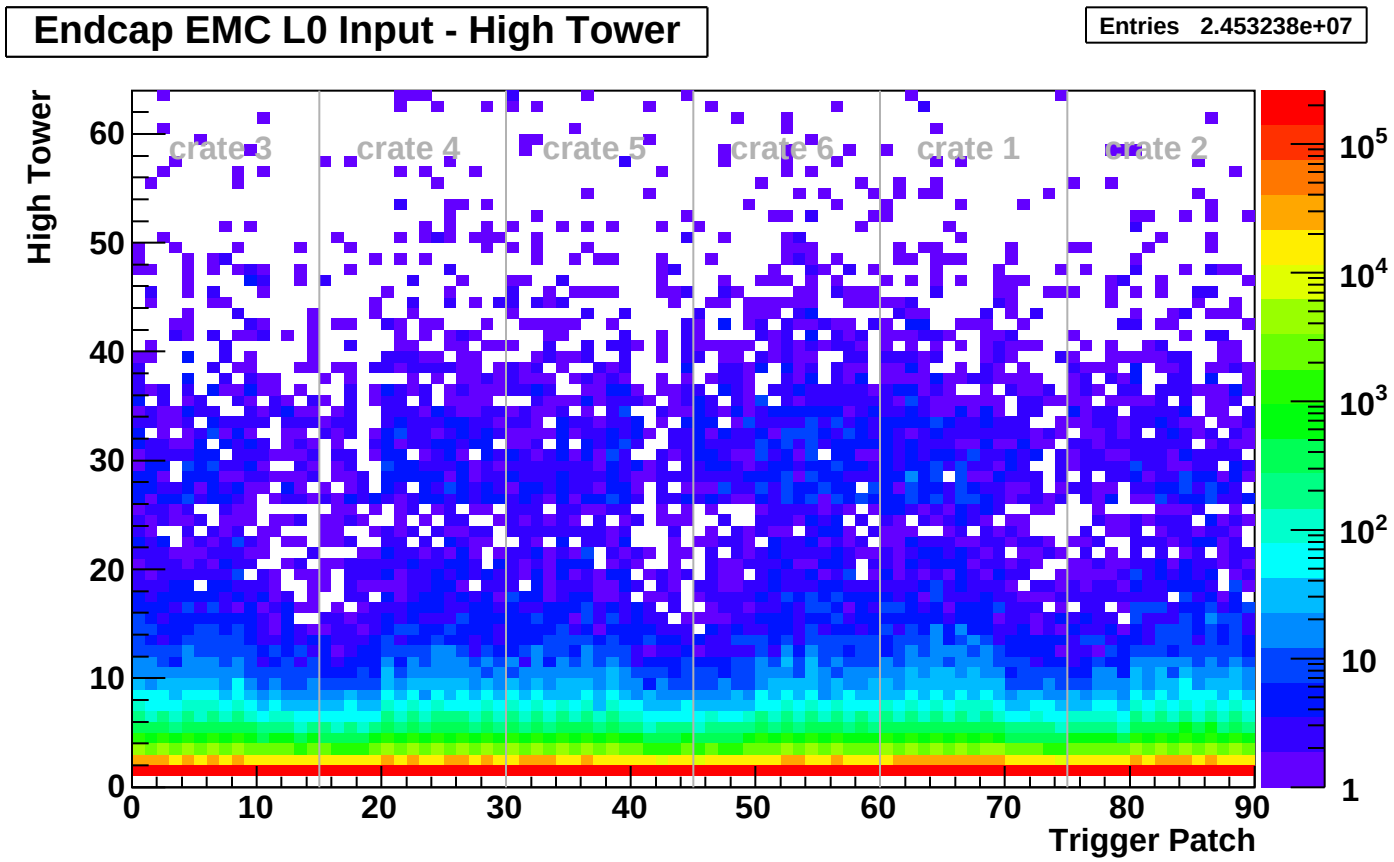

TCU bits
Entries 334300
TCU bits - Simulated
27010
58486
106518
3402
6314
131592
978
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

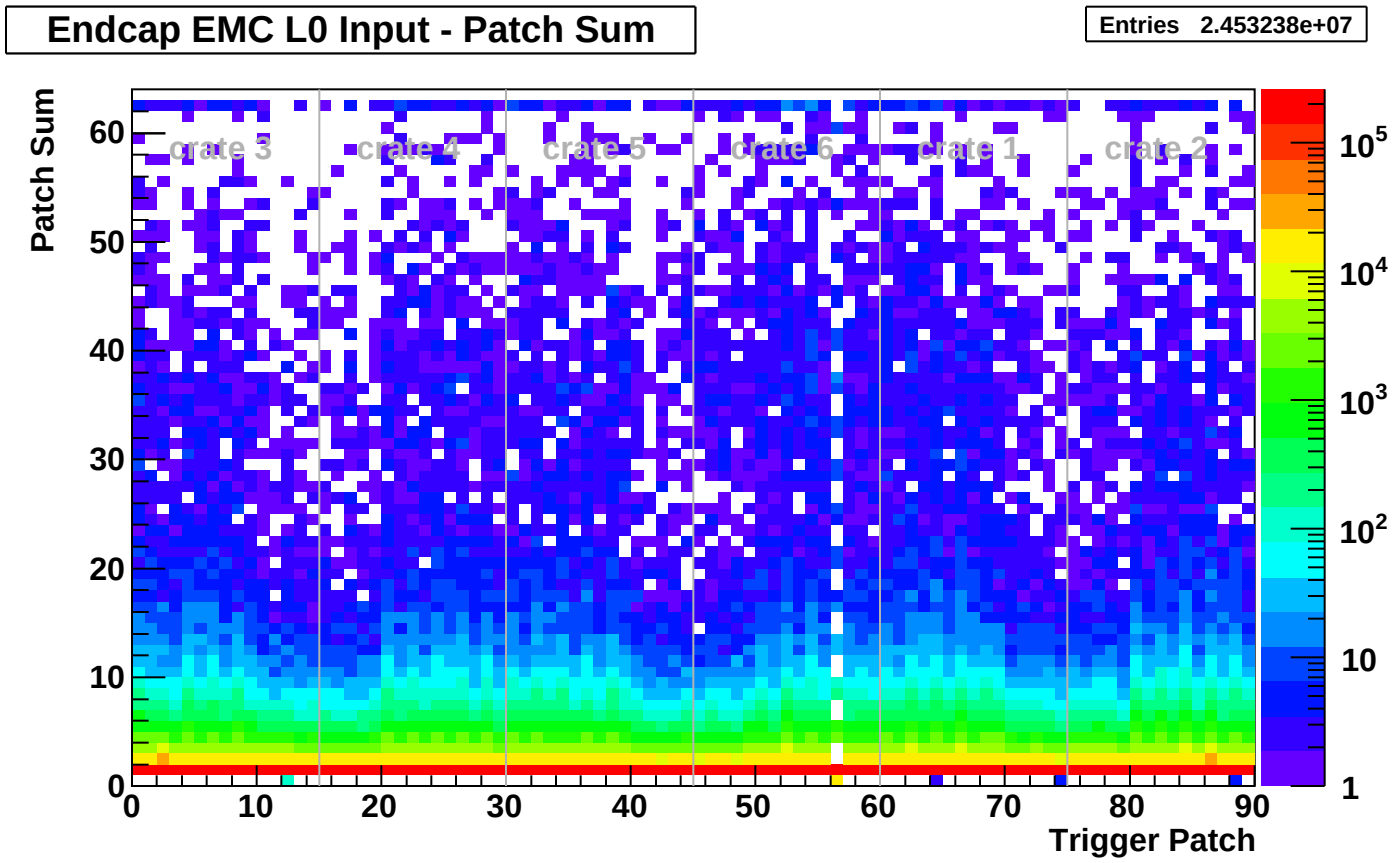

BunchId7Bit (BHT3)
Entries 4445

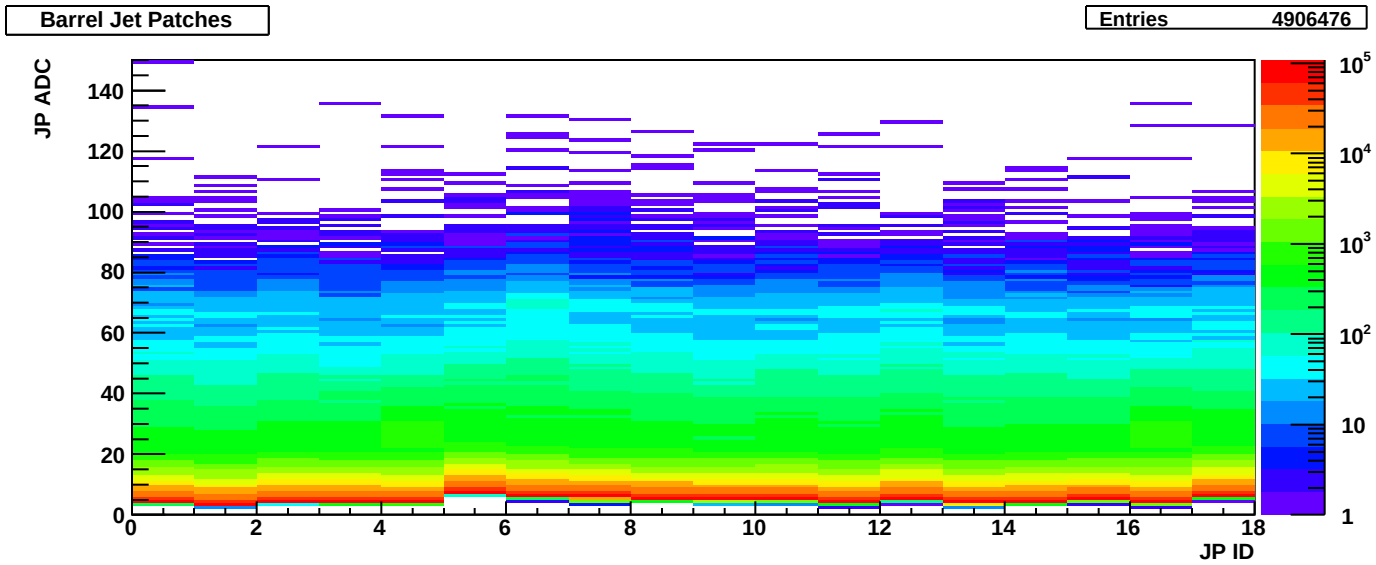

BunchId7Bit (all events)
Entries 272582

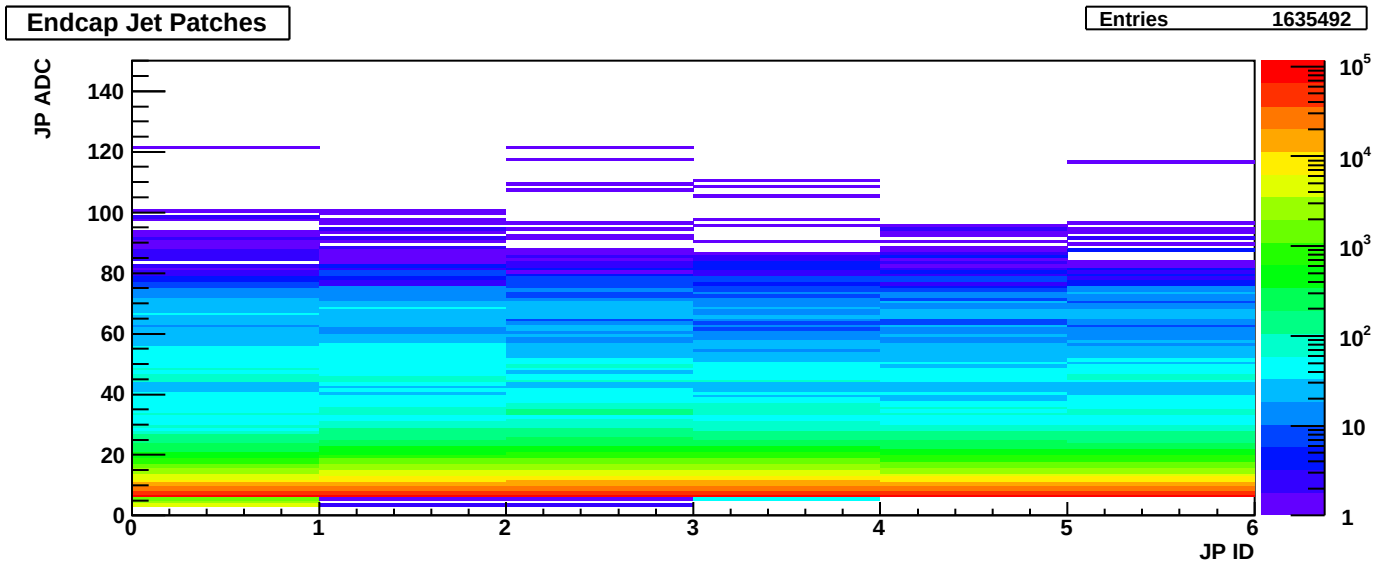

Barrel EMC L0 Input - High Tower
Entries 8.17746e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

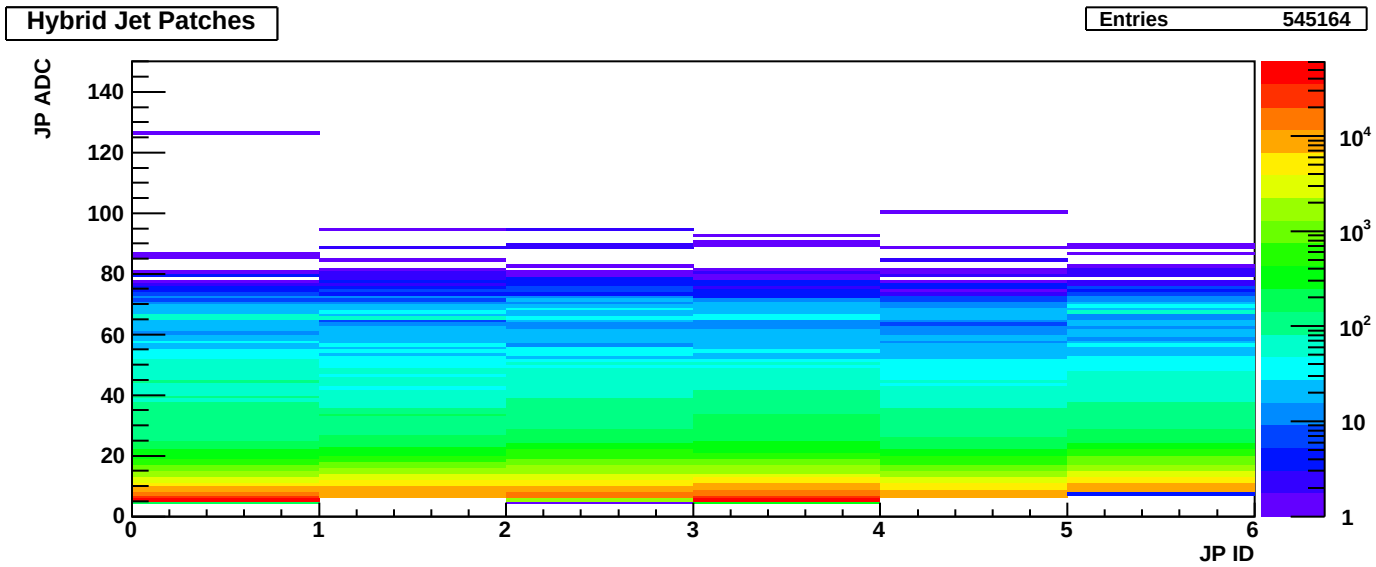

Barrel EMC L0 Input - Patch Sum
Entries 8.17746e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

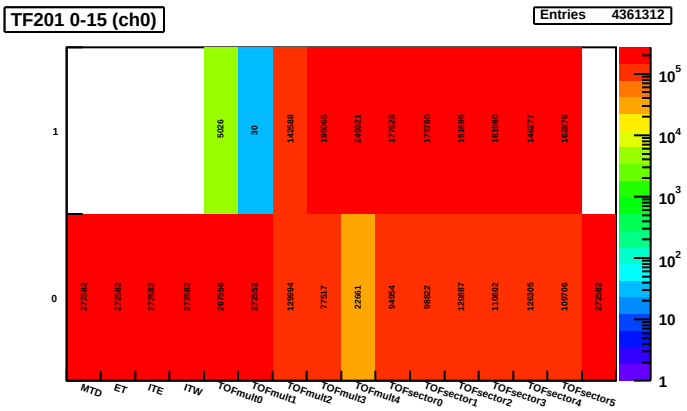

Endcap EMC L0 Input - High Tower
Entries 2.453238e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

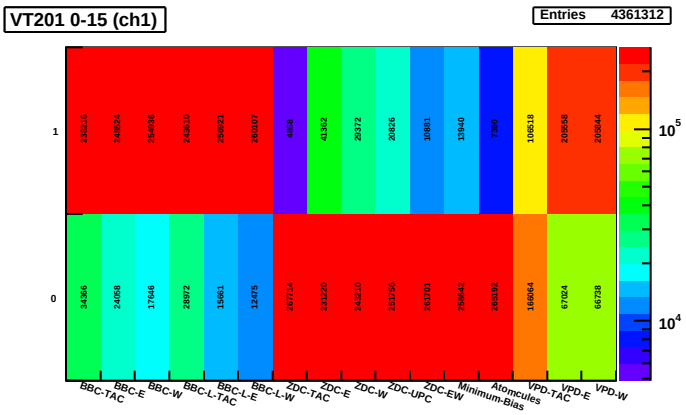

Endcap EMC L0 Input - Patch Sum
Entries 2.453238e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

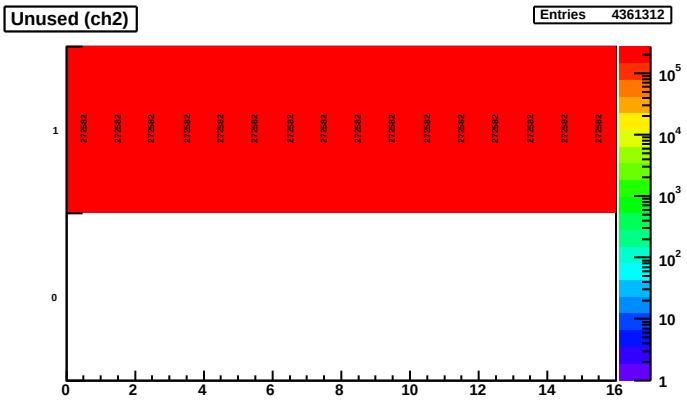

Barrel Jet Patches
Entries 4906476
JP ADC
JP ID

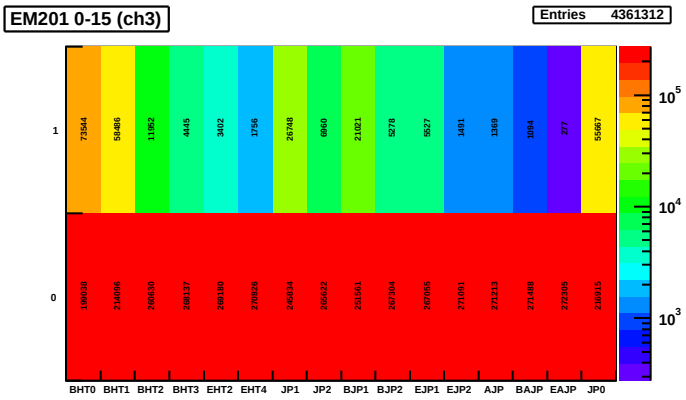

Endcap Jet Patches
Entries 1635492
JP ADC
JP ID

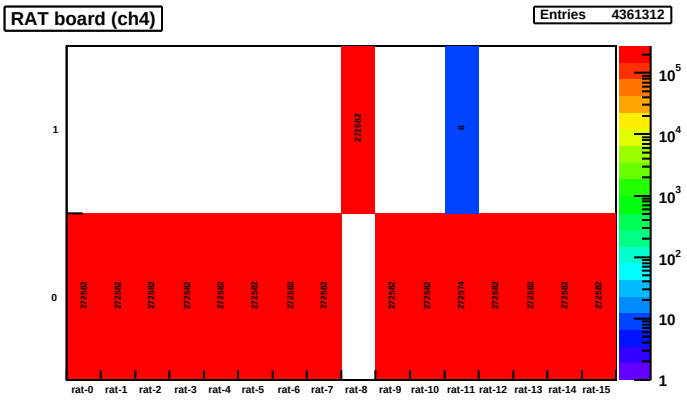

Hybrid Jet Patches
Entries 545164
JP ADC
JP ID

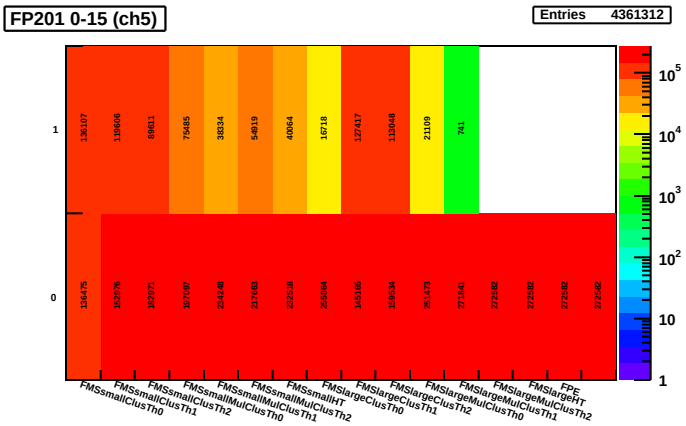

TF201 0-15 (ch0)
Entries 4361312
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

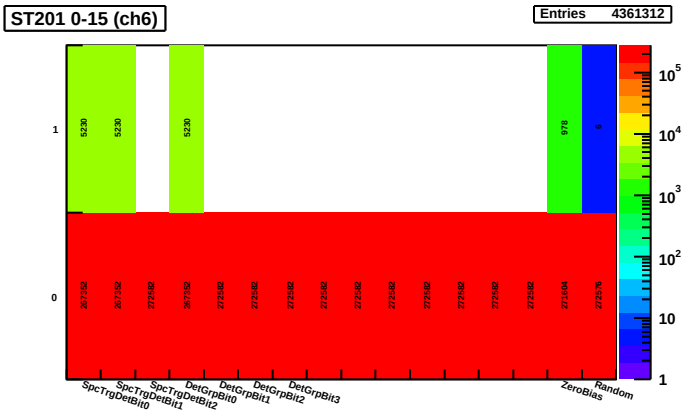

VT201 0-15 (ch1)
Entries 4361312
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

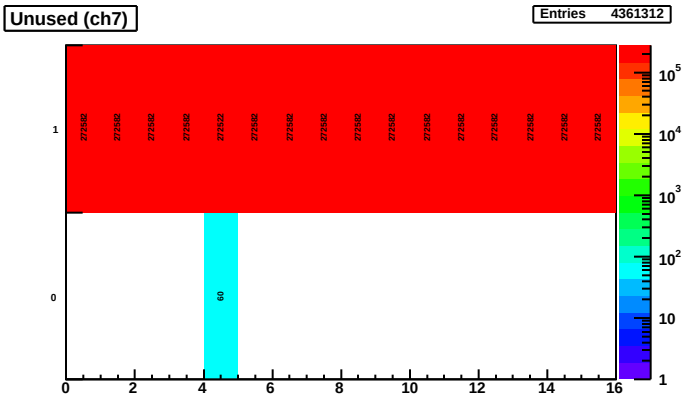

Unused (ch2)
Entries 4361312

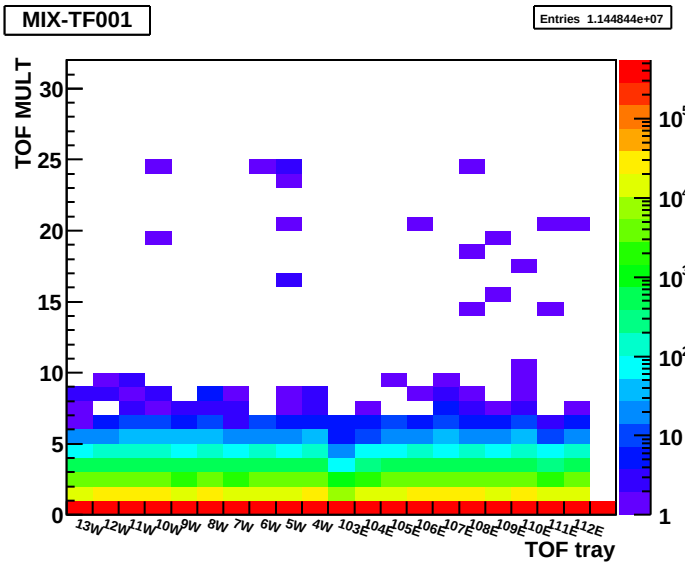

EM201 0-15 (ch3)
Entries 4361312
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

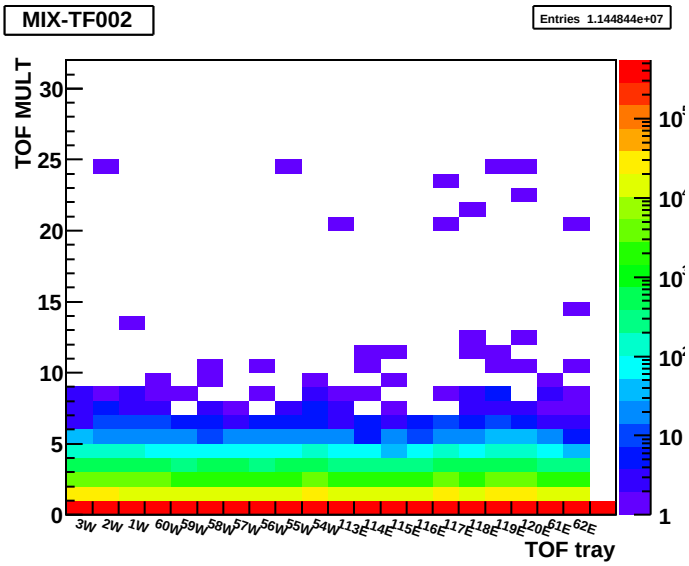

RAT board (ch4)
Entries 4361312
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

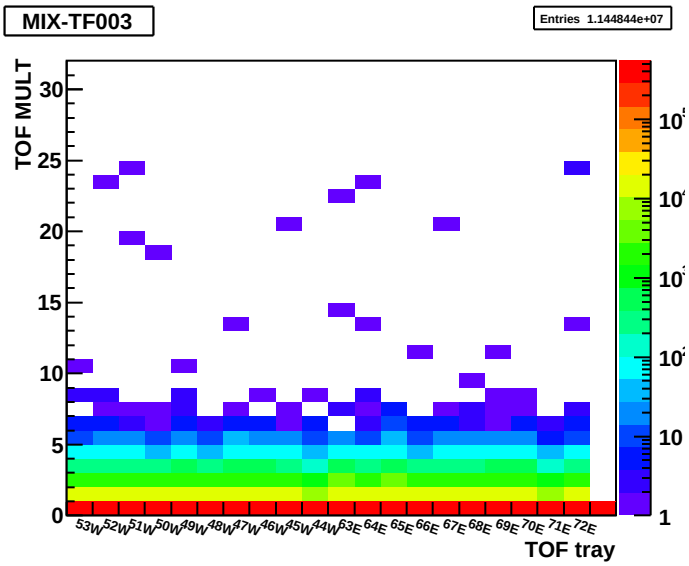

FP201 0-15 (ch5)
Entries 4361312
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

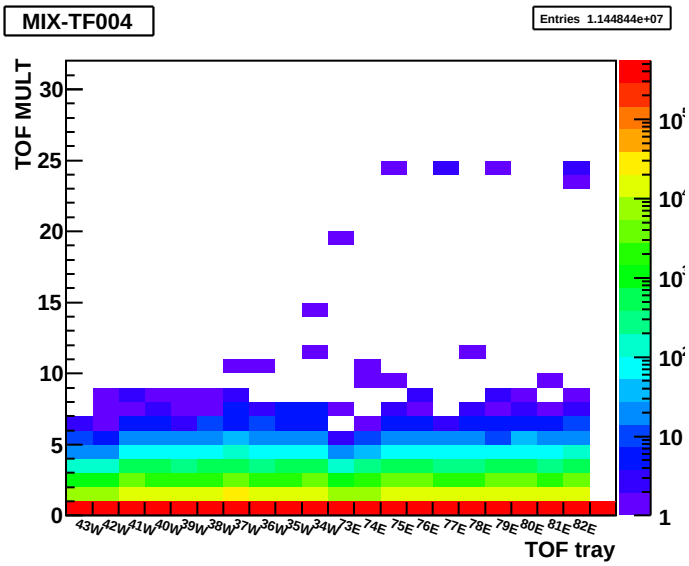

ST201 0-15 (ch6)
Entries 4361312
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

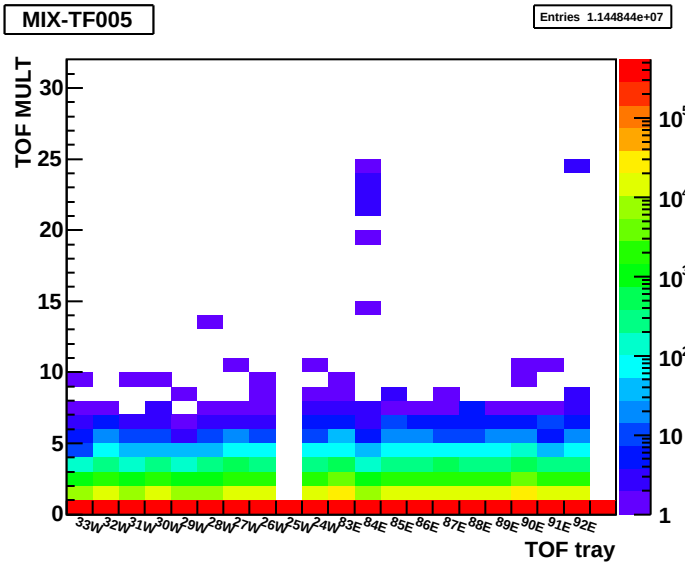

Unused (ch7)
Entries 4361312

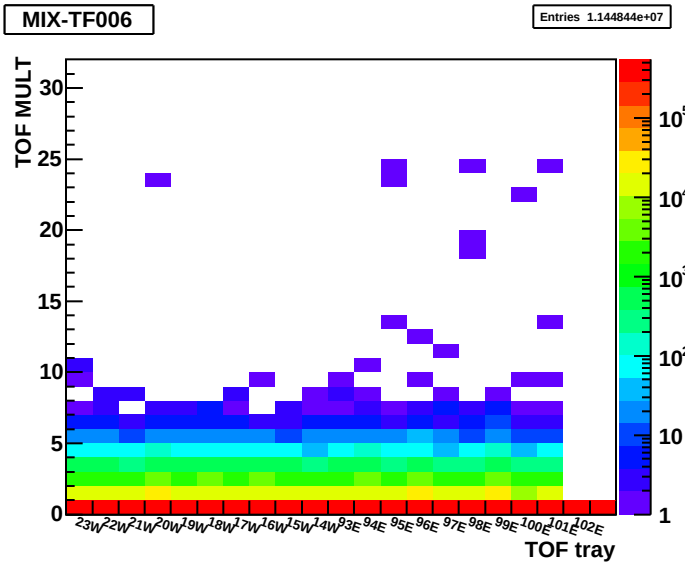

MIX-TF001
Entries 1.144844e+07
TOF MULT
TOF tray

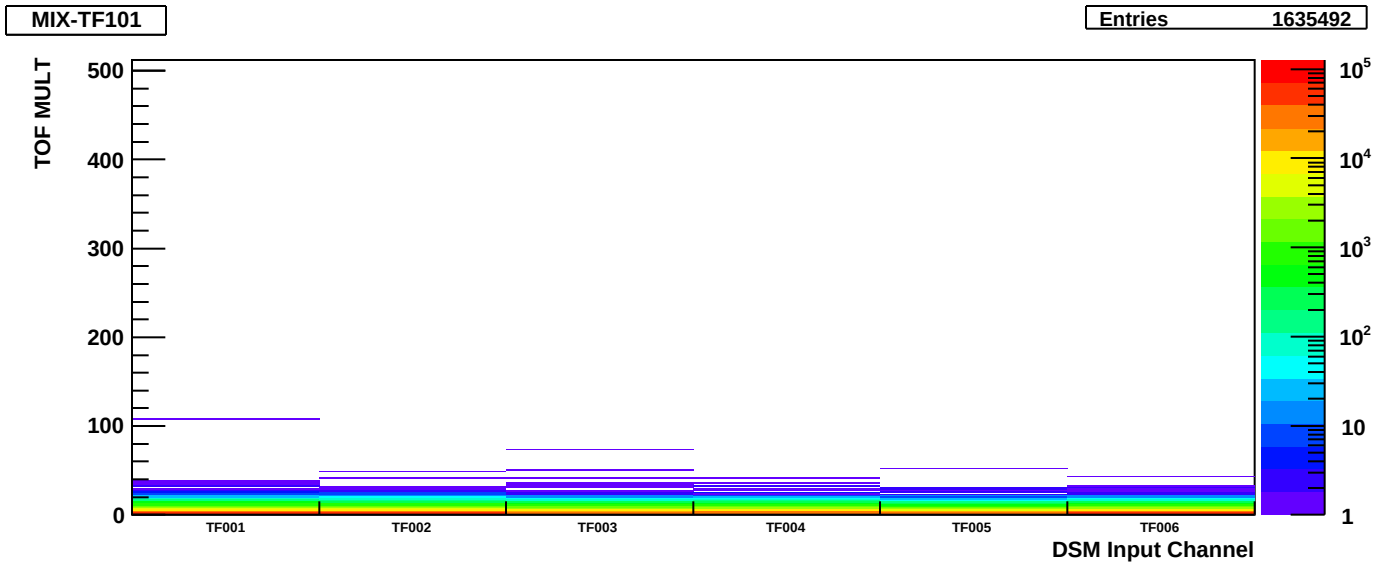

MIX-TF002
Entries 1.144844e+07
TOF MULT
TOF tray

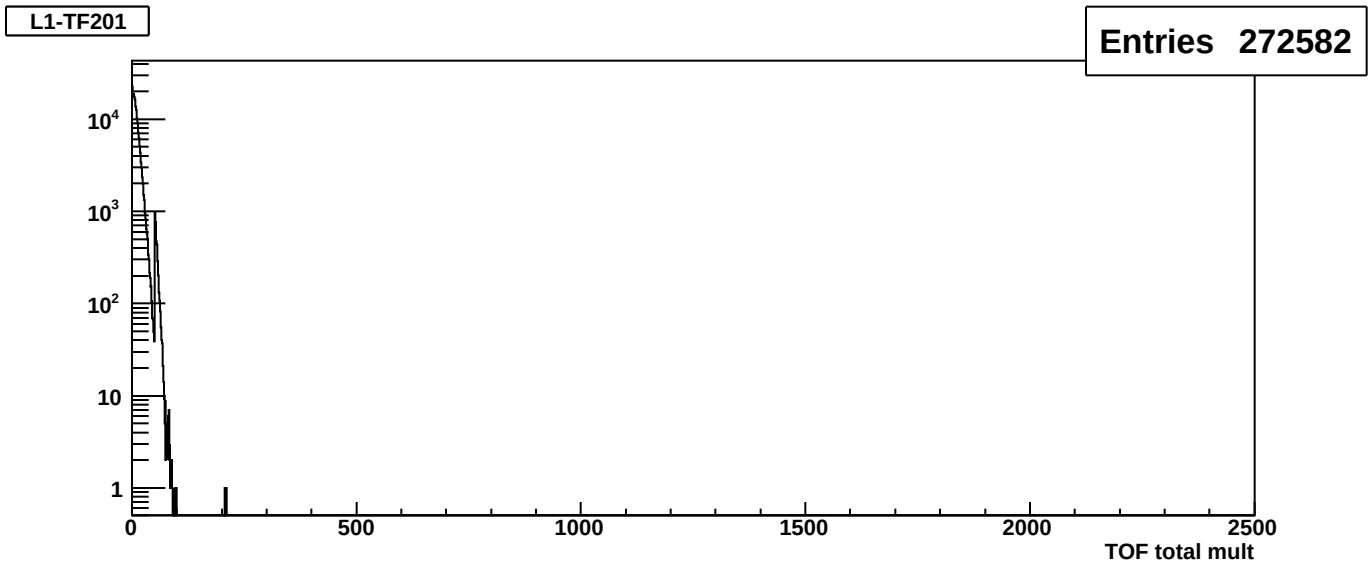

MIX-TF003
Entries 1.144844e+07
TOF MULT
TOF tray

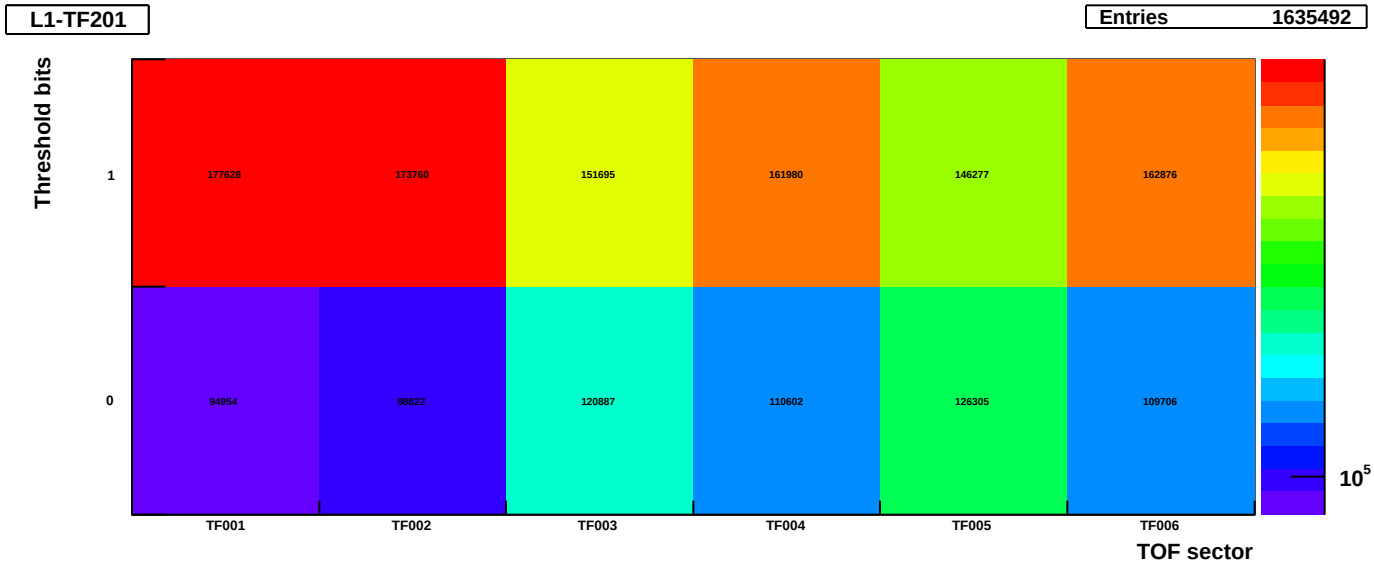

MIX-TF004
Entries 1.144844e+07
TOF MULT
TOF tray

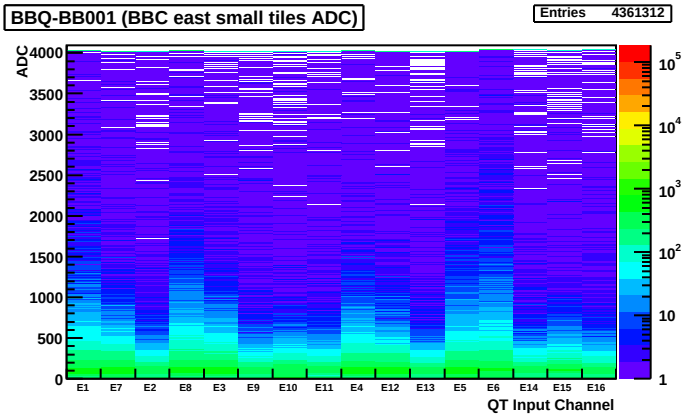

MIX-TF005
Entries 1.144844e+07
TOF MULT
TOF tray

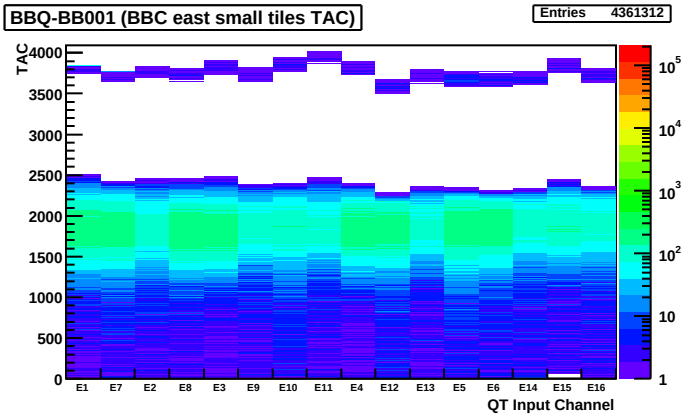

MIX-TF006
Entries 1.144844e+07
TOF MULT
TOF tray

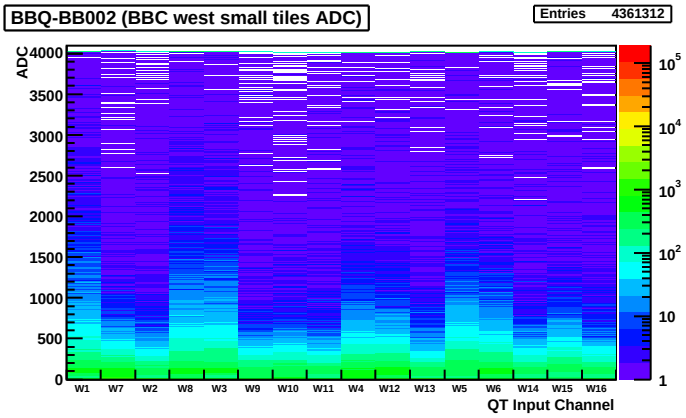

MIX-TF101
Entries 1635492
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

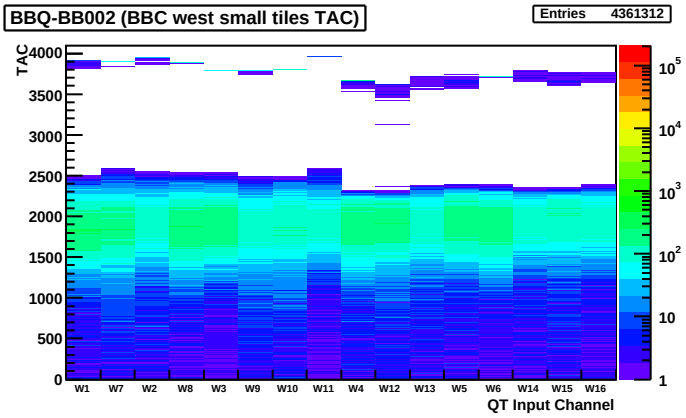

L1-TF201
Entries 272582
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

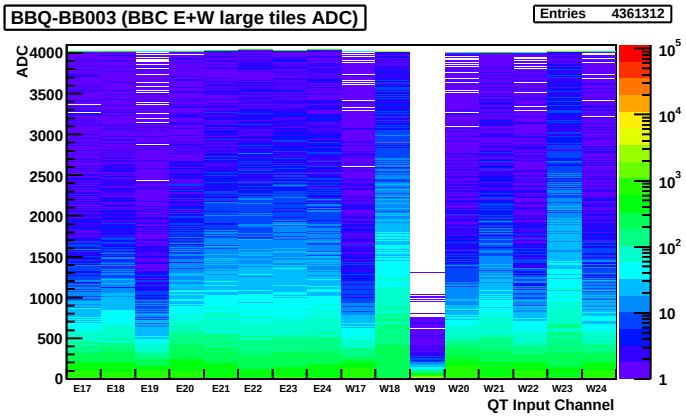

L1-TF201
Entries 1635492
Threshold bits
1
0
177628
173760
151695
161980
146277
162876
94954
98822
120887
110602
126305
109706
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

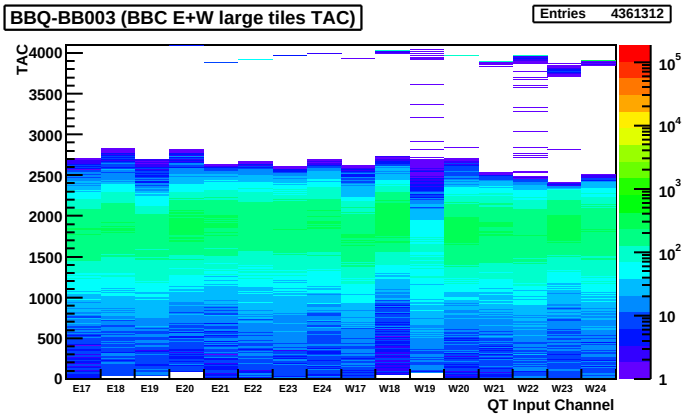

BBQ-BB001 (BBC east small tiles ADC)
Entries 4361312
ADC
QT Input Channel

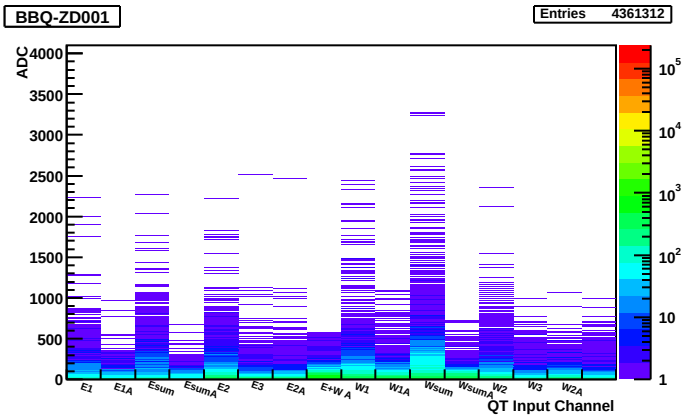

BBQ-BB001 (BBC east small tiles TAC)
Entries 4361312
TAC
QT Input Channel

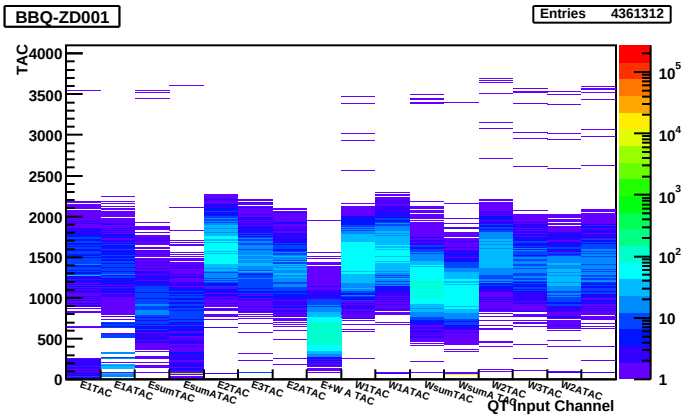

BBQ-BB002 (BBC west small tiles ADC)
Entries 4361312
ADC
QT Input Channel

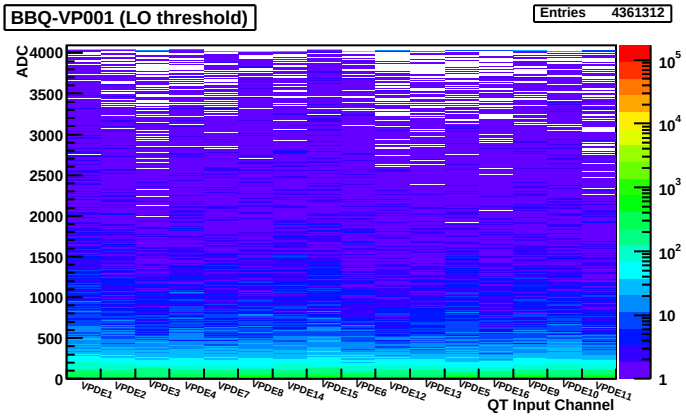

BBQ-BB002 (BBC west small tiles TAC)
Entries 4361312
TAC
QT Input Channel

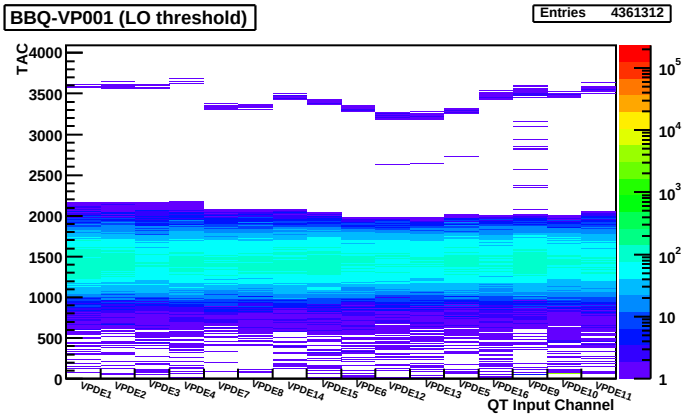

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4361312
ADC
QT Input Channel

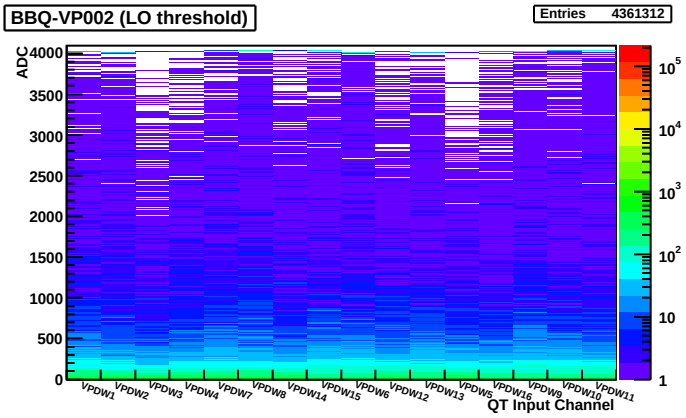

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4361312
TAC
QT Input Channel

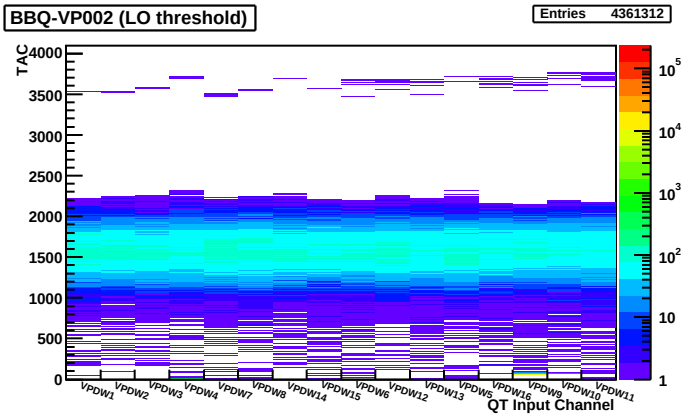

BBQ-ZD001
Entries 4361312
ADC
QT Input Channel

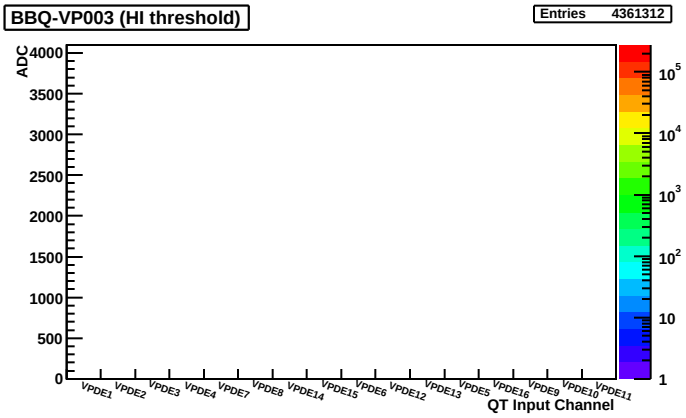

BBQ-ZD001
Entries 4361312
TAC
QT Input Channel

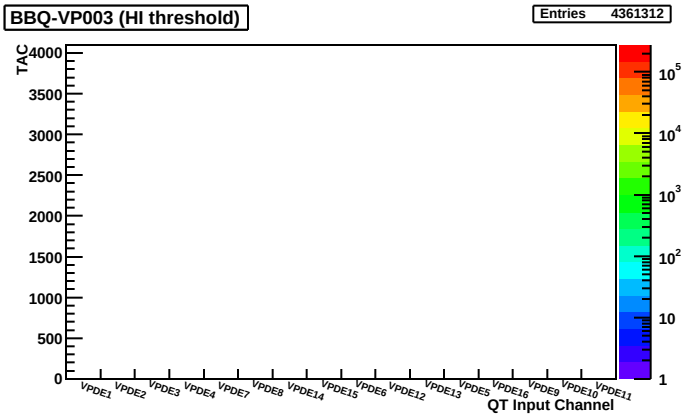

BBQ-VP001 (LO threshold)
Entries 4361312
ADC
QT Input Channel

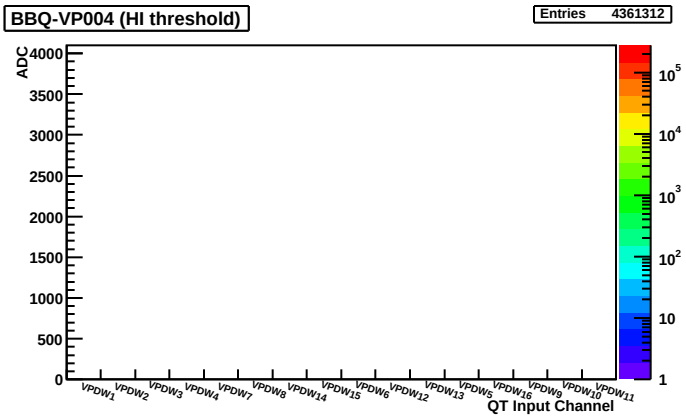

BBQ-VP001 (LO threshold)
Entries 4361312
TAC
QT Input Channel

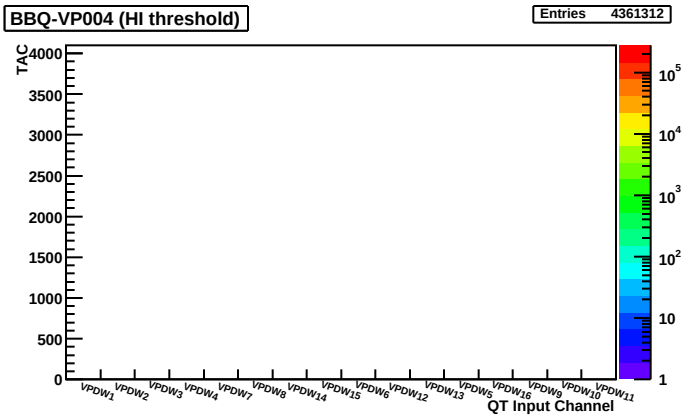

BBQ-VP002 (LO threshold)
Entries 4361312
ADC
QT Input Channel

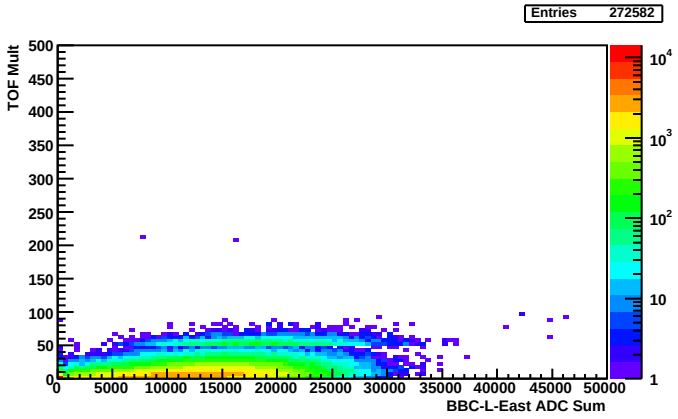

BBQ-VP002 (LO threshold)
Entries 4361312
TAC
QT Input Channel

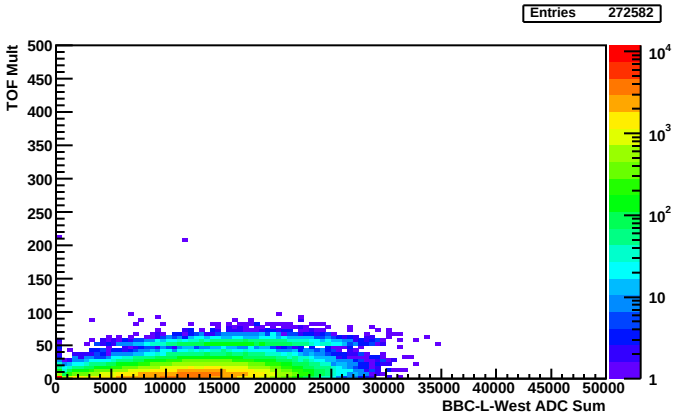

BBQ-VP003 (HI threshold)
Entries 4361312
ADC
QT Input Channel

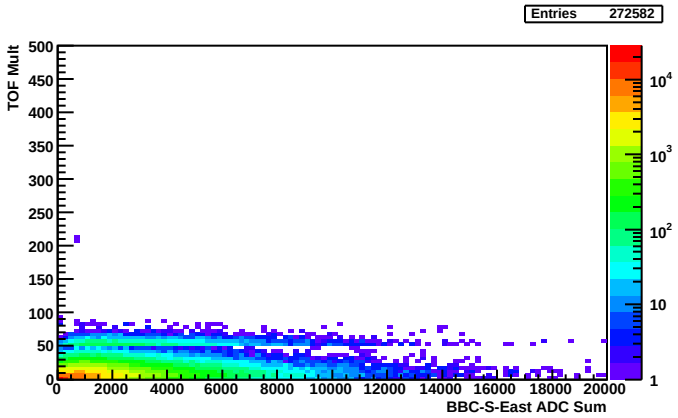

BBQ-VP003 (HI threshold)
Entries 4361312
TAC
QT Input Channel

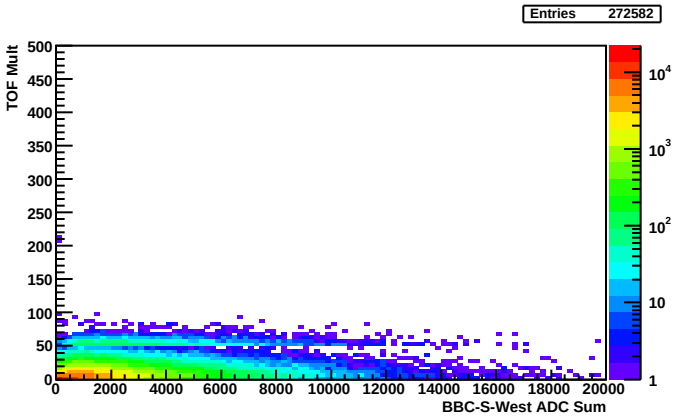

BBQ-VP004 (HI threshold)
Entries 4361312
ADC
QT Input Channel

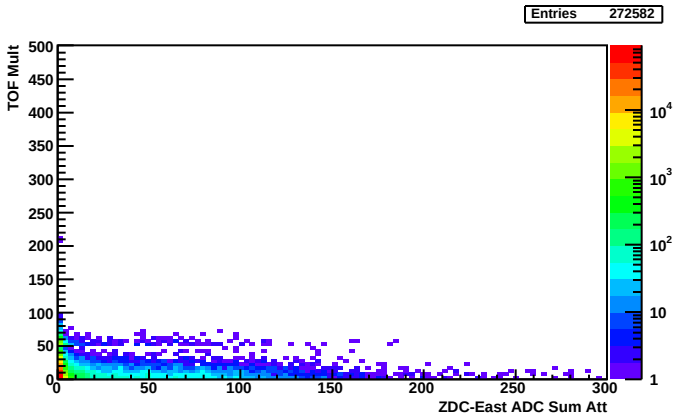

BBQ-VP004 (HI threshold)
Entries 4361312
TAC
QT Input Channel

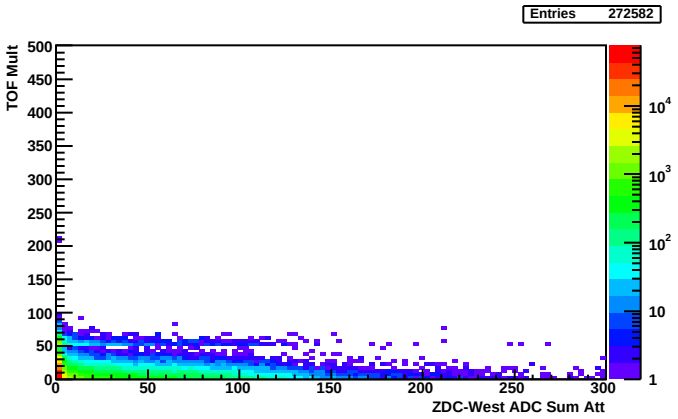

Entries 272582
TOF Mult
BBC-L-East ADC Sum

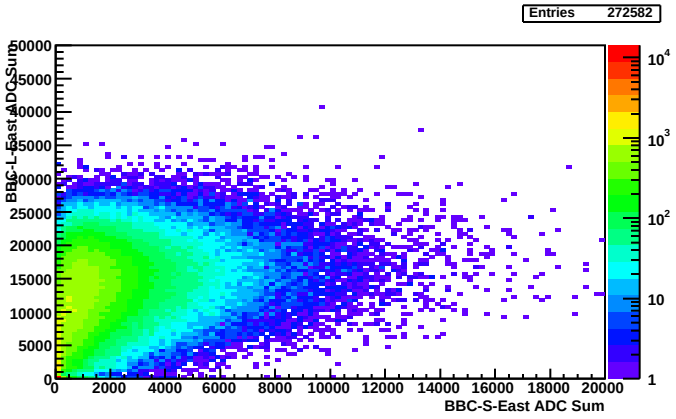

Entries 272582
TOF Mult
BBC-L-West ADC Sum

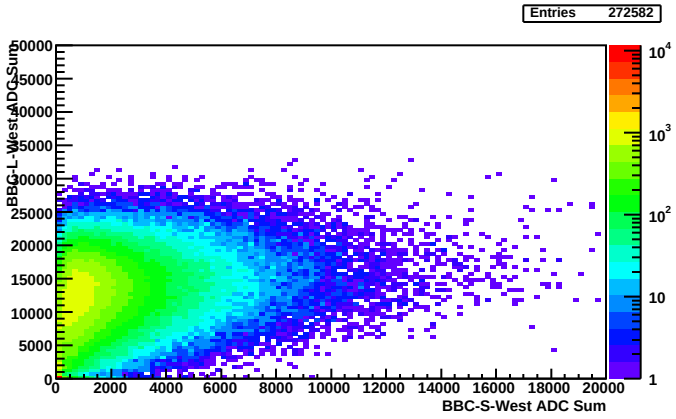

Entries 272582
TOF Mult
BBC-S-East ADC Sum

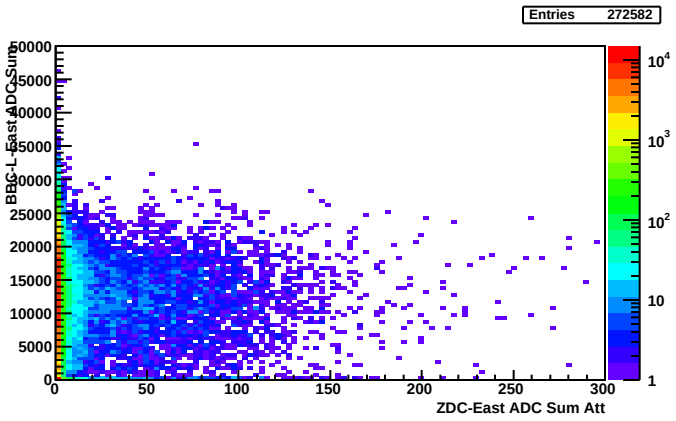

Entries 272582
TOF Mult
BBC-S-West ADC Sum

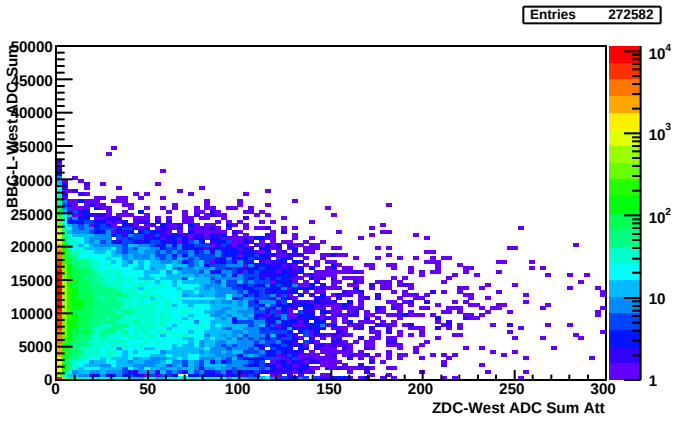

Entries 272582
TOF Mult
ZDC-East ADC Sum Att

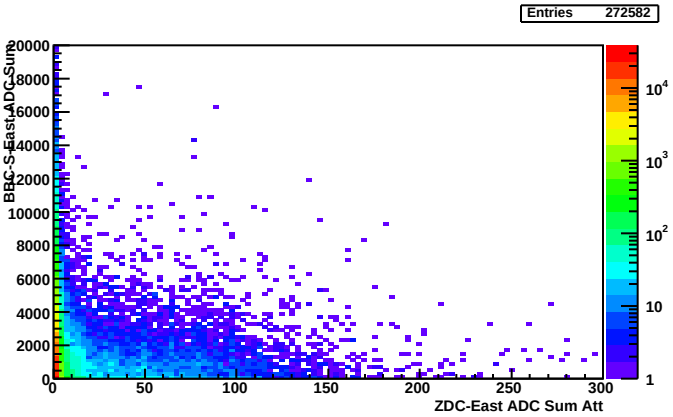

Entries 272582
TOF Mult
ZDC-West ADC Sum Att

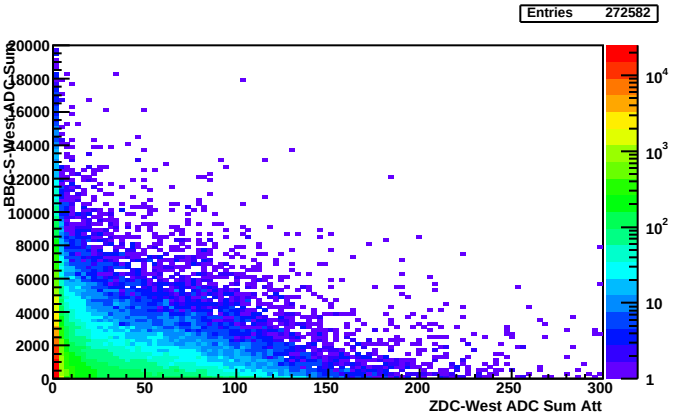

Entries 272582
BBC-L-East ADC Sum
BBC-S-East ADC Sum

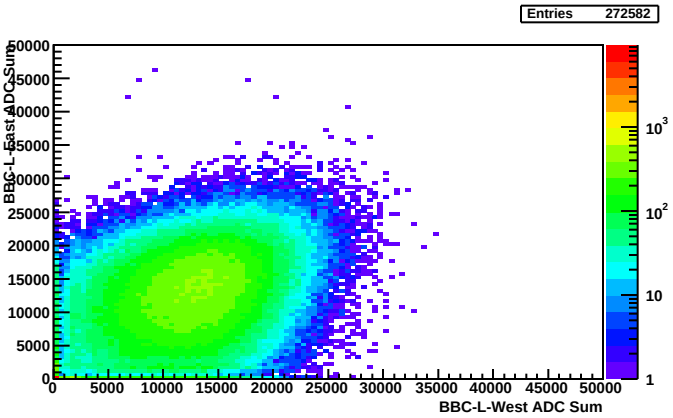

Entries 272582
BBC-L-West ADC Sum
BBC-S-West ADC Sum

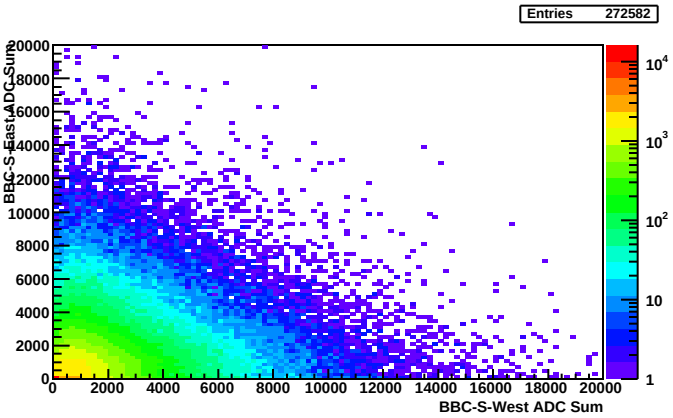

Entries 272582
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

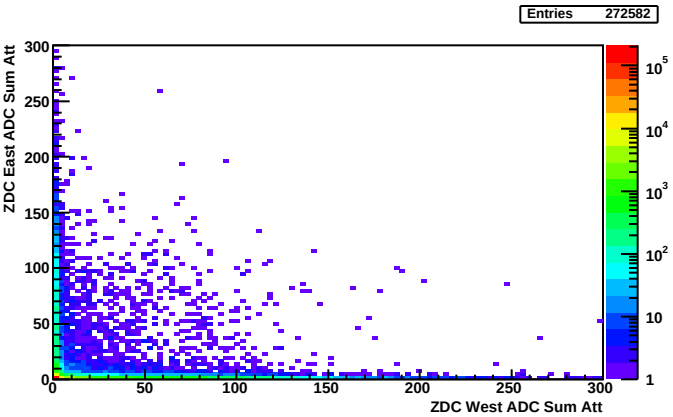

Entries 272582
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

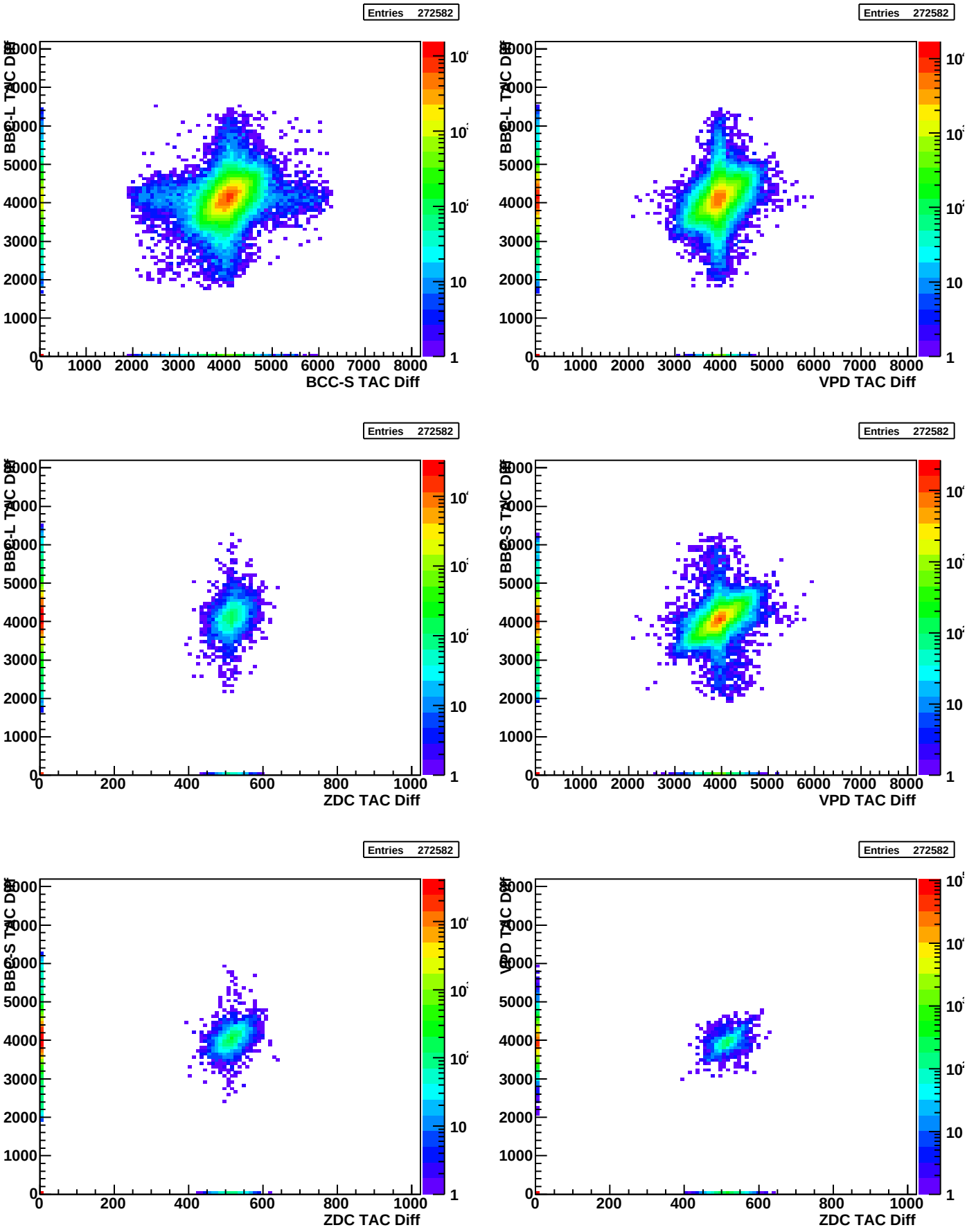

Entries 272582
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

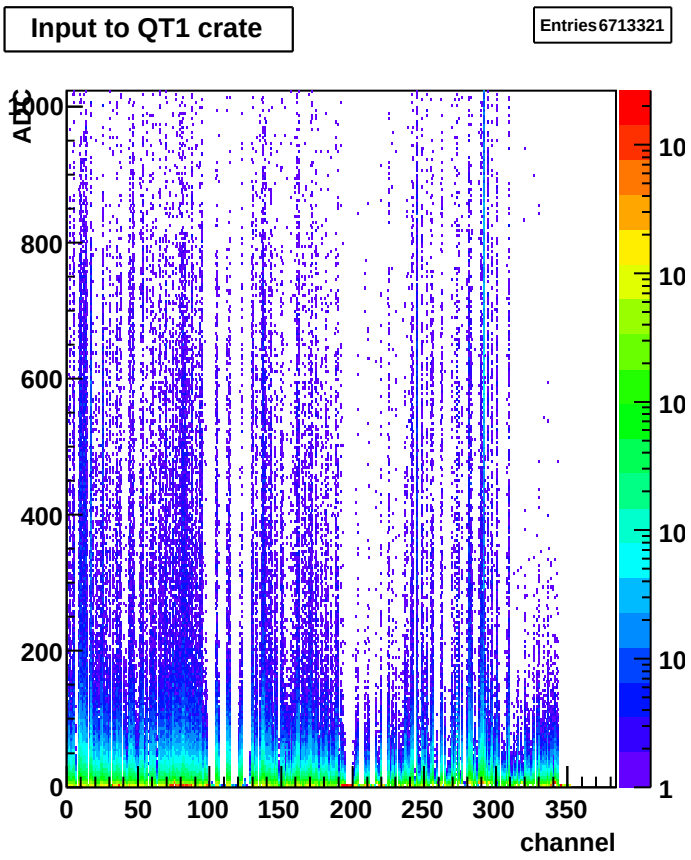

Entries 272582
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

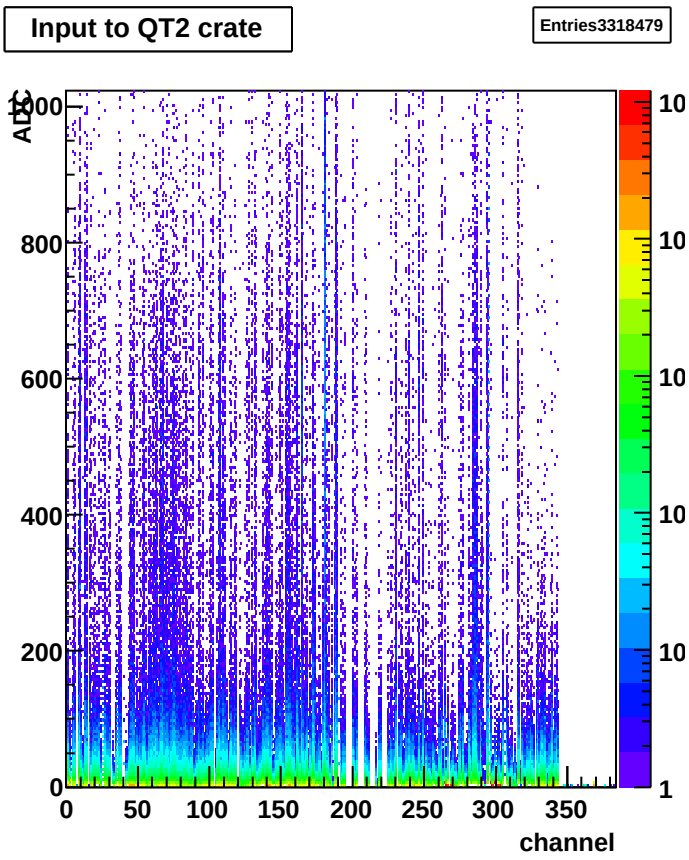

Entries 272582
BBC-L-East ADC Sum
BBC-L-West ADC Sum

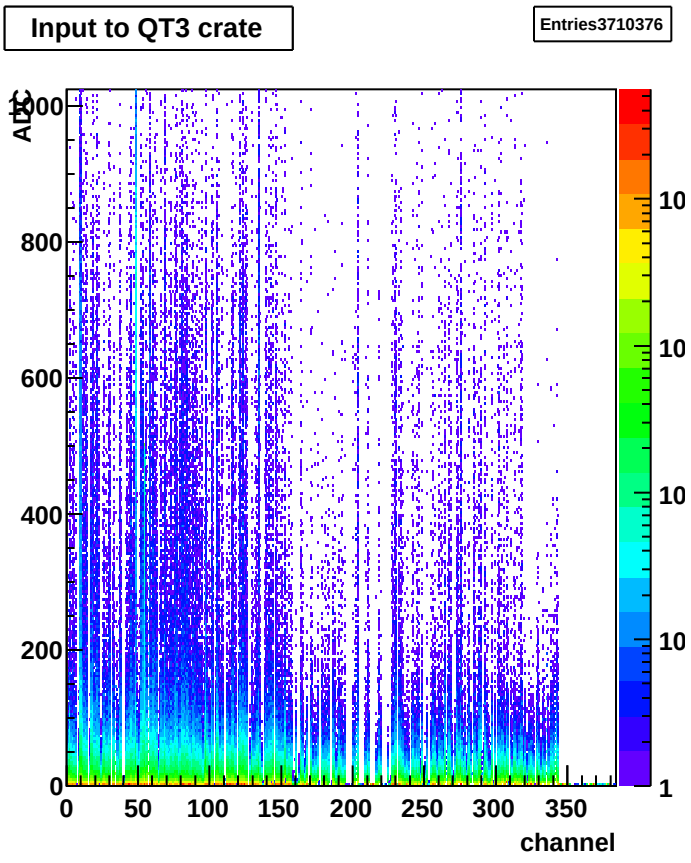

Entries 272582
BBC-S-East ADC Sum
BBC-S-West ADC Sum

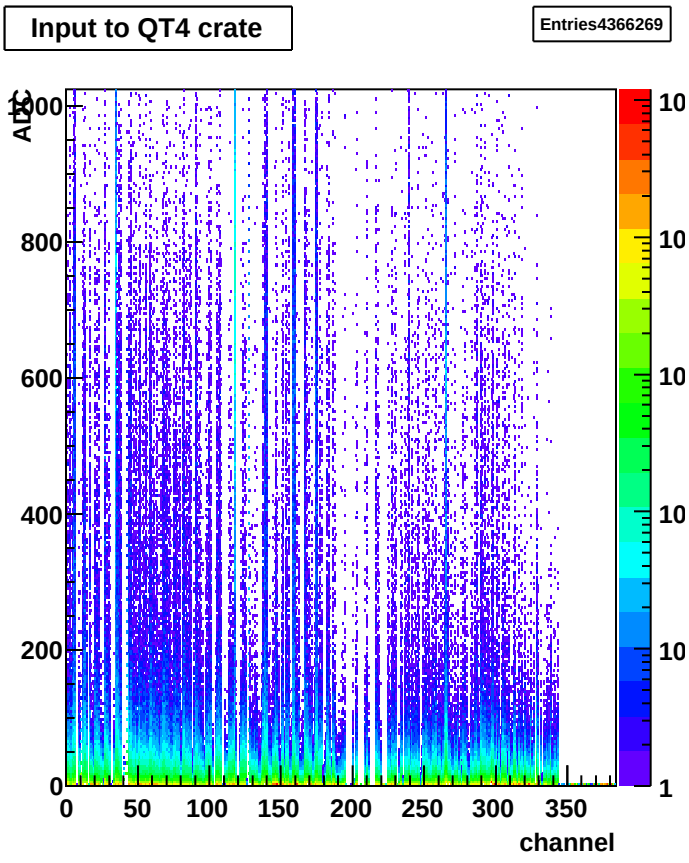

Entries 272582
ZDC East ADC Sum Att
ZDC West ADC Sum Att

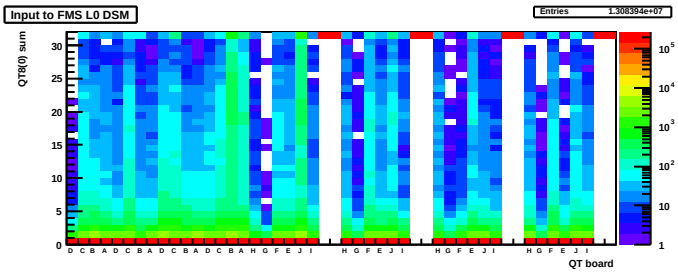

Entries 272582
BBC-L TAC Diff
BCC-S TAC Diff
Entries 272582
BBC-L TAC Diff
VPD TAC Diff
Entries 272582
BBC-L TAC Diff
ZDC TAC Diff
Entries 272582
BBC-S TAC Diff
VPD TAC Diff
Entries 272582
BBC-S TAC Diff
ZDC TAC Diff
Entries 272582
VPD TAC Diff
ZDC TAC Diff

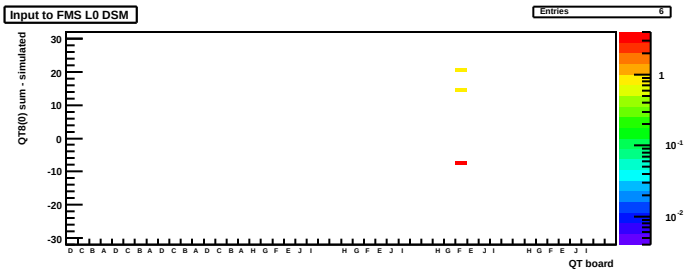

Input to QT1 crate
Entries 6713321
ADC
channel

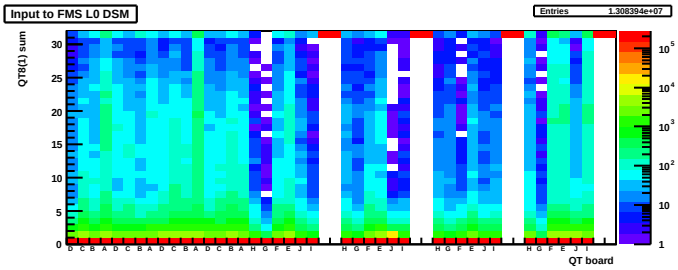

Input to QT2 crate
Entries 3318479
ADC
channel

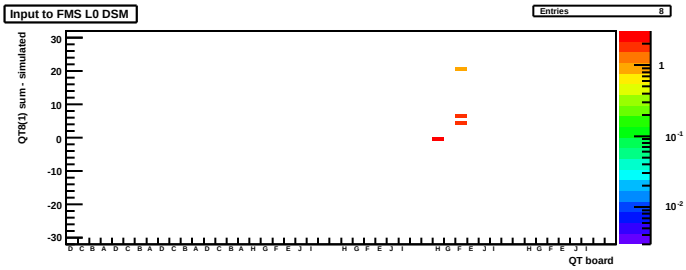

Input to QT3 crate
Entries 3710376
ADC
channel

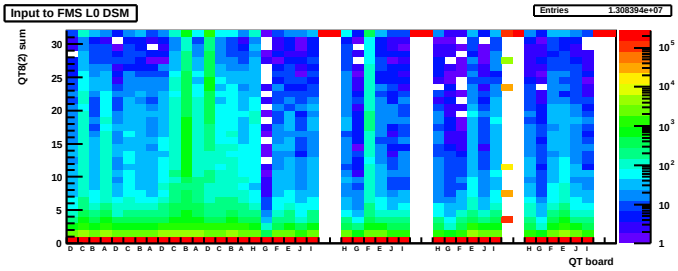

Input to QT4 crate
Entries 4366269
ADC
channel

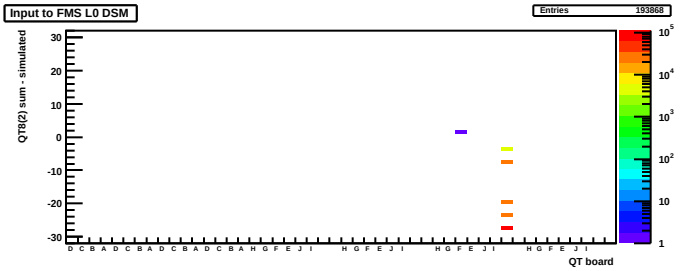

Input to FMS L0 DSM
Entries 1.308394e+07
QT8(0) sum
QT board

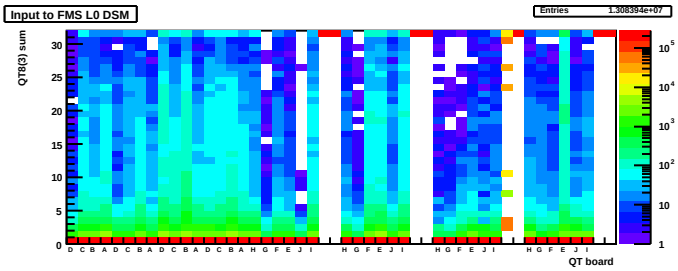

Input to FMS L0 DSM
Entries 6
QT8(0) sum - simulated
QT board

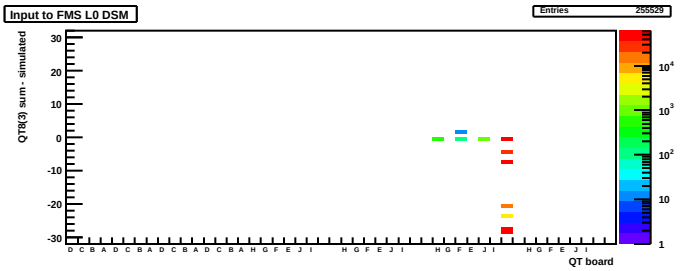

Input to FMS L0 DSM
Entries 1.308394e+07
QT8(1) sum
QT board

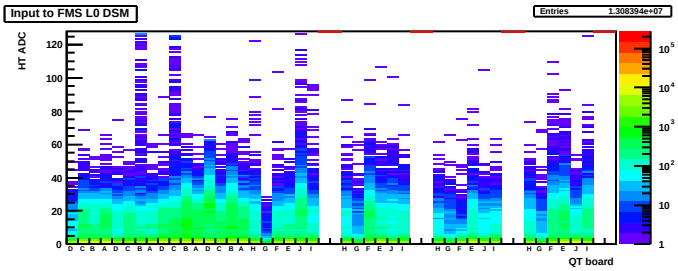

Input to FMS L0 DSM
Entries 8
QT8(1) sum - simulated
QT board

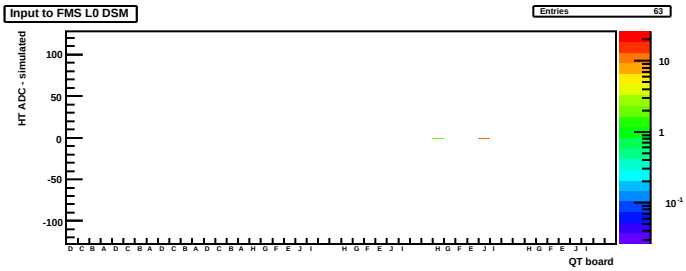

Input to FMS L0 DSM
Entries 1.308394e+07
QT8(2) sum
QT board

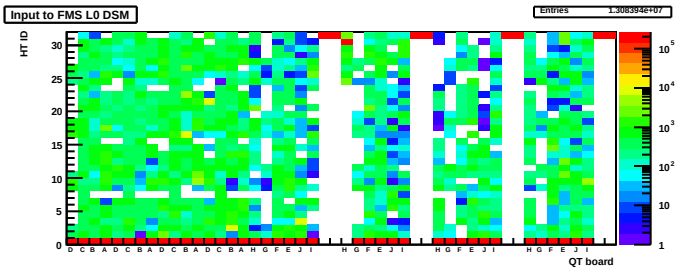

Input to FMS L0 DSM
Entries 193868
QT8(2) sum - simulated
QT board

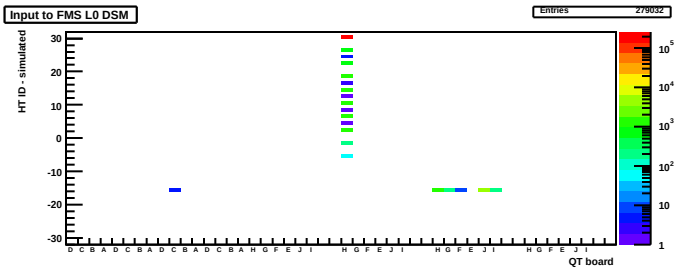

Input to FMS L0 DSM
Entries 1.308394e+07
QT8(3) sum
QT board

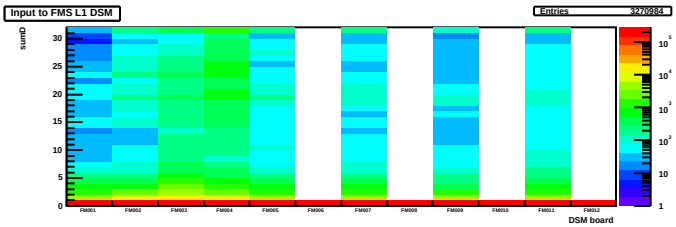

Input to FMS L0 DSM
Entries 255529
QT8(3) sum - simulated
QT board

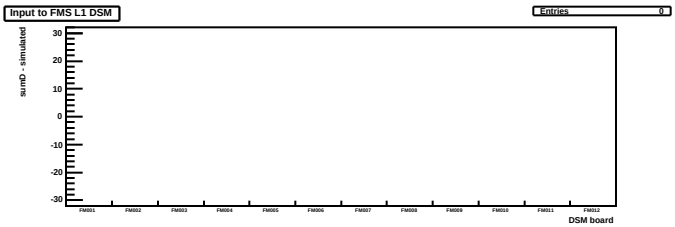

Input to FMS L0 DSM
Entries 1.308394e+07
HT ADC
QT board

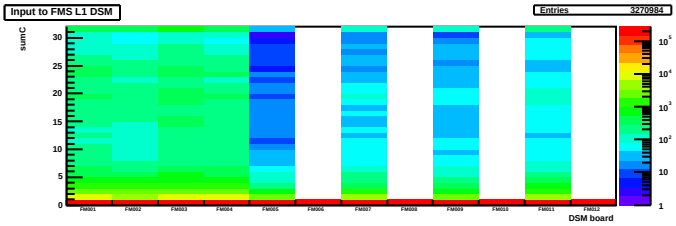

Input to FMS L0 DSM
Entries 63
HT ADC - simulated
QT board

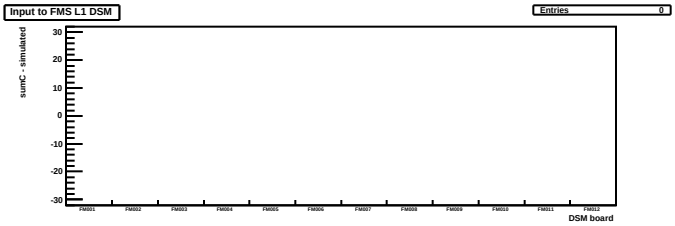

Input to FMS L0 DSM
Entries 1.308394e+07
HT ID
QT board

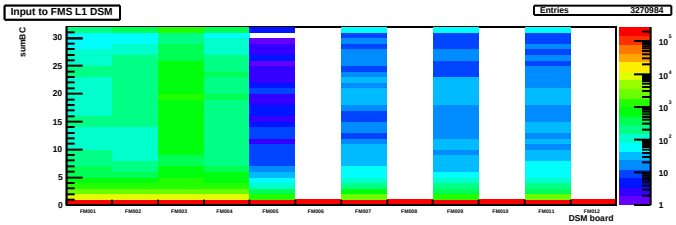

Input to FMS L0 DSM
Entries 279032
HT ID - simulated
QT board

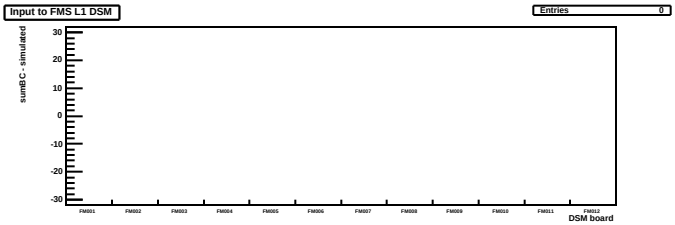

Input to FMS L1 DSM
Entries 3270984
sumD
DSM board

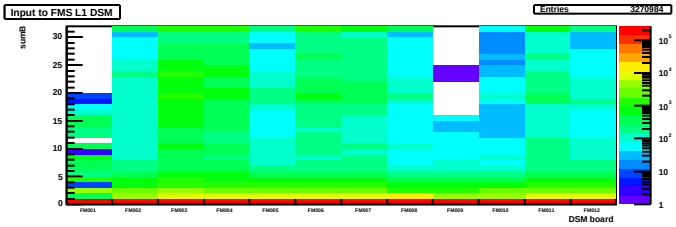

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

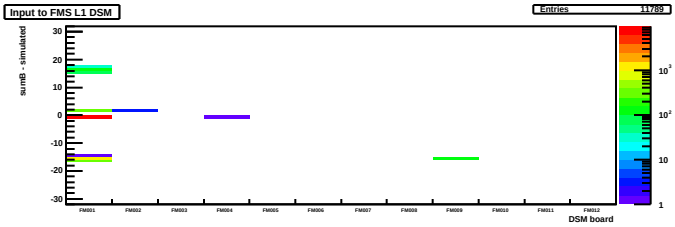

Input to FMS L1 DSM
Entries 3270984
sumC
DSM board

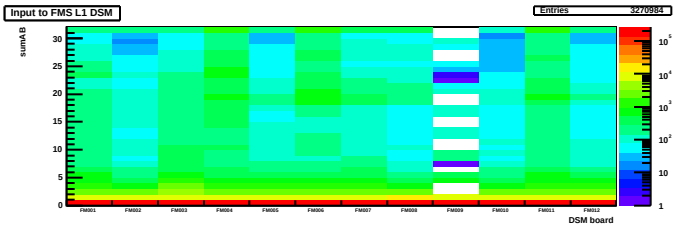

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

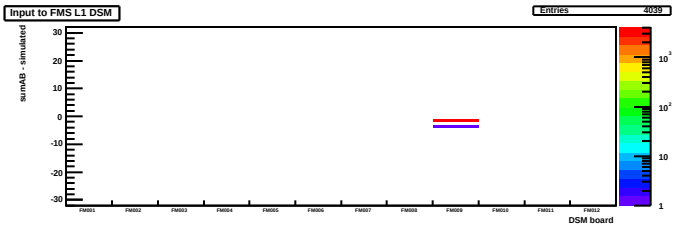

Input to FMS L1 DSM
Entries 3270984
sumBC
DSM board

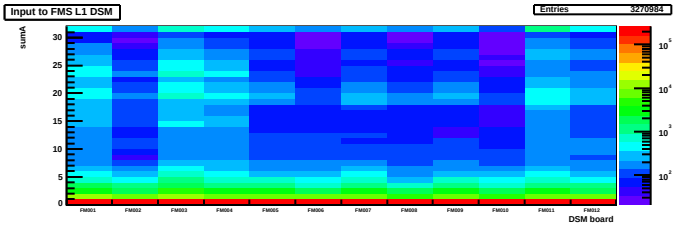

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

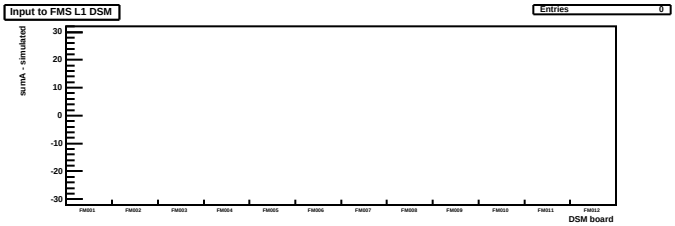

Input to FMS L1 DSM
Entries 3270984
sumB
DSM board

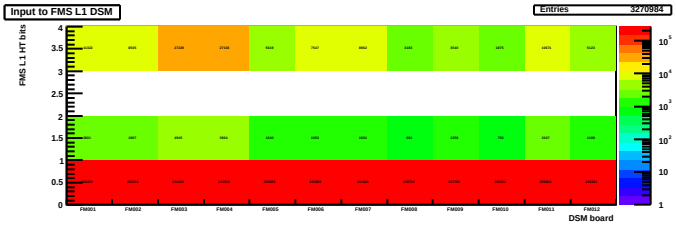

Input to FMS L1 DSM
Entries 11789
sumB - simulated
DSM board

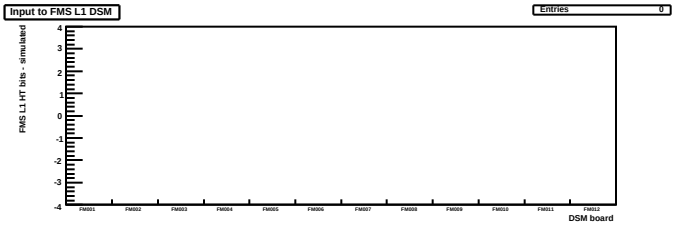

Input to FMS L1 DSM
Entries 3270984
sumAB
DSM board

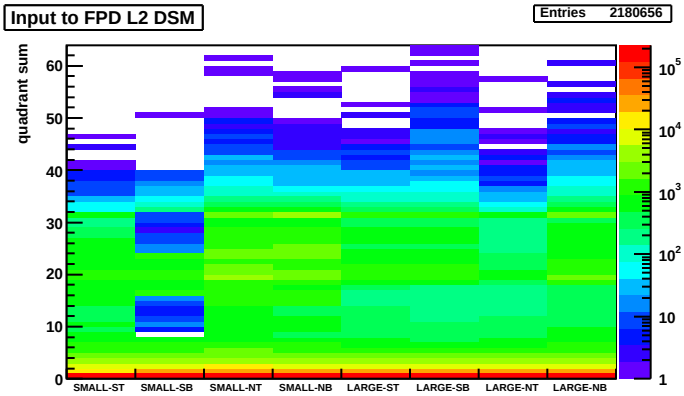

Input to FMS L1 DSM
Entries 4039
sumAB - simulated
DSM board

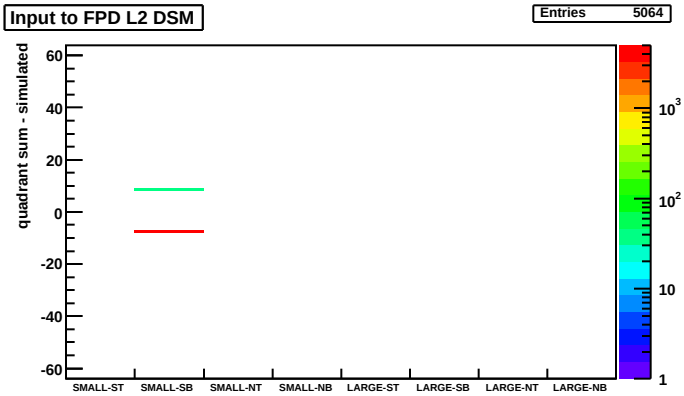

Input to FMS L1 DSM
Entries 3270984
sumA
DSM board

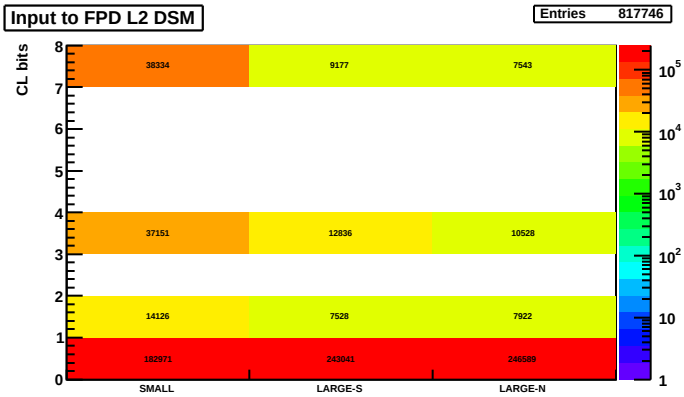

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

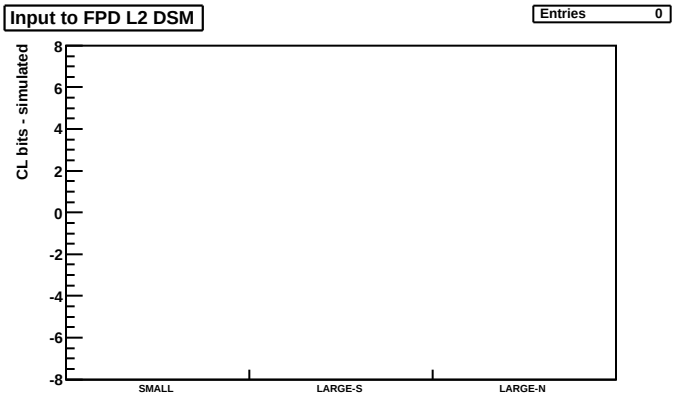

Input to FMS L1 DSM
Entries 3270984
FMS L1 HT bits
DSM board

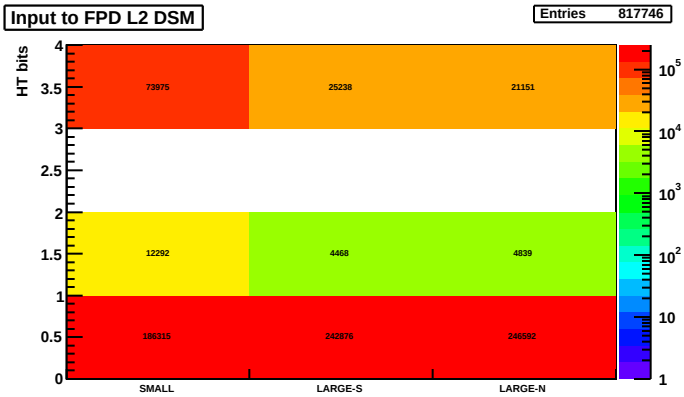

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

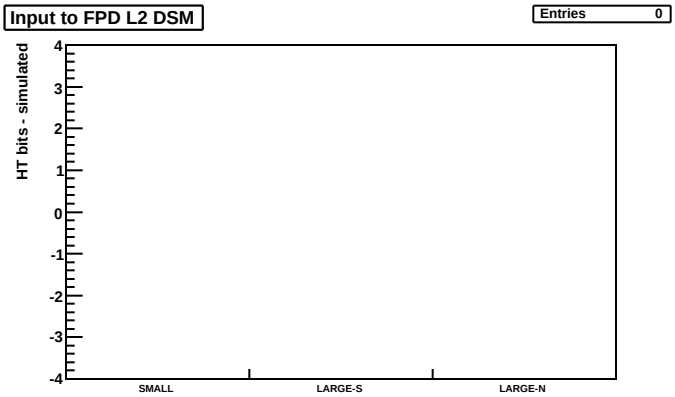

Input to FPD L2 DSM
Entries 2180656
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

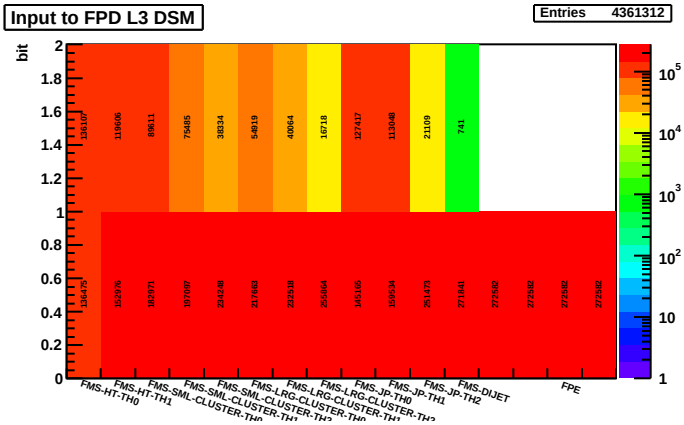

Input to FPD L2 DSM
Entries 5064
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

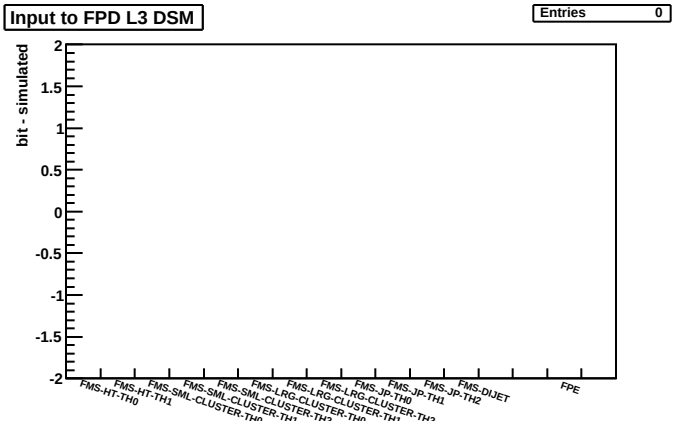

Input to FPD L2 DSM
Entries 817746
CL bits
38334
9177
7543
37151
12836
10528
14126
7528
7922
182971
243041
246589
SMALL
LARGE-S
LARGE-N

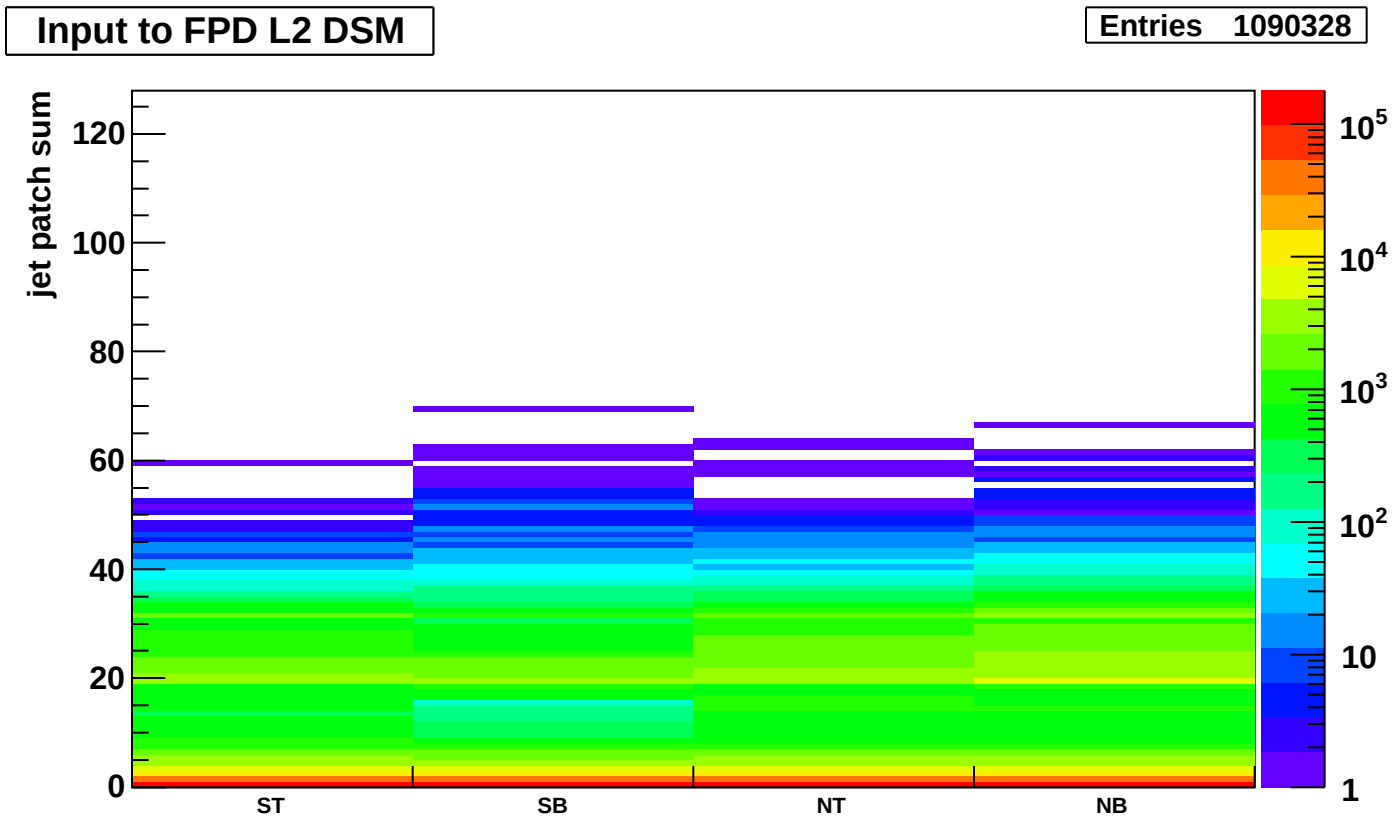

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

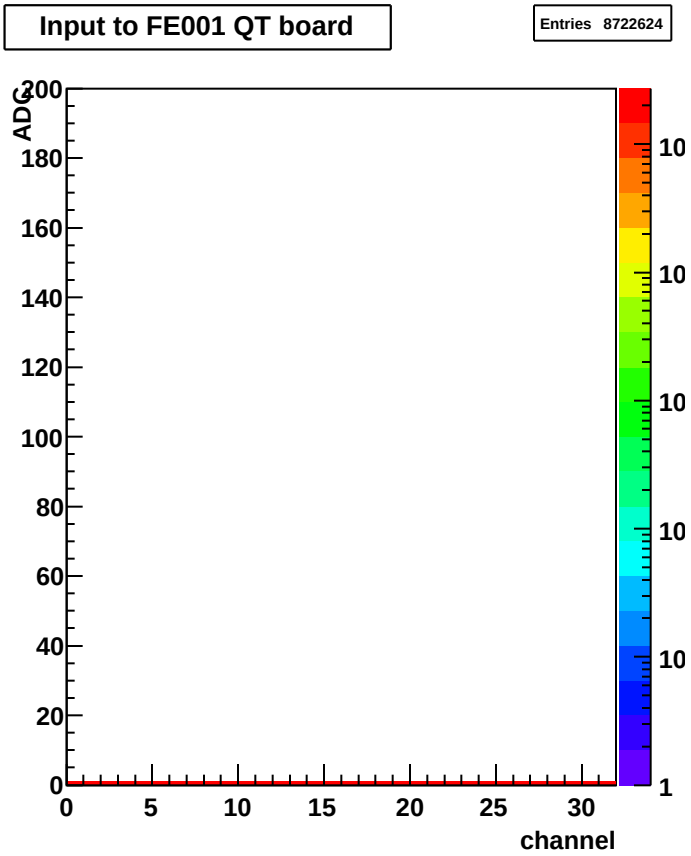

Input to FPD L2 DSM
Entries 817746
HT bits
73975
25238
21151
12292
4468
4839
186315
242876
246592
SMALL
LARGE-S
LARGE-N

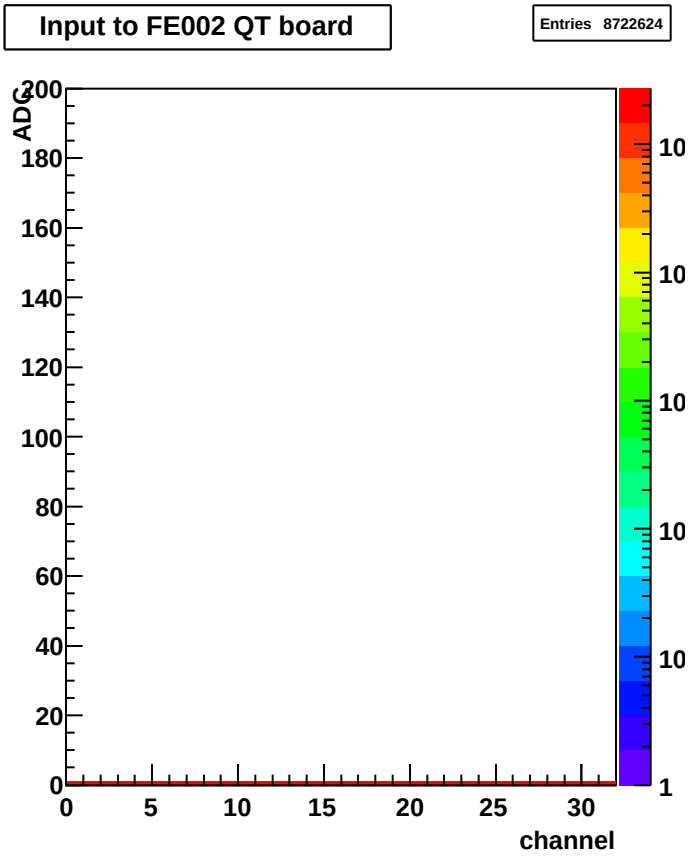

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

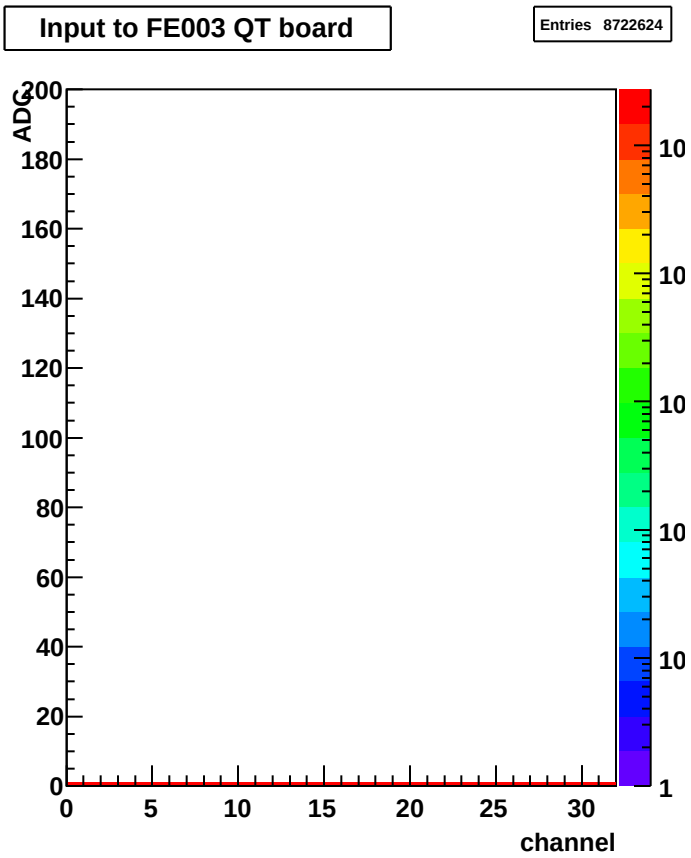

Input to FPD L3 DSM
Entries 4361312
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

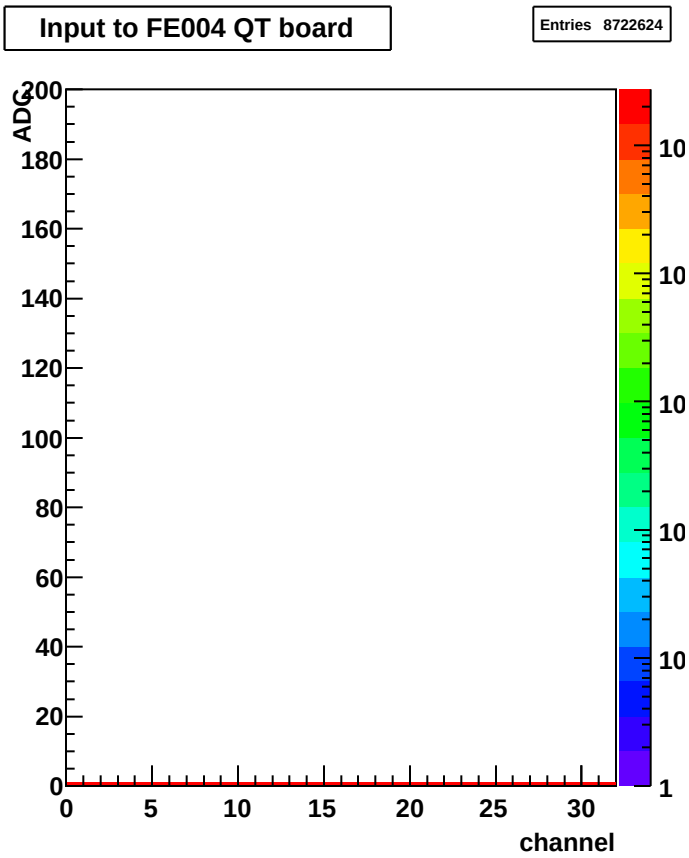

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

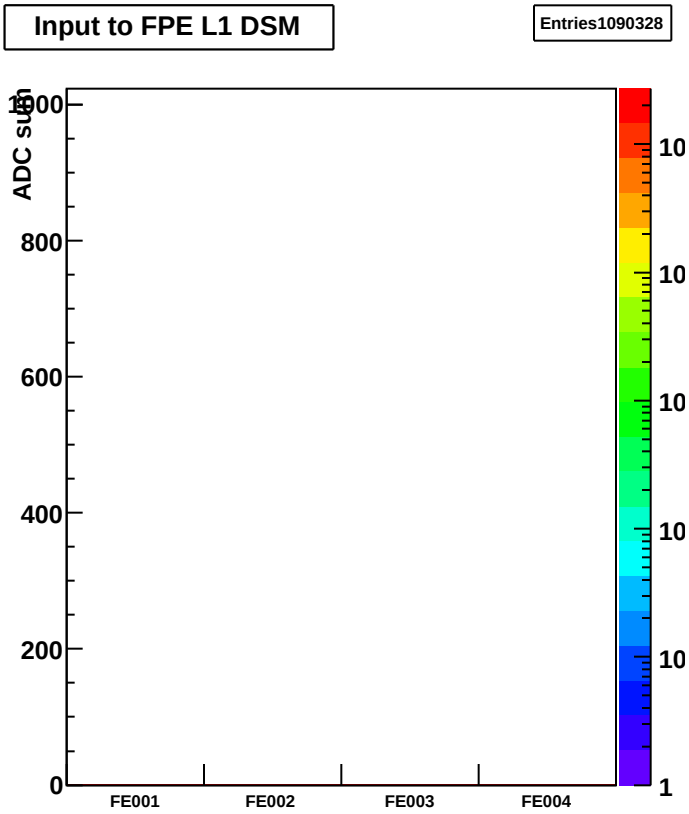

Input to FPD L2 DSM
Entries 1090328
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

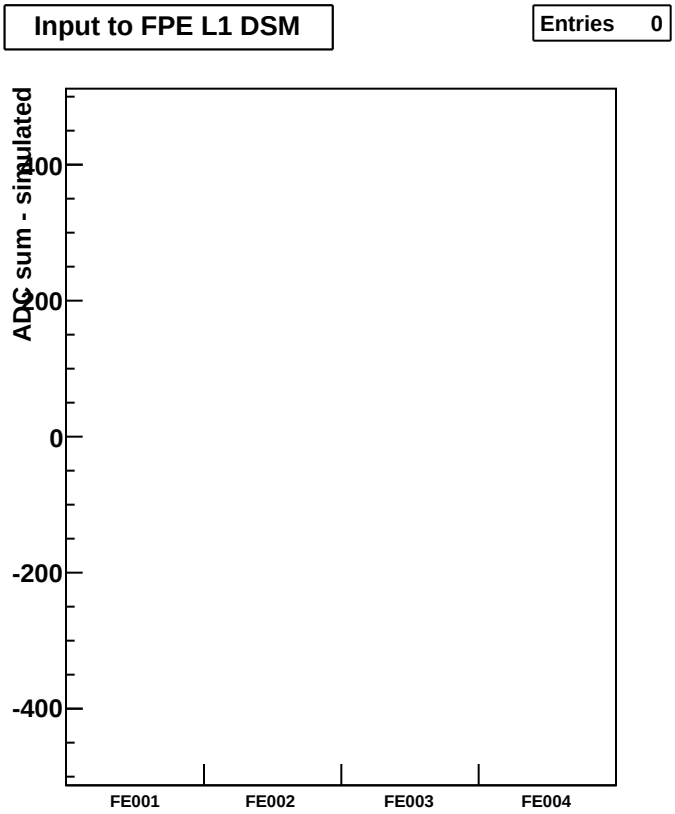

Input to FE001 QT board
Entries 8722624
ADC
channel

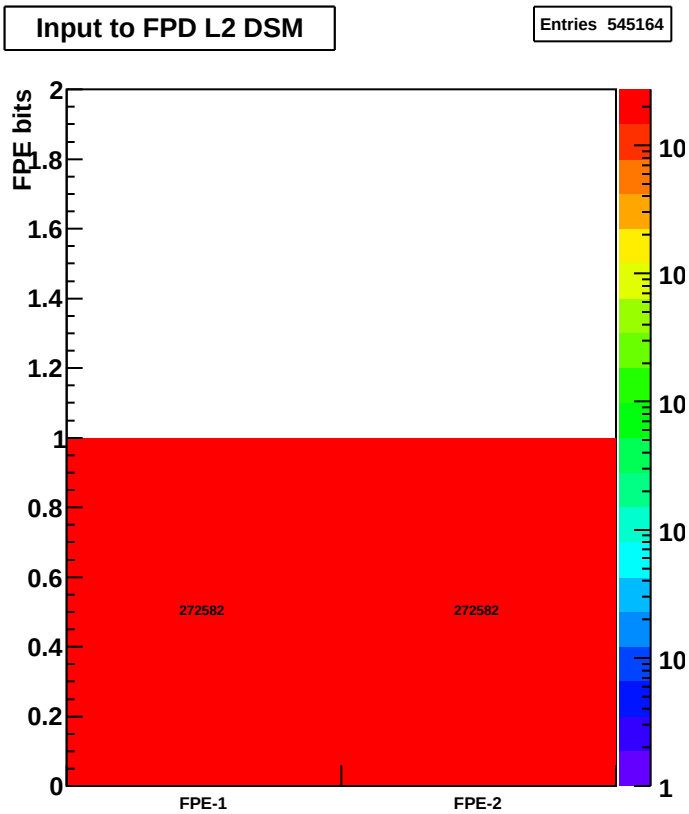

Input to FE002 QT board
Entries 8722624
ADC
channel

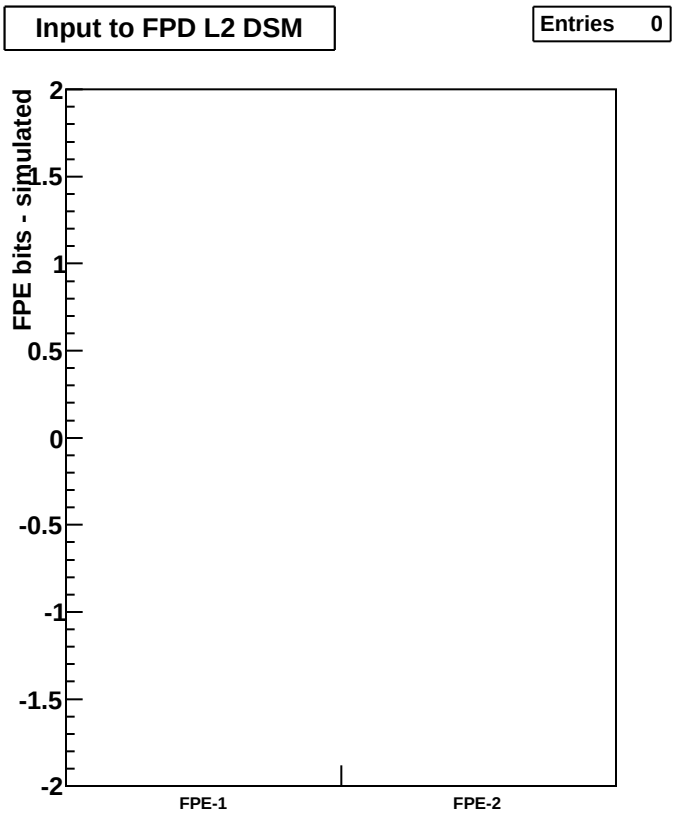

Input to FE003 QT board
Entries 8722624
ADC
channel


Input to FE004 QT board
Entries 8722624
ADC
channel


Input to FPE L1 DSM
Entries1090328
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 545164
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
272582
272582
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2