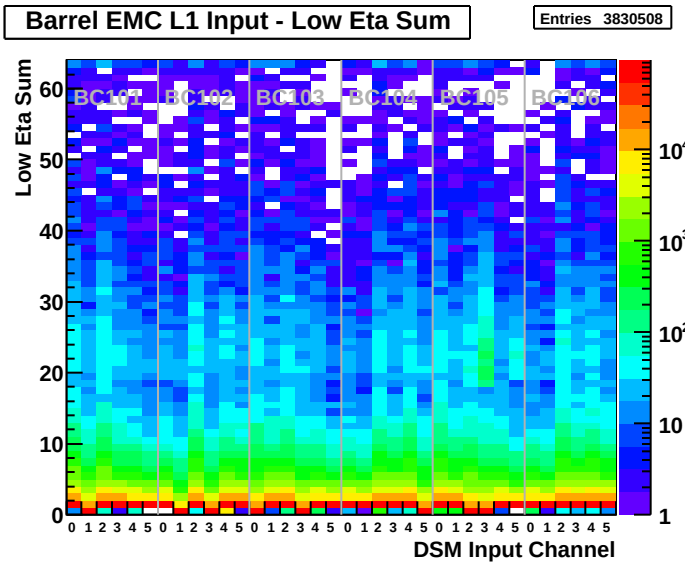
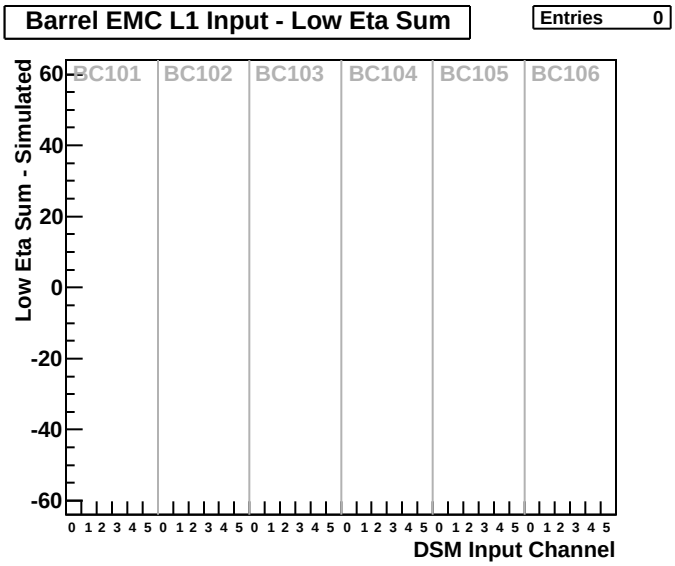
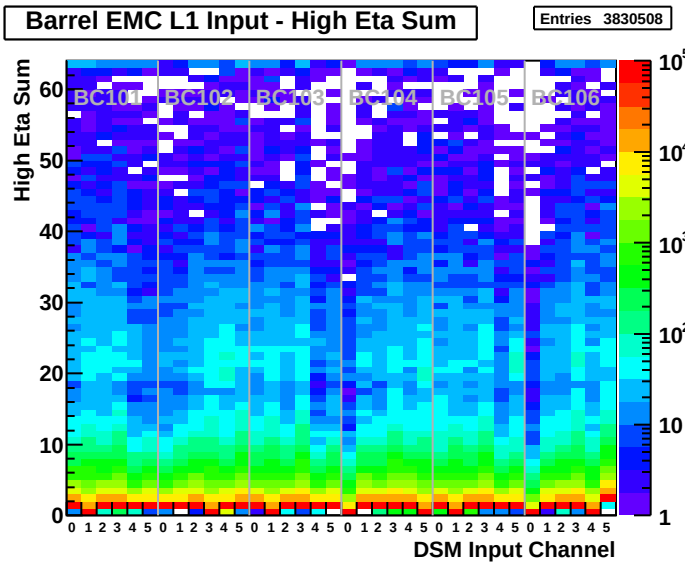
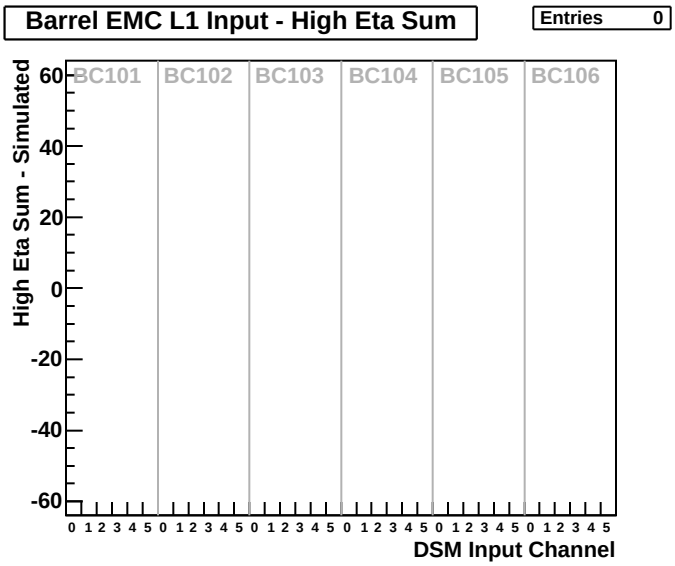
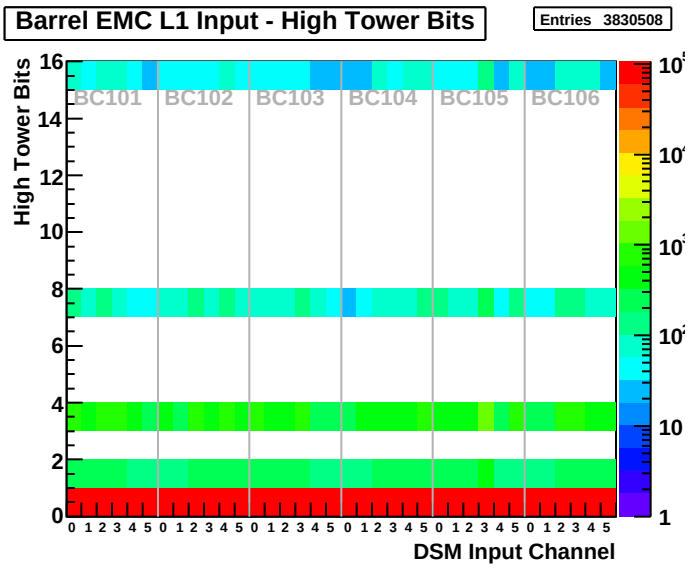
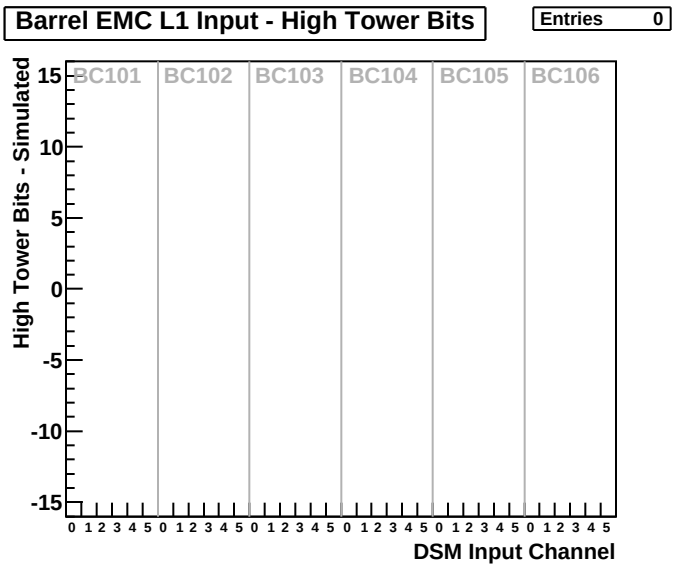

Barrel EMC L1 Input - Low Eta Sum
Entries 3830508
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

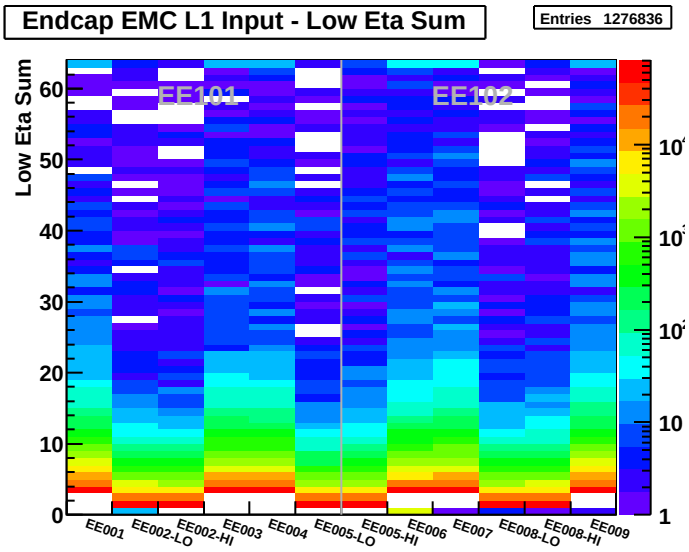

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

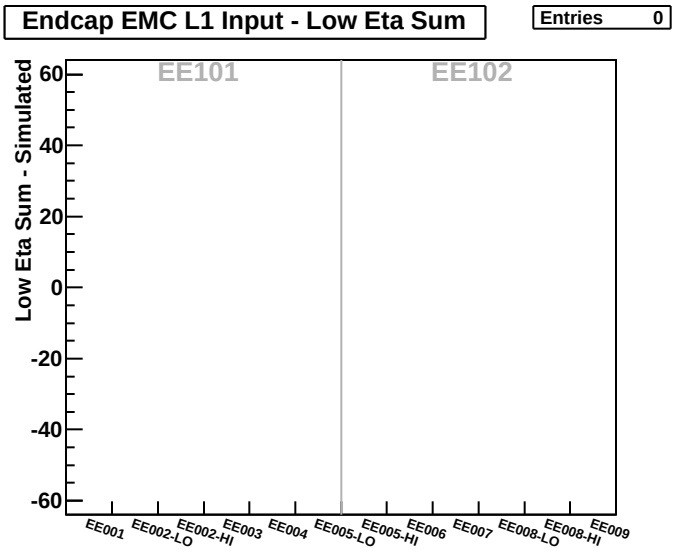

Barrel EMC L1 Input - High Eta Sum
Entries 3830508
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

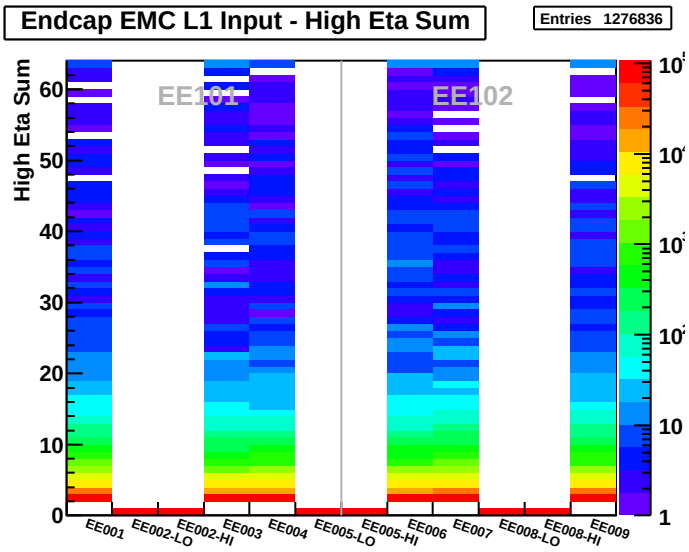

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

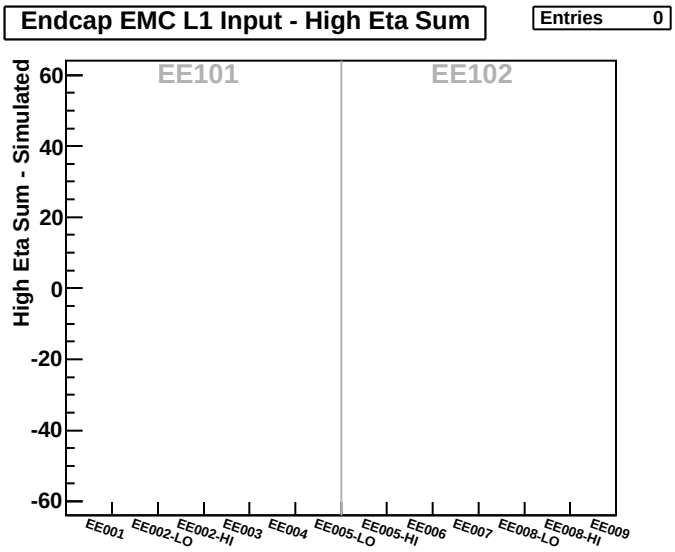

Barrel EMC L1 Input - High Tower Bits
Entries 3830508
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

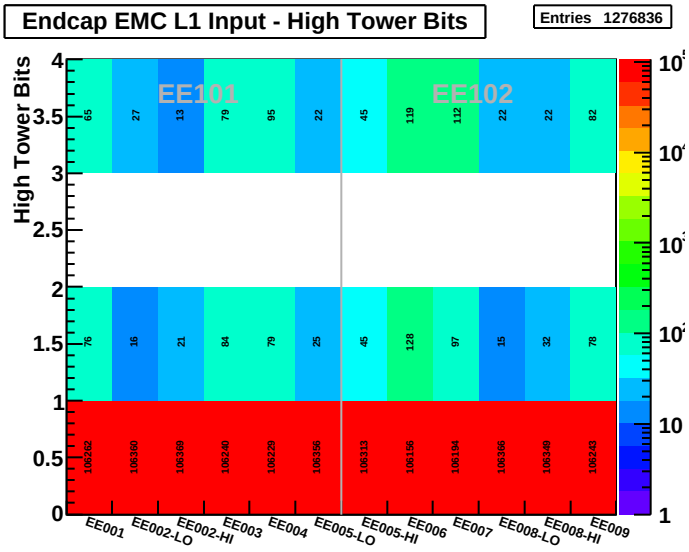

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

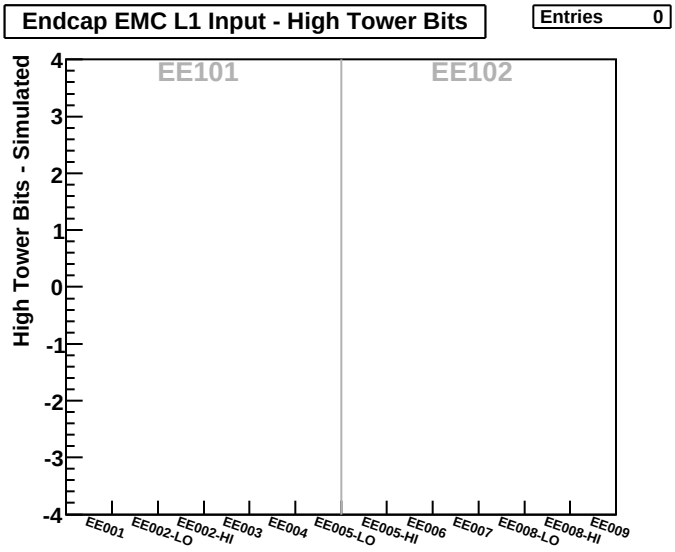

Endcap EMC L1 Input - Low Eta Sum
Entries 1276836
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

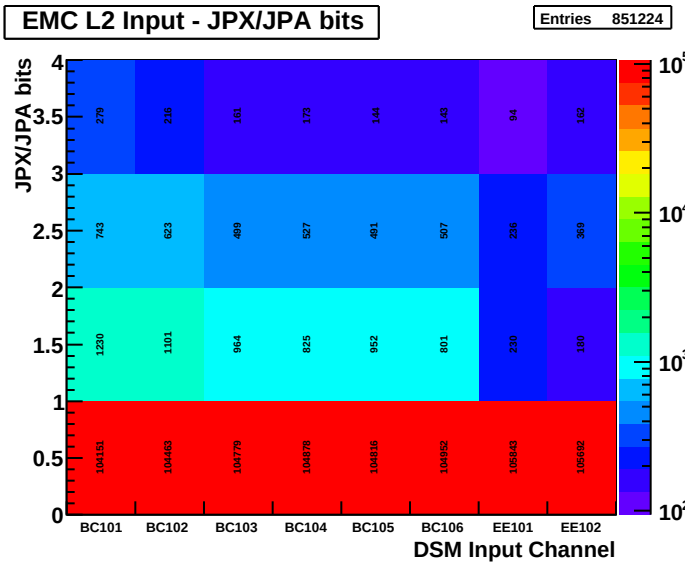

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

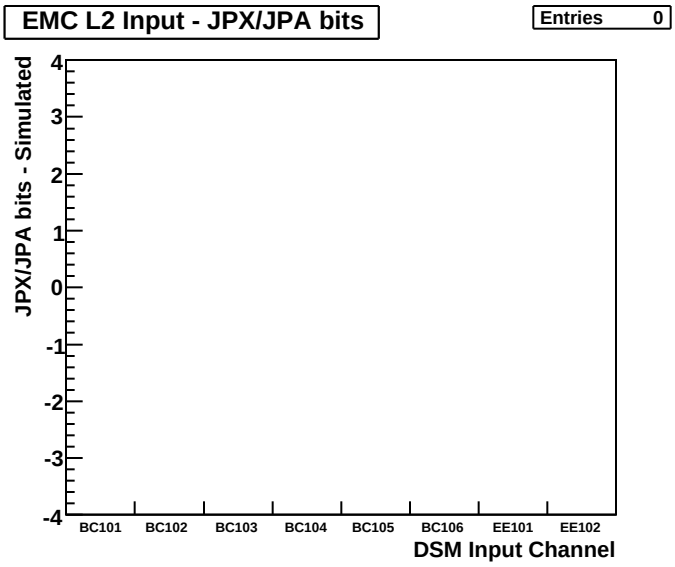

Endcap EMC L1 Input - High Eta Sum
Entries 1276836
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

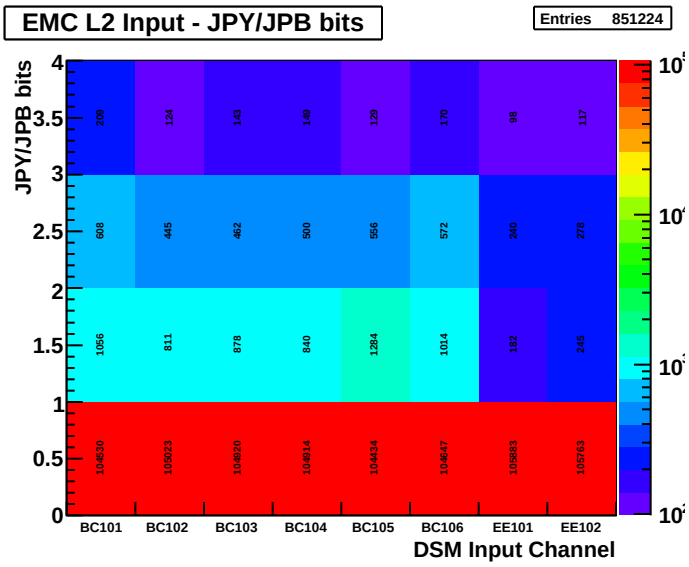

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

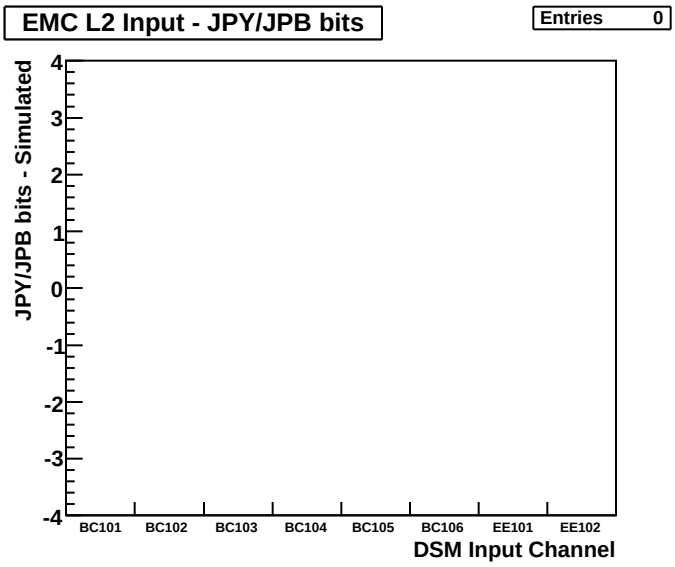

Endcap EMC L1 Input - High Tower Bits
Entries 1276836
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

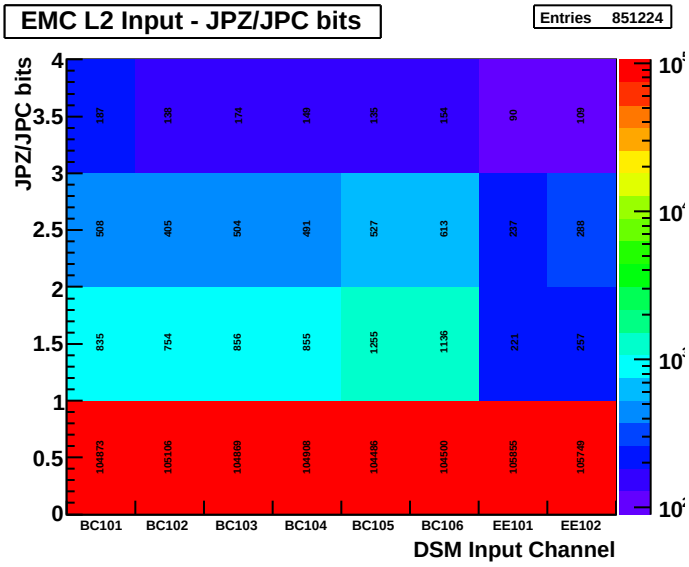

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

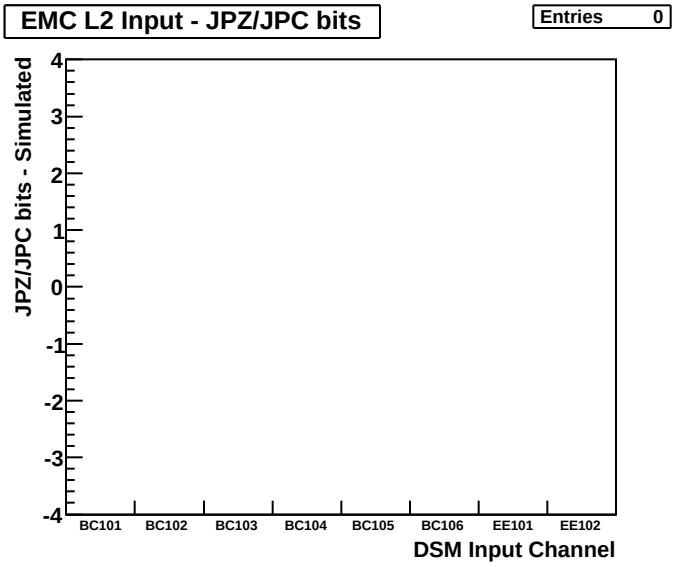

EMC L2 Input - JPX/JPA bits
Entries 851224
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
279 216 161 173 144 143 94 162
743 623 499 527 491 507 236 389
1230 1101 964 825 952 801 230 180
104151 104463 104779 104878 104816 104952 105843 105692

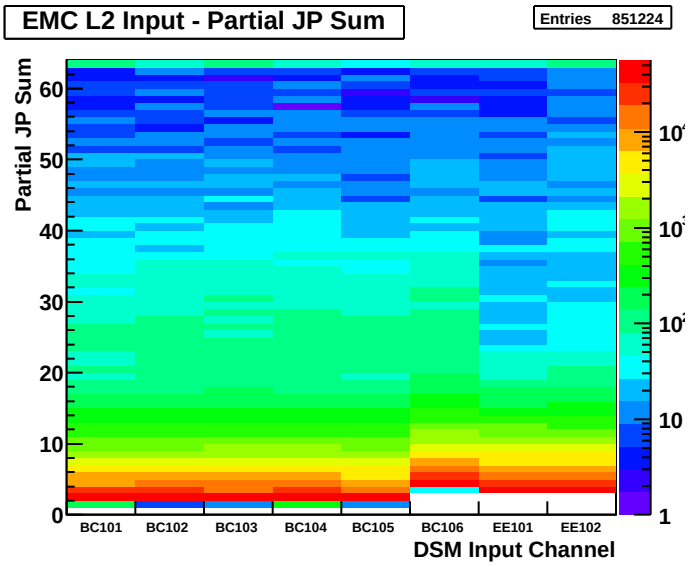

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

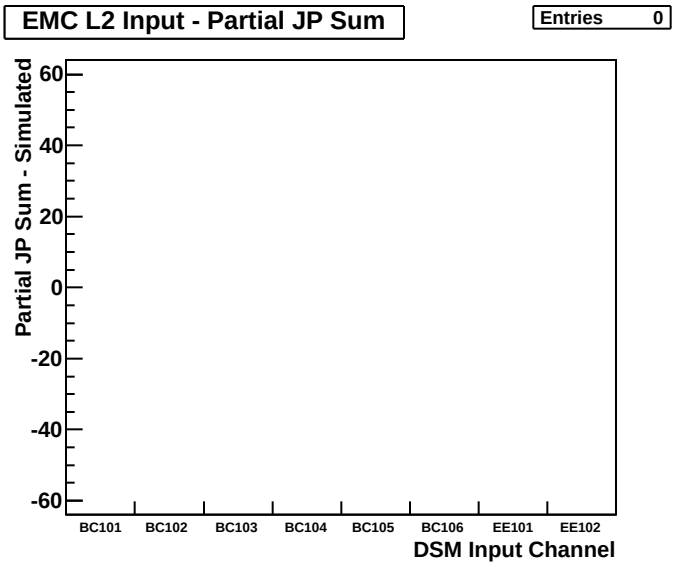

EMC L2 Input - JPY/JPB bits
Entries 851224
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
206 124 143 140 129 170 98 117
608 445 462 500 556 572 240 278
1056 811 878 840 1284 1014 182 245
104530 105023 104920 104914 104434 104647 105883 105763

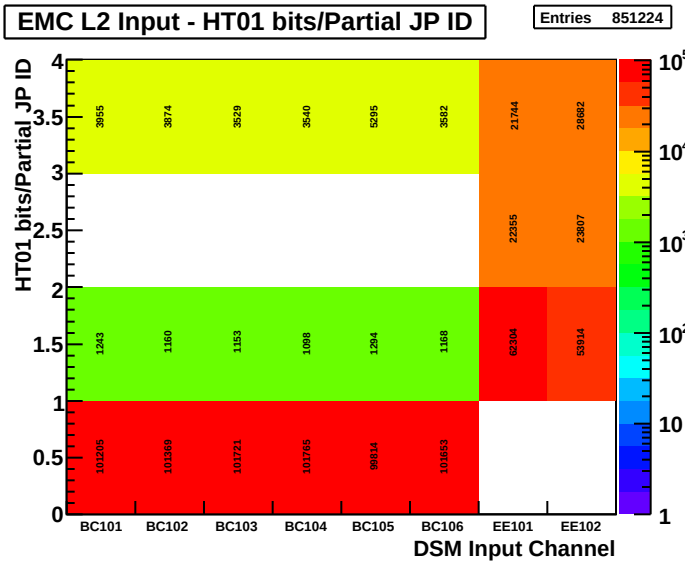

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

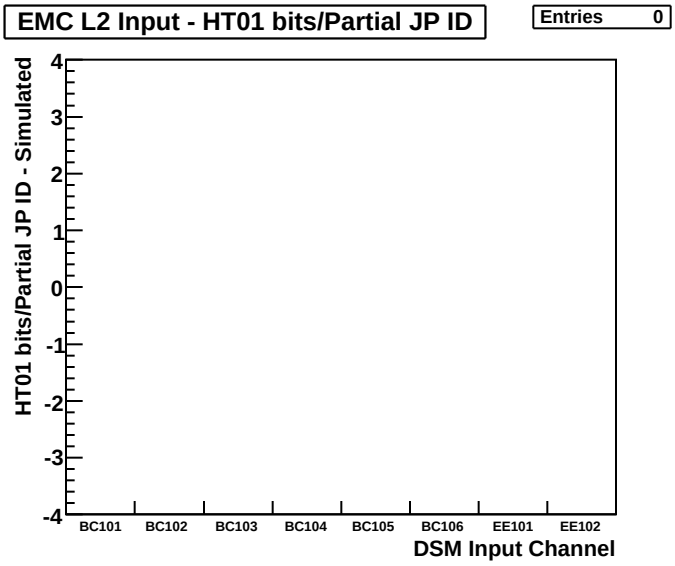

EMC L2 Input - JPZ/JPC bits
Entries 851224
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
187 138 174 149 135 154 90 109
508 405 504 491 527 613 237 238
835 754 856 855 1255 1136 221 257
104873 105106 104869 104908 104486 104500 105955 105749

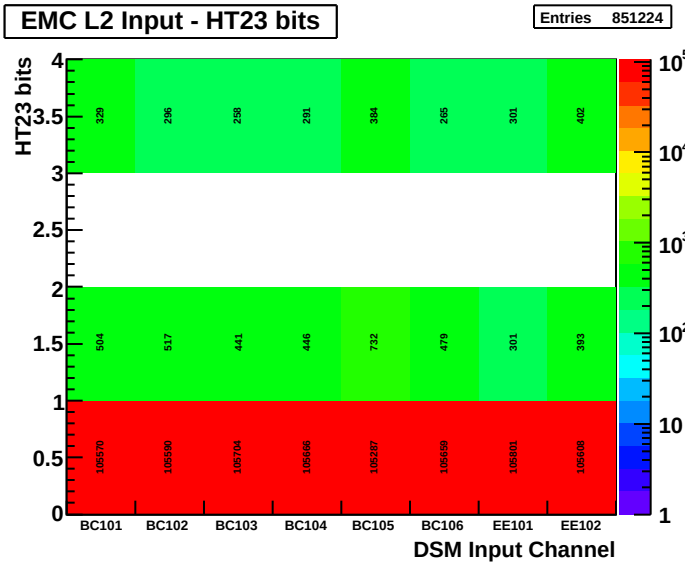

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

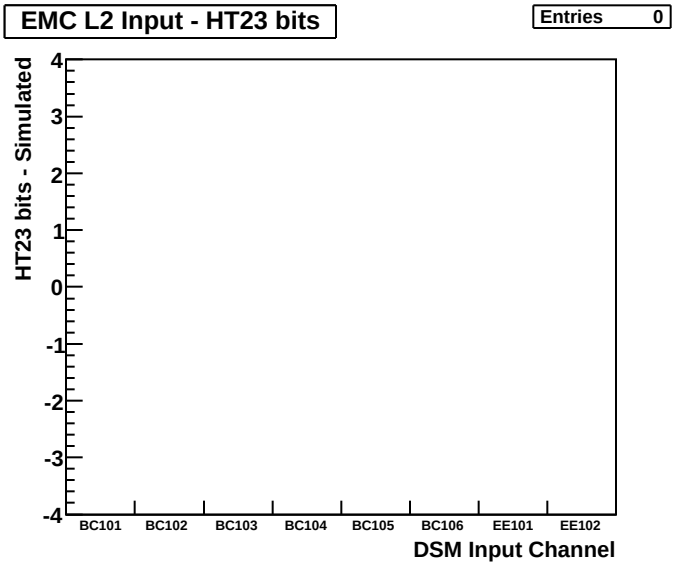

EMC L2 Input - Partial JP Sum
Entries 851224
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

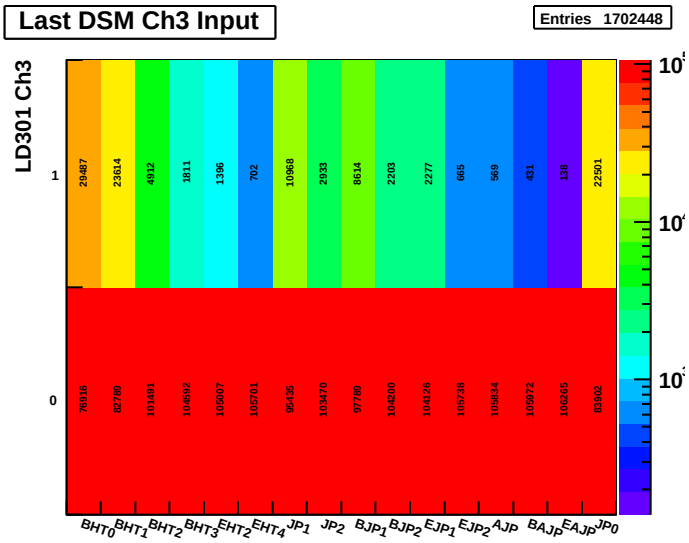

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

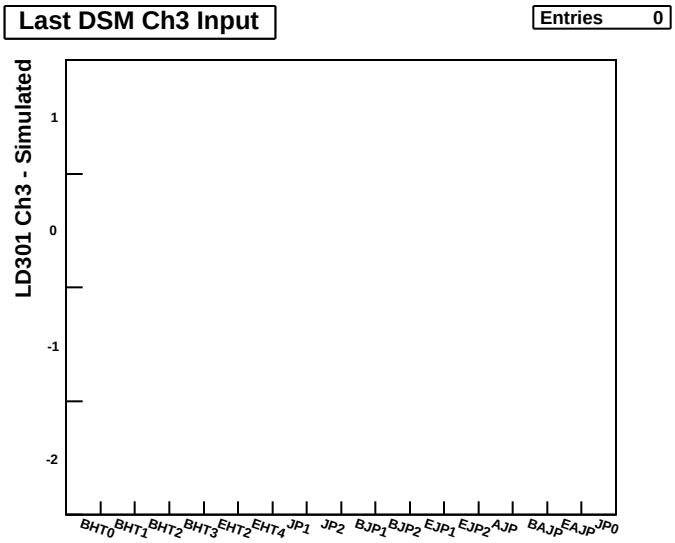

EMC L2 Input - HT01 bits/Partial JP ID
Entries 851224
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

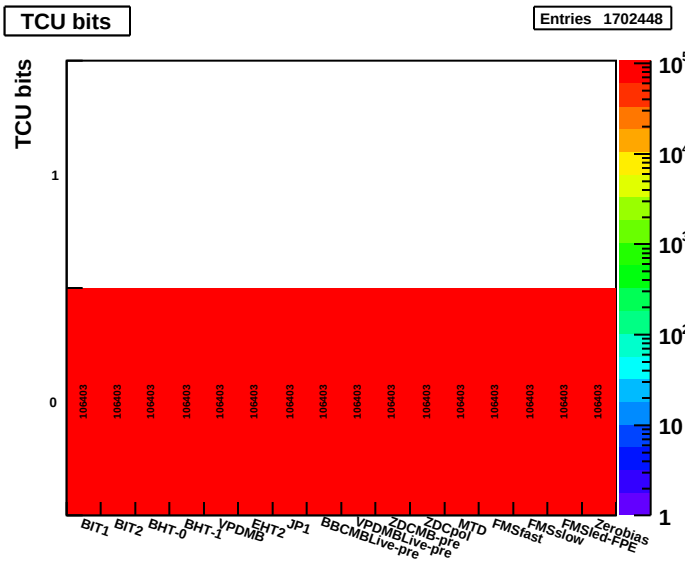

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

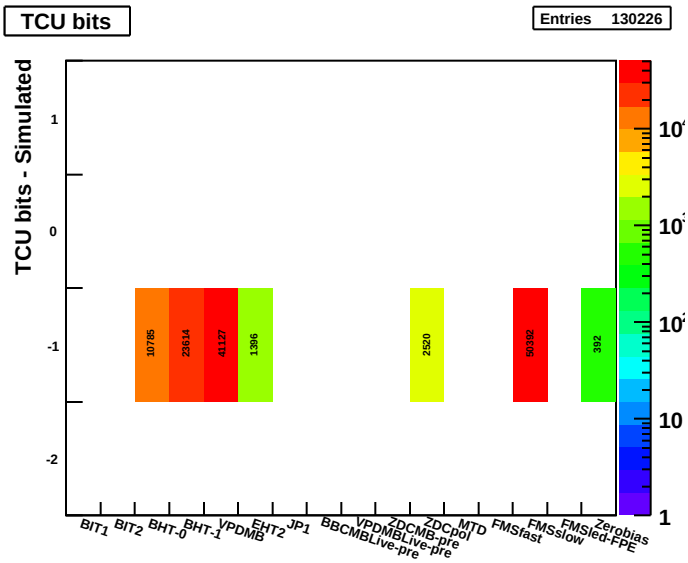

EMC L2 Input - HT23 bits
Entries 851224
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

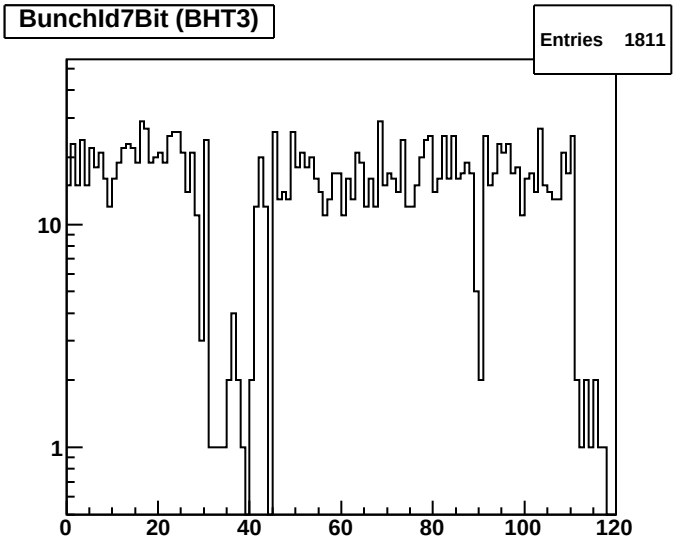

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

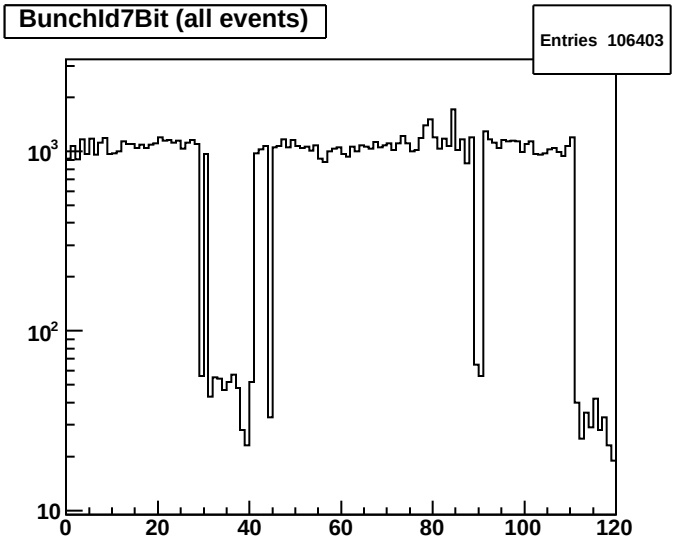

Last DSM Ch3 Input
Entries 1702448
LD301 Ch3
29487 23614 4912 1811 1396 702 10968 2933 8614 2203 2277 665 569 431 138 22501
76916 82789 101491 104592 105907 105701 95435 103470 97789 104200 104126 105738 105834 105972 106255 83902
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

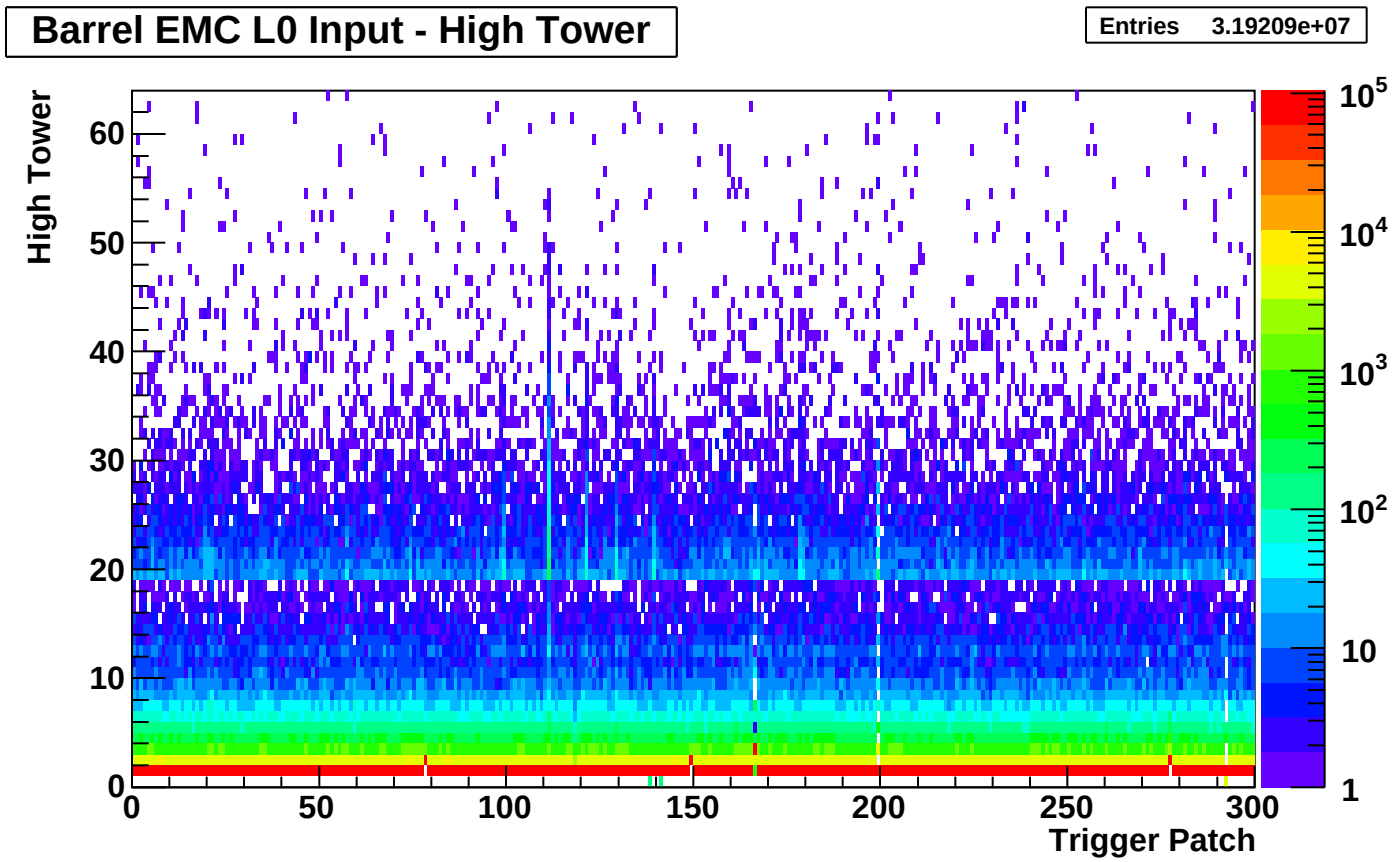

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

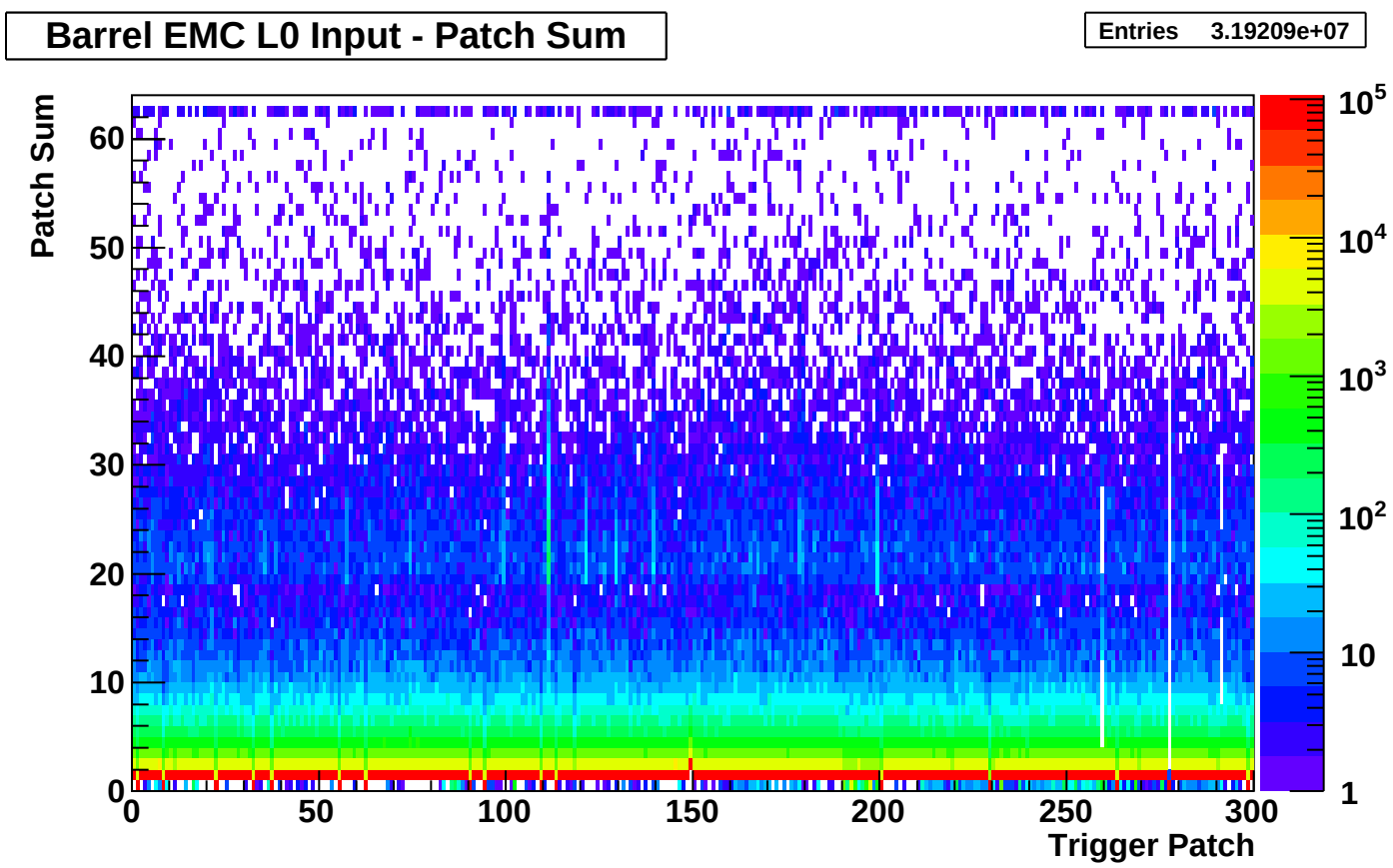

TCU bits
Entries 1702448
TCU bits
106403 106403 106403 106403 106403 106403 106403 106403 106403 106403 106403 106403 106403 106403 106403 106403
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

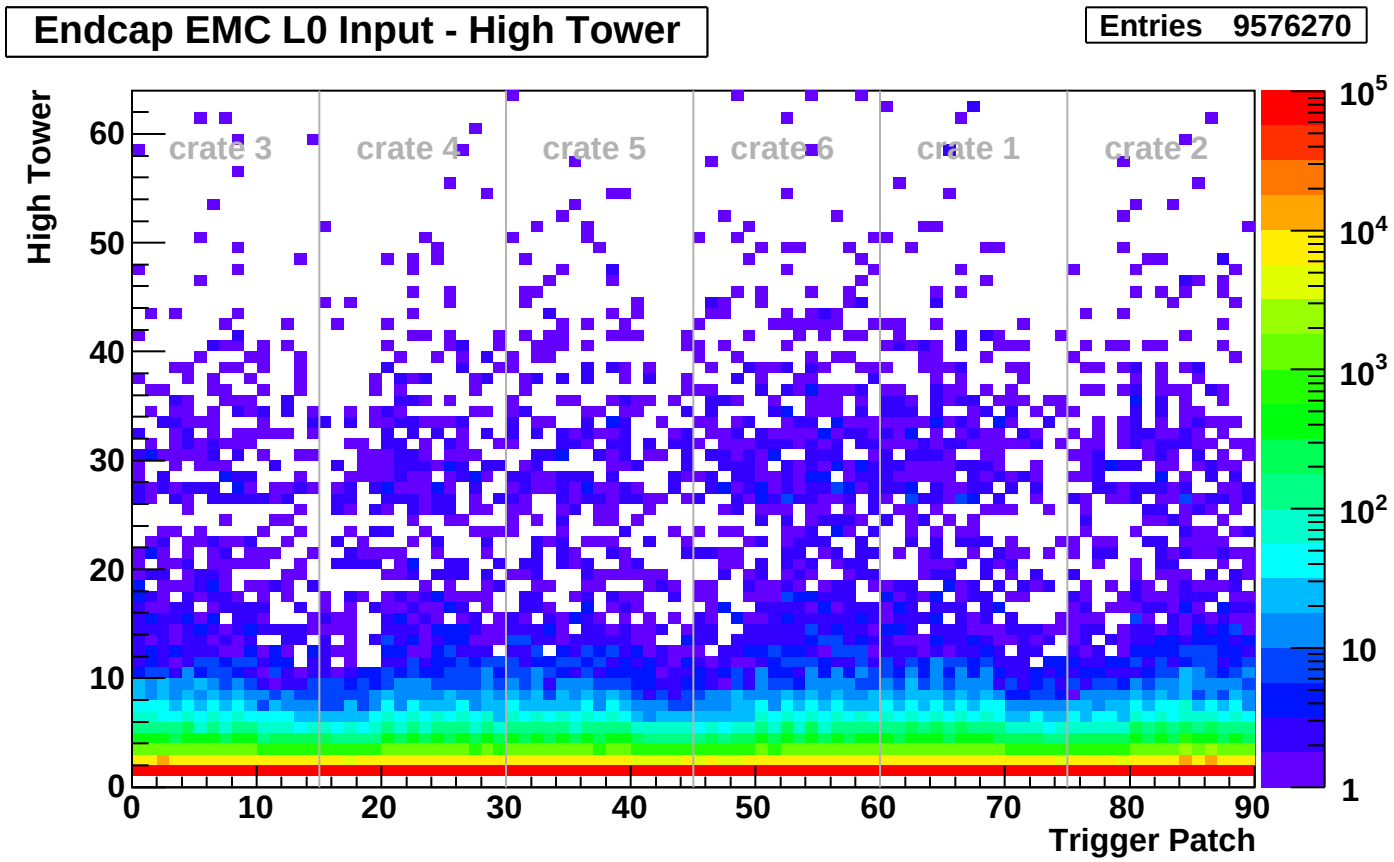

TCU bits
Entries 130226
TCU bits - Simulated
10785 23614 41127 1396 2520 50392 392
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

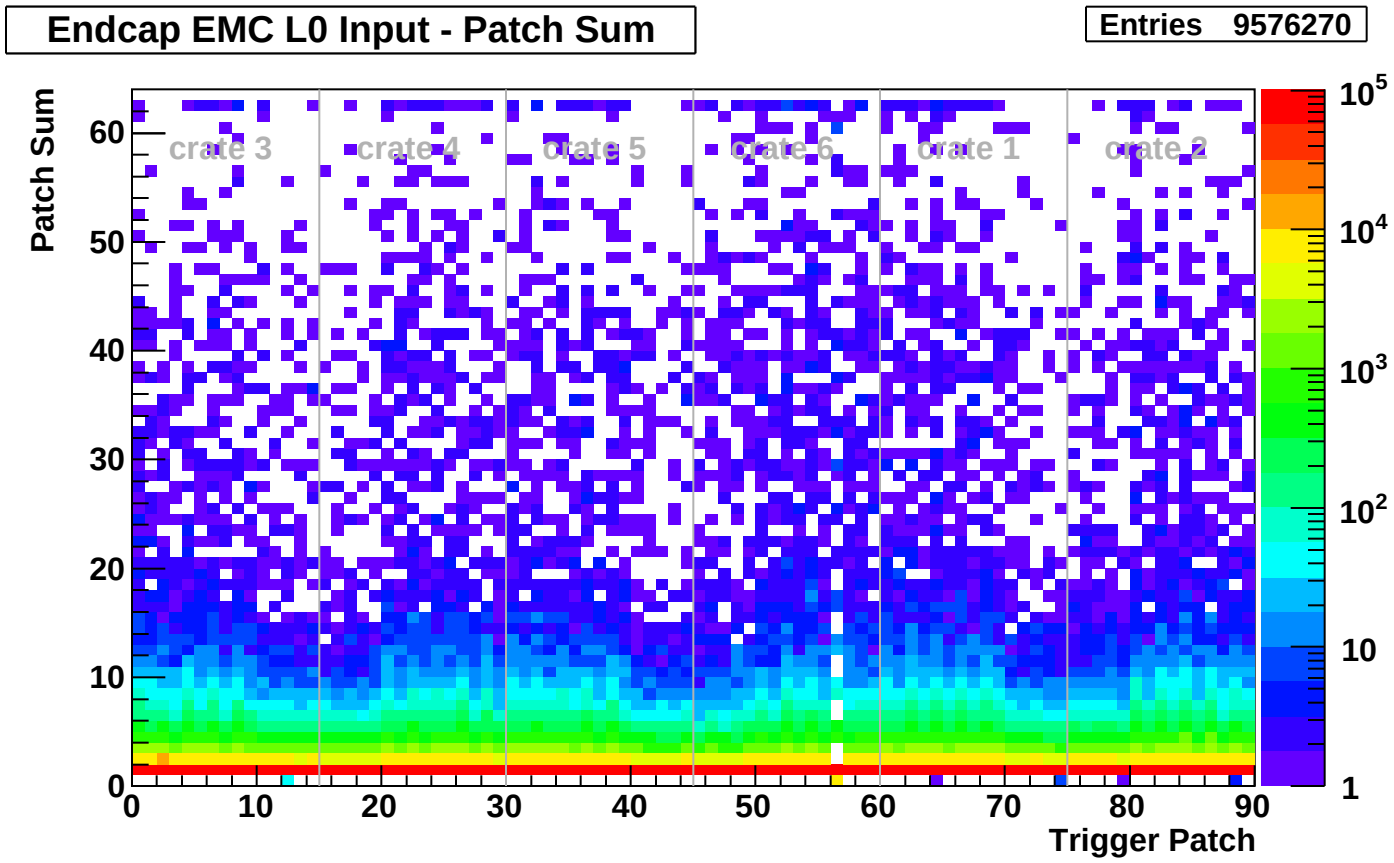

BunchId7Bit (BHT3)
Entries 1811

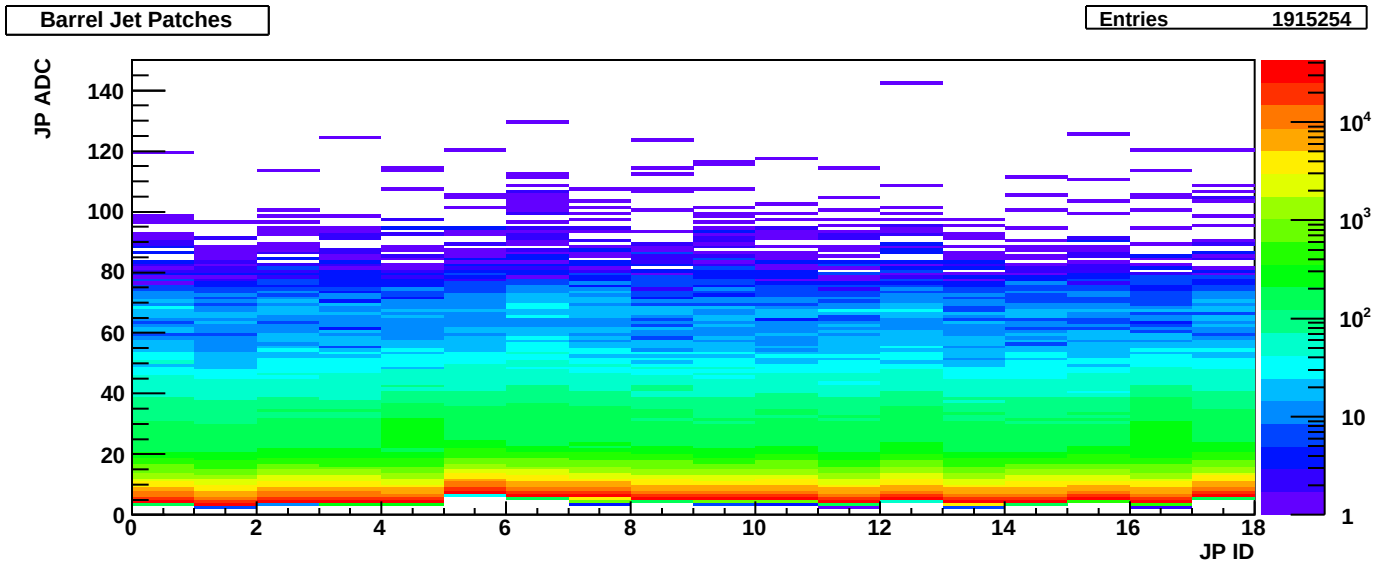

BunchId7Bit (all events)
Entries 106403

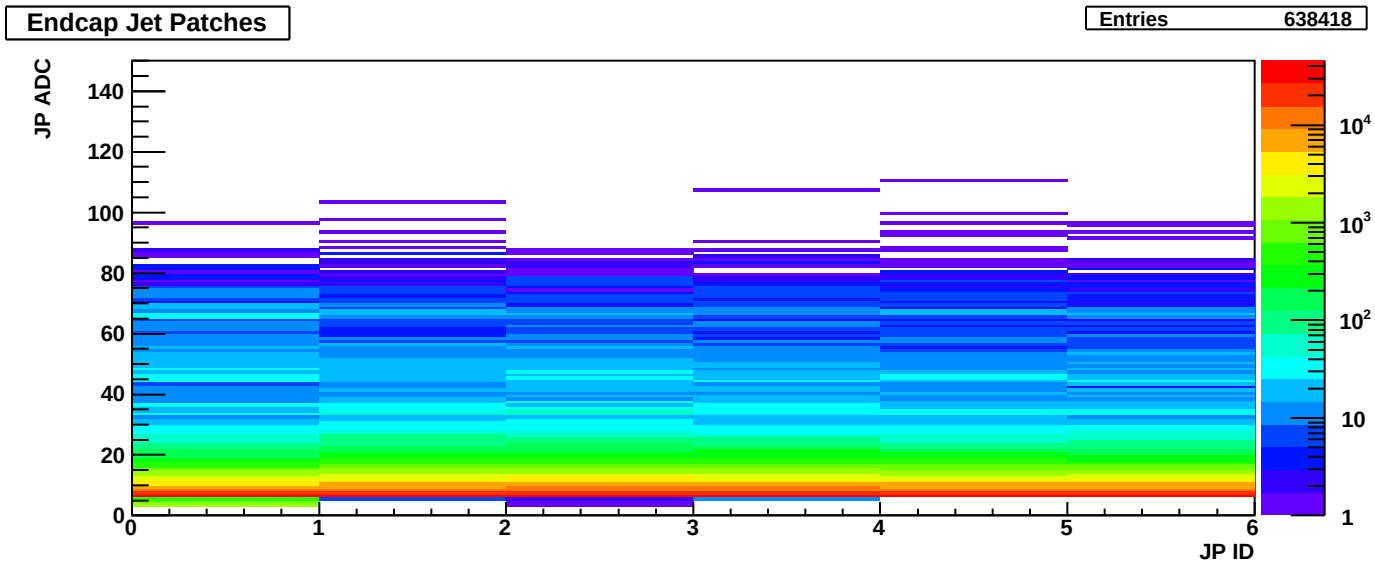

Barrel EMC L0 Input - High Tower
Entries 3.19209e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

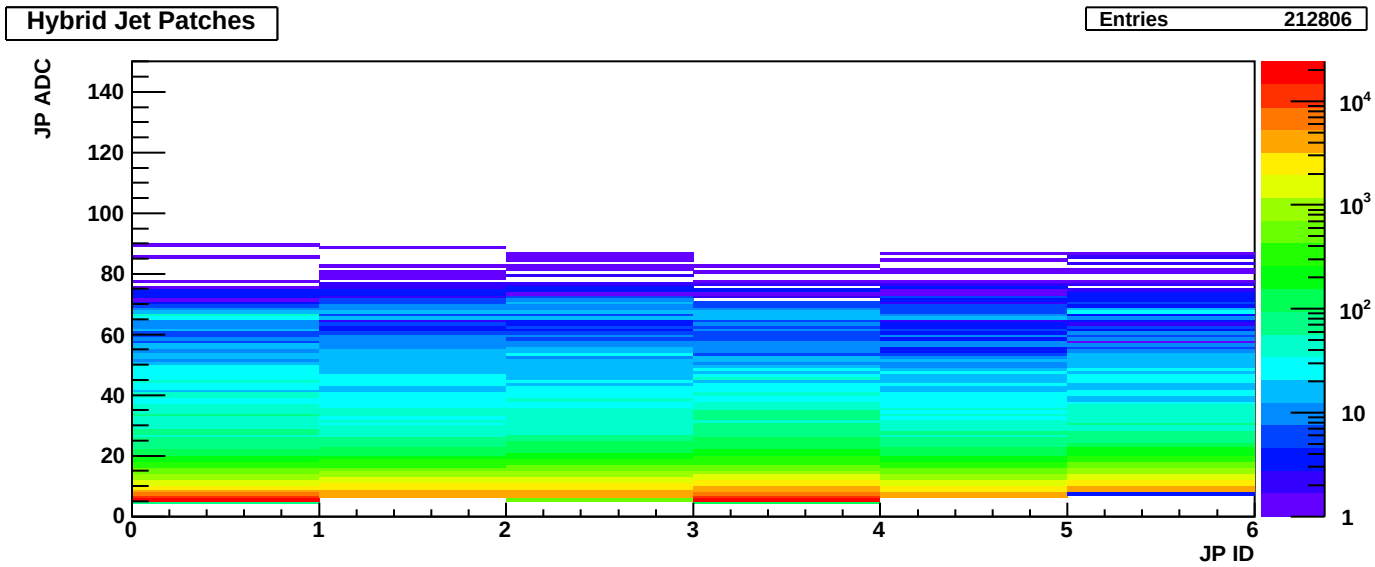

Barrel EMC L0 Input - Patch Sum
Entries 3.19209e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

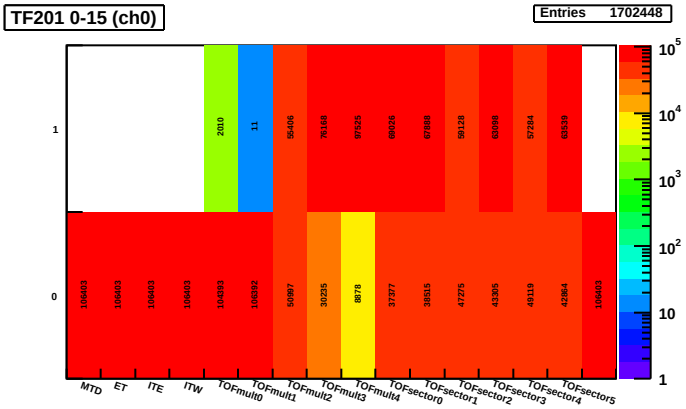

Endcap EMC L0 Input - High Tower
Entries 9576270
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

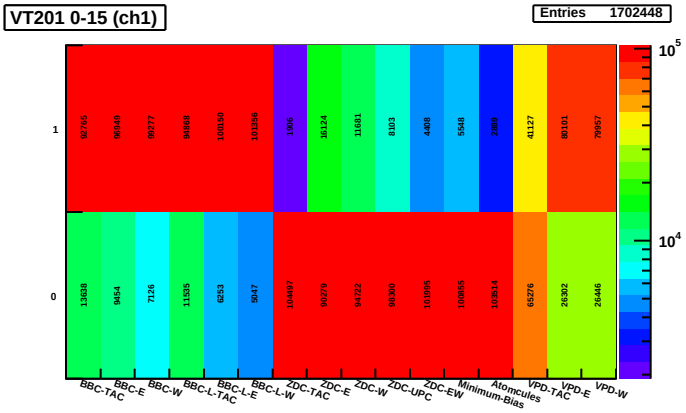

Endcap EMC L0 Input - Patch Sum
Entries 9576270
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

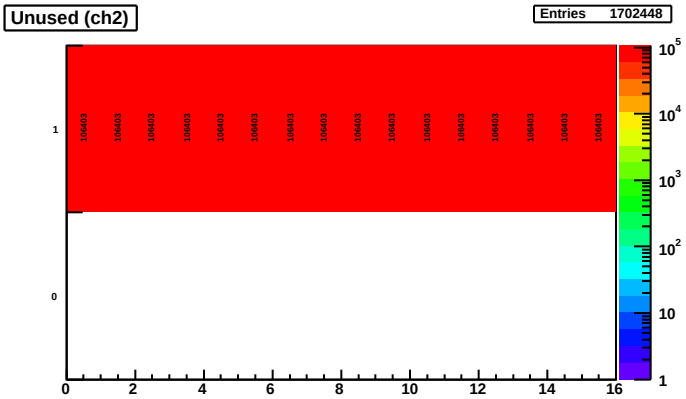

Barrel Jet Patches
Entries 1915254
JP ADC
JP ID

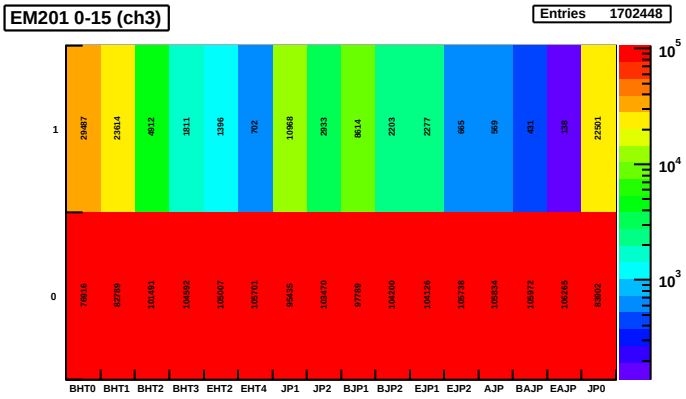

Endcap Jet Patches
Entries 638418
JP ADC
JP ID

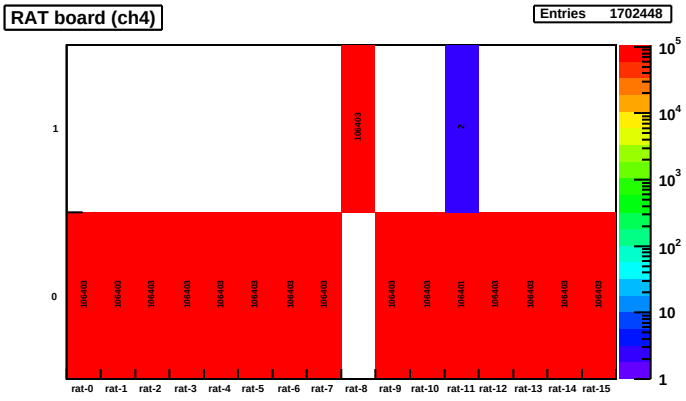

Hybrid Jet Patches
Entries 212806
JP ADC
JP ID

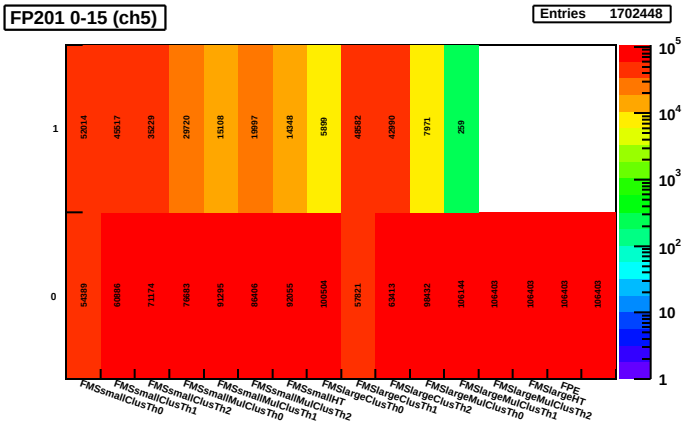

TF201 0-15 (ch0)
Entries 1702448
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

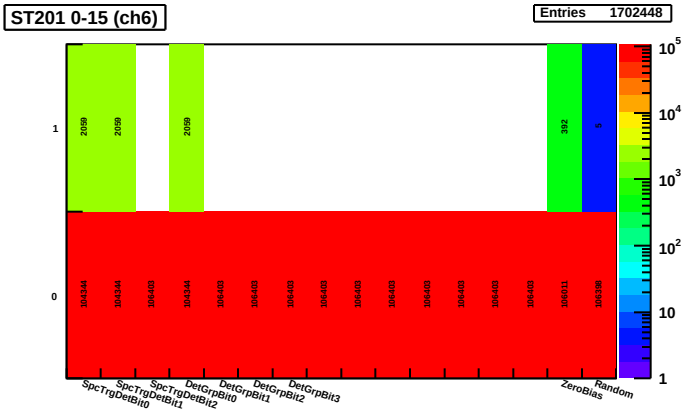

VT201 0-15 (ch1)
Entries 1702448
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

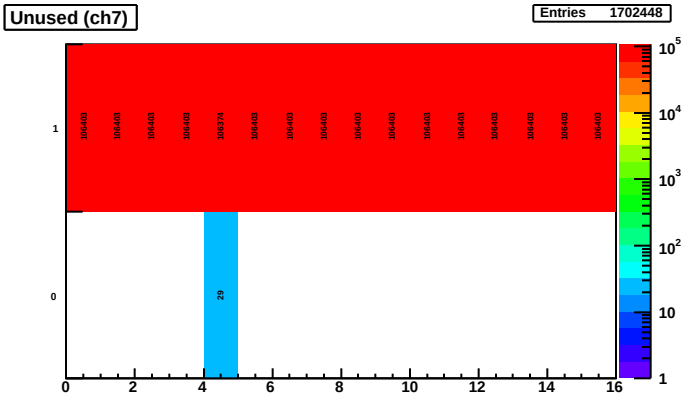

Unused (ch2)
Entries 1702448

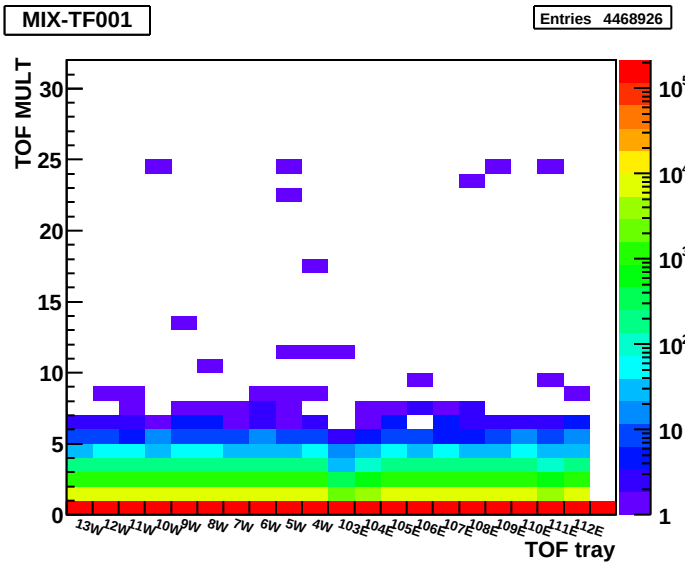

EM201 0-15 (ch3)
Entries 1702448
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

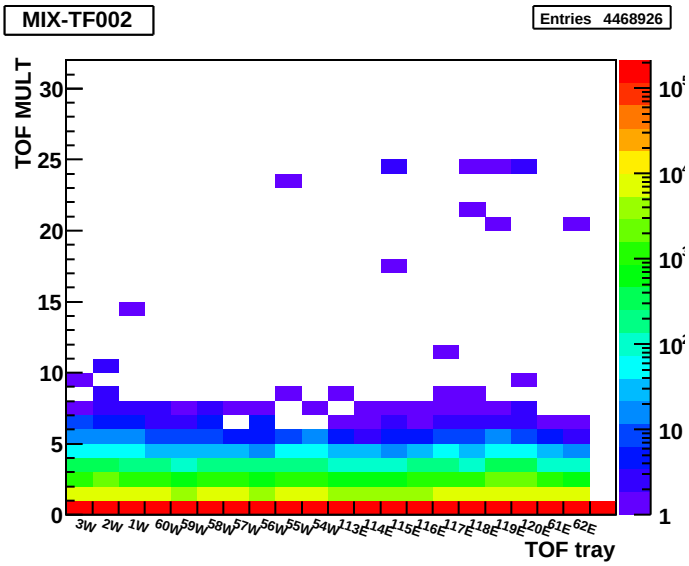

RAT board (ch4)
Entries 1702448
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

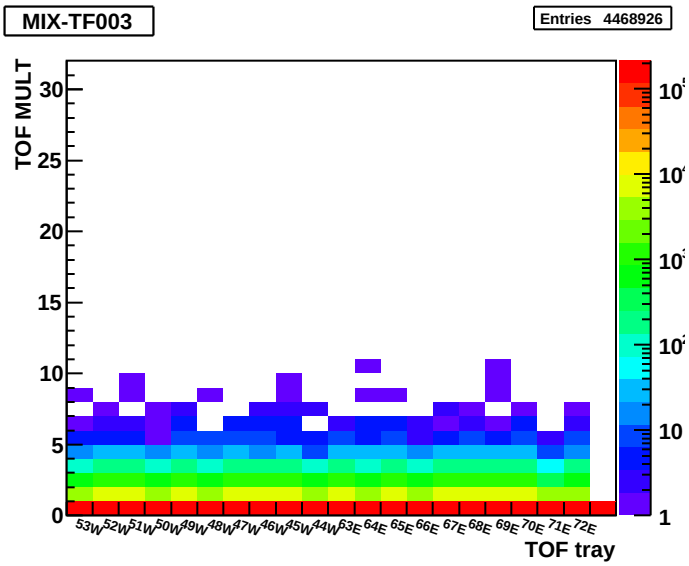

FP201 0-15 (ch5)
Entries 1702448
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

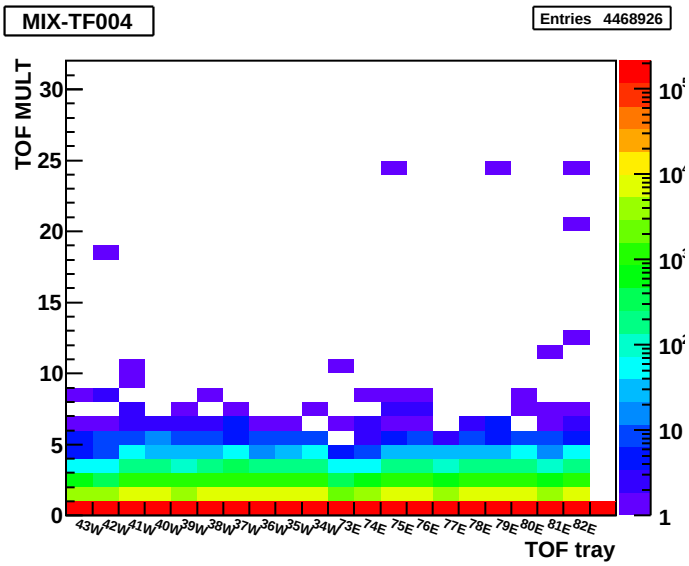

ST201 0-15 (ch6)
Entries 1702448
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

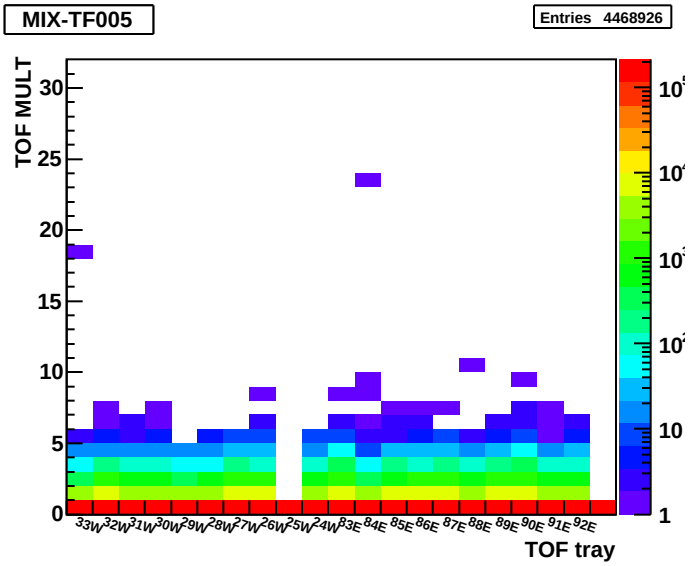

Unused (ch7)
Entries 1702448

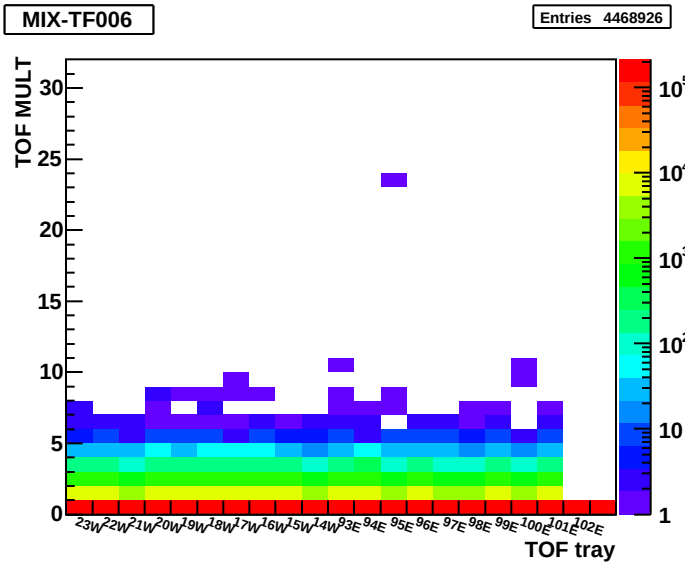

MIX-TF001
Entries 4468926
TOF MULT
TOF tray

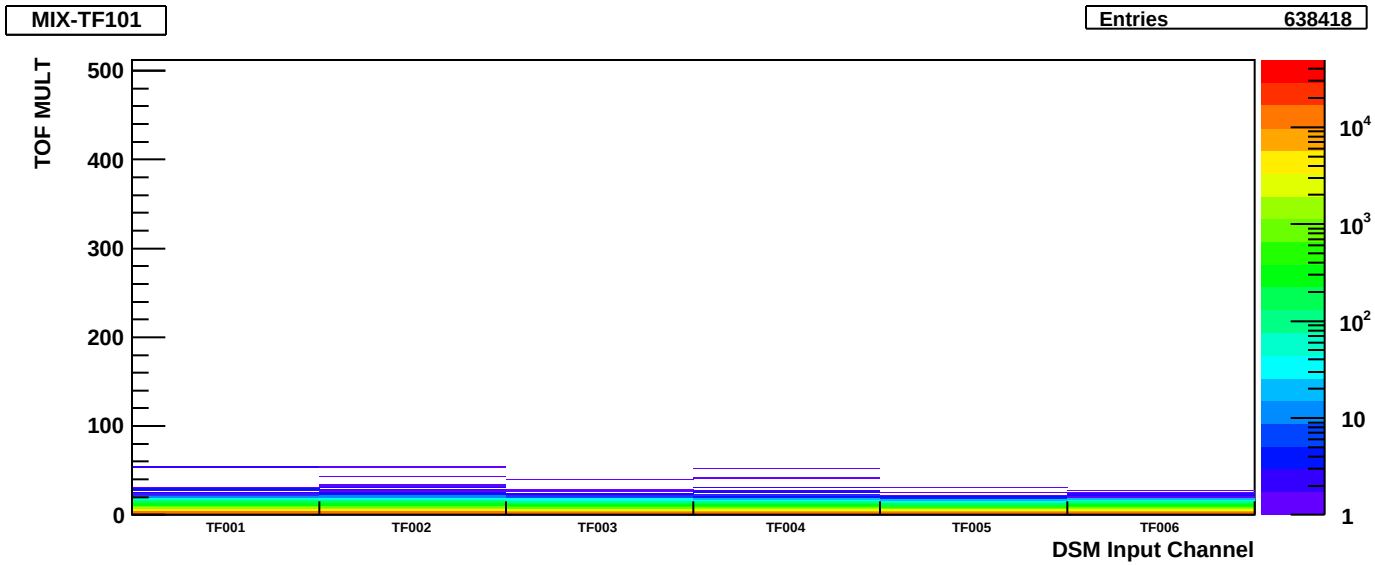

MIX-TF002
Entries 4468926
TOF MULT
TOF tray

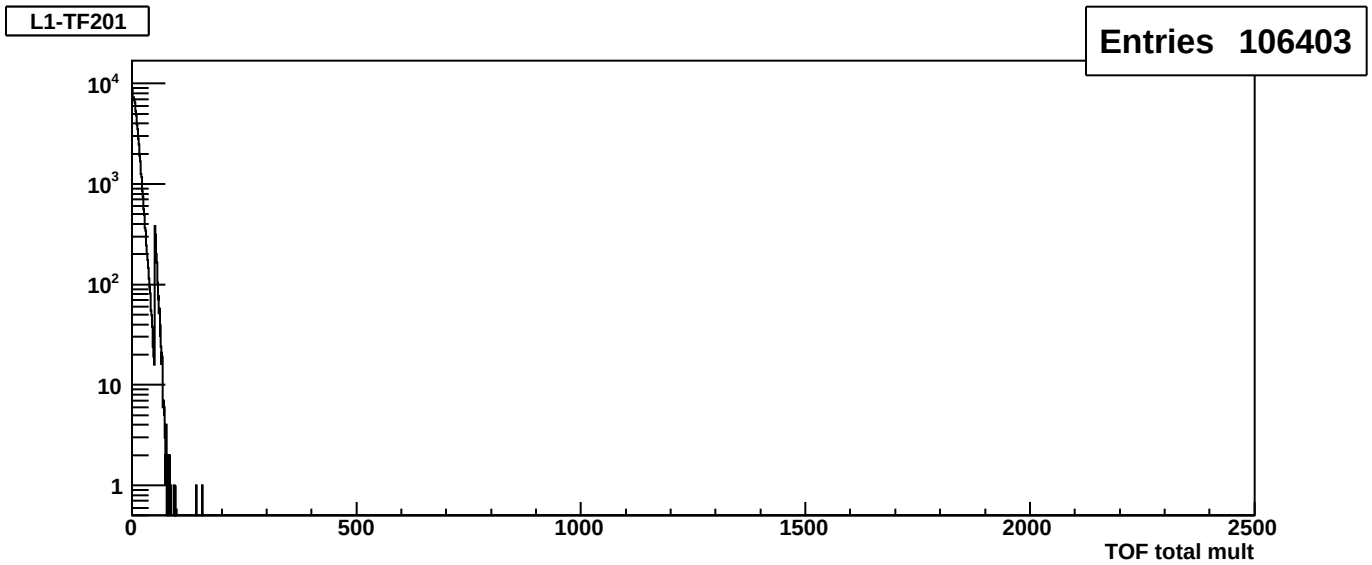

MIX-TF003
Entries 4468926
TOF MULT
TOF tray

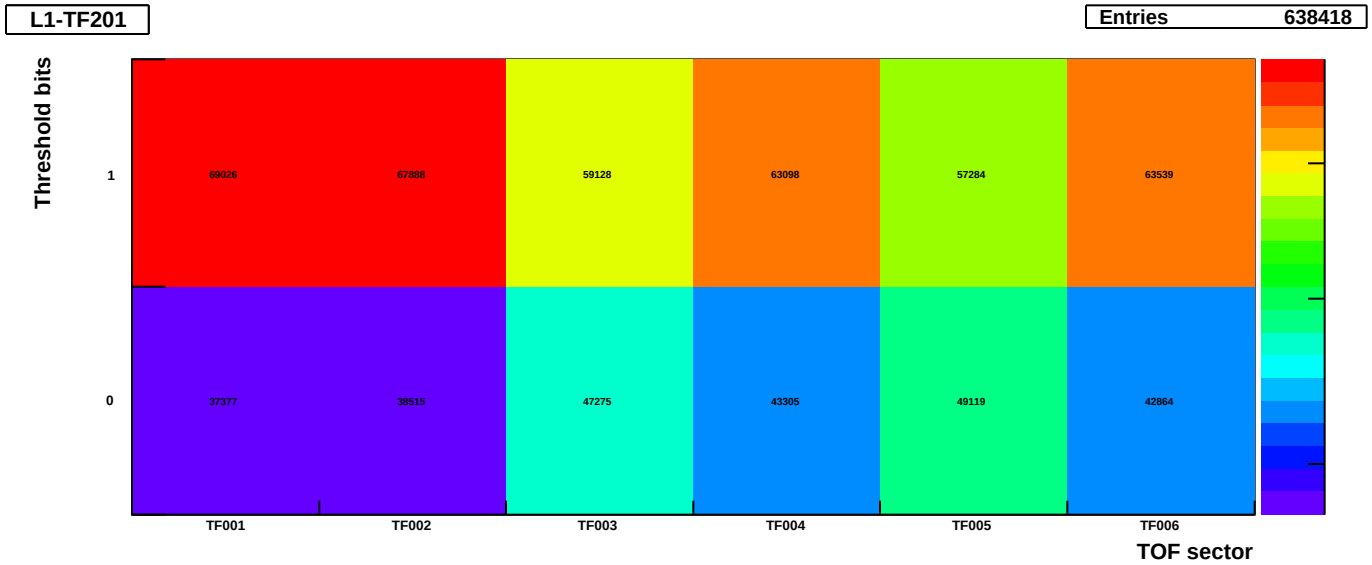

MIX-TF004
Entries 4468926
TOF MULT
TOF tray

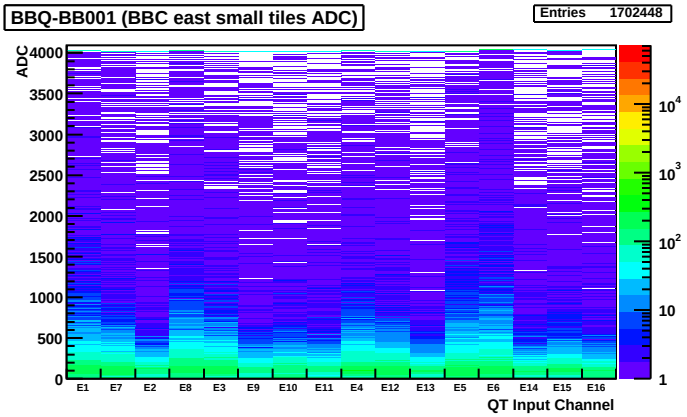

MIX-TF005
Entries 4468926
TOF MULT
TOF tray

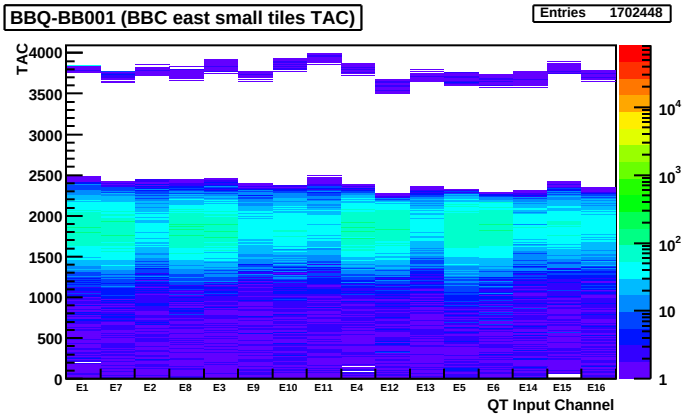

MIX-TF006
Entries 4468926
TOF MULT
TOF tray

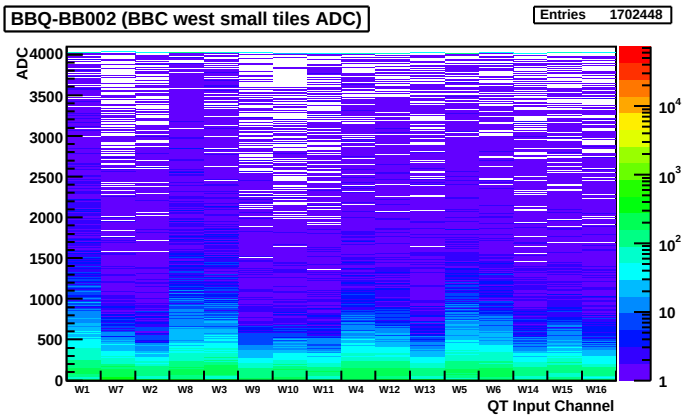

MIX-TF101
Entries 638418
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

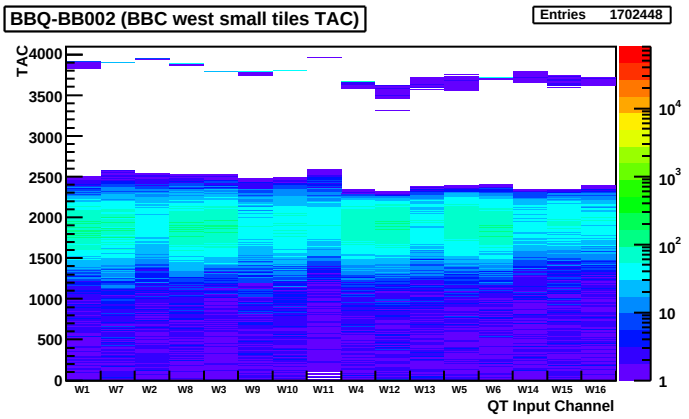

L1-TF201
Entries 106403
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

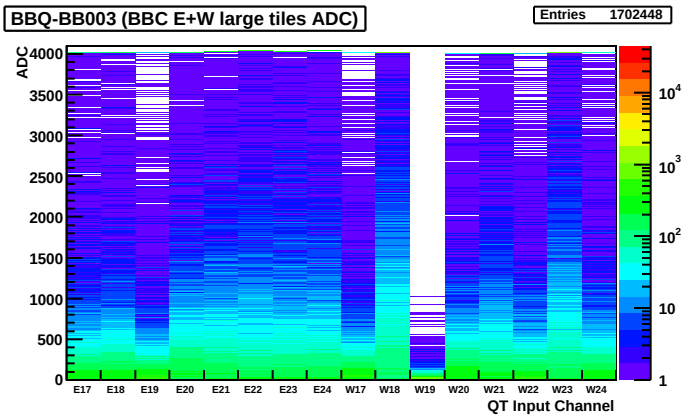

L1-TF201
Entries 638418
Threshold bits
1
0
69026
67888
59128
63098
57284
63539
37377
38515
47275
43305
49119
42864
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

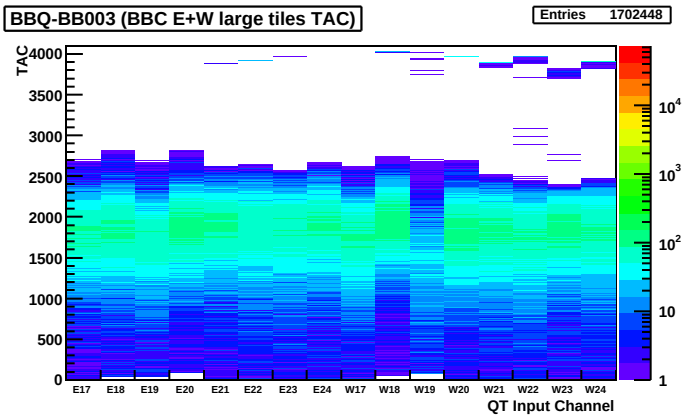

BBQ-BB001 (BBC east small tiles ADC)
Entries 1702448
ADC
QT Input Channel

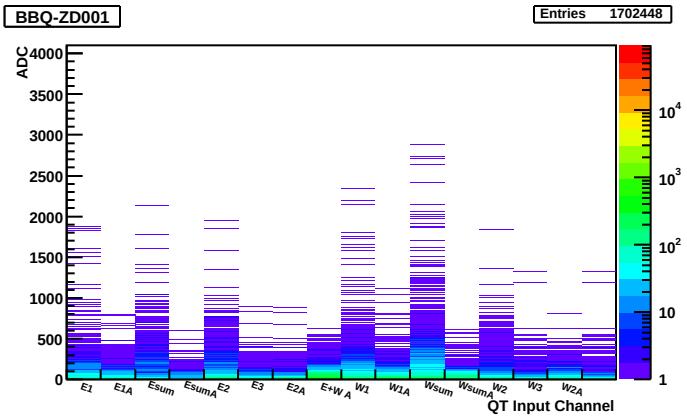

BBQ-BB001 (BBC east small tiles TAC)
Entries 1702448
TAC
QT Input Channel

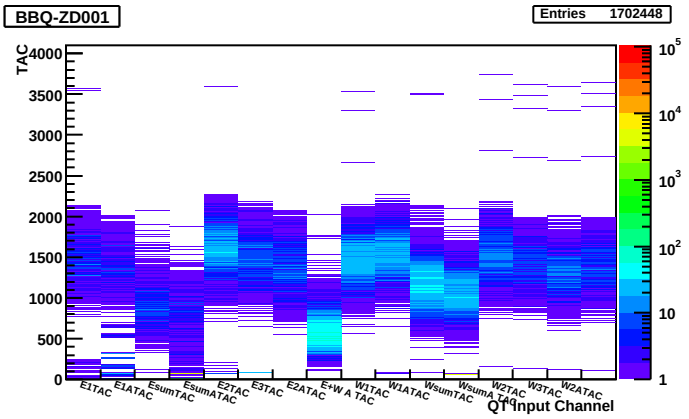

BBQ-BB002 (BBC west small tiles ADC)
Entries 1702448
ADC
QT Input Channel

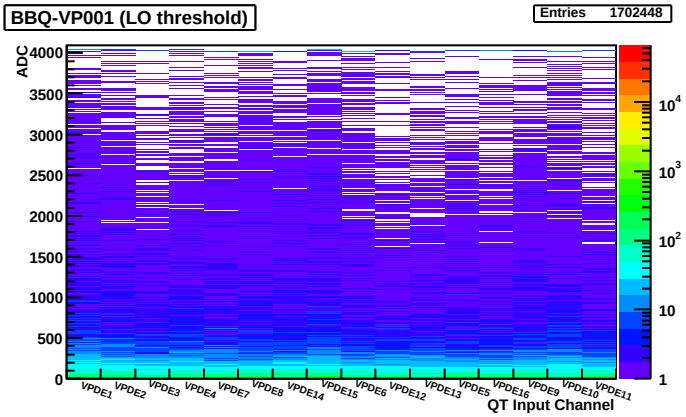

BBQ-BB002 (BBC west small tiles TAC)
Entries 1702448
TAC
QT Input Channel

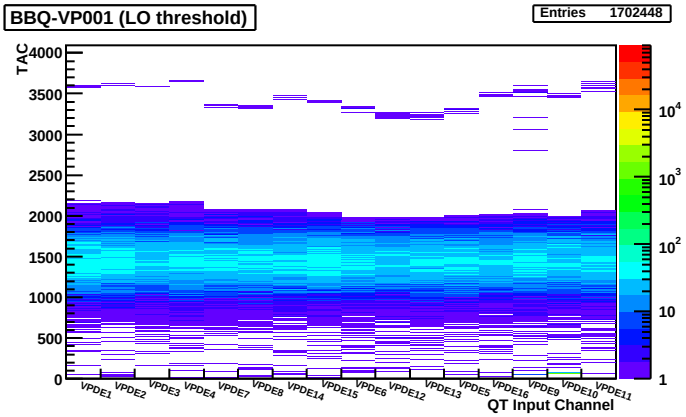

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1702448
ADC
QT Input Channel

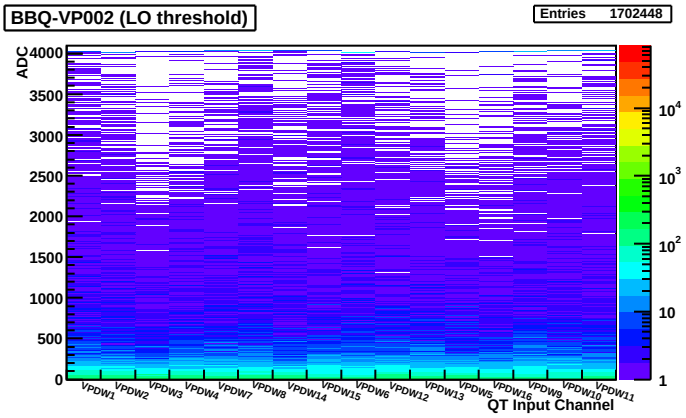

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1702448
TAC
QT Input Channel

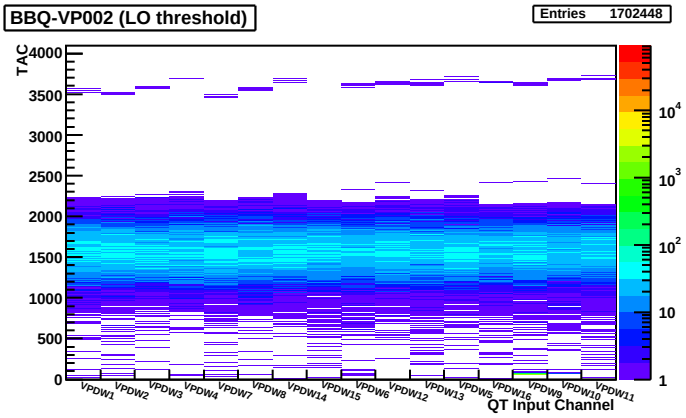

BBQ-ZD001
Entries 1702448
ADC
QT Input Channel

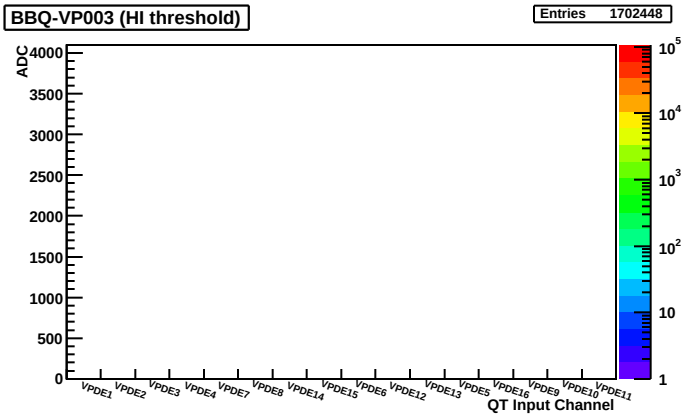

BBQ-ZD001
Entries 1702448
TAC
QT Input Channel

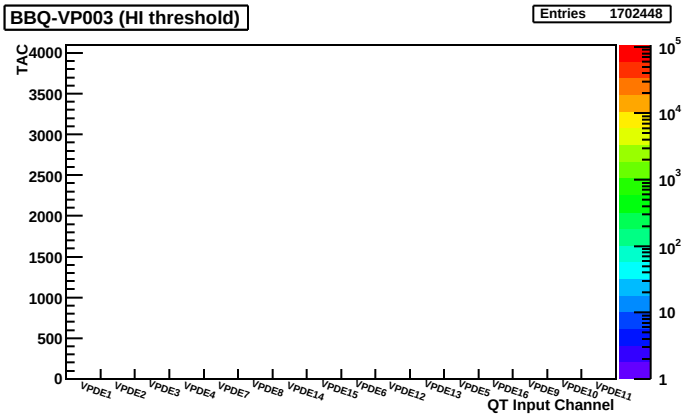

BBQ-VP001 (LO threshold)
Entries 1702448
ADC
QT Input Channel

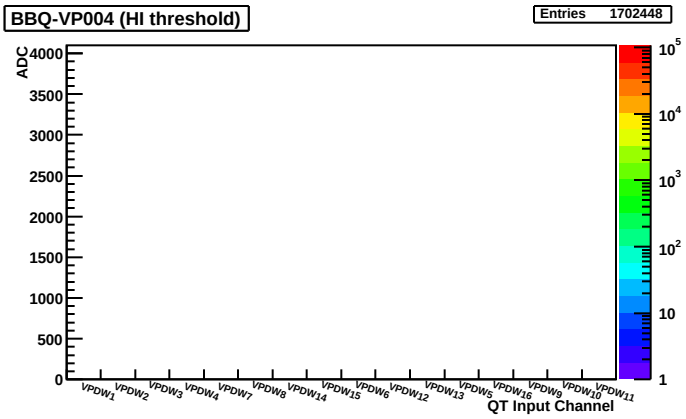

BBQ-VP001 (LO threshold)
Entries 1702448
TAC
QT Input Channel

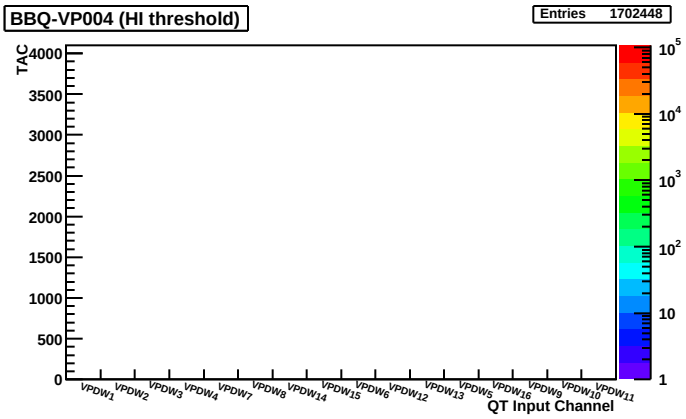

BBQ-VP002 (LO threshold)
Entries 1702448
ADC
QT Input Channel

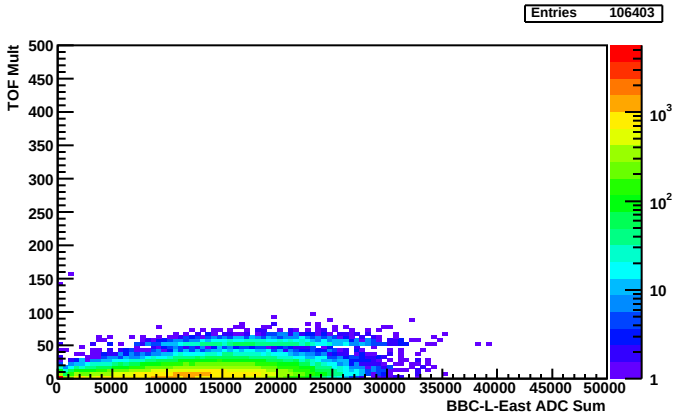

BBQ-VP002 (LO threshold)
Entries 1702448
TAC
QT Input Channel

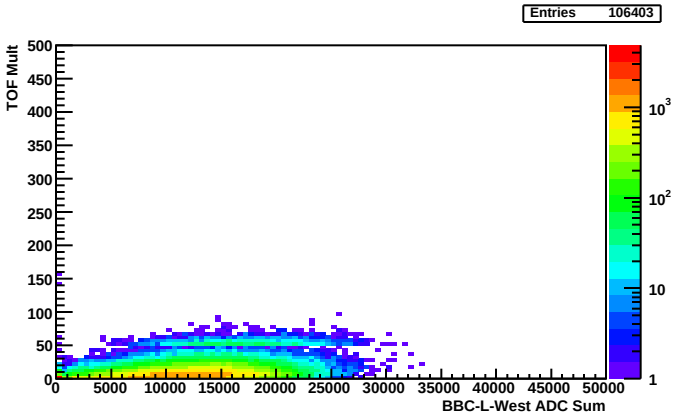

BBQ-VP003 (HI threshold)
Entries 1702448
ADC
QT Input Channel

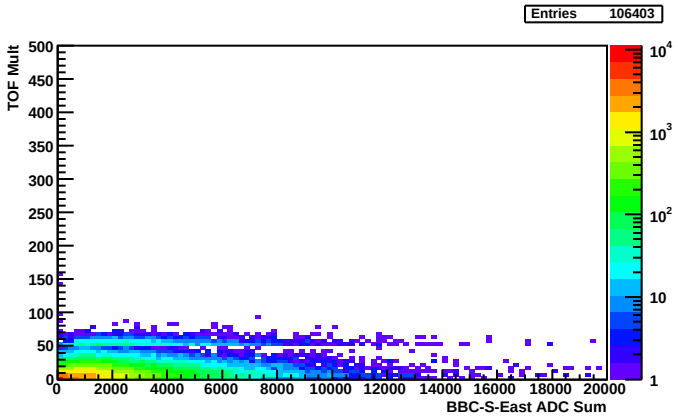

BBQ-VP003 (HI threshold)
Entries 1702448
TAC
QT Input Channel

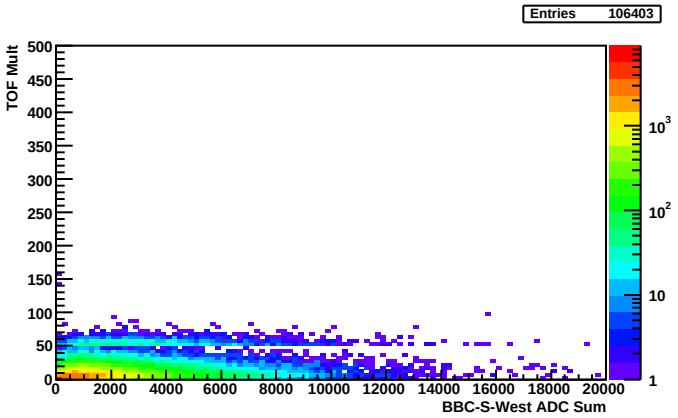

BBQ-VP004 (HI threshold)
Entries 1702448
ADC
QT Input Channel

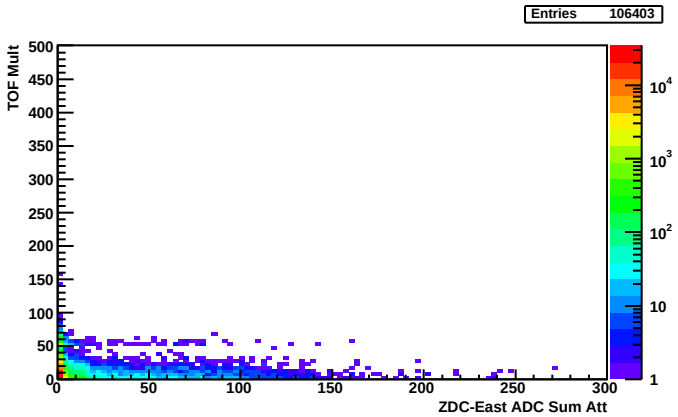

BBQ-VP004 (HI threshold)
Entries 1702448
TAC
QT Input Channel

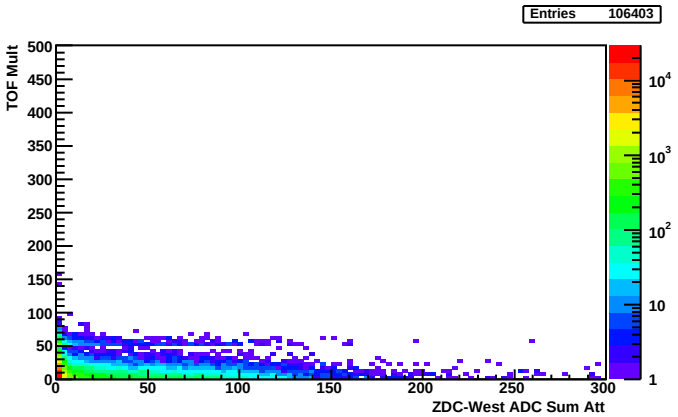

Entries 106403
TOF Mult
BBC-L-East ADC Sum

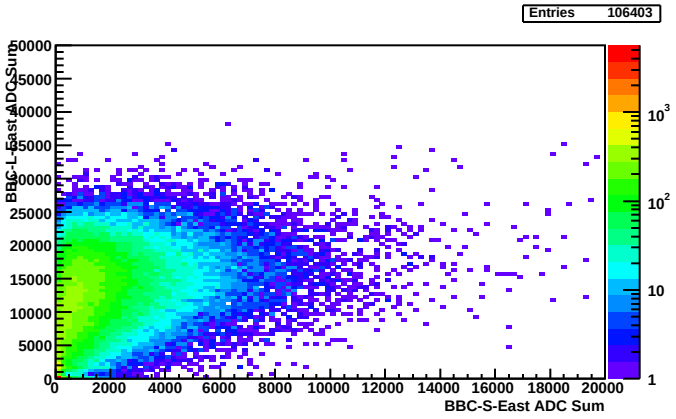

Entries 106403
TOF Mult
BBC-L-West ADC Sum

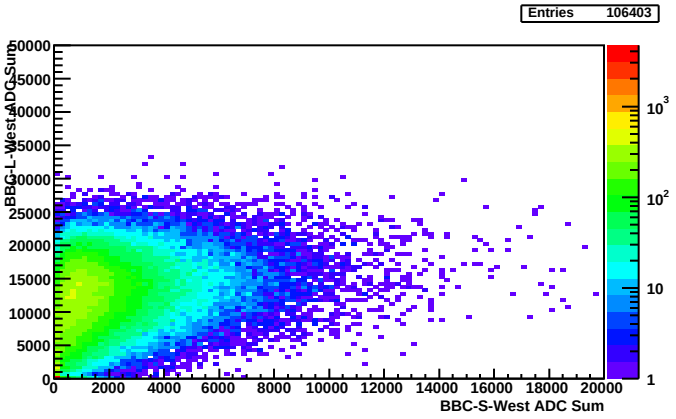

Entries 106403
TOF Mult
BBC-S-East ADC Sum

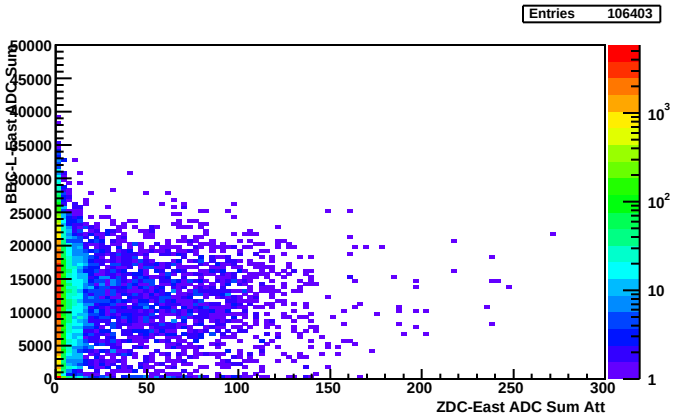

Entries 106403
TOF Mult
BBC-S-West ADC Sum

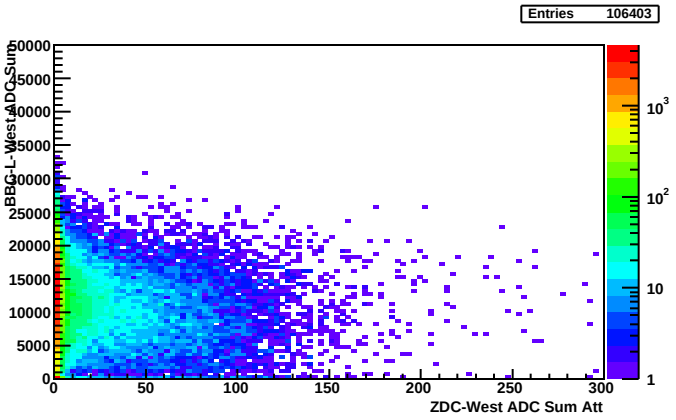

Entries 106403
TOF Mult
ZDC-East ADC Sum Att

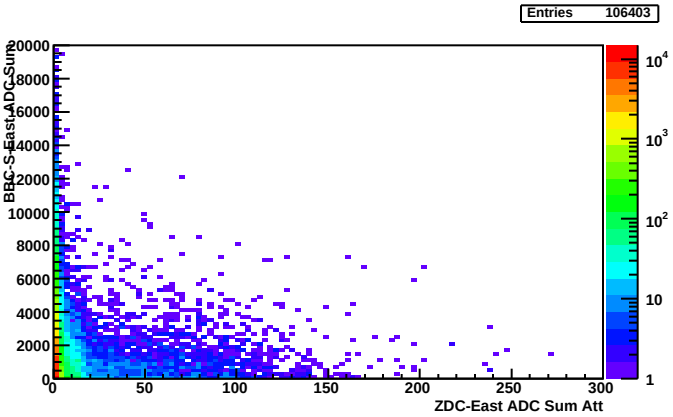

Entries 106403
TOF Mult
ZDC-West ADC Sum Att

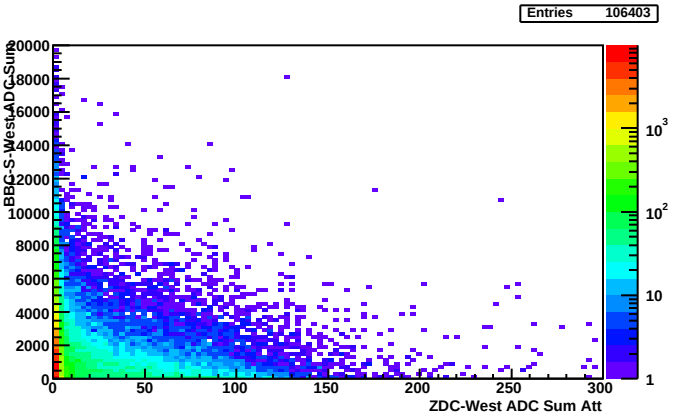

Entries 106403
BBC-L-East ADC Sum
BBC-S-East ADC Sum

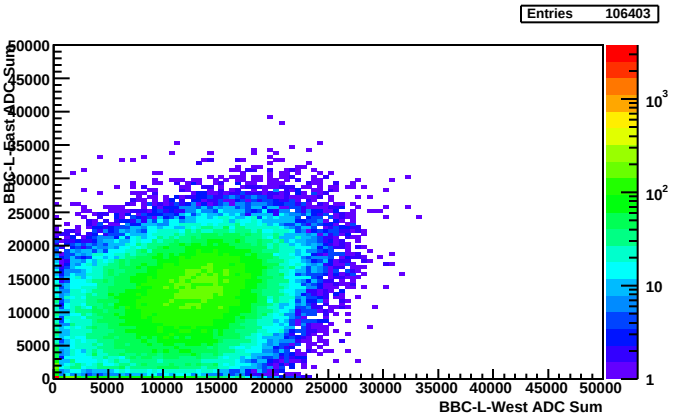

Entries 106403
BBC-L-West ADC Sum
BBC-S-West ADC Sum

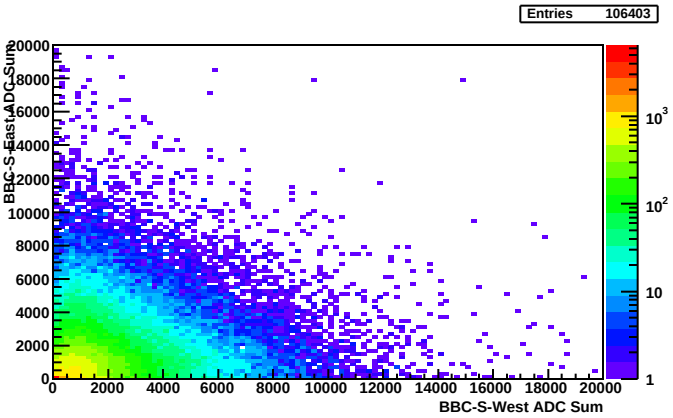

Entries 106403
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

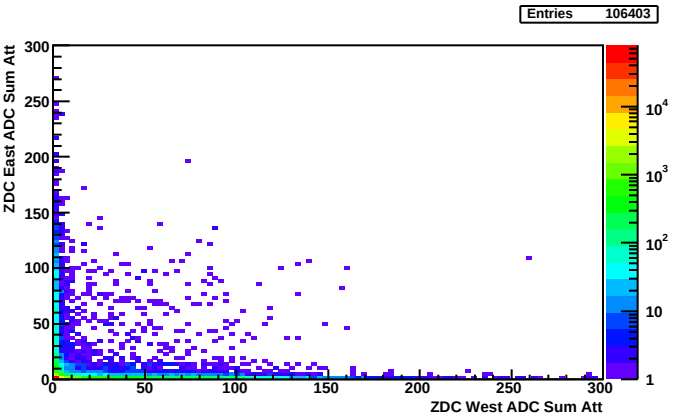

Entries 106403
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

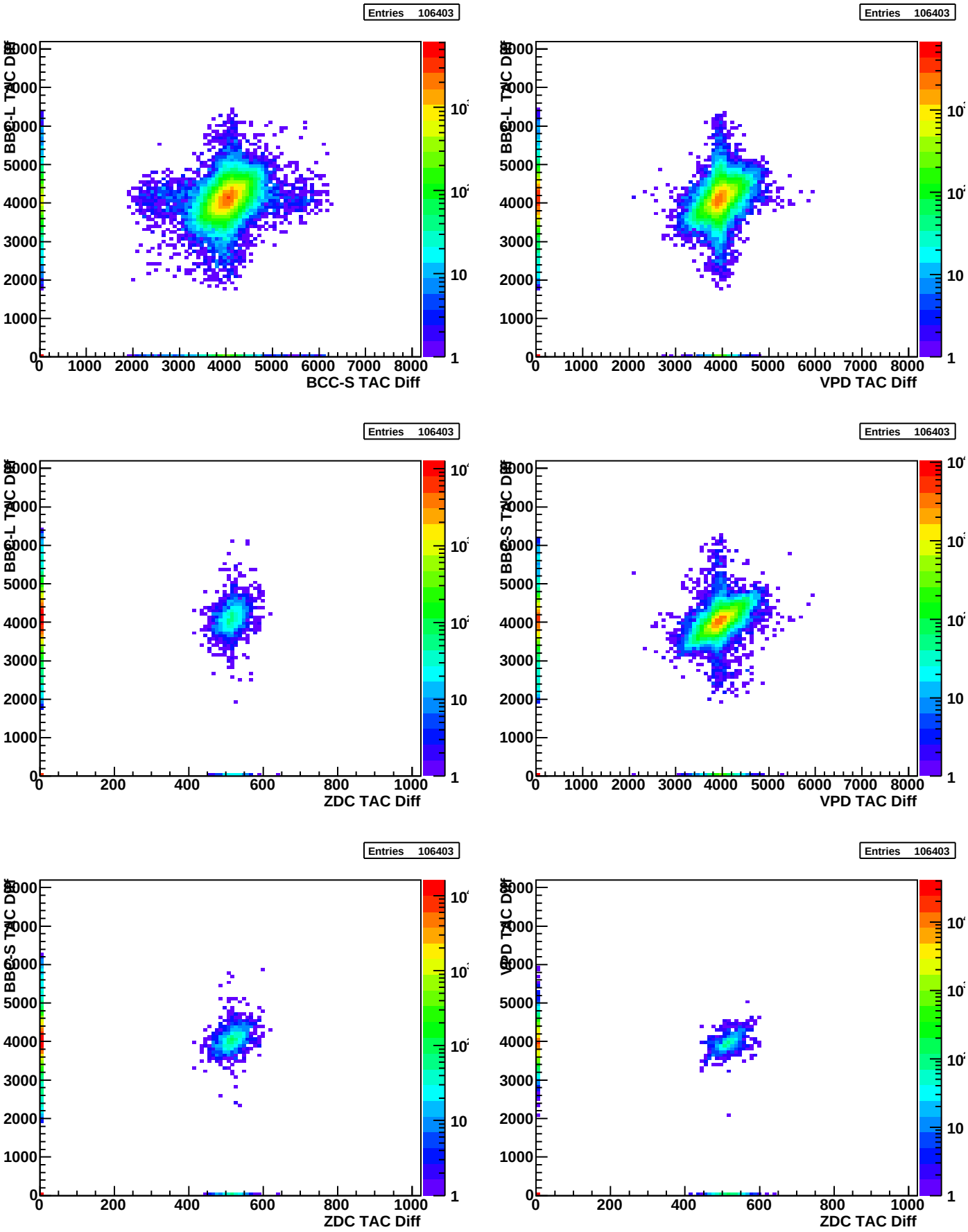

Entries 106403
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

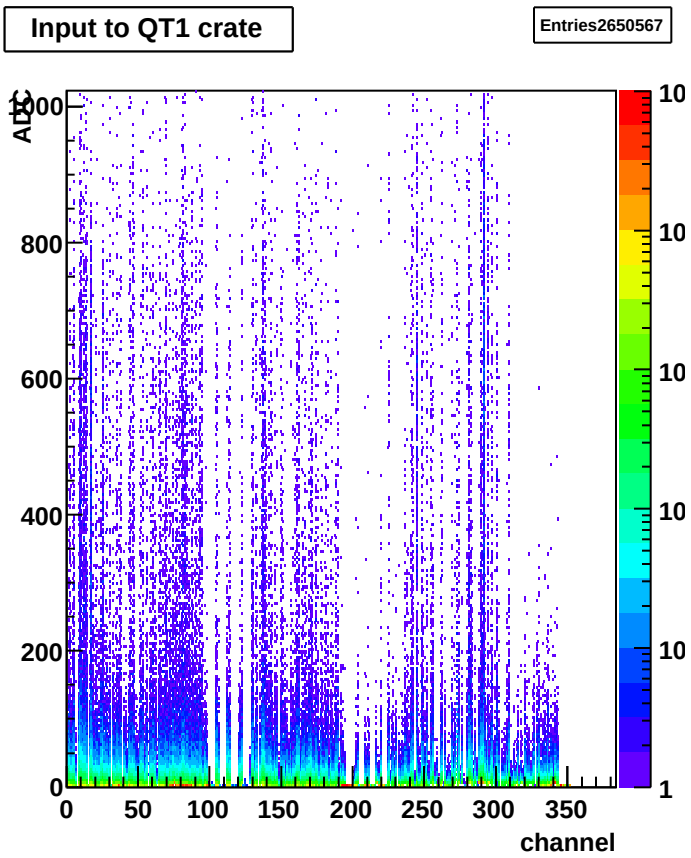

Entries 106403
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

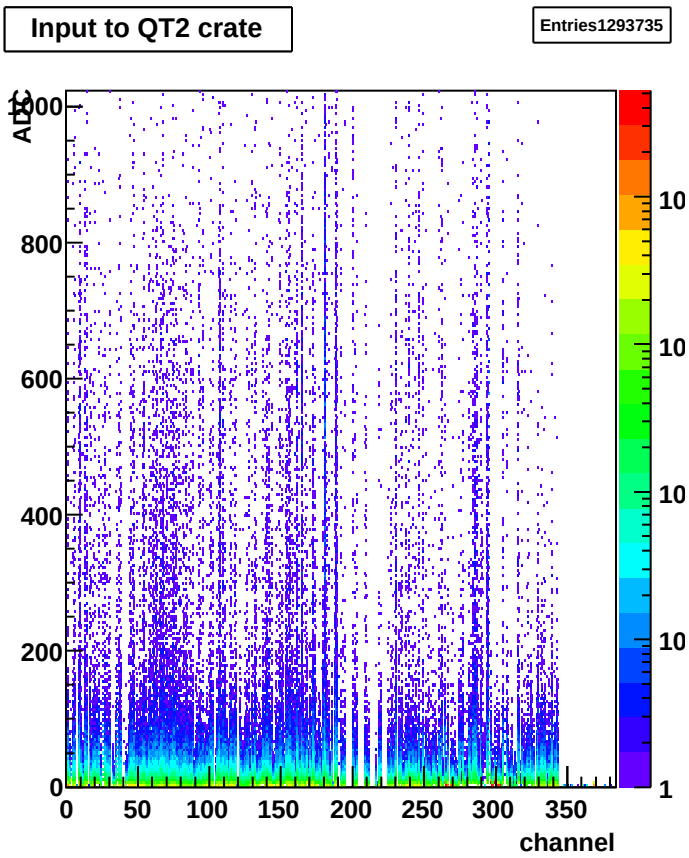

Entries 106403
BBC-L-East ADC Sum
BBC-L-West ADC Sum

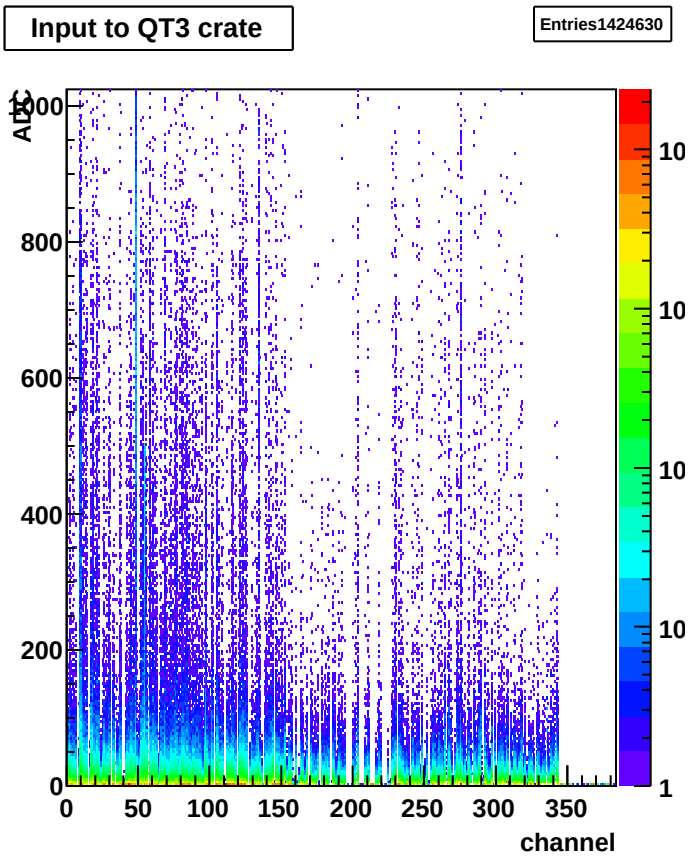

Entries 106403
BBC-S-East ADC Sum
BBC-S-West ADC Sum

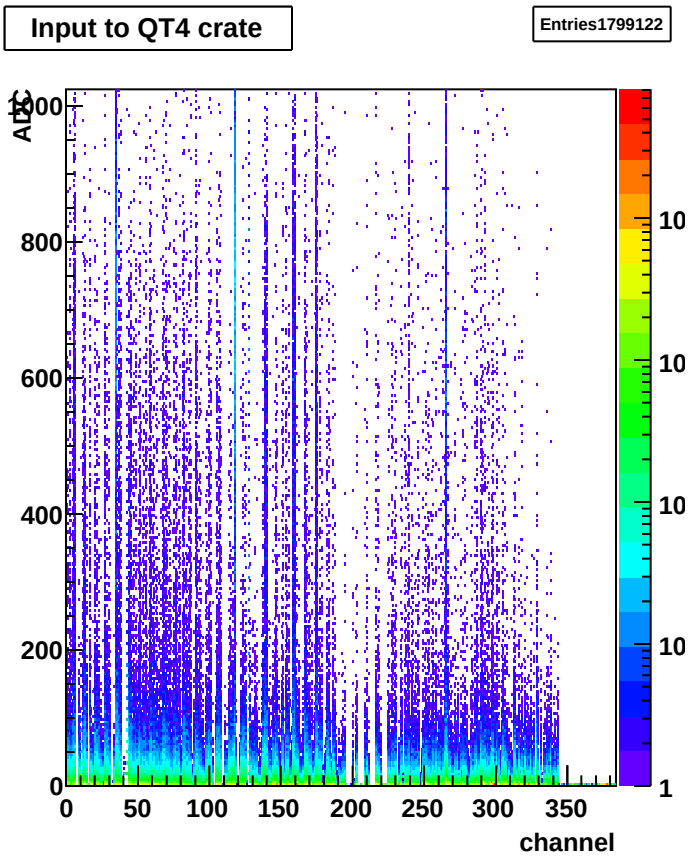

Entries 106403
ZDC East ADC Sum Att
ZDC West ADC Sum Att

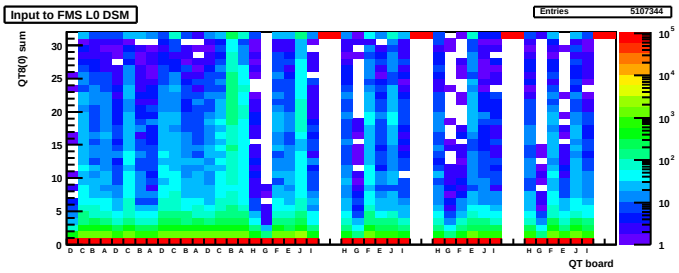

Entries 106403
BBC-L TAC Diff
BCC-S TAC Diff
Entries 106403
BBC-L TAC Diff
VPD TAC Diff
Entries 106403
BBC-L TAC Diff
ZDC TAC Diff
Entries 106403
BBC-S TAC Diff
VPD TAC Diff
Entries 106403
BBC-S TAC Diff
ZDC TAC Diff
Entries 106403
VPD TAC Diff
ZDC TAC Diff

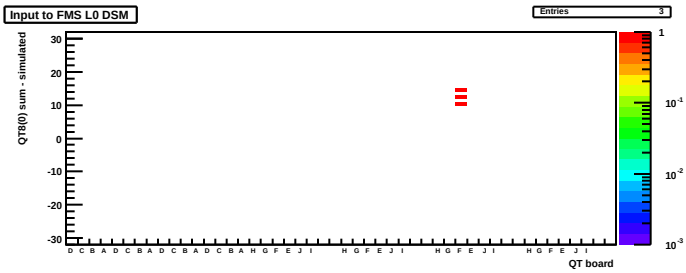

Input to QT1 crate
Entries2650567
ADC
channel

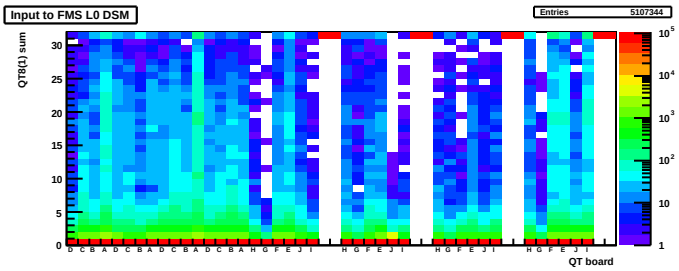

Input to QT2 crate
Entries1293735
ADC
channel

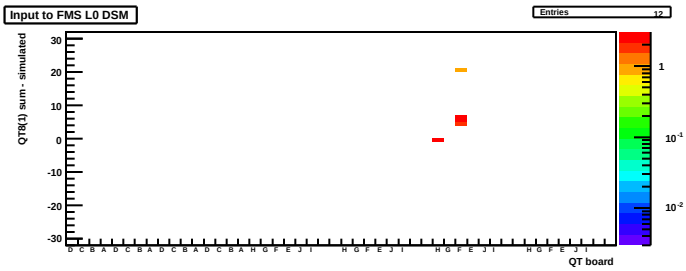

Input to QT3 crate
Entries1424630
ADC
channel

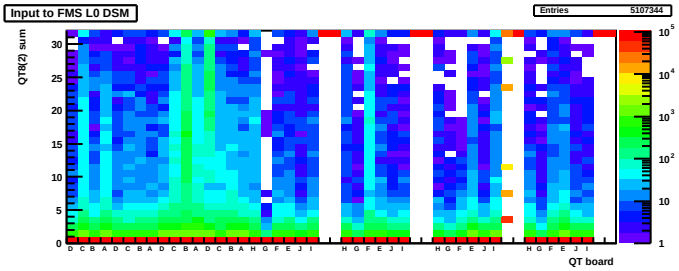

Input to QT4 crate
Entries1799122
ADC
channel

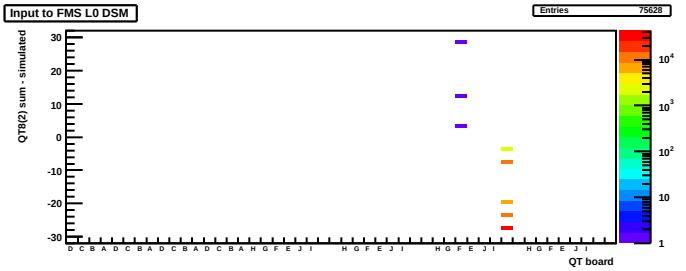

Input to FMS L0 DSM
Entries 5107344
QT8(0) sum
QT board

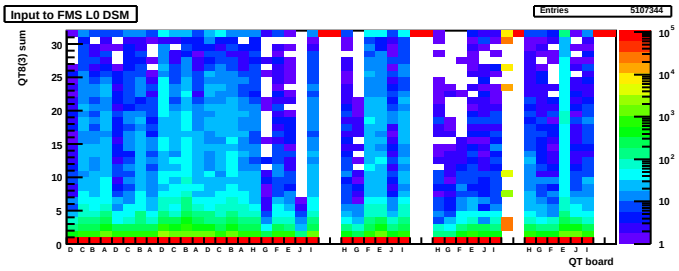

Input to FMS L0 DSM
Entries 3
QT8(0) sum - simulated
QT board

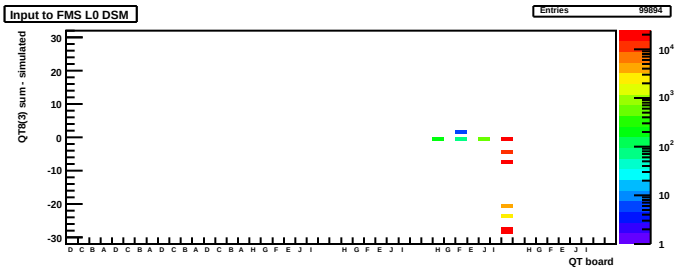

Input to FMS L0 DSM
Entries 5107344
QT8(1) sum
QT board

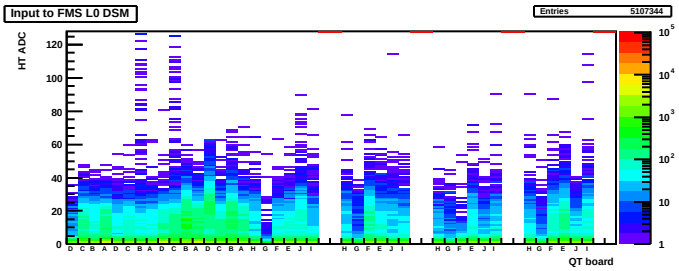

Input to FMS L0 DSM
Entries 12
QT8(1) sum - simulated
QT board

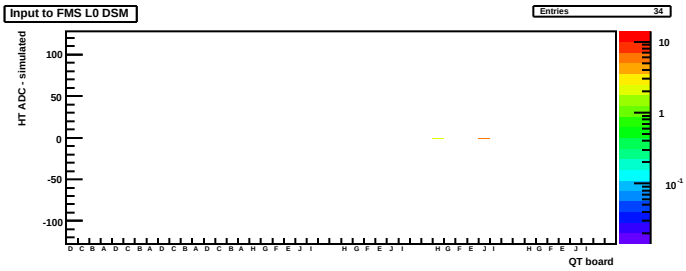

Input to FMS L0 DSM
Entries 5107344
QT8(2) sum
QT board

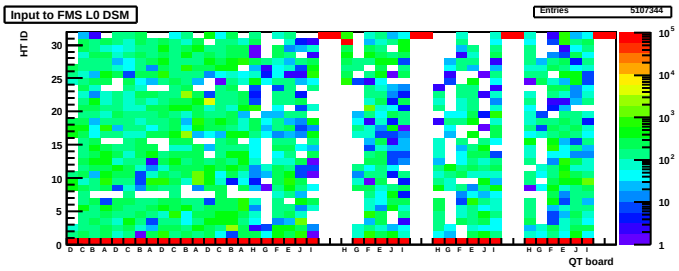

Input to FMS L0 DSM
Entries 75628
QT8(2) sum - simulated
QT board

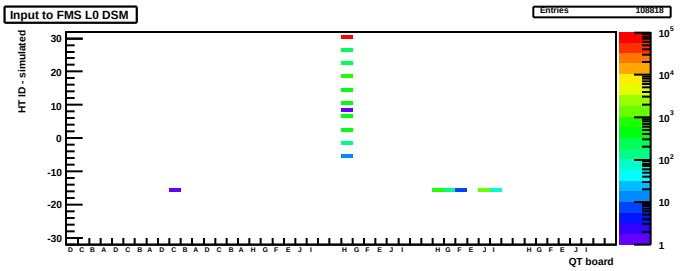

Input to FMS L0 DSM
Entries 5107344
QT8(3) sum
QT board

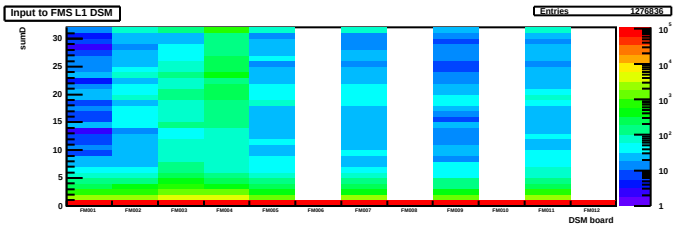

Input to FMS L0 DSM
Entries 99894
QT8(3) sum - simulated
QT board

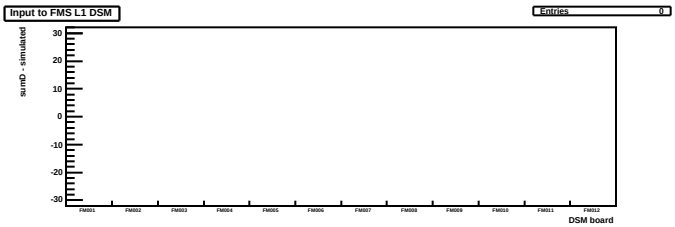

Input to FMS L0 DSM
Entries 5107344
HT ADC
QT board

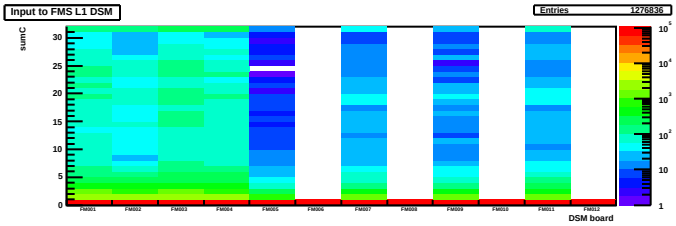

Input to FMS L0 DSM
Entries 34
HT ADC - simulated
QT board

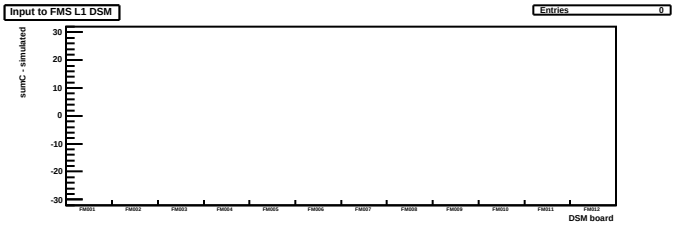

Input to FMS L0 DSM
Entries 5107344
HT ID
QT board

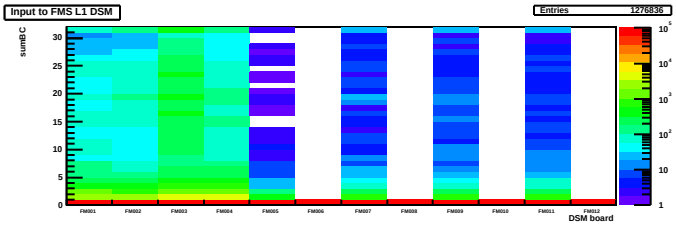

Input to FMS L0 DSM
Entries 108818
HT ID - simulated
QT board

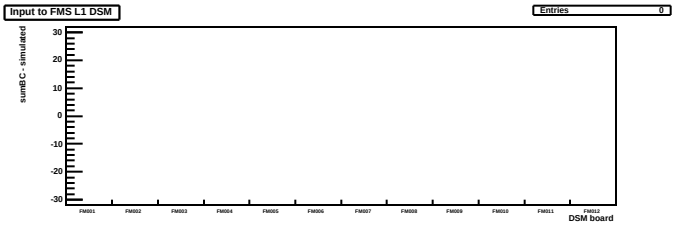

Input to FMS L1 DSM
Entries 1276836
sumD
DSM board

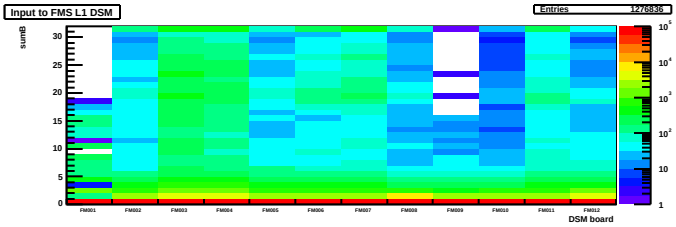

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

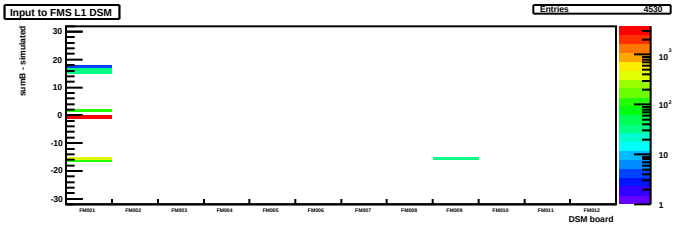

Input to FMS L1 DSM
Entries 1276836
sumC
DSM board

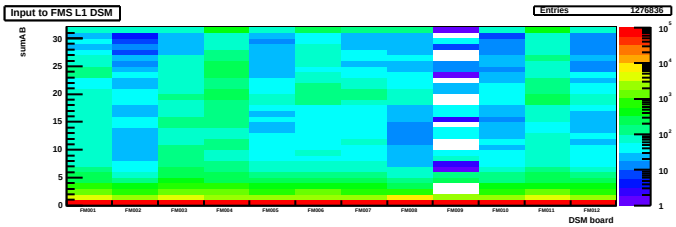

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

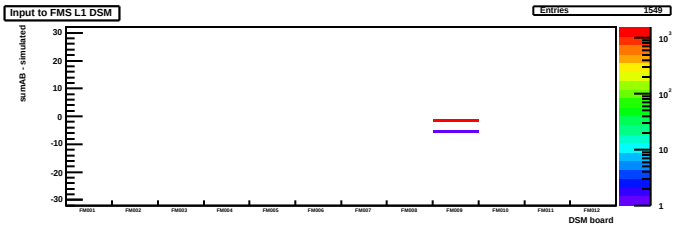

Input to FMS L1 DSM
Entries 1276836
sumBC
DSM board

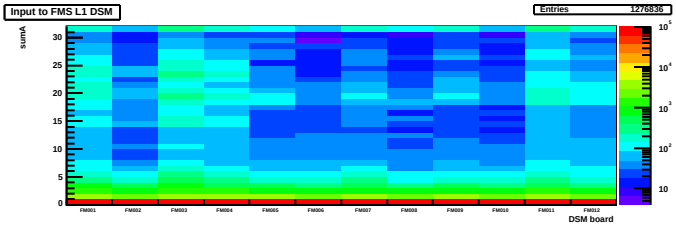

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

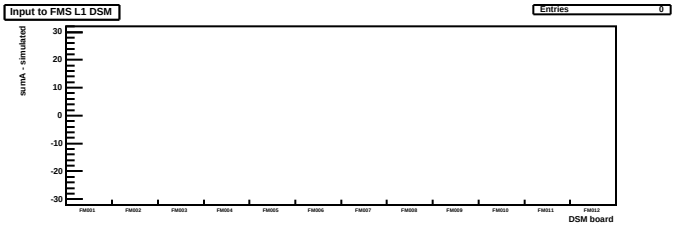

Input to FMS L1 DSM
Entries 1276836
sumB
DSM board

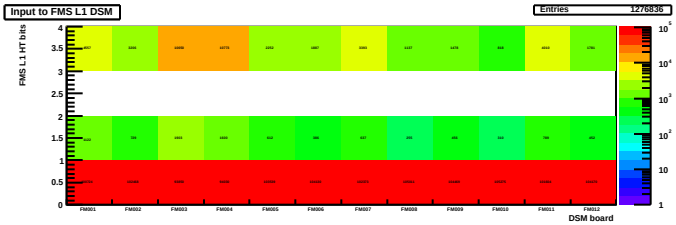

Input to FMS L1 DSM
Entries 4530
sumB - simulated
DSM board

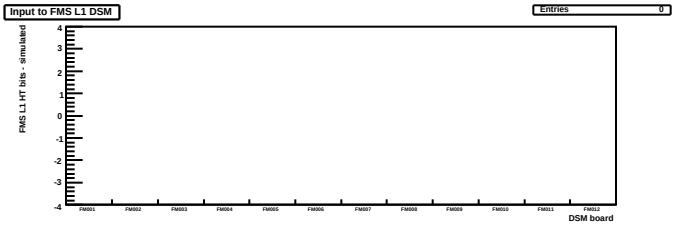

Input to FMS L1 DSM
Entries 1276836
sumAB
DSM board

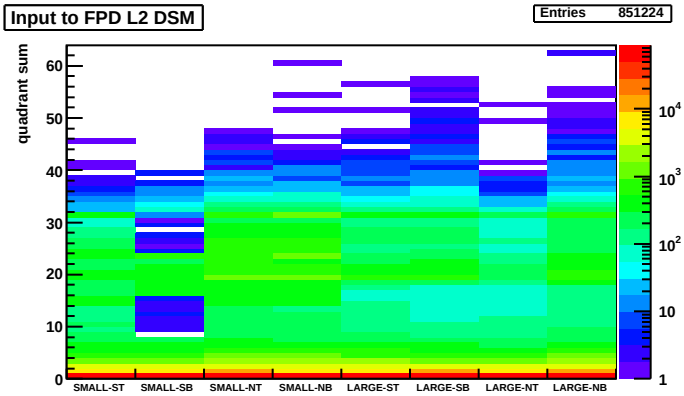

Input to FMS L1 DSM
Entries 1549
sumAB - simulated
DSM board

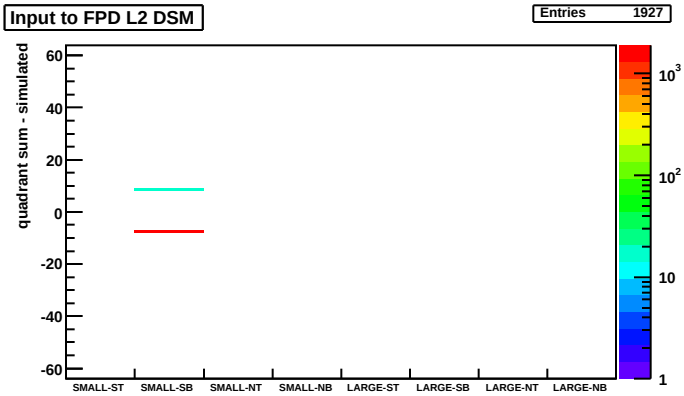

Input to FMS L1 DSM
Entries 1276836
sumA
DSM board

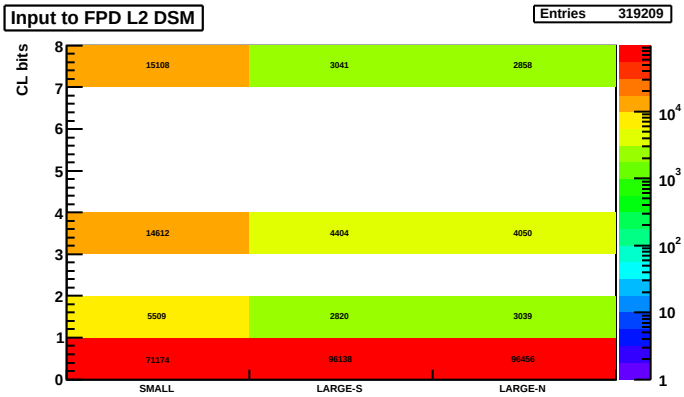

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

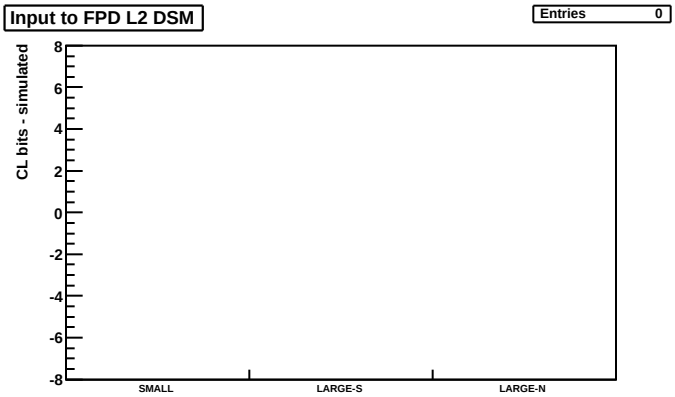

Input to FMS L1 DSM
Entries 1276836
FMS L1 HT bits
DSM board

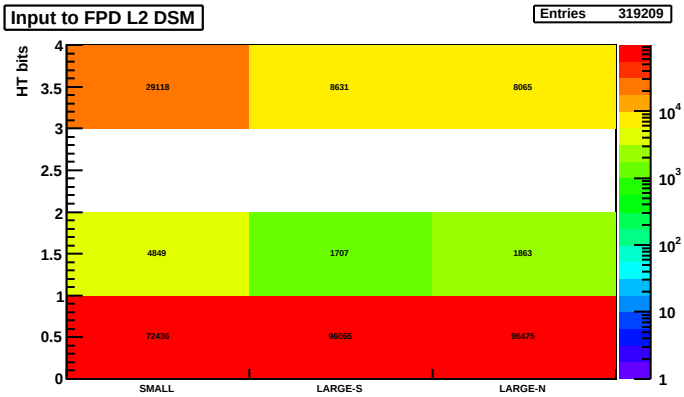

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

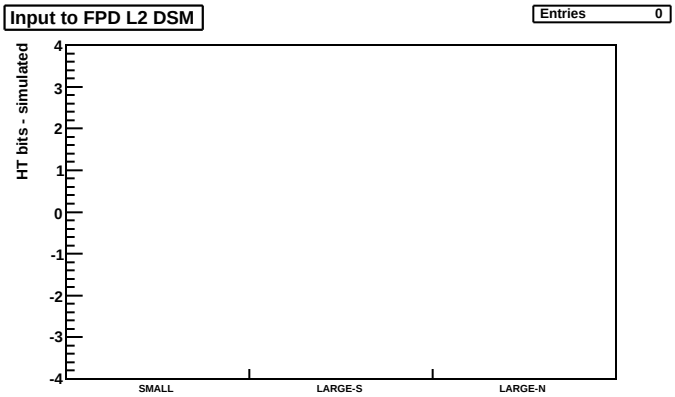

Input to FPD L2 DSM
Entries 851224
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

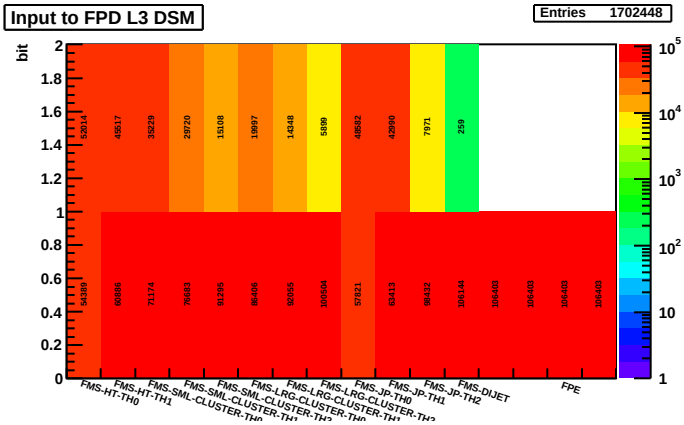

Input to FPD L2 DSM
Entries 1927
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

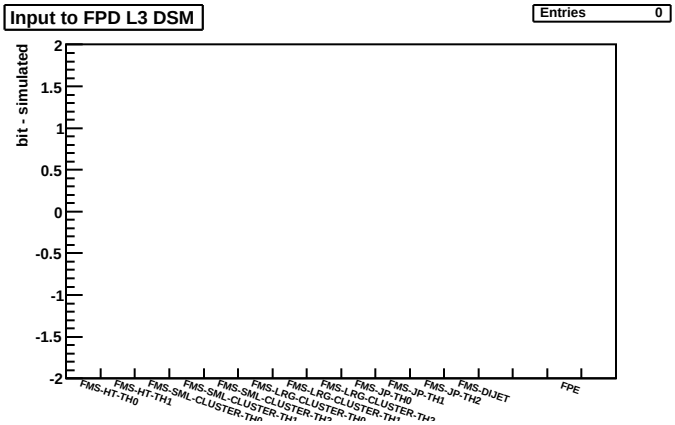

Input to FPD L2 DSM
Entries 319209
CL bits
15108
3041
2858
14612
4404
4050
5509
2820
3039
71174
96138
96456
SMALL
LARGE-S
LARGE-N

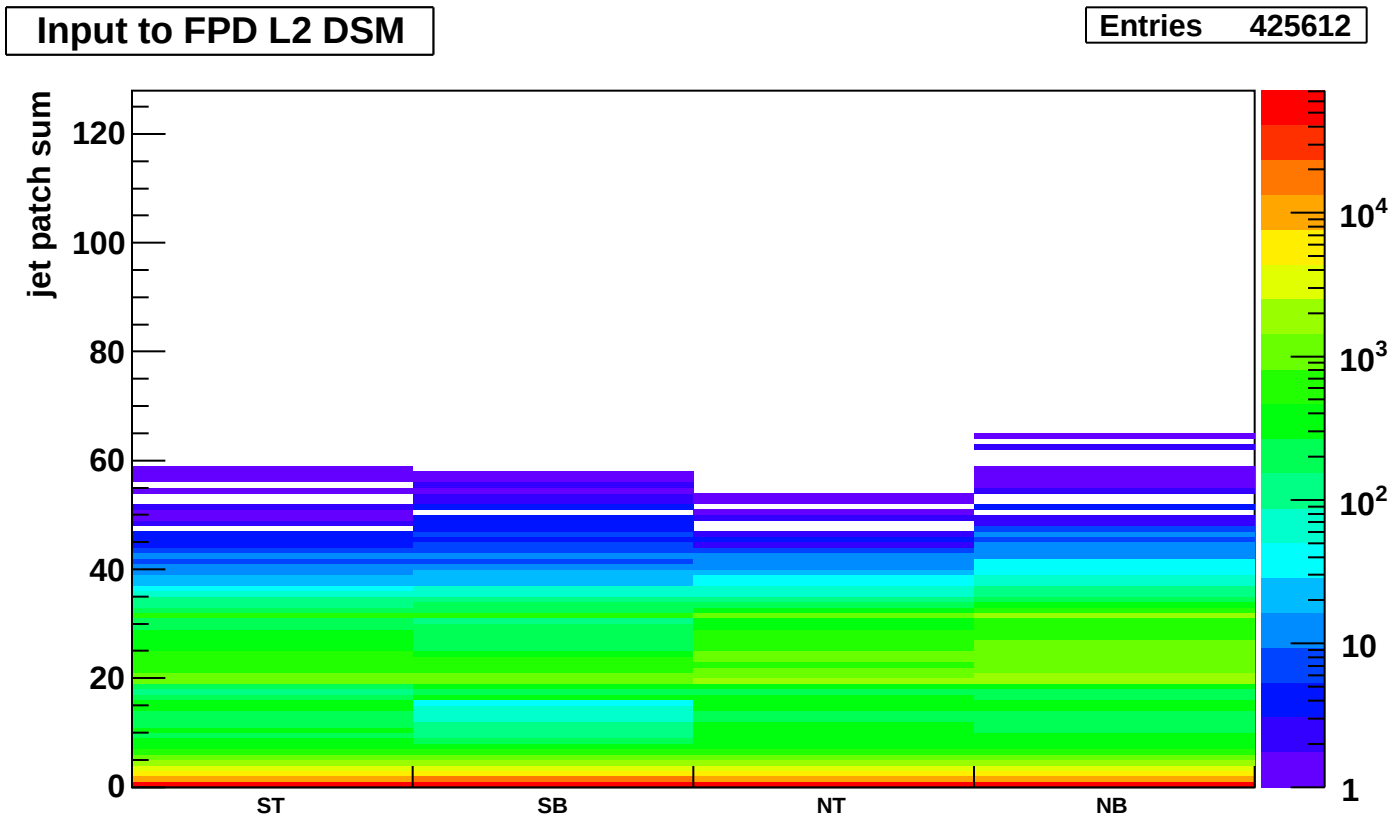

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

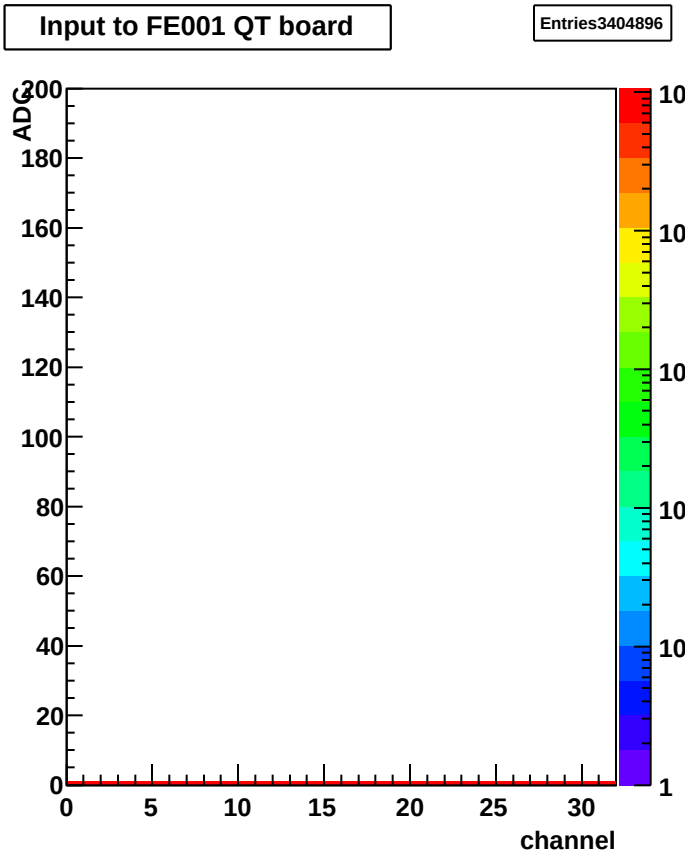

Input to FPD L2 DSM
Entries 319209
HT bits
29118
8631
8065
4849
1707
1863
72436
96065
96475
SMALL
LARGE-S
LARGE-N

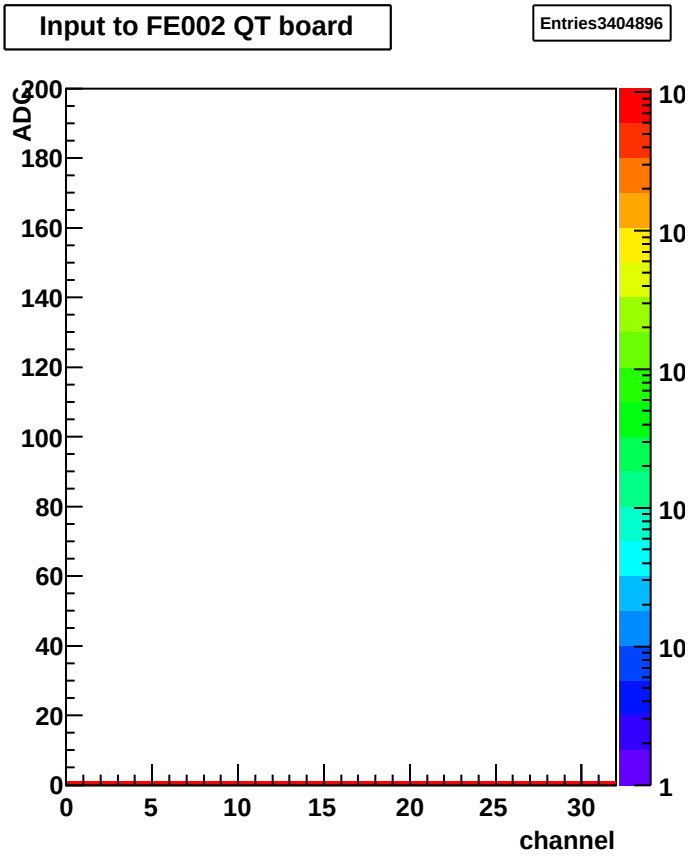

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

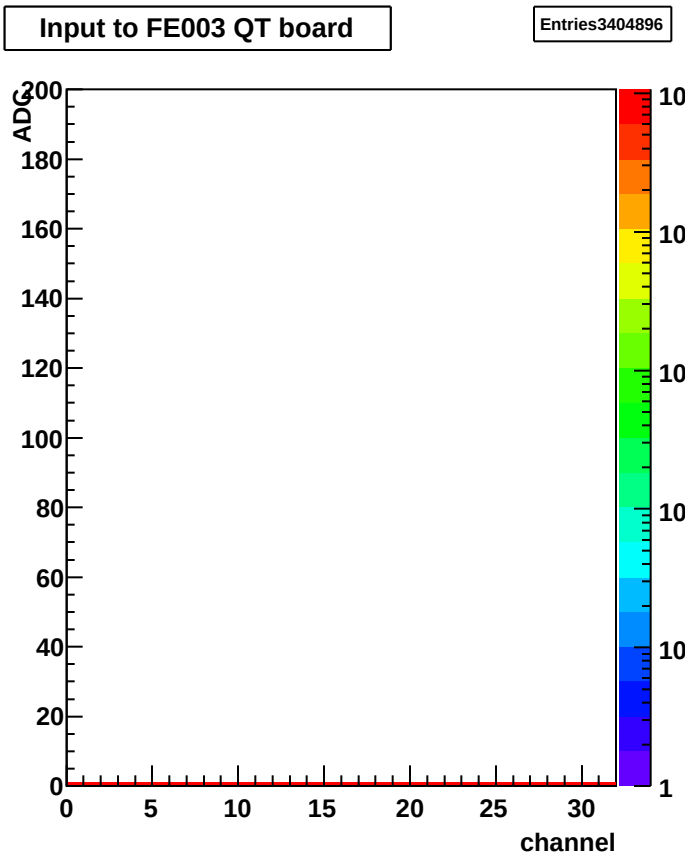

Input to FPD L3 DSM
Entries 1702448
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

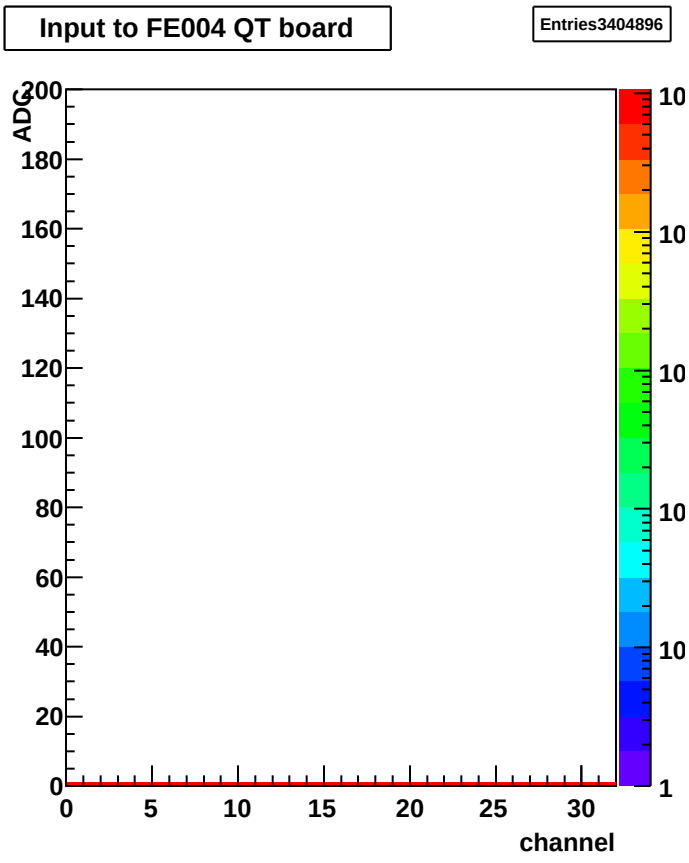

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

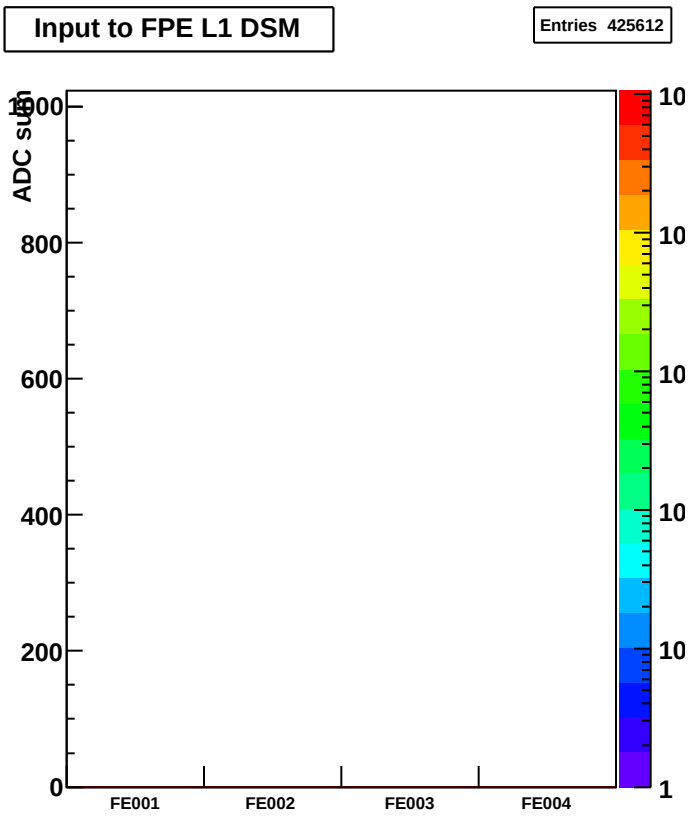

Input to FPD L2 DSM
Entries 425612
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

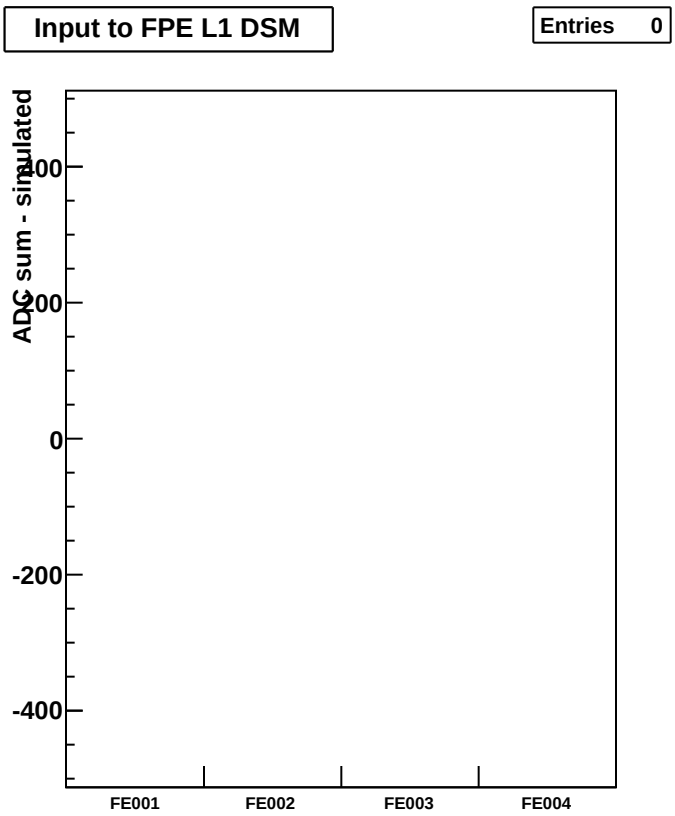

Input to FE001 QT board
Entries3404896
ADC
channel

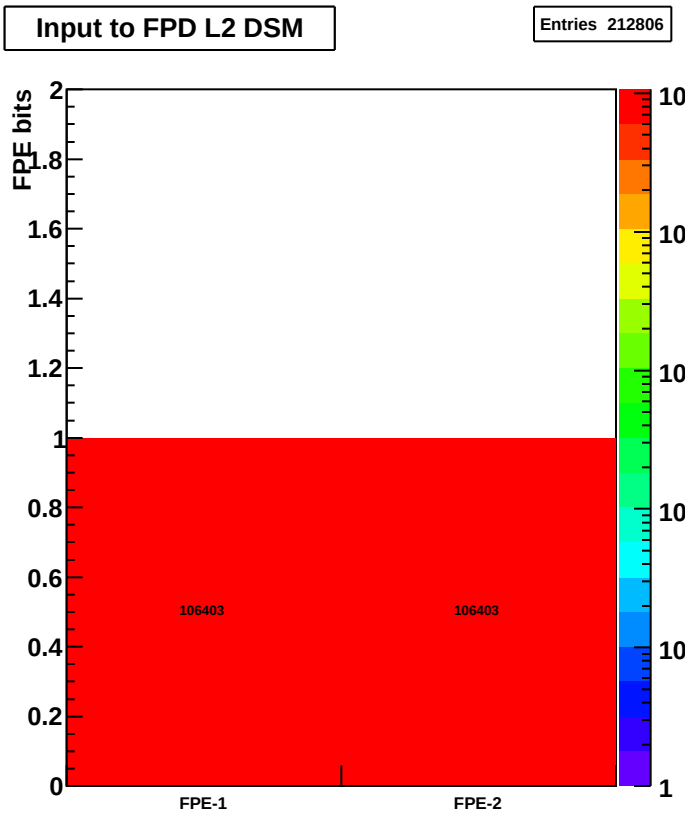

Input to FE002 QT board
Entries3404896
ADC
channel

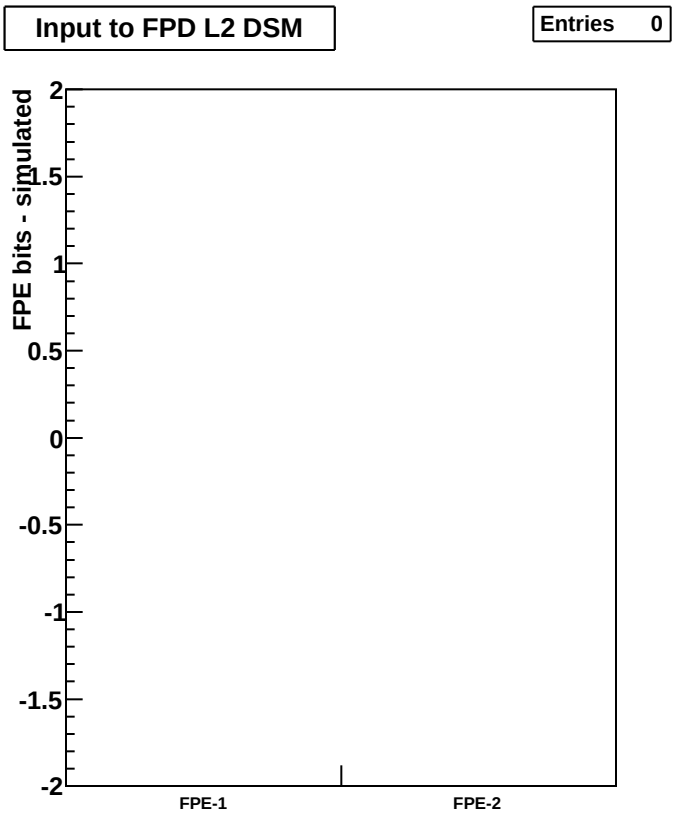

Input to FE003 QT board
Entries3404896
ADC
channel


Input to FE004 QT board
Entries3404896
ADC
channel


Input to FPE L1 DSM
Entries 425612
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 212806
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
106403
106403
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2