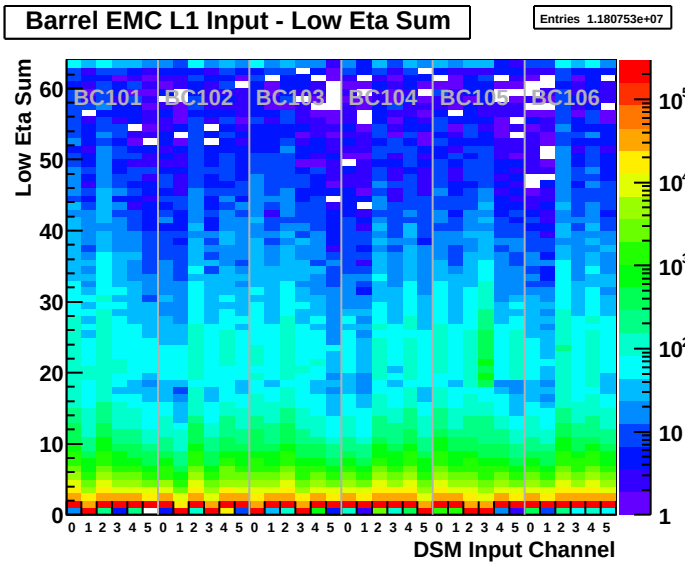
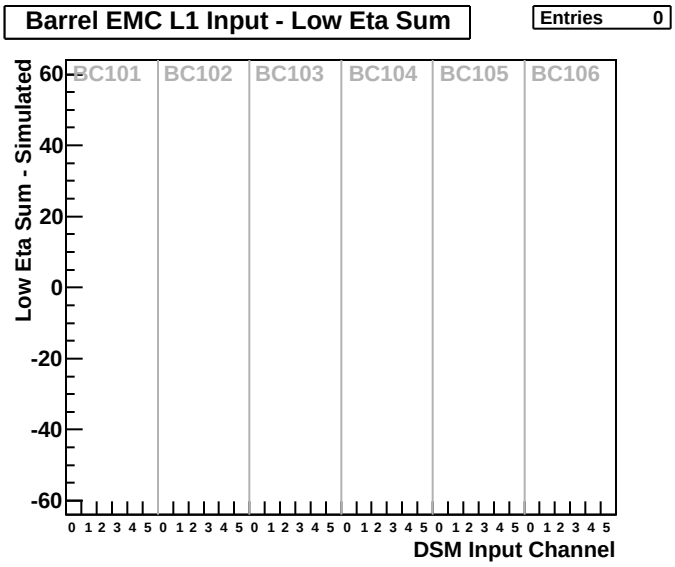
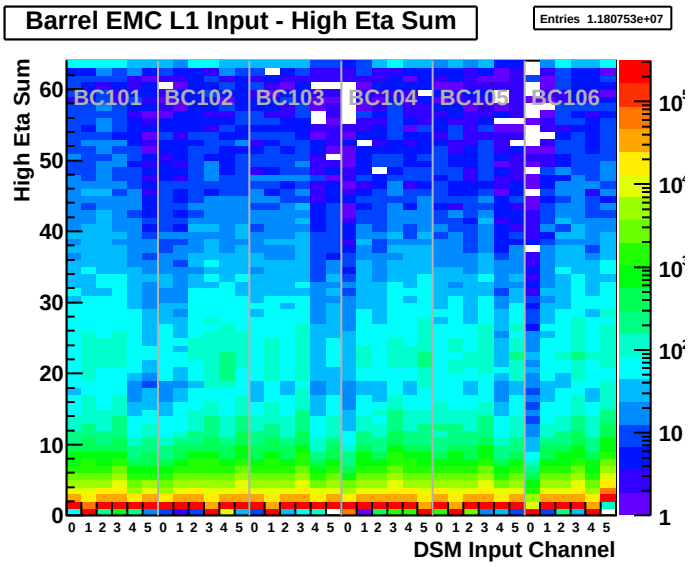
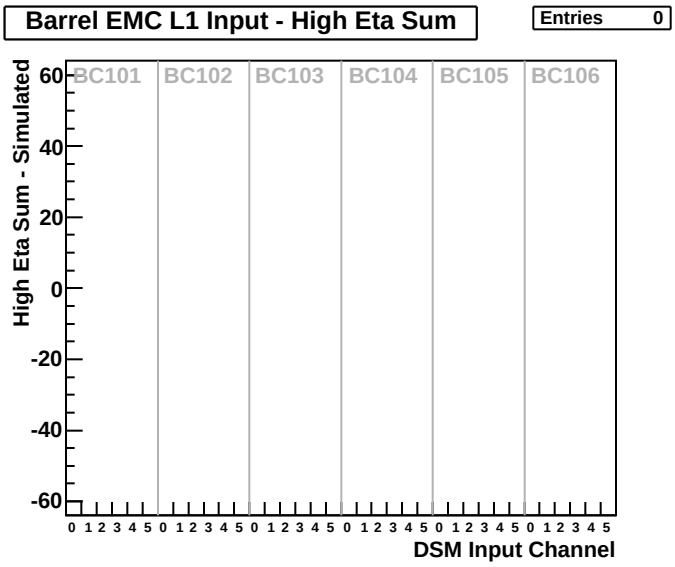
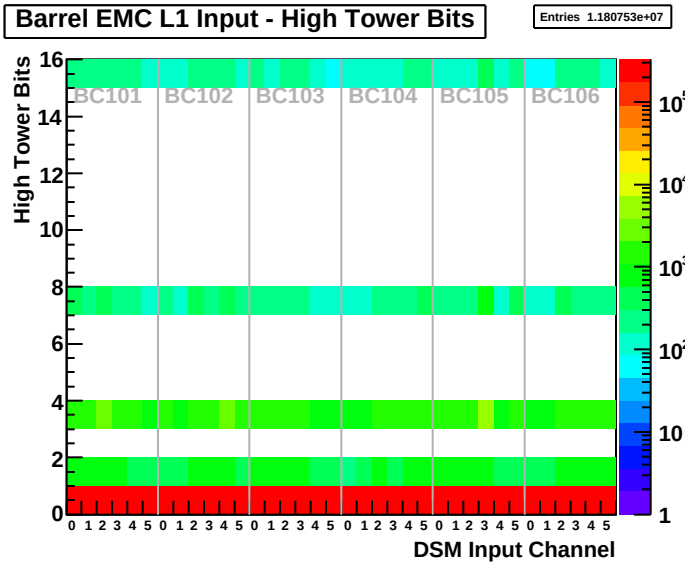
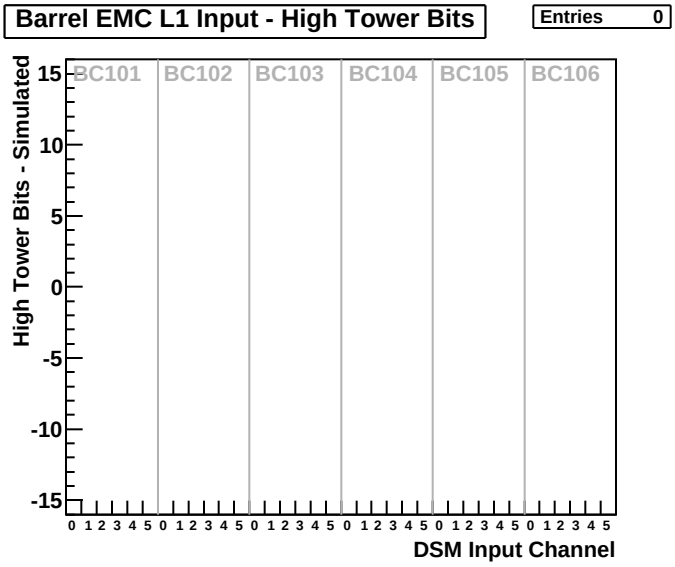

Barrel EMC L1 Input - Low Eta Sum
Entries 1.180753e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

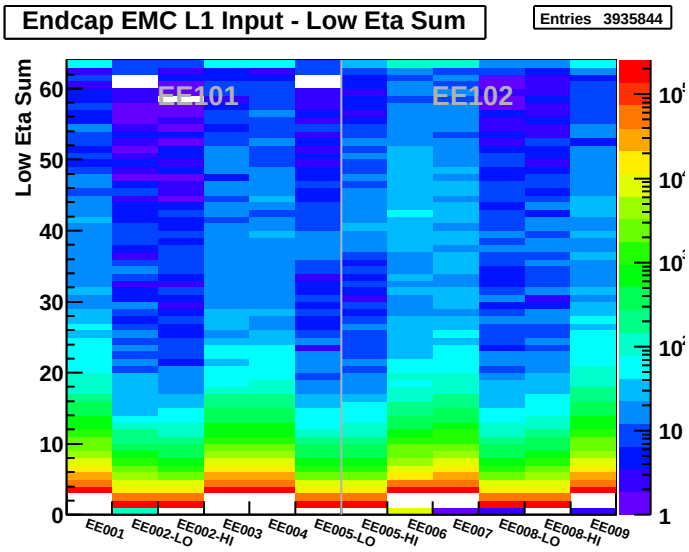

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

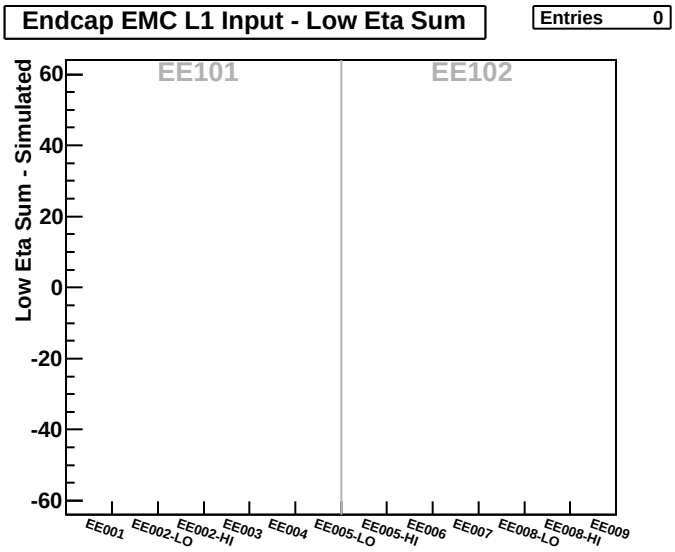

Barrel EMC L1 Input - High Eta Sum
Entries 1.180753e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

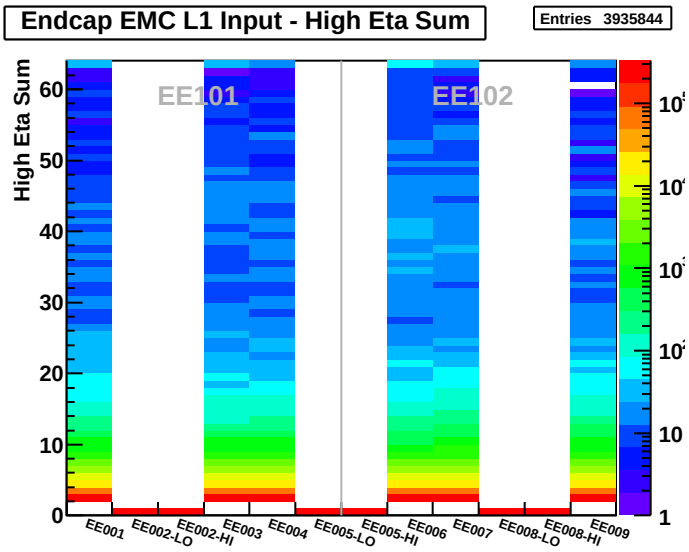

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

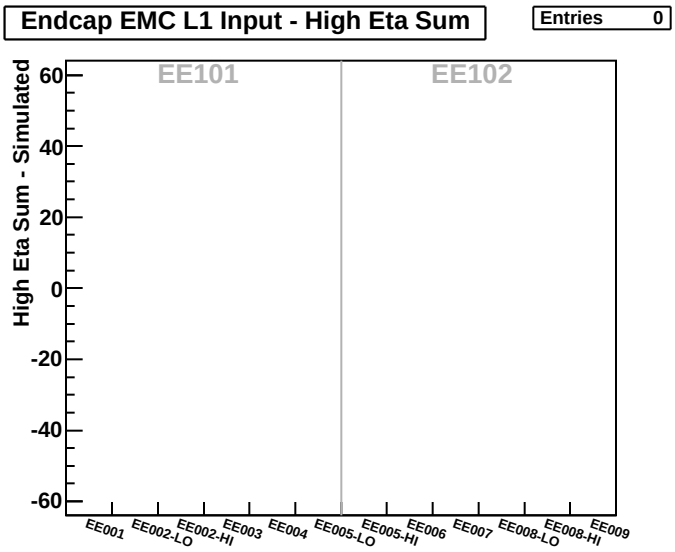

Barrel EMC L1 Input - High Tower Bits
Entries 1.180753e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

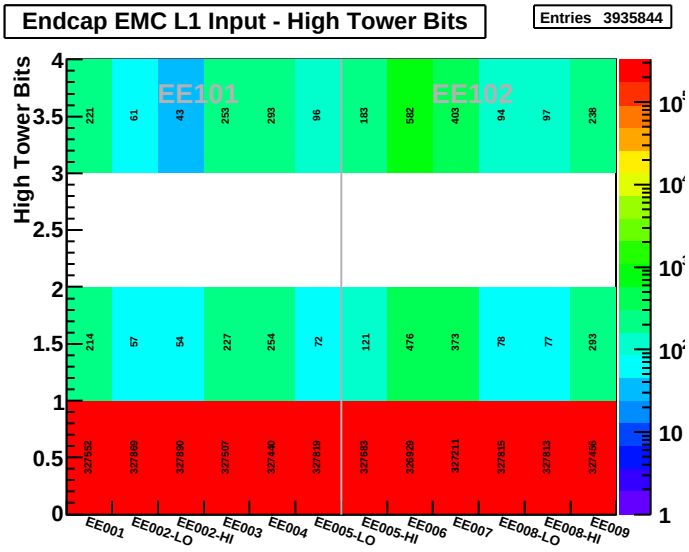

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

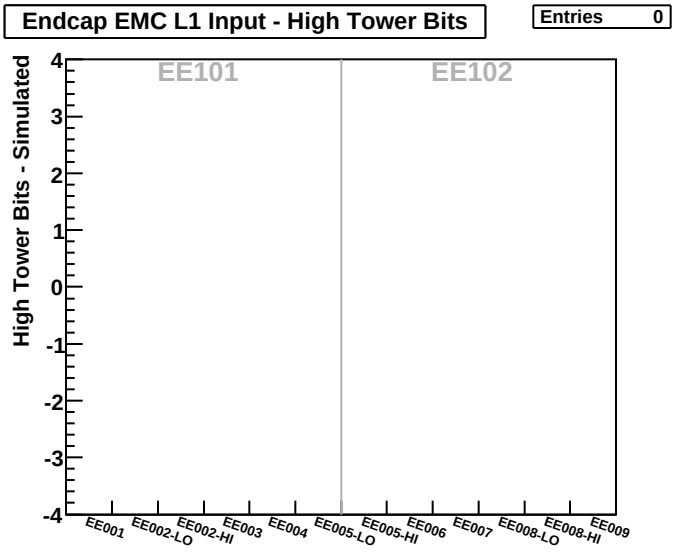

Endcap EMC L1 Input - Low Eta Sum
Entries 3935844
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

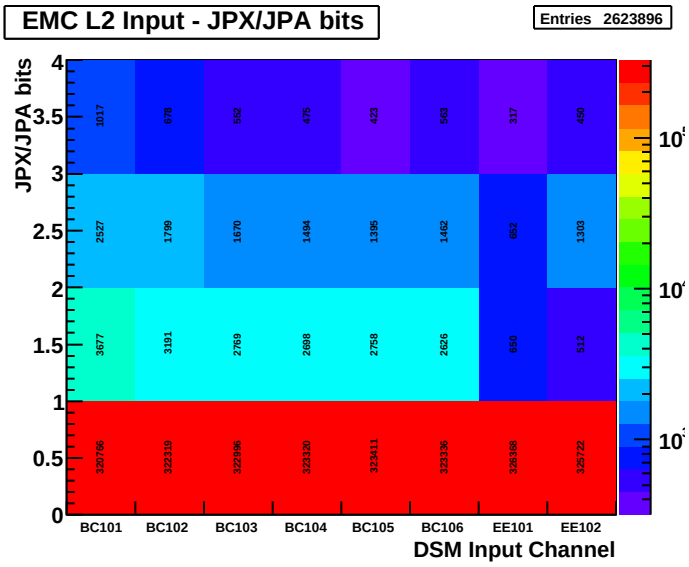

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

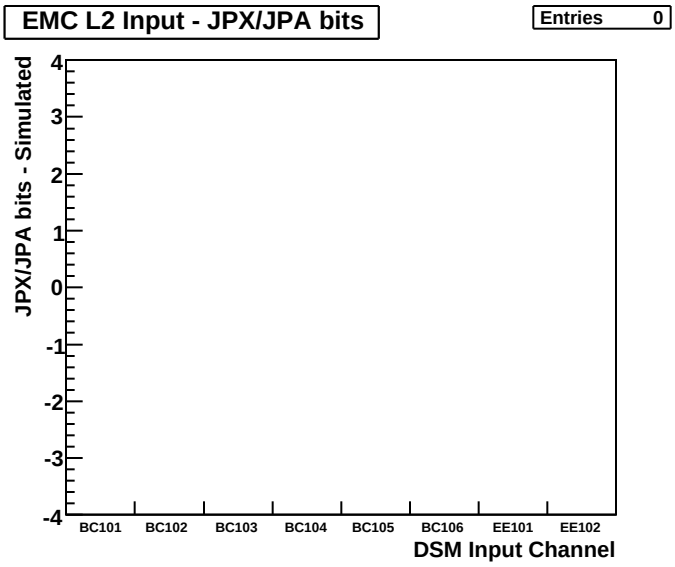

Endcap EMC L1 Input - High Eta Sum
Entries 3935844
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

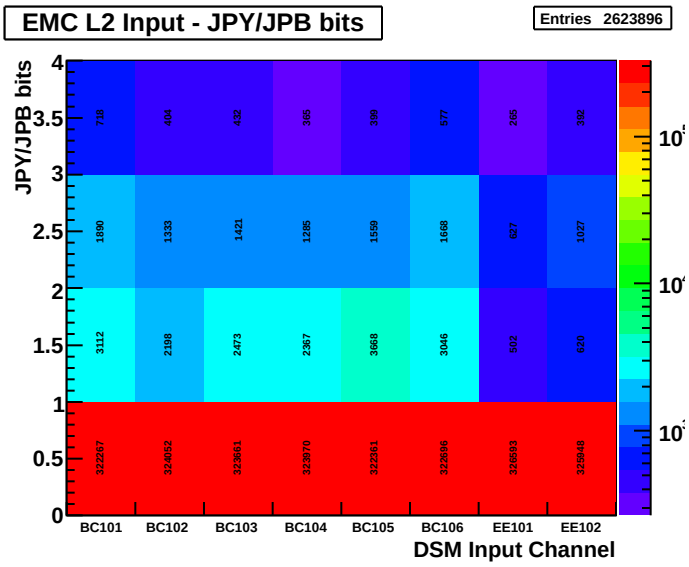

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

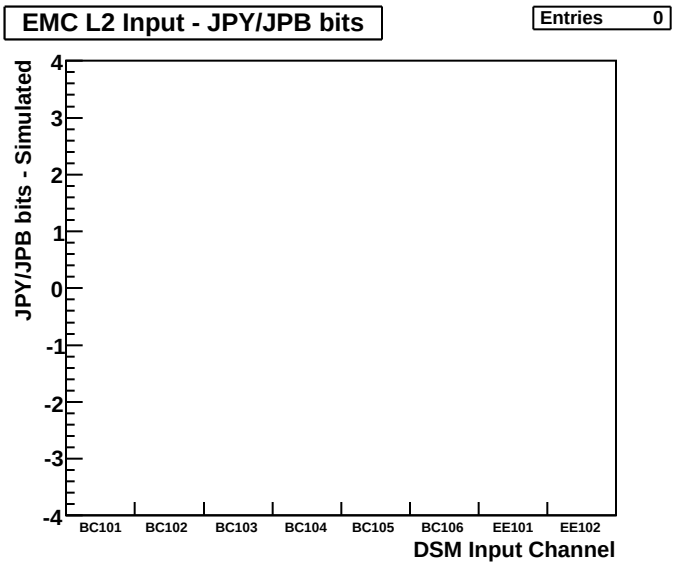

Endcap EMC L1 Input - High Tower Bits
Entries 3935844
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

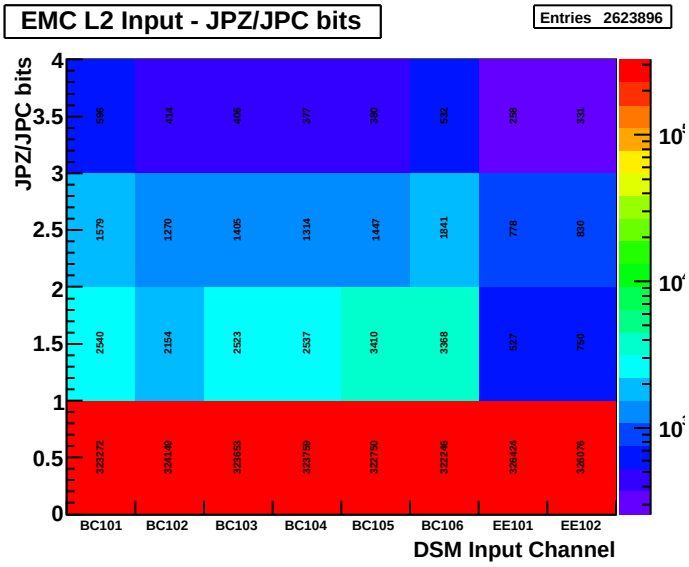

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

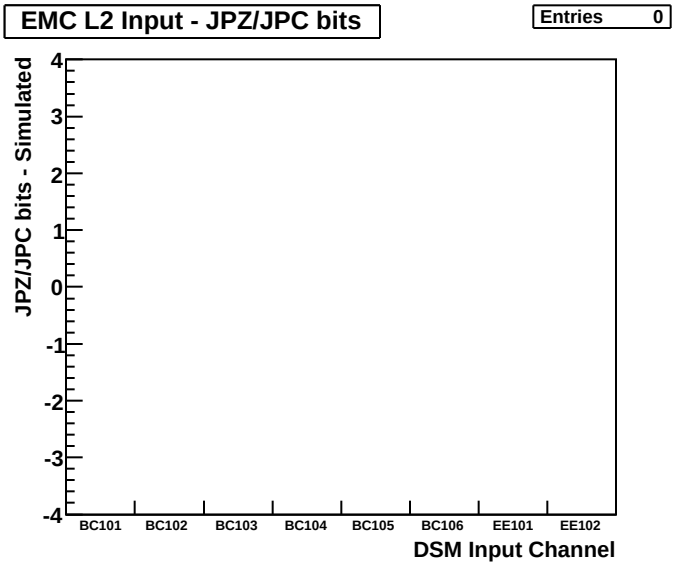

EMC L2 Input - JPX/JPA bits
Entries 2623896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1017 678 552 475 423 563 317 450
2527 1799 1670 1494 1395 1462 652 1303
3677 3191 2769 2698 2758 2626 650 512
320766 322319 322996 323320 323411 323336 325368 325722

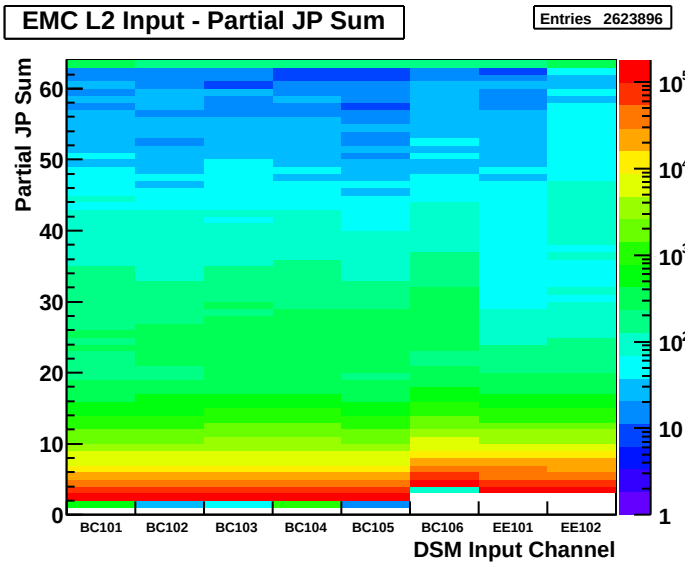

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

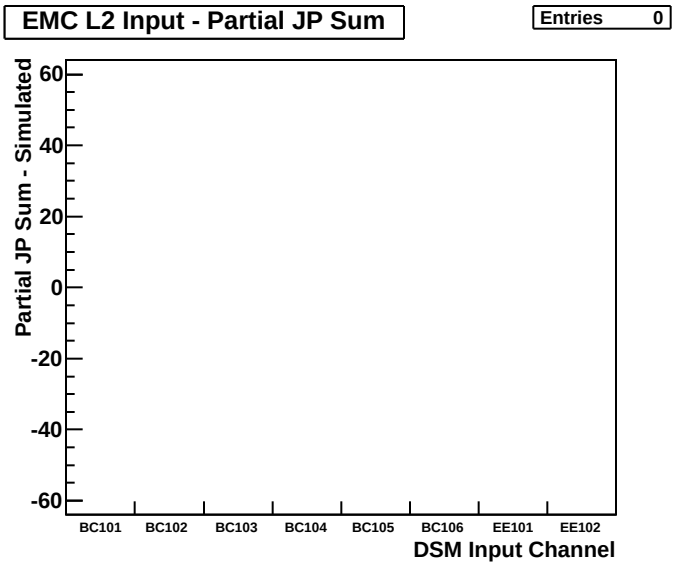

EMC L2 Input - JPY/JPB bits
Entries 2623896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
716 404 432 365 399 577 265 392
1890 1333 1421 1285 1599 1688 627 1027
3112 2198 2473 2367 3668 3046 502 620
322267 324052 323661 323970 322361 322596 326593 325948

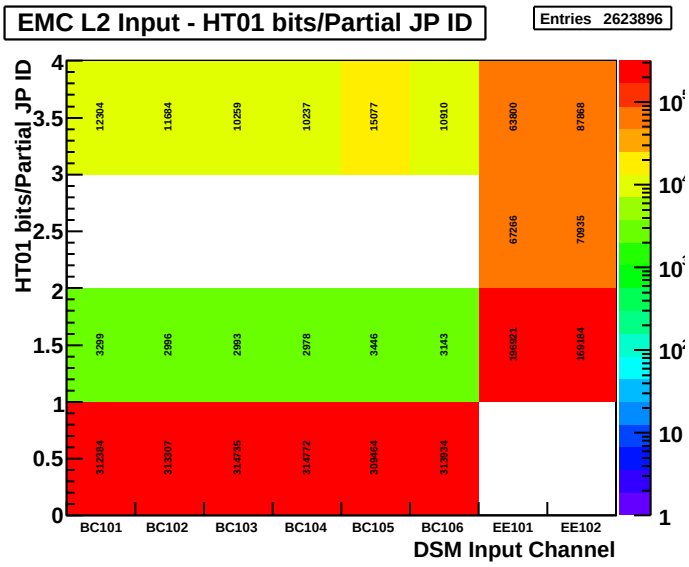

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

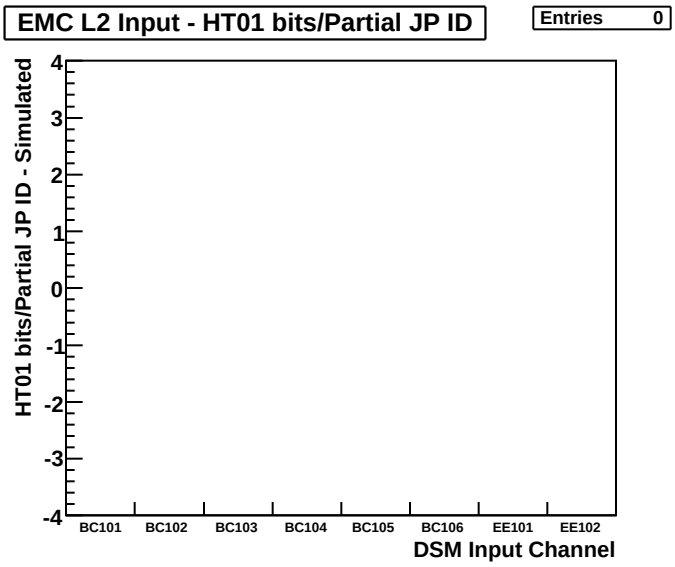

EMC L2 Input - JPZ/JPC bits
Entries 2623896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
536 414 406 377 390 532 258 331
1579 1270 1405 1314 1447 1041 778 830
2540 2154 2523 2537 3410 3368 527 750
323272 324149 323653 323759 322750 322246 326424 326076

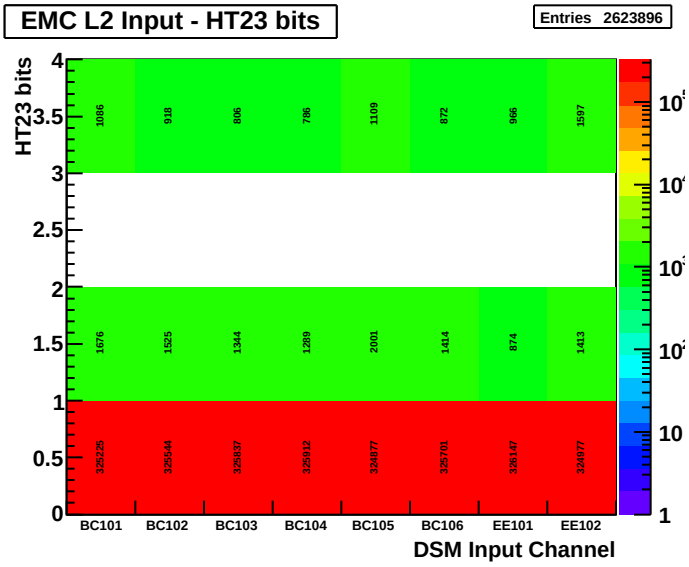

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

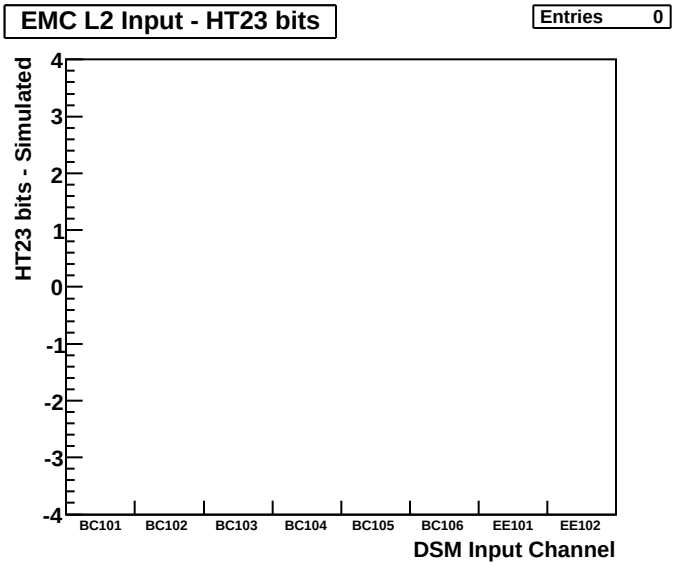

EMC L2 Input - Partial JP Sum
Entries 2623896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

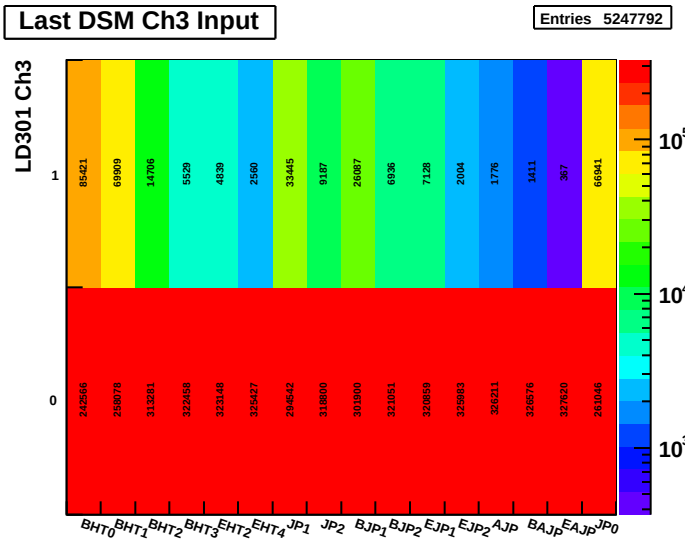

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

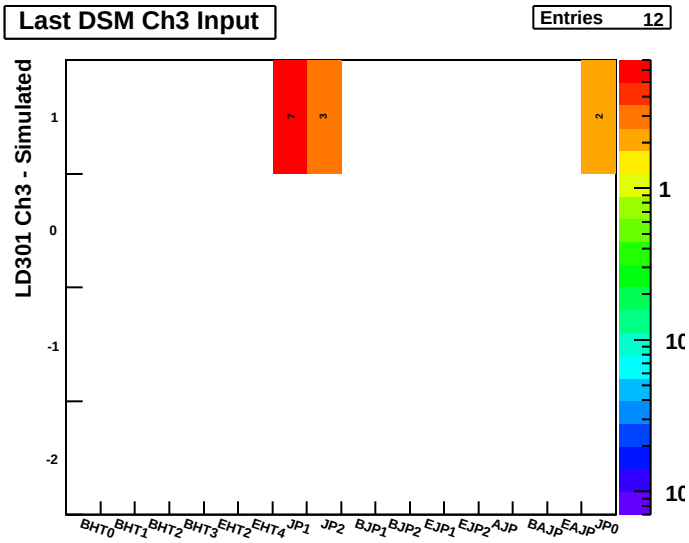

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2623896
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

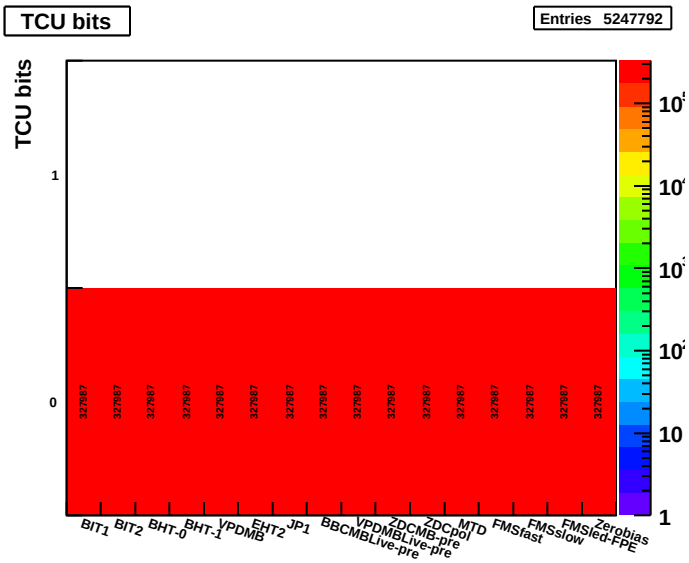

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

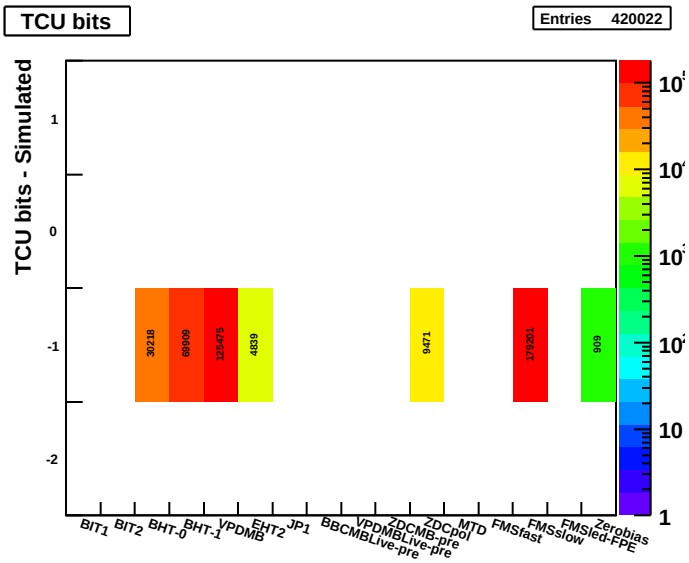

EMC L2 Input - HT23 bits
Entries 2623896
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

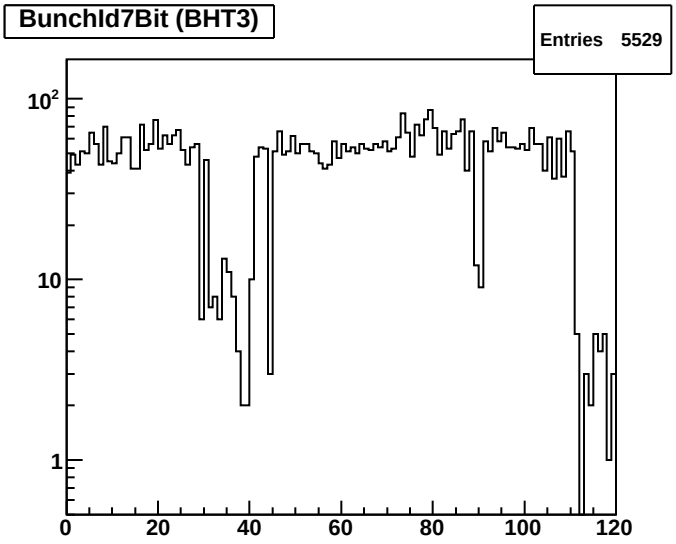

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

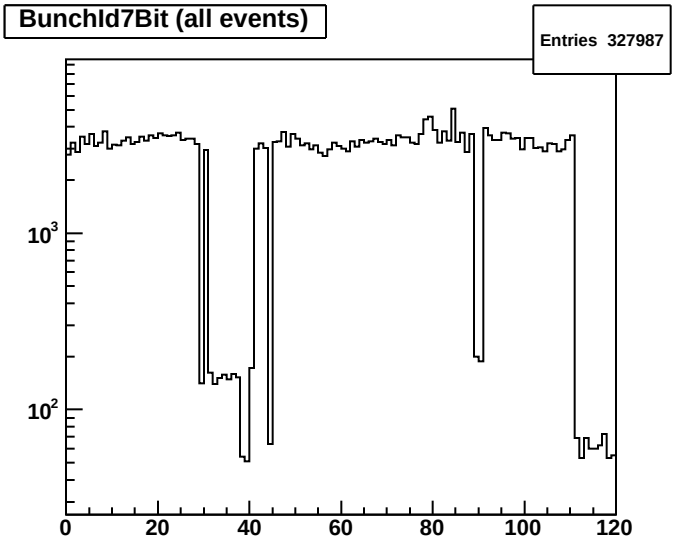

Last DSM Ch3 Input
Entries 5247792
LD301 Ch3
85421 69909 14706 5529 4839 2560 33445 9187 26087 6936 7128 2004 1776 1411 367 66941
242566 258078 313281 322458 323148 325427 294542 318800 301900 321051 320859 325983 326211 326576 327620 261046
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

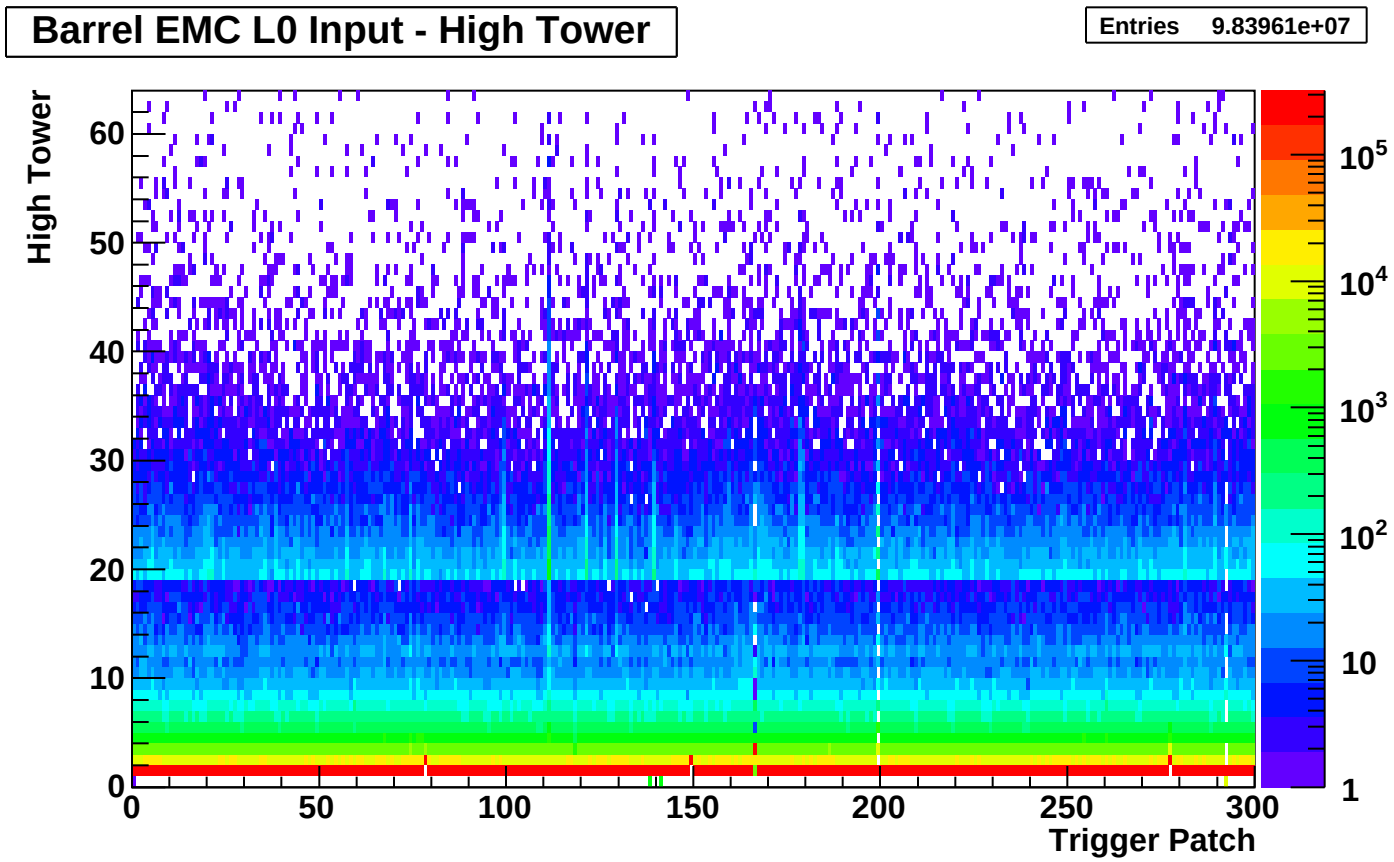

Last DSM Ch3 Input
Entries 12
LD301 Ch3 - Simulated
7 3 2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

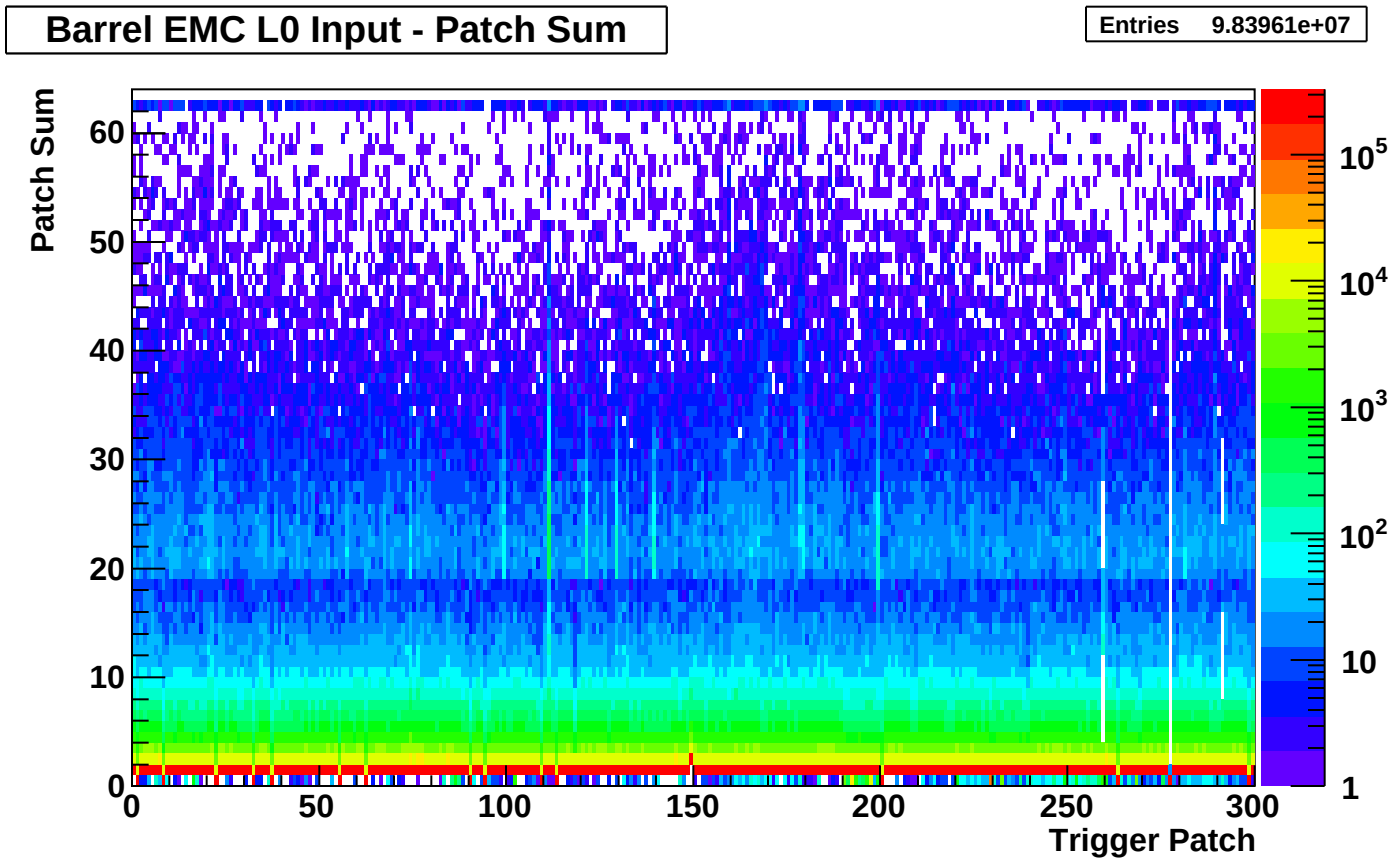

TCU bits
Entries 5247792
TCU bits
327987 327987 327987 327987 327987 327987 327987 327987 327987 327987 327987 327987 327987 327987 327987 327987
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

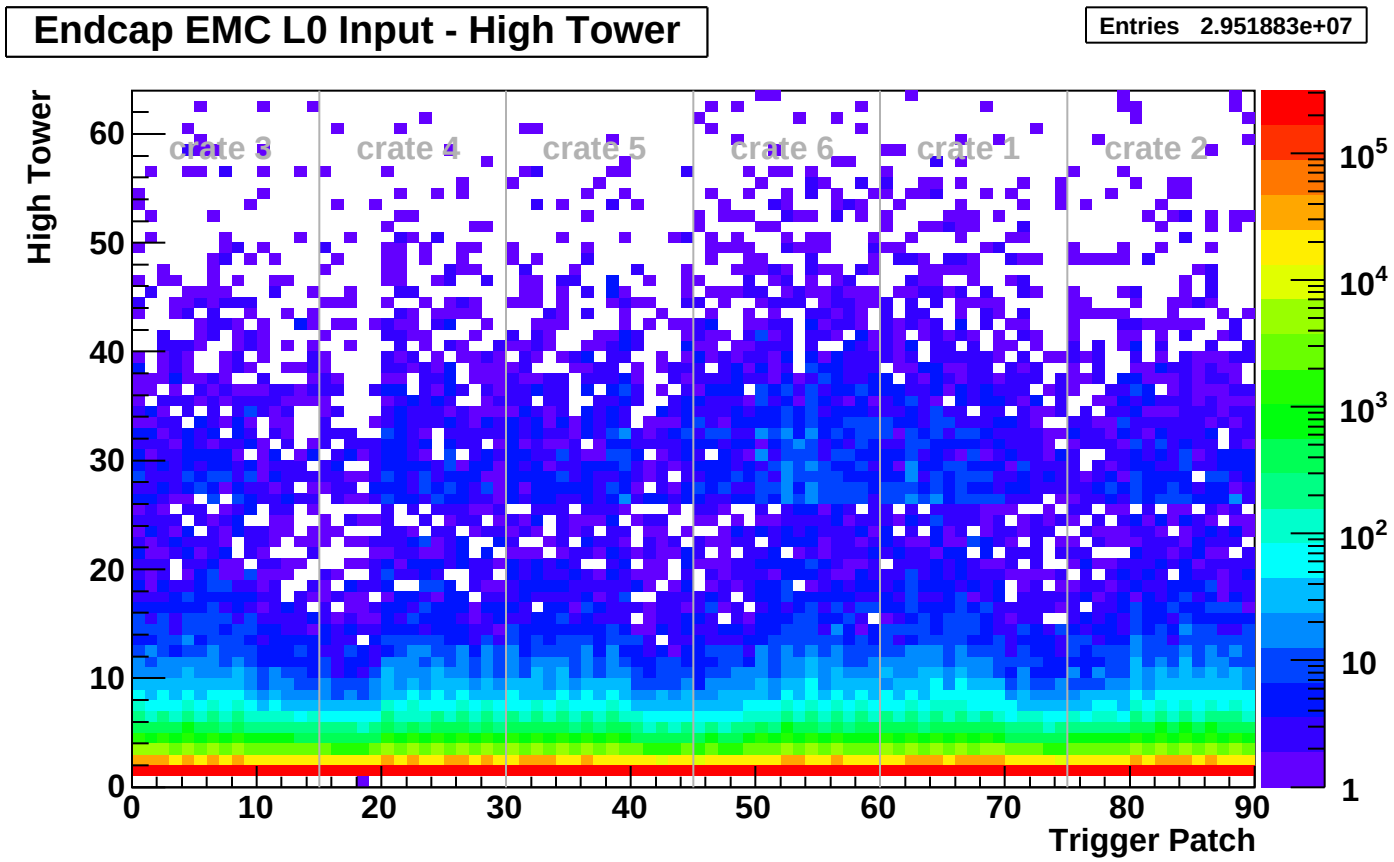

TCU bits
Entries 420022
TCU bits - Simulated
30218 69909 125475 4839 9471 178201 909
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

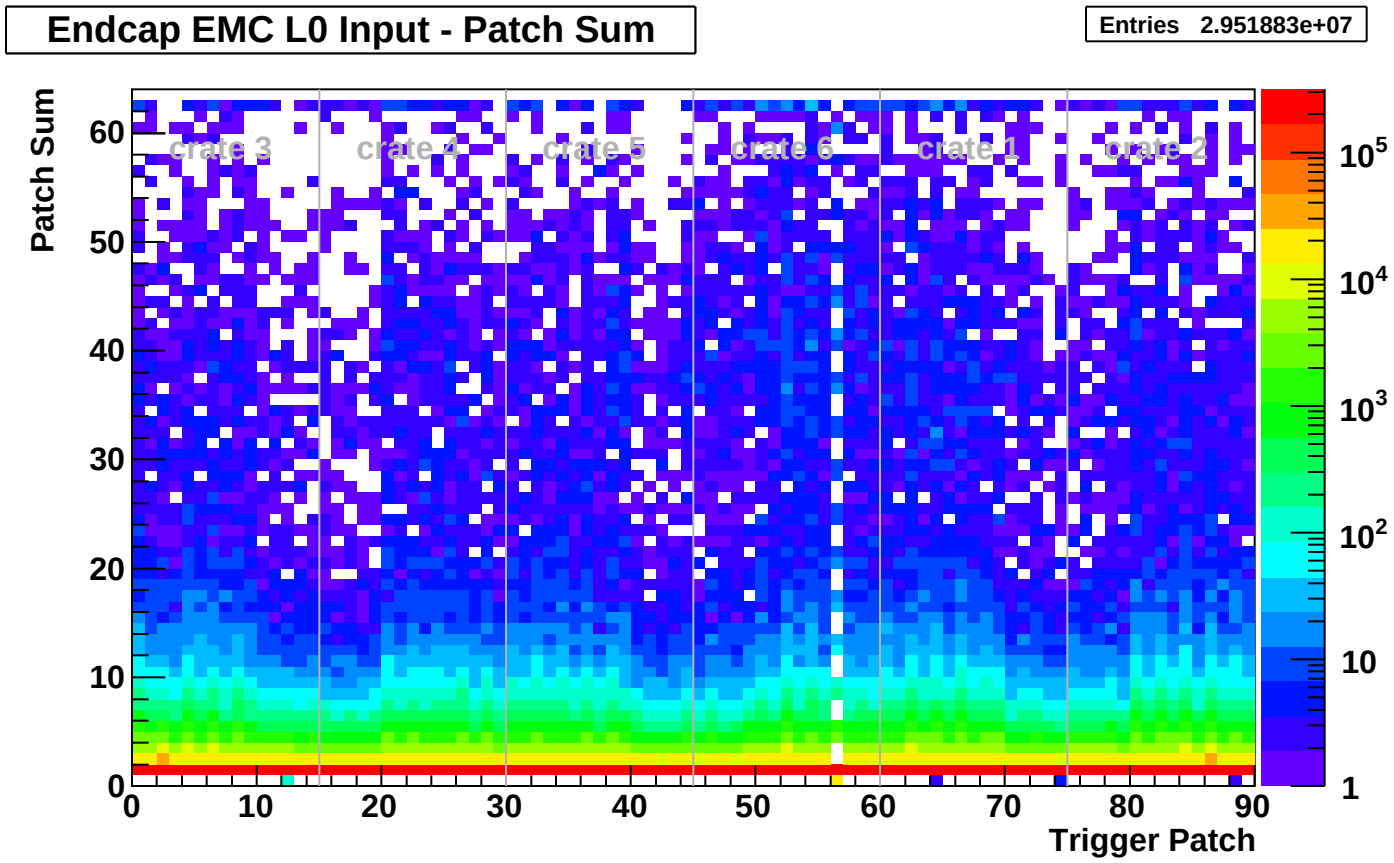

BunchId7Bit (BHT3)
Entries 5529

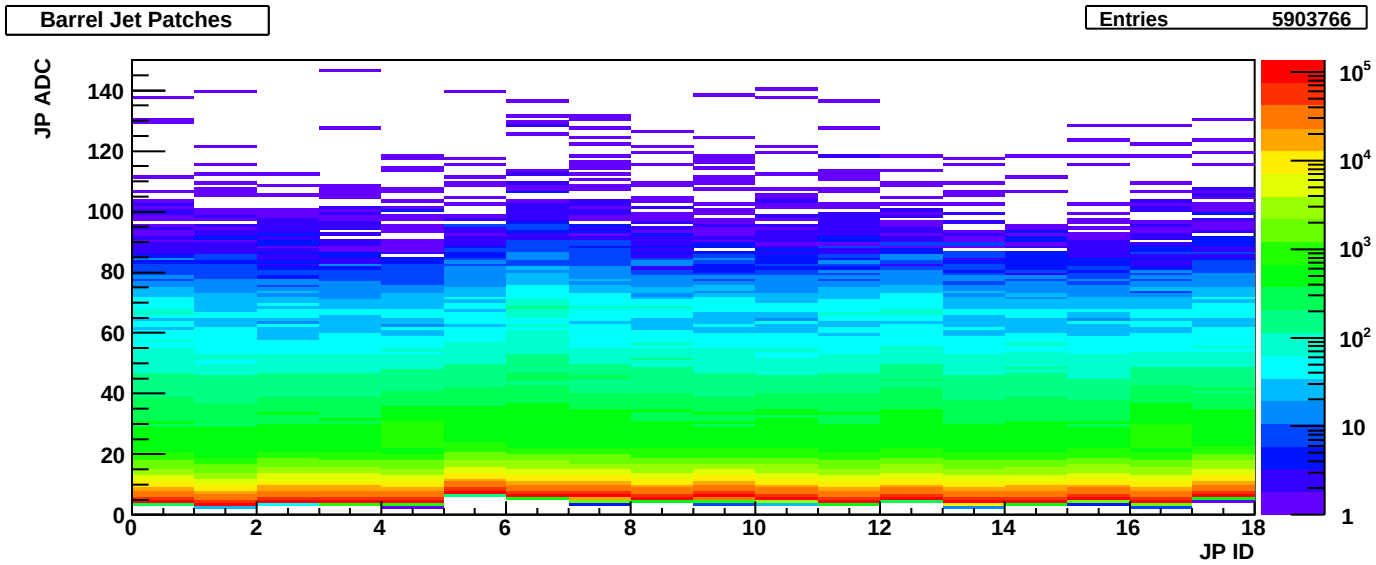

BunchId7Bit (all events)
Entries 327987

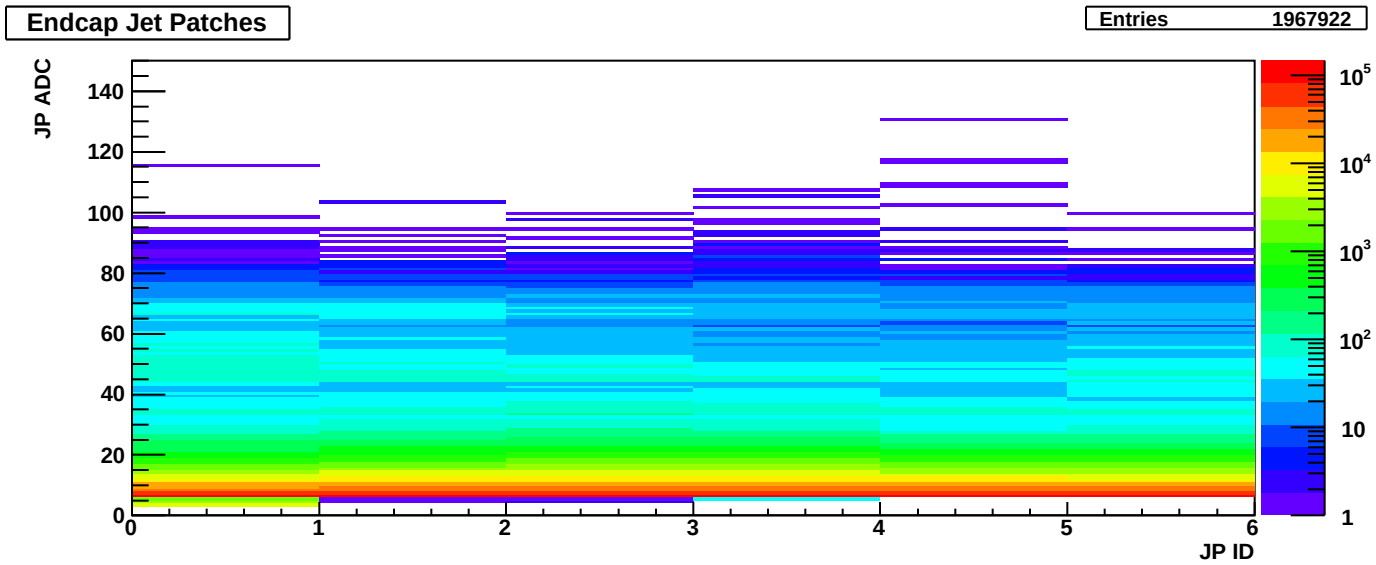

Barrel EMC L0 Input - High Tower
Entries 9.83961e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

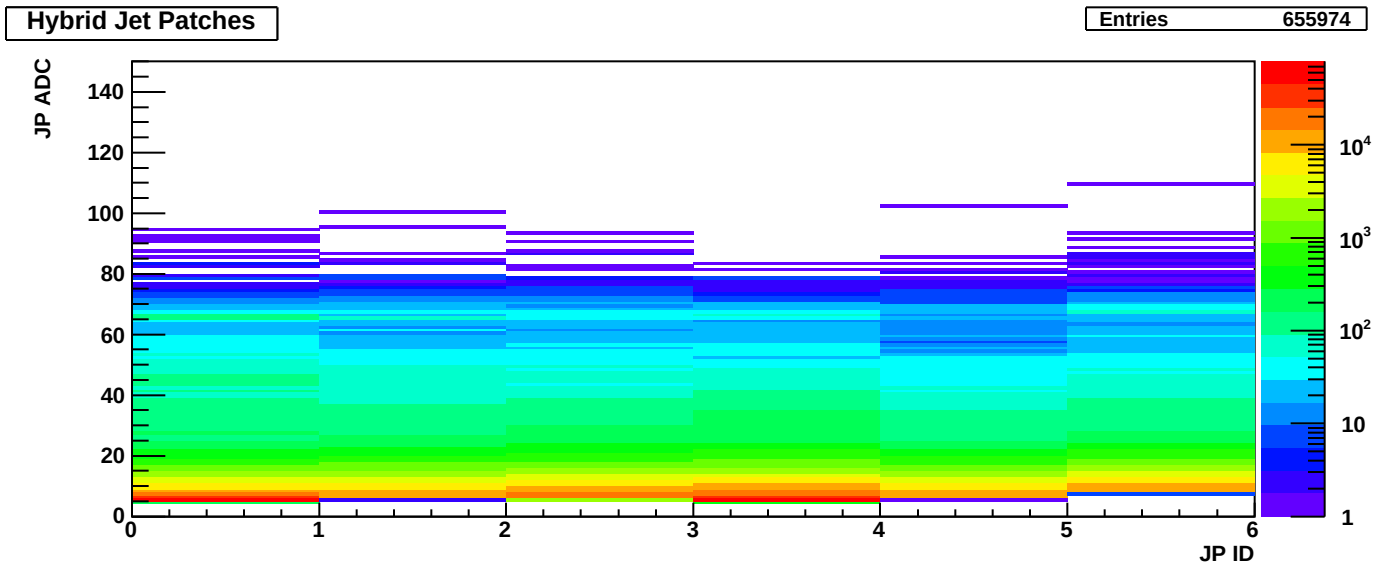

Barrel EMC L0 Input - Patch Sum
Entries 9.83961e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

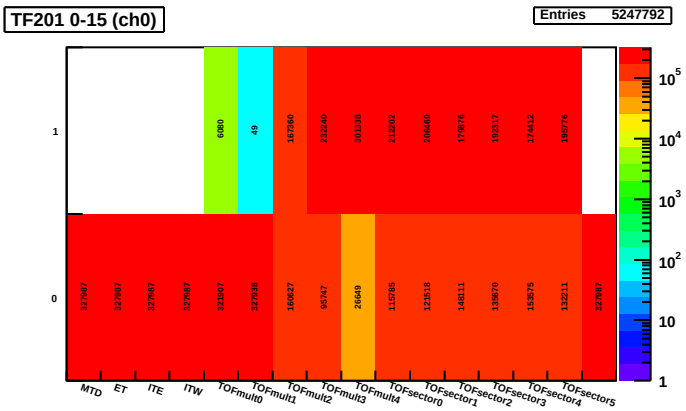

Endcap EMC L0 Input - High Tower
Entries 2.951883e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

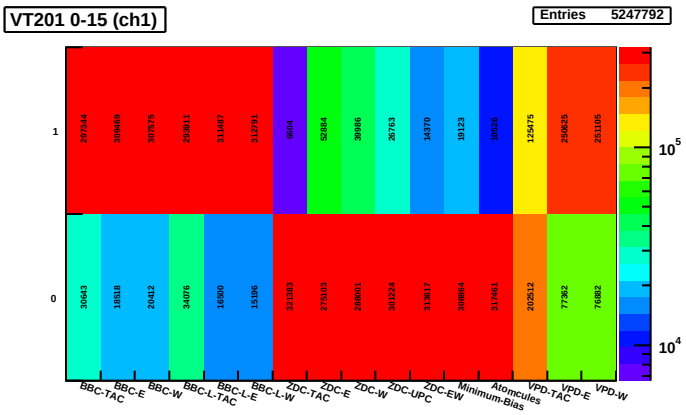

Endcap EMC L0 Input - Patch Sum
Entries 2.951883e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

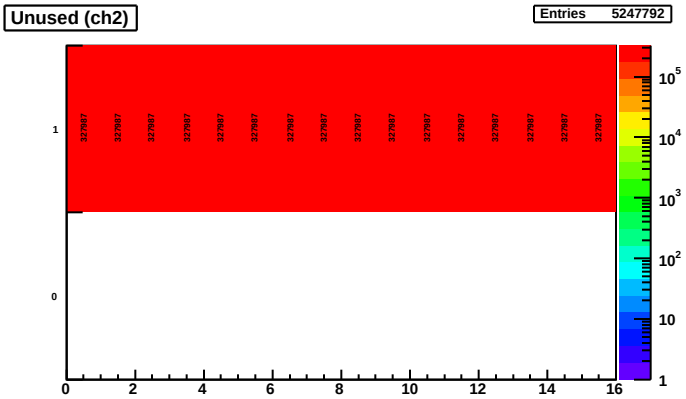

Barrel Jet Patches
Entries 5903766
JP ADC
JP ID

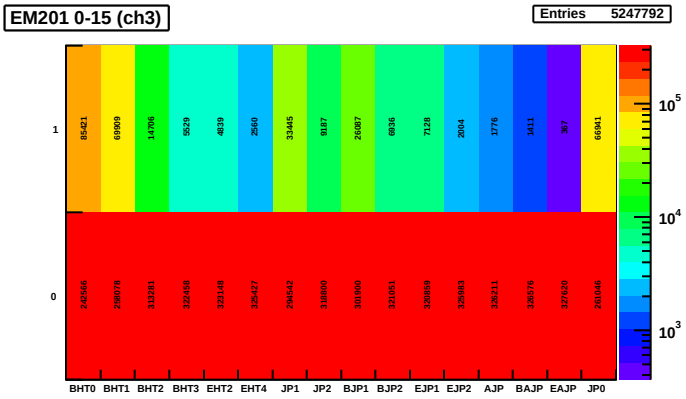

Endcap Jet Patches
Entries 1967922
JP ADC
JP ID

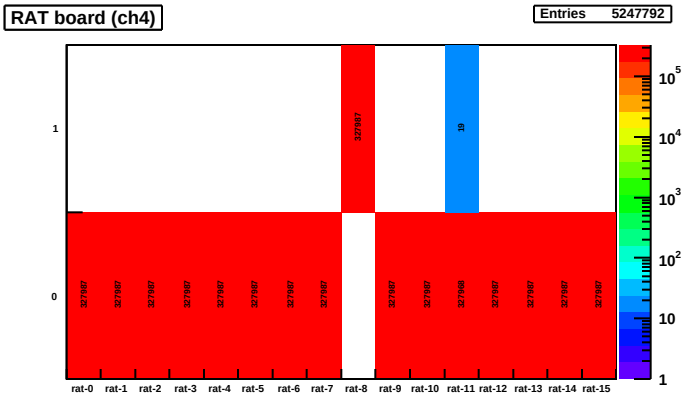

Hybrid Jet Patches
Entries 655974
JP ADC
JP ID

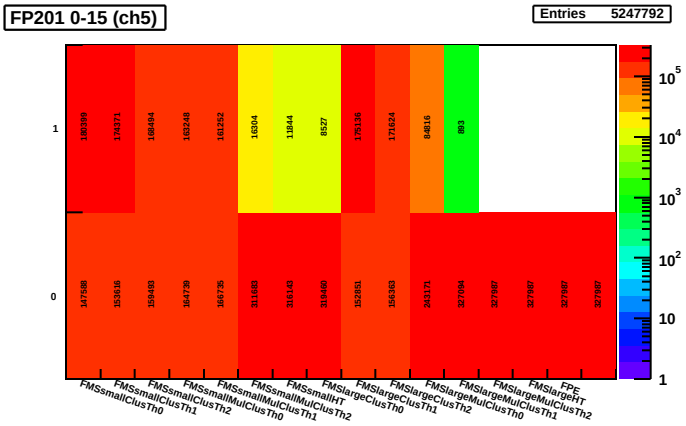

TF201 0-15 (ch0)
Entries 5247792
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

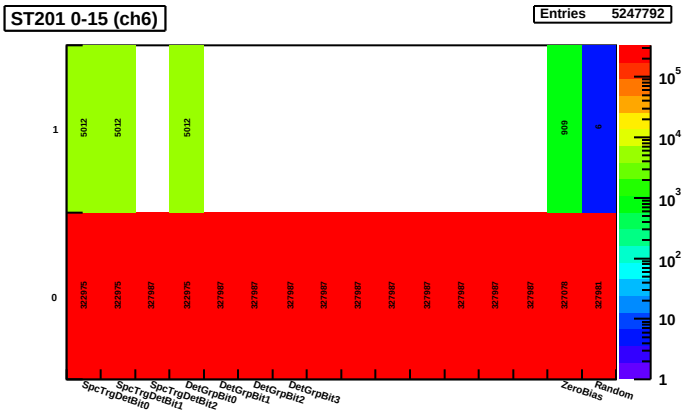

VT201 0-15 (ch1)
Entries 5247792
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

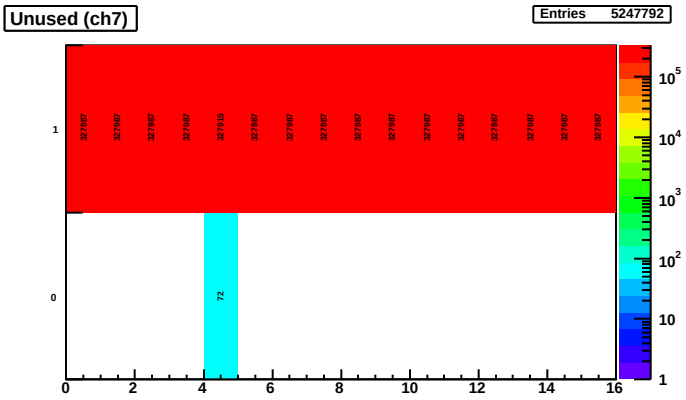

Unused (ch2)
Entries 5247792

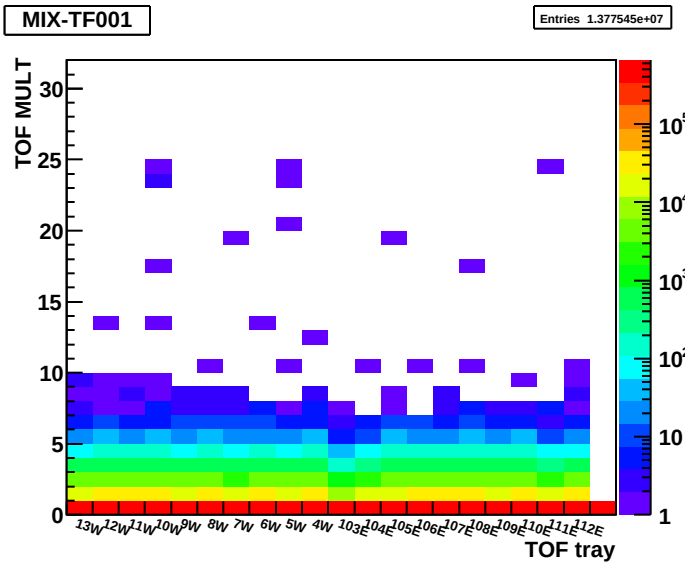

EM201 0-15 (ch3)
Entries 5247792
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

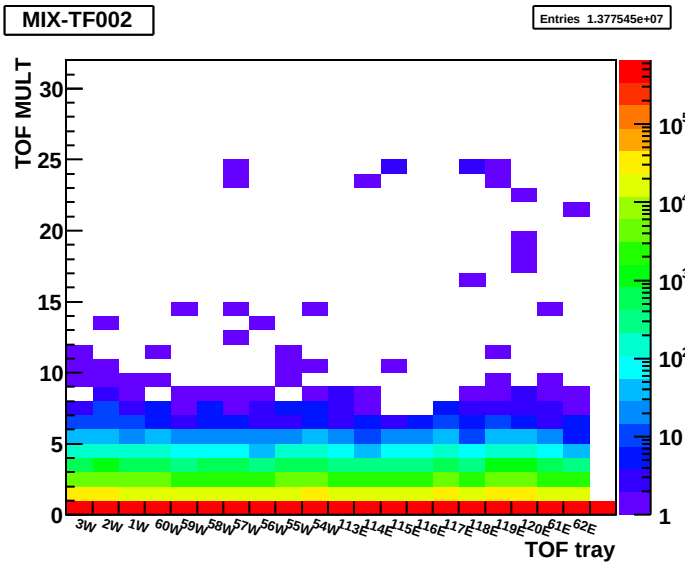

RAT board (ch4)
Entries 5247792
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

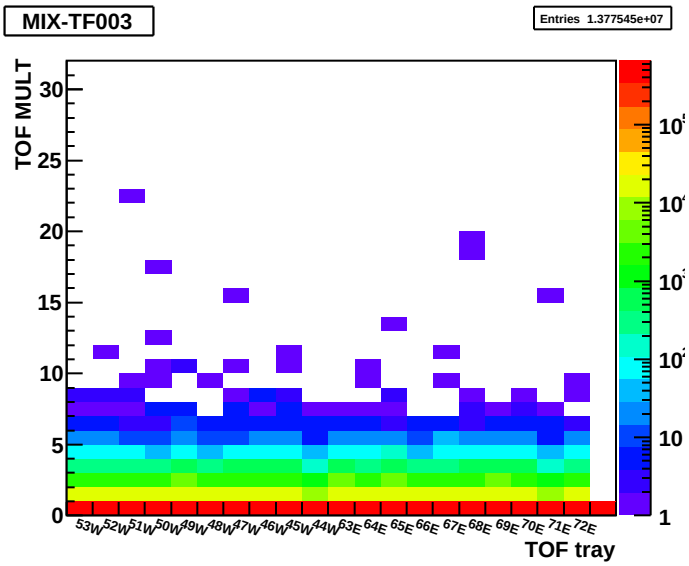

FP201 0-15 (ch5)
Entries 5247792
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

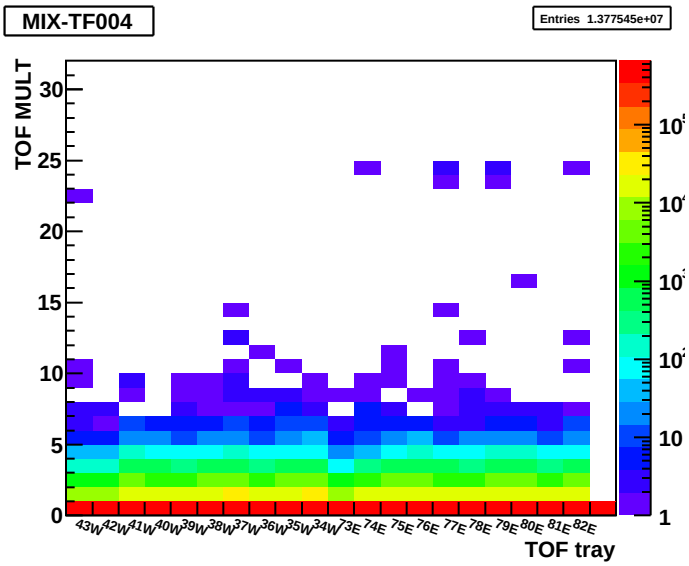

ST201 0-15 (ch6)
Entries 5247792
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

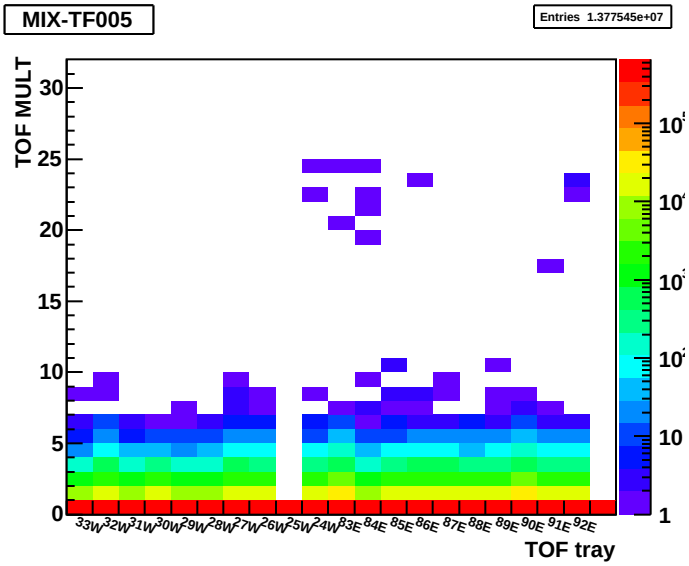

Unused (ch7)
Entries 5247792

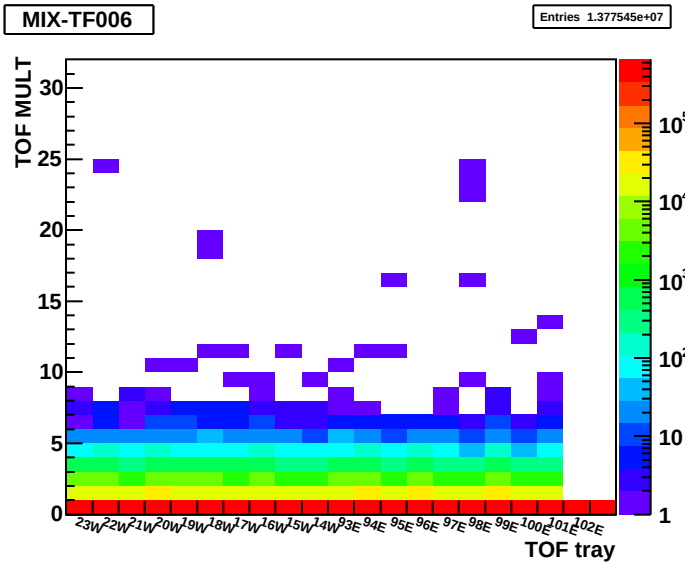

MIX-TF001
Entries 1.377545e+07
TOF MULT
TOF tray

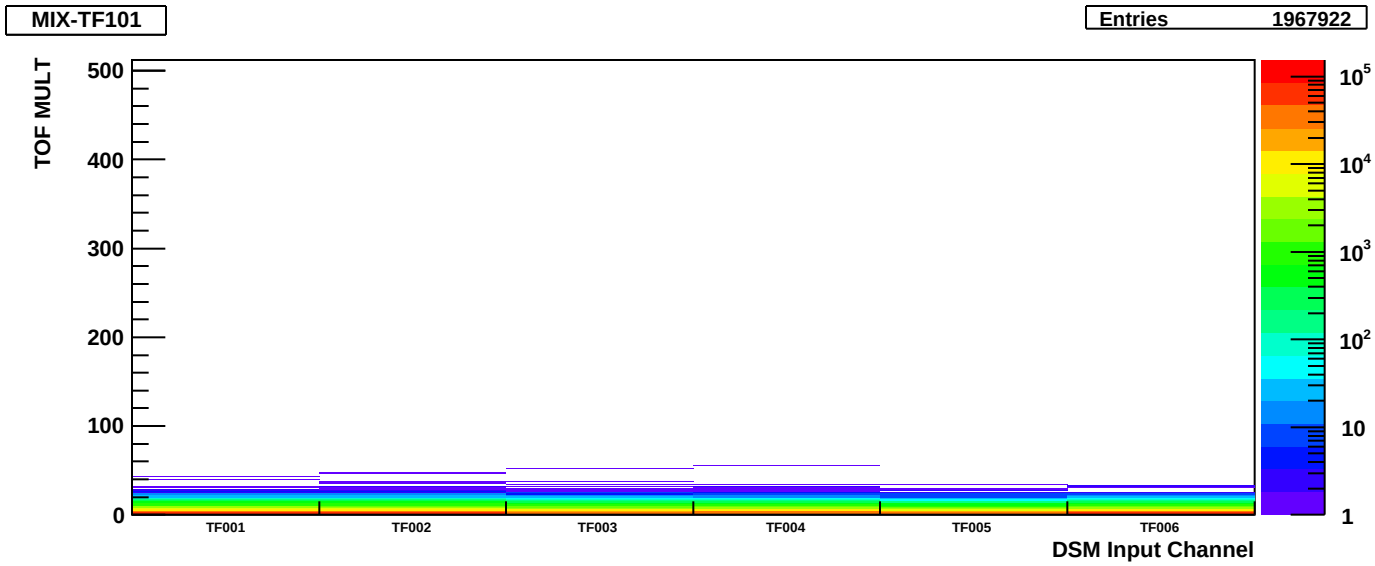

MIX-TF002
Entries 1.377545e+07
TOF MULT
TOF tray

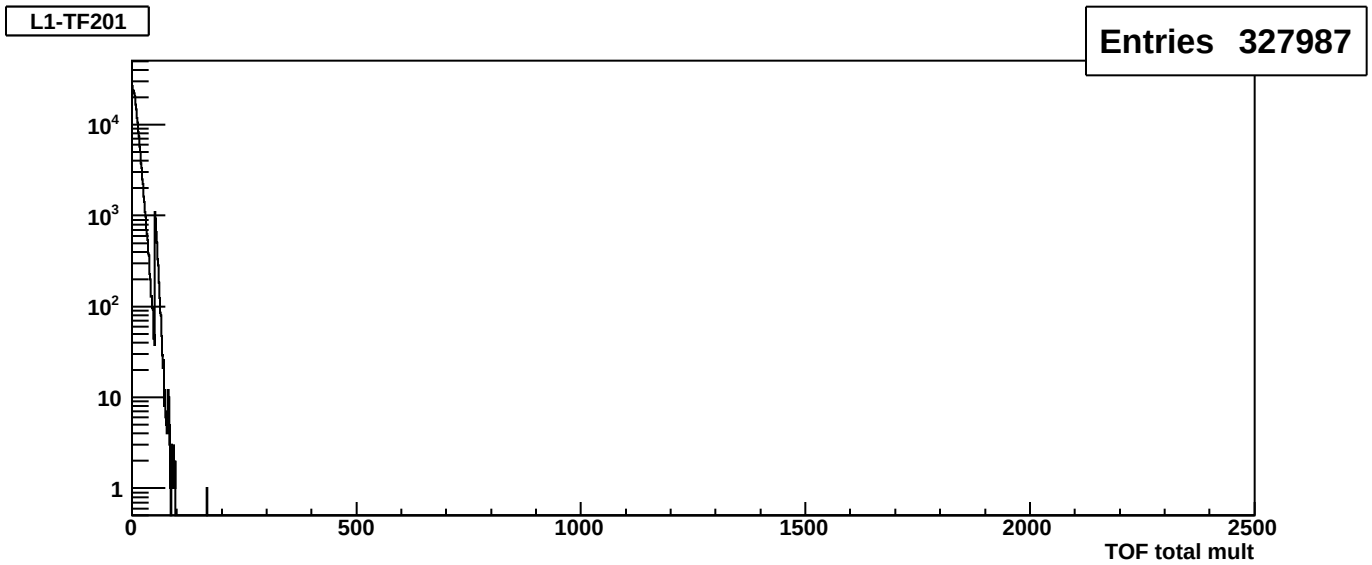

MIX-TF003
Entries 1.377545e+07
TOF MULT
TOF tray

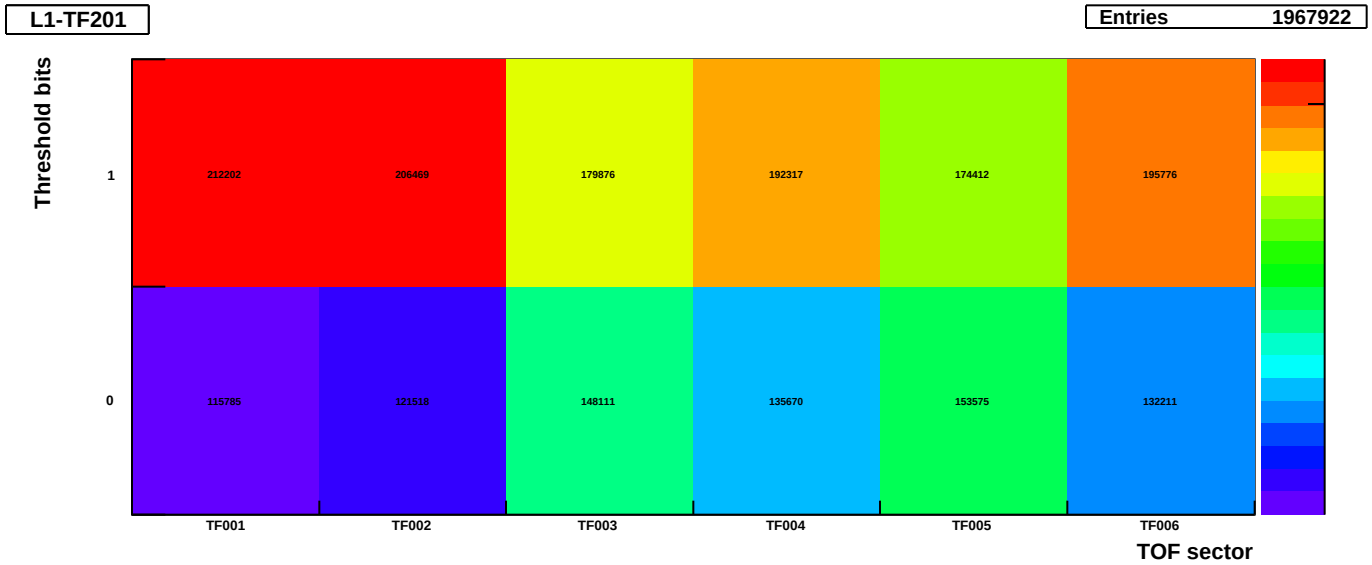

MIX-TF004
Entries 1.377545e+07
TOF MULT
TOF tray

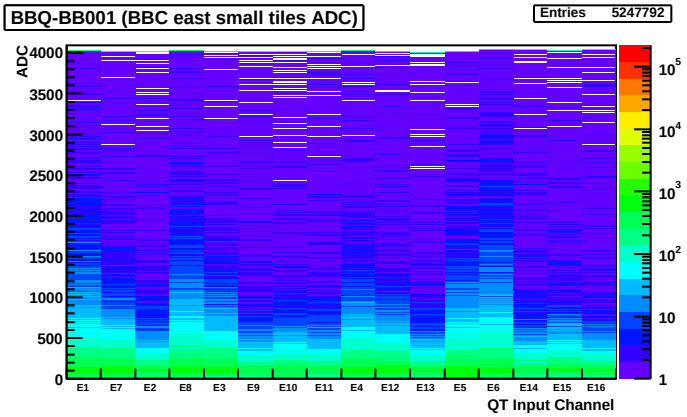

MIX-TF005
Entries 1.377545e+07
TOF MULT
TOF tray

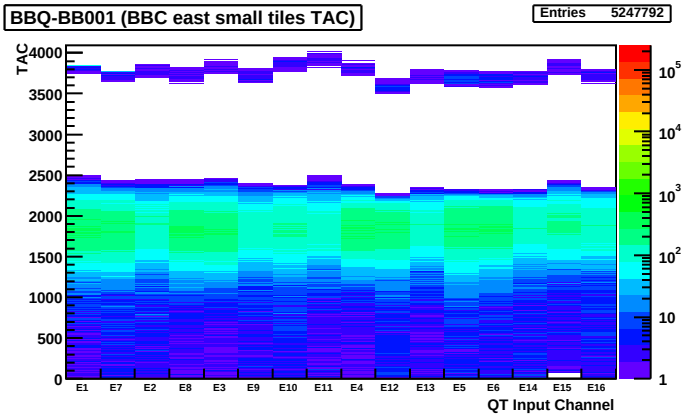

MIX-TF006
Entries 1.377545e+07
TOF MULT
TOF tray

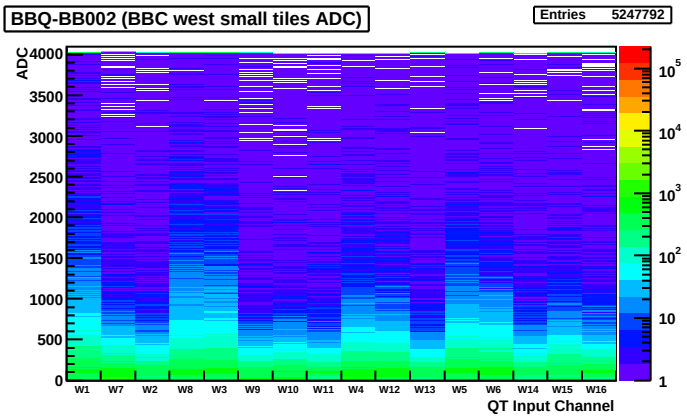

MIX-TF101
Entries 1967922
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

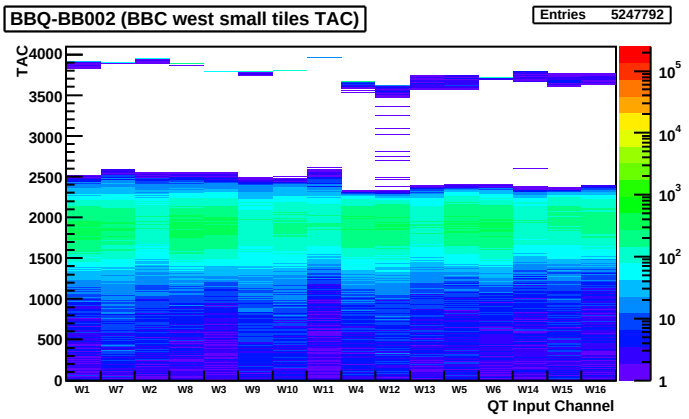

L1-TF201
Entries 327987
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

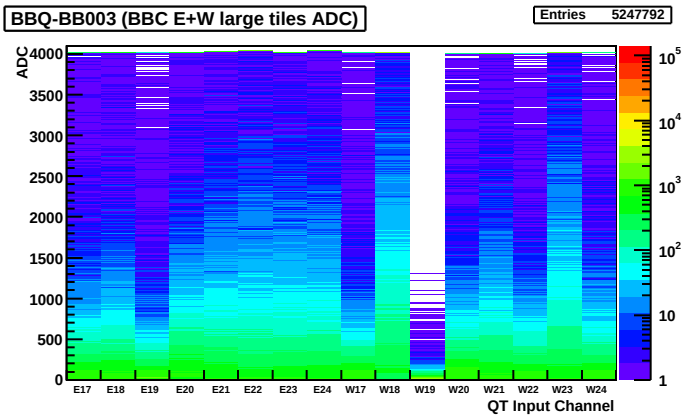

L1-TF201
Entries 1967922
Threshold bits
1
0
212202
206469
179876
192317
174412
195776
115785
121518
148111
135670
153575
132211
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

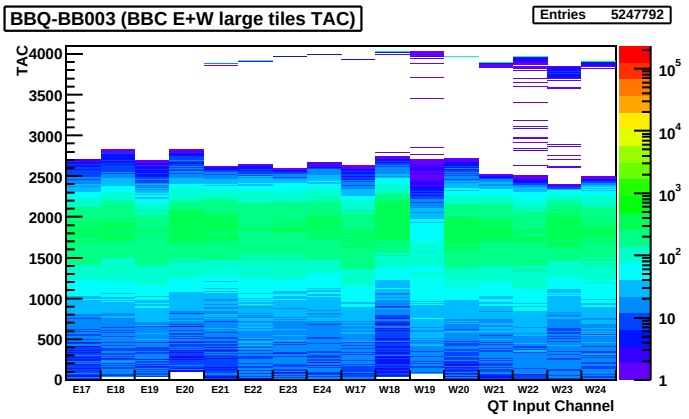

BBQ-BB001 (BBC east small tiles ADC)
Entries 5247792
ADC
QT Input Channel

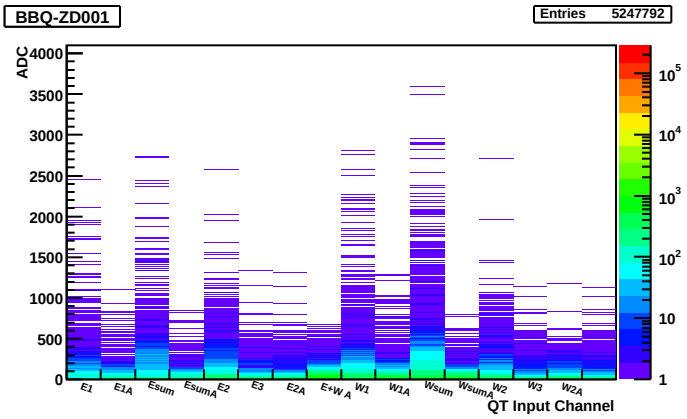

BBQ-BB001 (BBC east small tiles TAC)
Entries 5247792
TAC
QT Input Channel

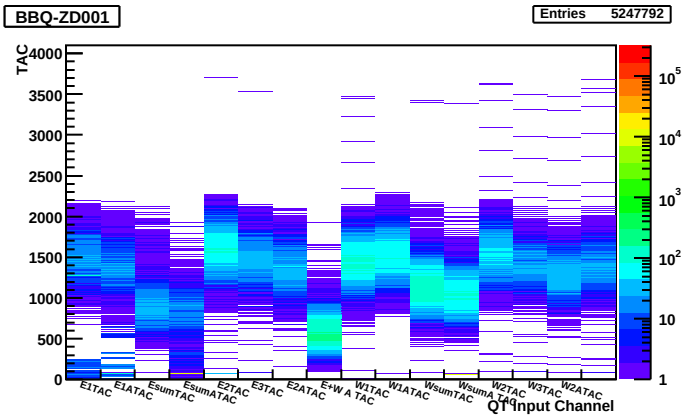

BBQ-BB002 (BBC west small tiles ADC)
Entries 5247792
ADC
QT Input Channel

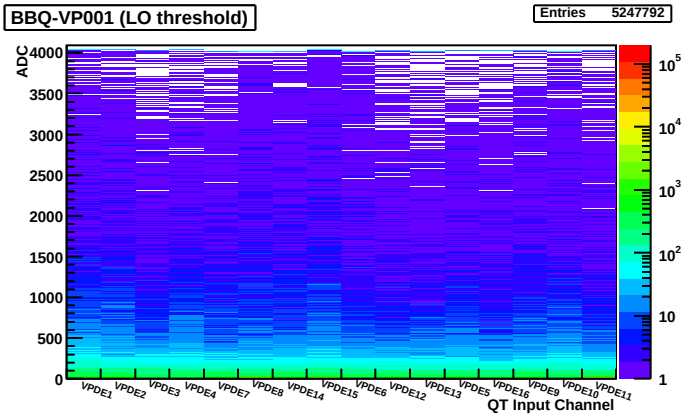

BBQ-BB002 (BBC west small tiles TAC)
Entries 5247792
TAC
QT Input Channel

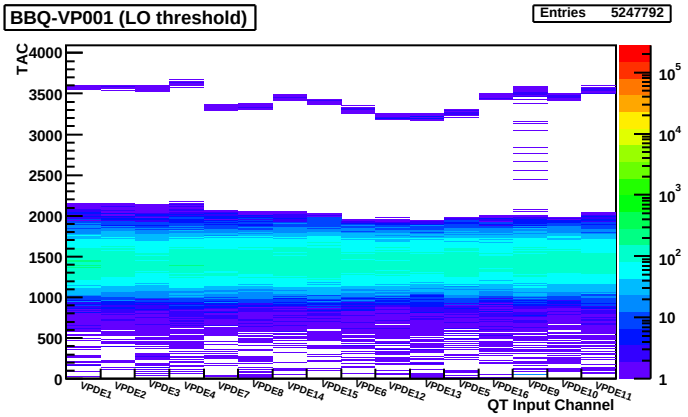

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5247792
ADC
QT Input Channel

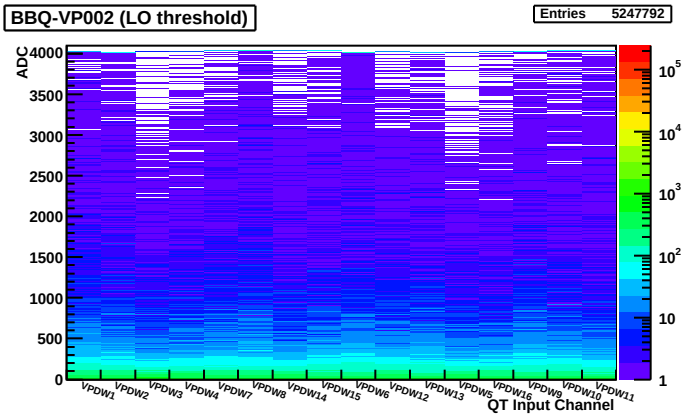

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5247792
TAC
QT Input Channel

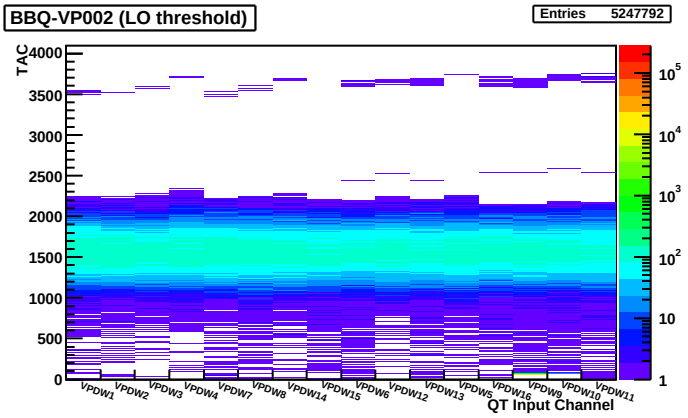

BBQ-ZD001
Entries 5247792
ADC
QT Input Channel

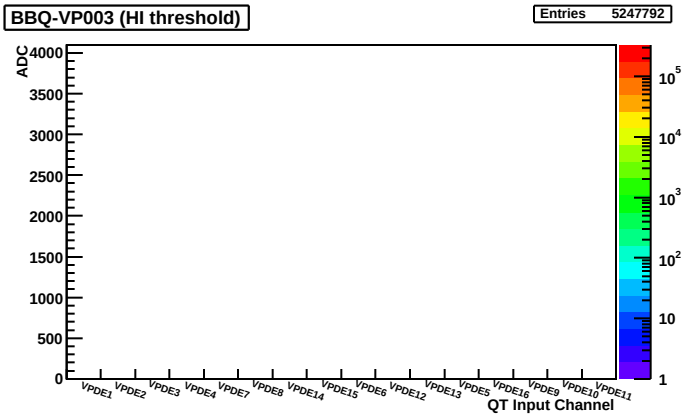

BBQ-ZD001
Entries 5247792
TAC
QT Input Channel

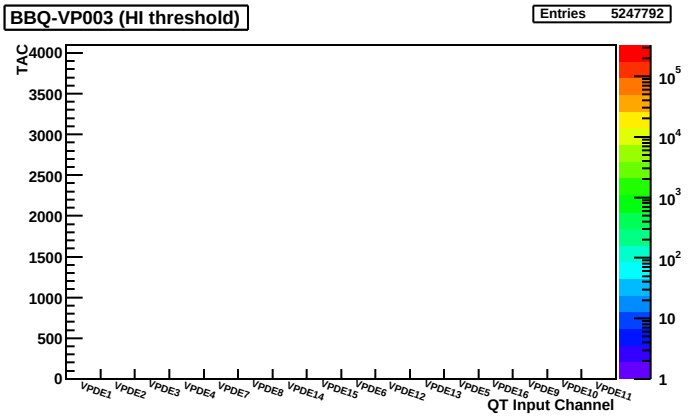

BBQ-VP001 (LO threshold)
Entries 5247792
ADC
QT Input Channel

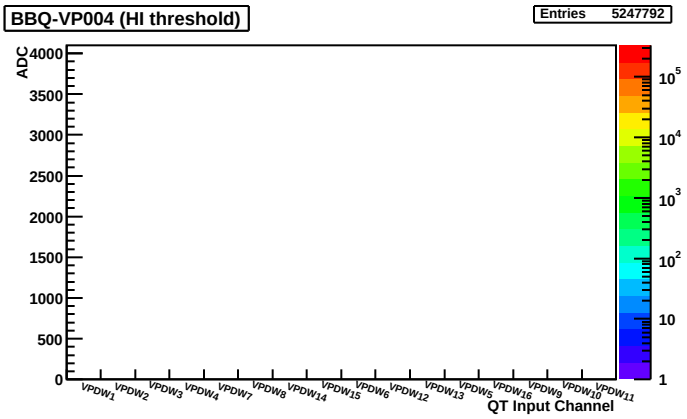

BBQ-VP001 (LO threshold)
Entries 5247792
TAC
QT Input Channel

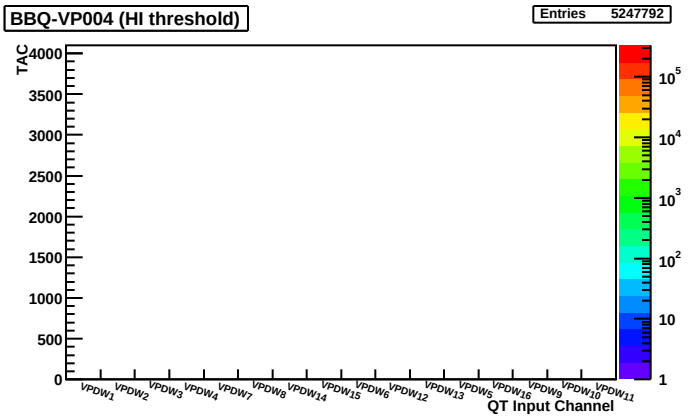

BBQ-VP002 (LO threshold)
Entries 5247792
ADC
QT Input Channel

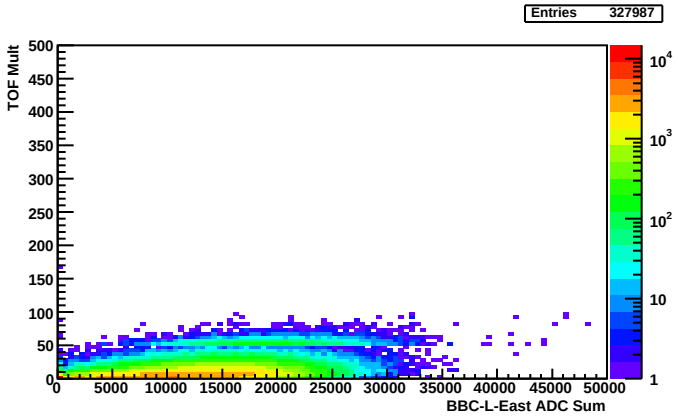

BBQ-VP002 (LO threshold)
Entries 5247792
TAC
QT Input Channel

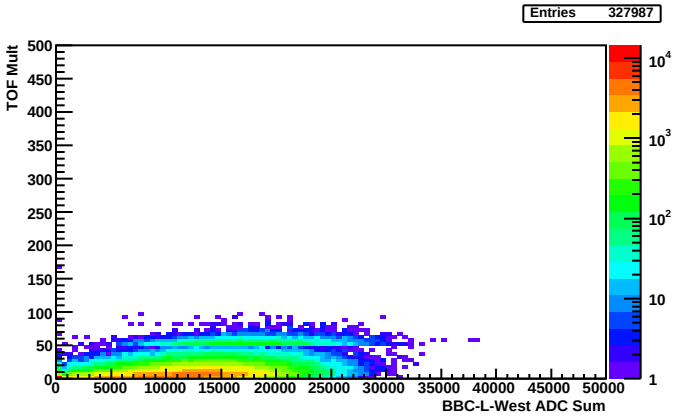

BBQ-VP003 (HI threshold)
Entries 5247792
ADC
QT Input Channel

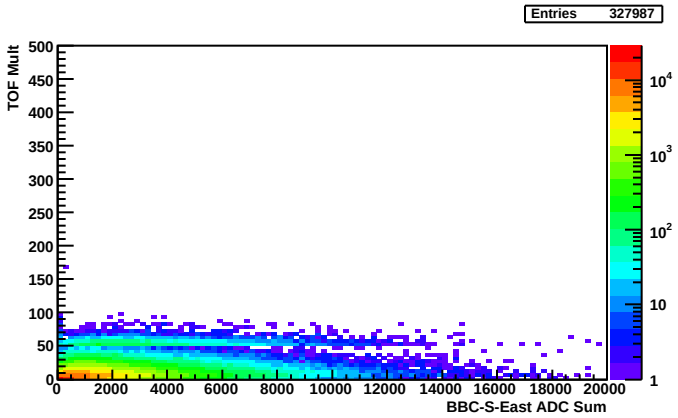

BBQ-VP003 (HI threshold)
Entries 5247792
TAC
QT Input Channel

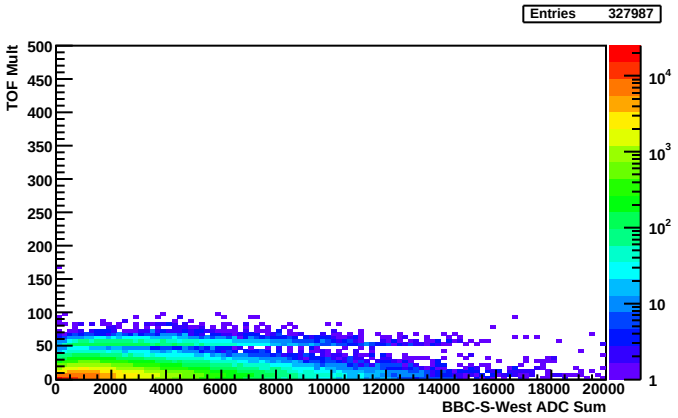

BBQ-VP004 (HI threshold)
Entries 5247792
ADC
QT Input Channel

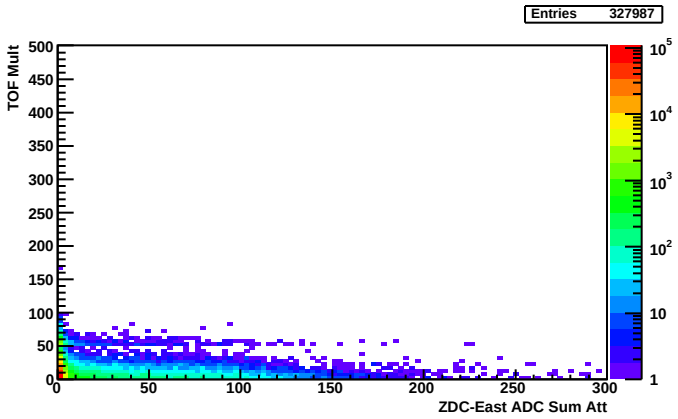

BBQ-VP004 (HI threshold)
Entries 5247792
TAC
QT Input Channel

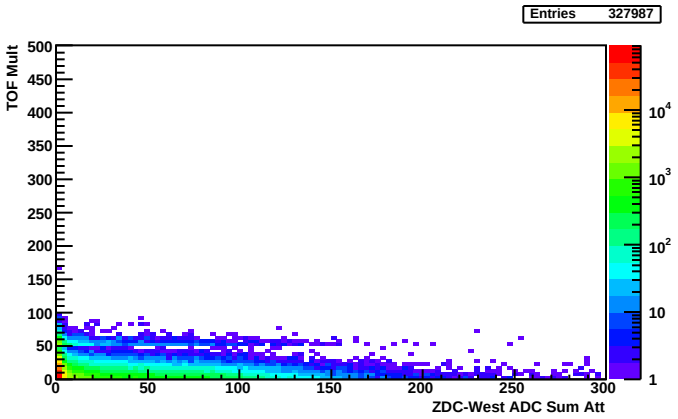

Entries 327987
TOF Mult
BBC-L-East ADC Sum

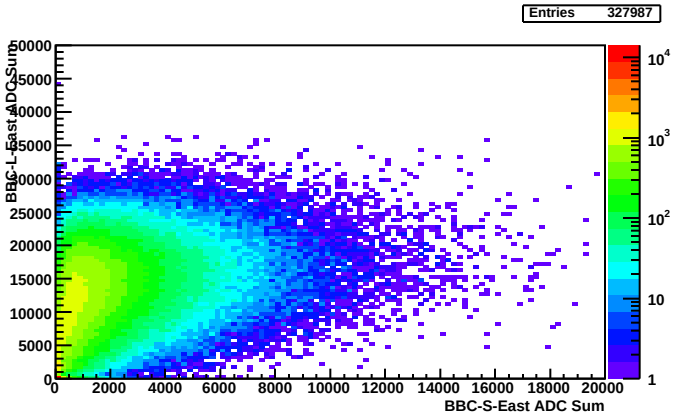

Entries 327987
TOF Mult
BBC-L-West ADC Sum

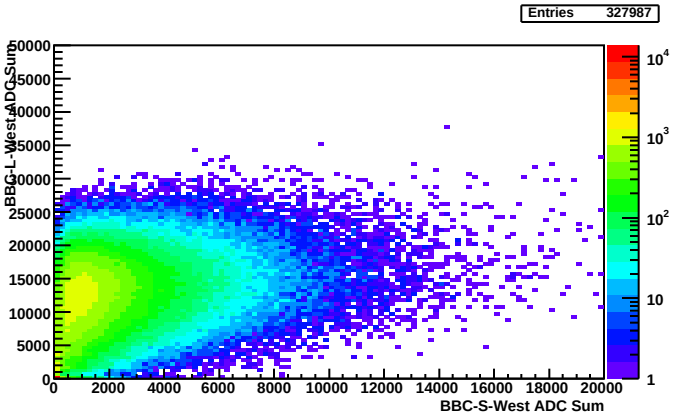

Entries 327987
TOF Mult
BBC-S-East ADC Sum

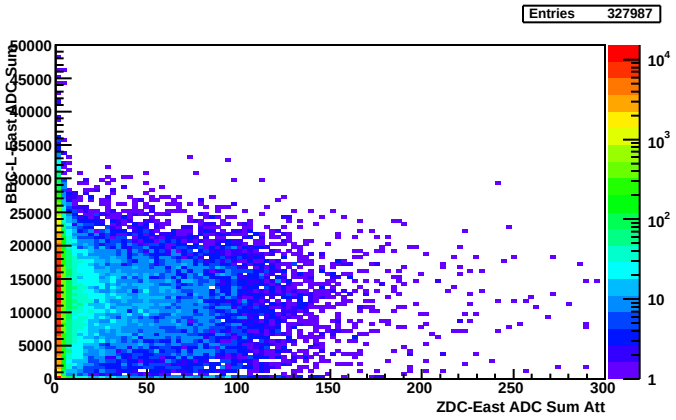

Entries 327987
TOF Mult
BBC-S-West ADC Sum

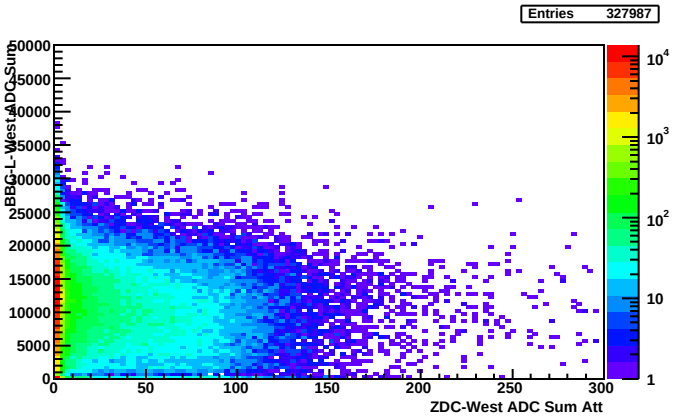

Entries 327987
TOF Mult
ZDC-East ADC Sum Att

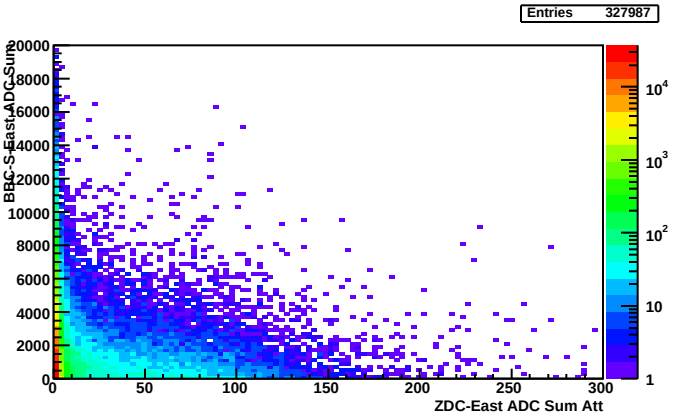

Entries 327987
TOF Mult
ZDC-West ADC Sum Att

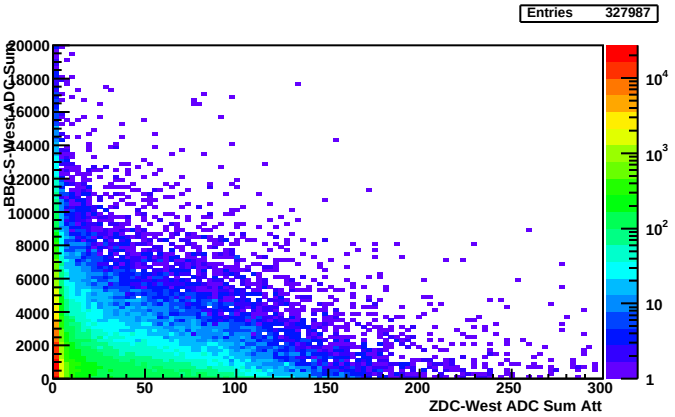

Entries 327987
BBC-L-East ADC Sum
BBC-S-East ADC Sum

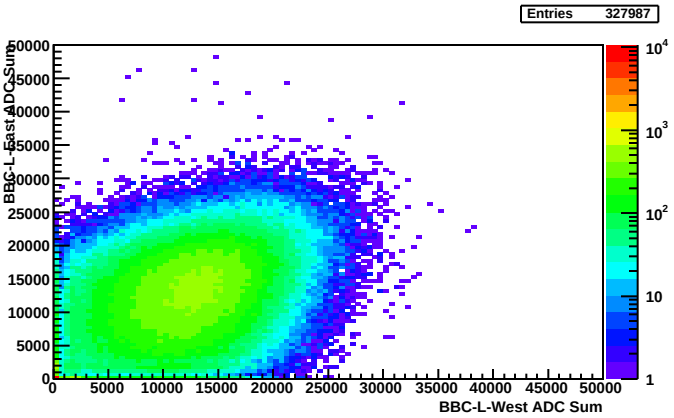

Entries 327987
BBC-L-West ADC Sum
BBC-S-West ADC Sum

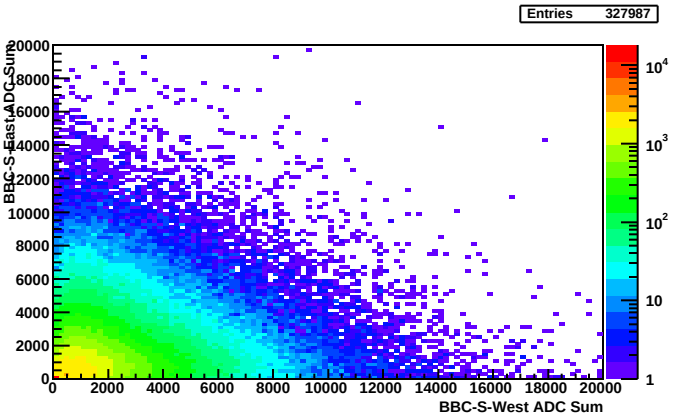

Entries 327987
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

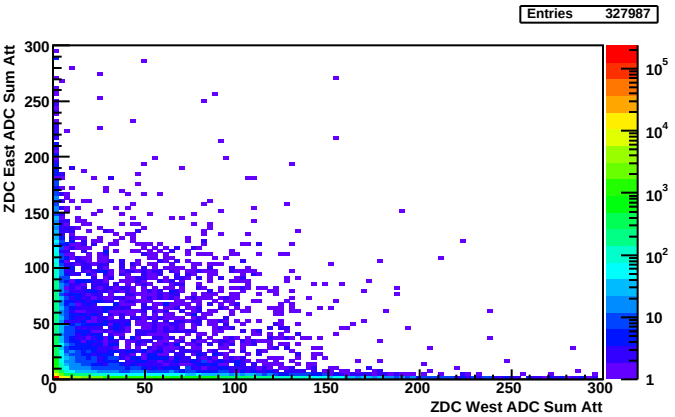

Entries 327987
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

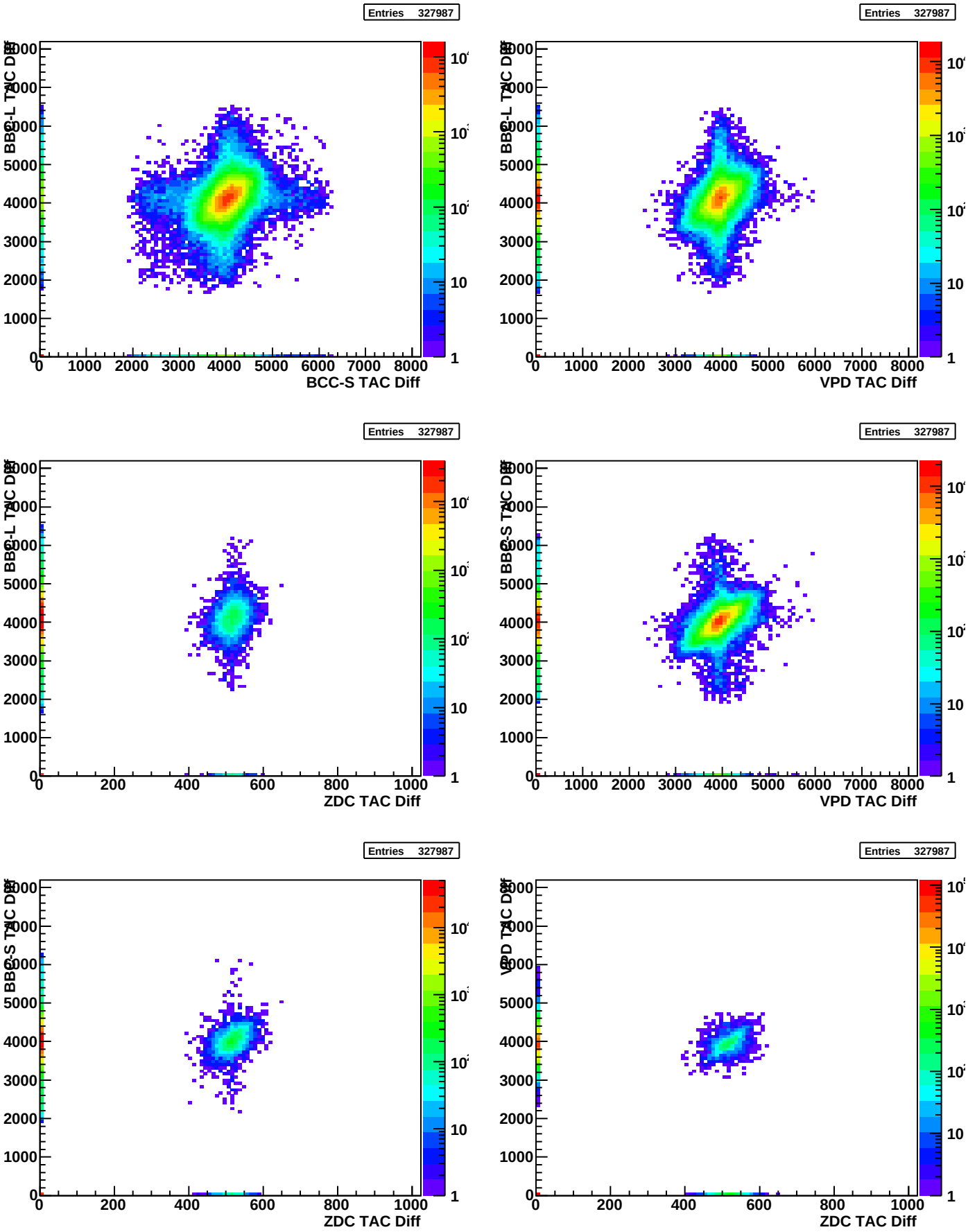

Entries 327987
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

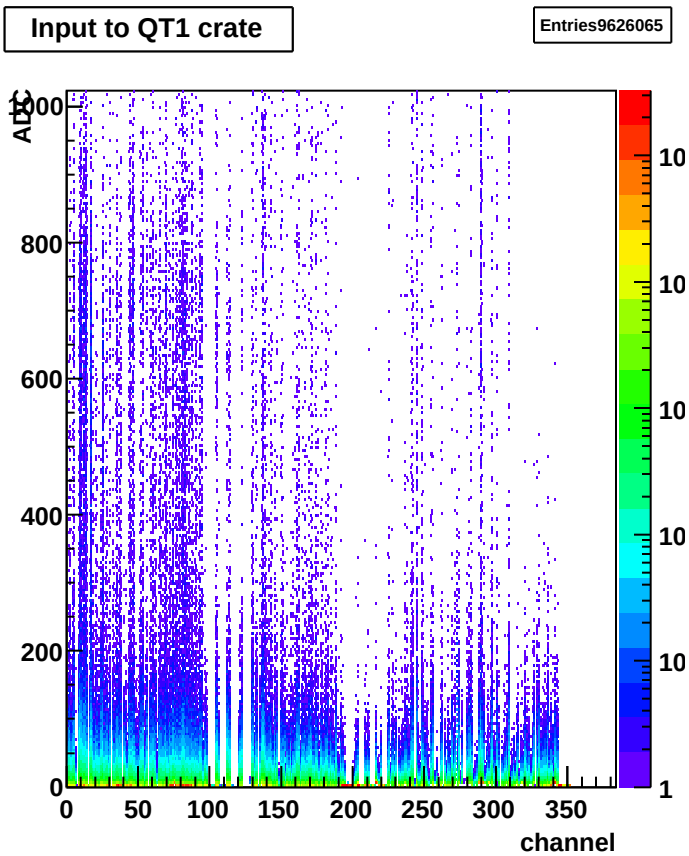

Entries 327987
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

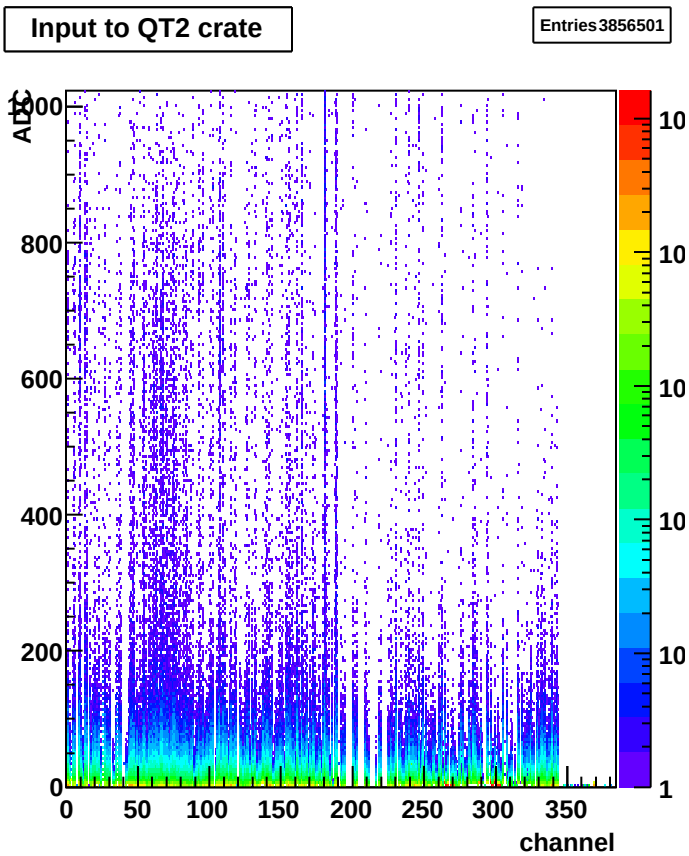

Entries 327987
BBC-L-East ADC Sum
BBC-L-West ADC Sum

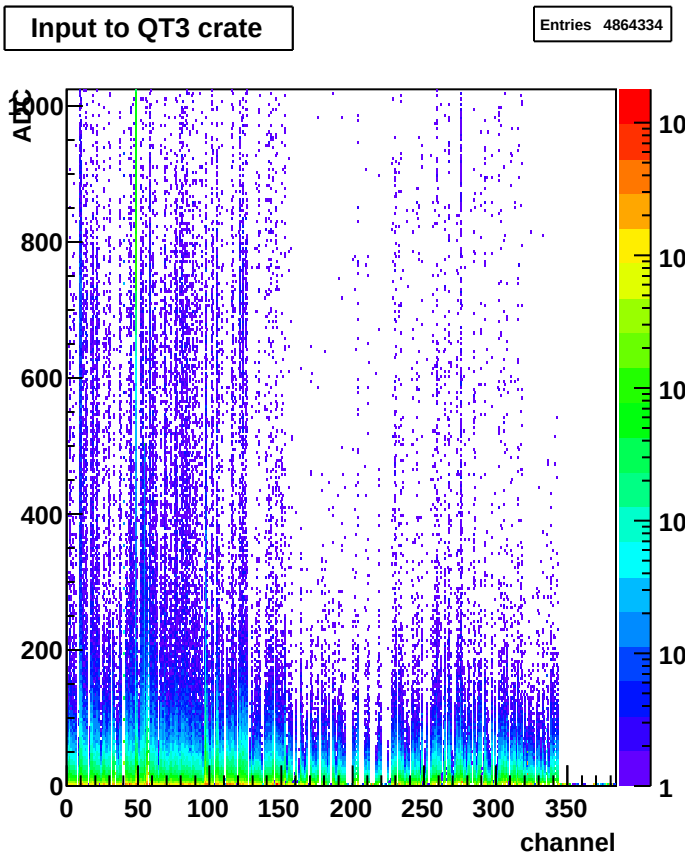

Entries 327987
BBC-S-East ADC Sum
BBC-S-West ADC Sum

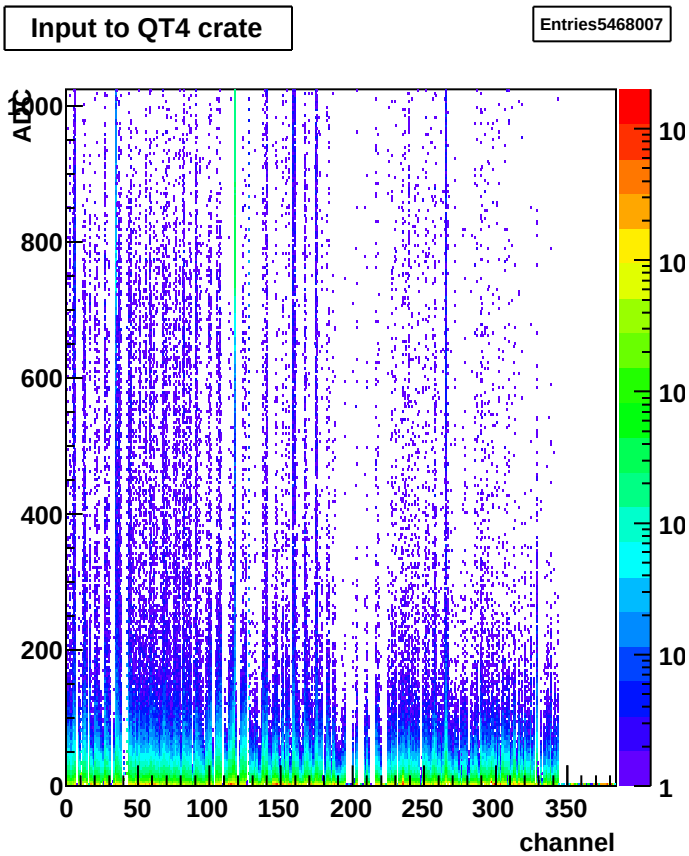

Entries 327987
ZDC East ADC Sum Att
ZDC West ADC Sum Att

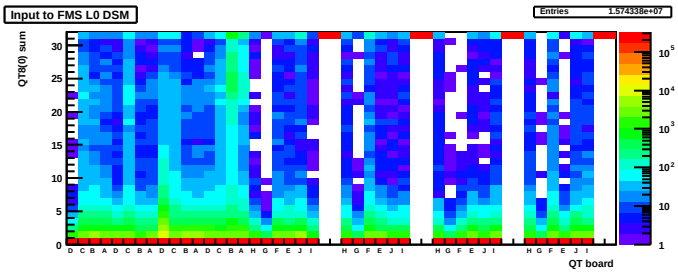

Entries 327987
BBC-L TAC Diff
BCC-S TAC Diff
Entries 327987
BBC-L TAC Diff
VPD TAC Diff
Entries 327987
BBC-L TAC Diff
ZDC TAC Diff
Entries 327987
BBC-S TAC Diff
VPD TAC Diff
Entries 327987
BBC-S TAC Diff
ZDC TAC Diff
Entries 327987
VPD TAC Diff
ZDC TAC Diff

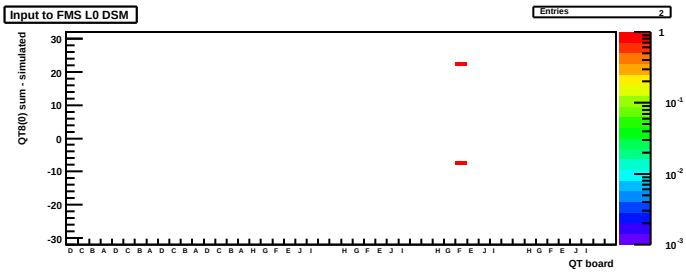

Input to QT1 crate
Entries9626065
ADC
channel

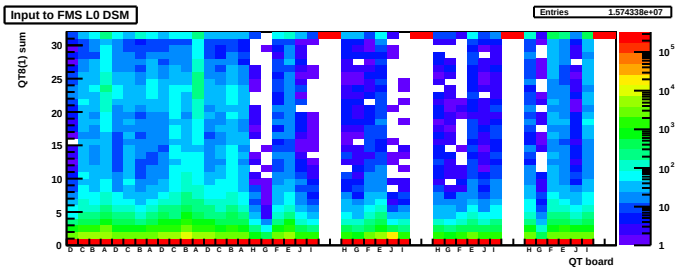

Input to QT2 crate
Entries3856501
ADC
channel

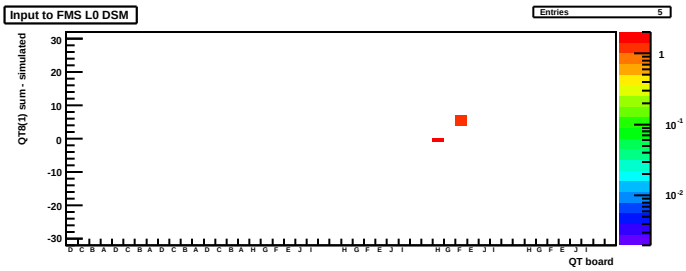

Input to QT3 crate
Entries 4864334
ADC
channel

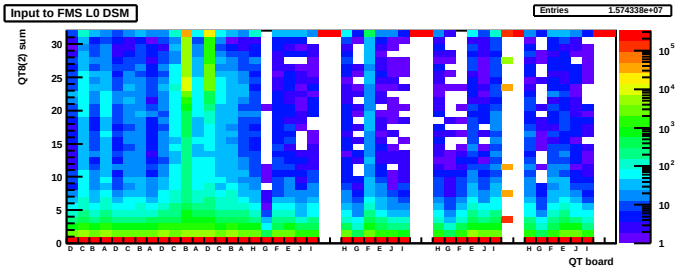

Input to QT4 crate
Entries5468007
ADC
channel

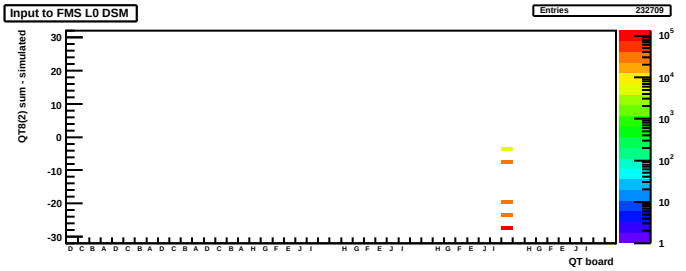

Input to FMS L0 DSM
Entries 1.574338e+07
QT8(0) sum
QT board

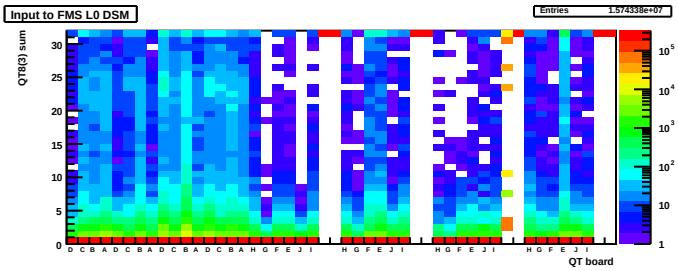

Input to FMS L0 DSM
Entries 2
QT8(0) sum - simulated
QT board

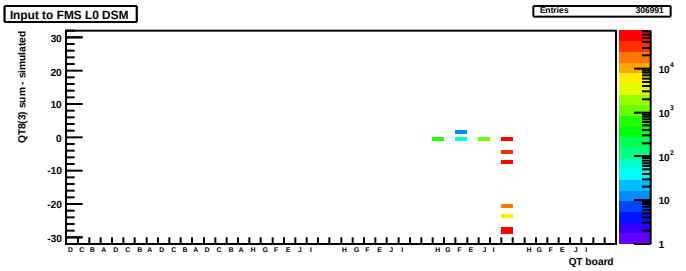

Input to FMS L0 DSM
Entries 1.574338e+07
QT8(1) sum
QT board

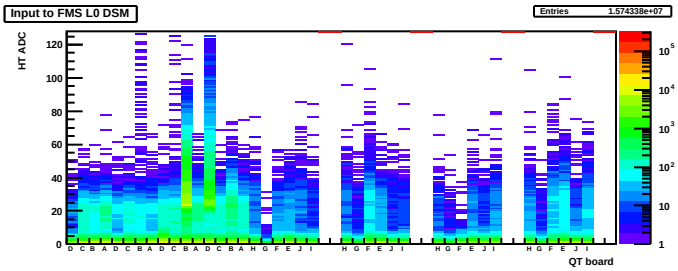

Input to FMS L0 DSM
Entries 5
QT8(1) sum - simulated
QT board

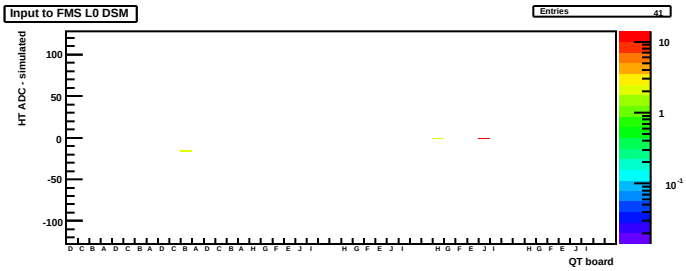

Input to FMS L0 DSM
Entries 1.574338e+07
QT8(2) sum
QT board

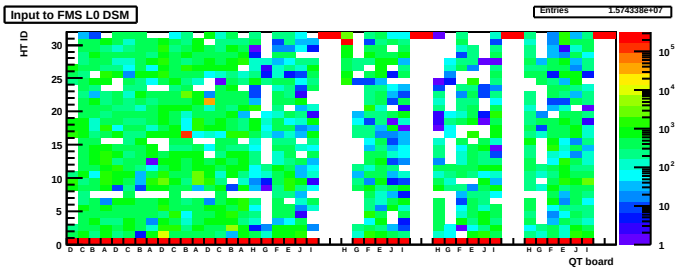

Input to FMS L0 DSM
Entries 232709
QT8(2) sum - simulated
QT board

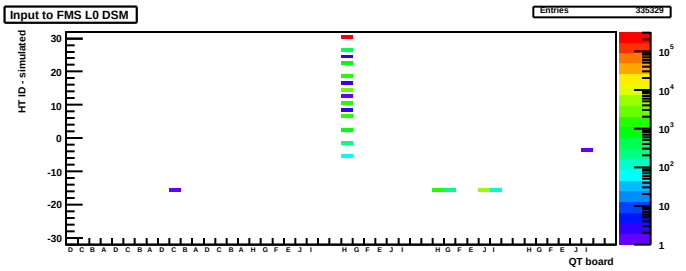

Input to FMS L0 DSM
Entries 1.574338e+07
QT8(3) sum
QT board

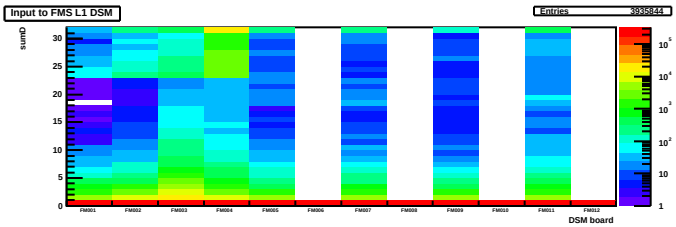

Input to FMS L0 DSM
Entries 306991
QT8(3) sum - simulated
QT board

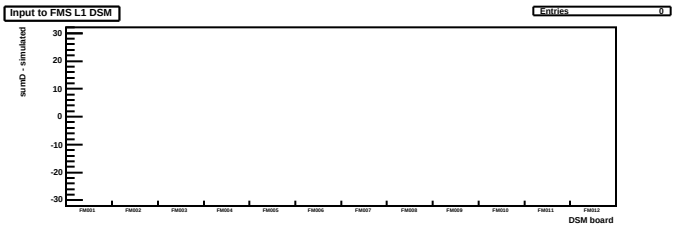

Input to FMS L0 DSM
Entries 1.574338e+07
HT ADC
QT board

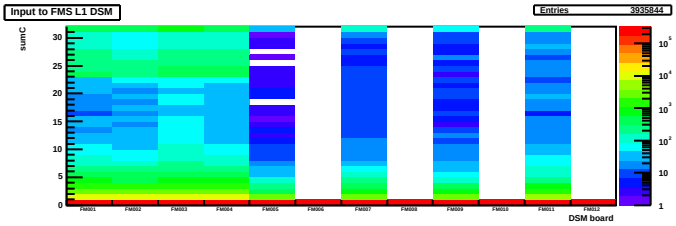

Input to FMS L0 DSM
Entries 41
HT ADC - simulated
QT board

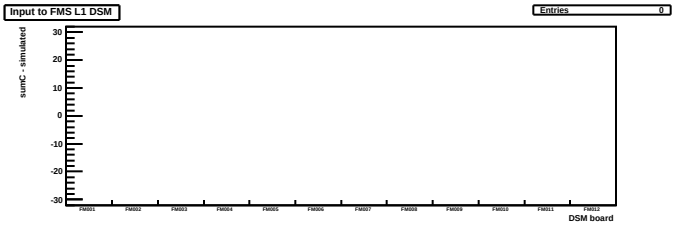

Input to FMS L0 DSM
Entries 1.574338e+07
HT ID
QT board

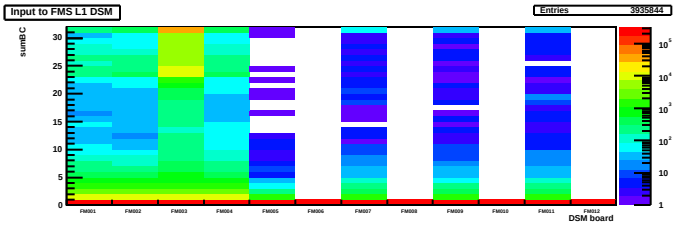

Input to FMS L0 DSM
Entries 335329
HT ID - simulated
QT board

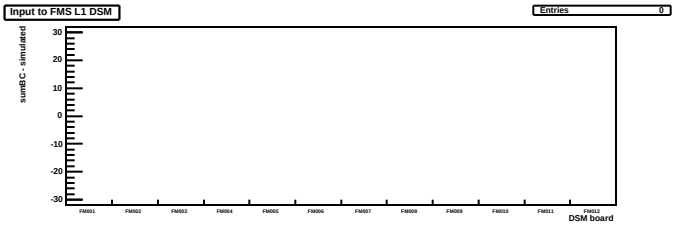

Input to FMS L1 DSM
Entries 3935844
sumD
DSM board

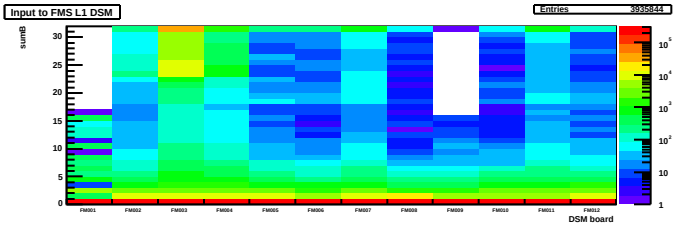

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

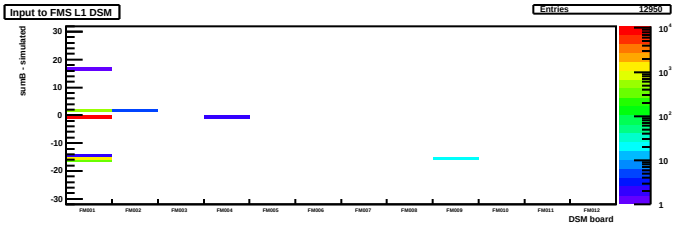

Input to FMS L1 DSM
Entries 3935844
sumC
DSM board

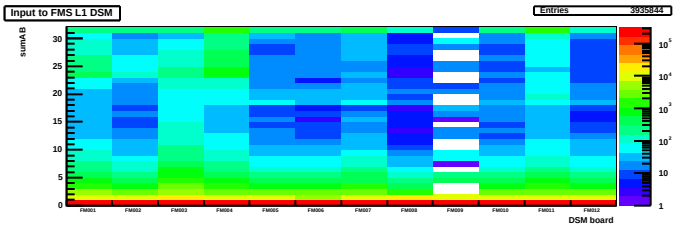

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

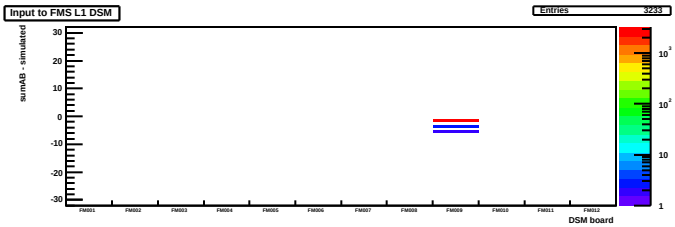

Input to FMS L1 DSM
Entries 3935844
sumBC
DSM board

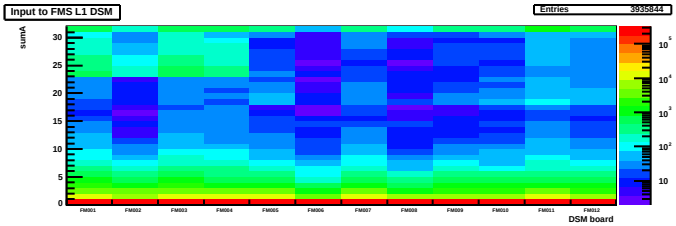

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

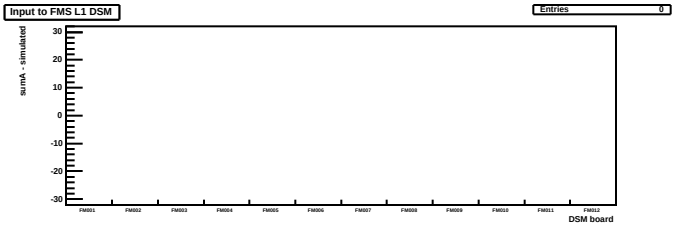

Input to FMS L1 DSM
Entries 3935844
sumB
DSM board

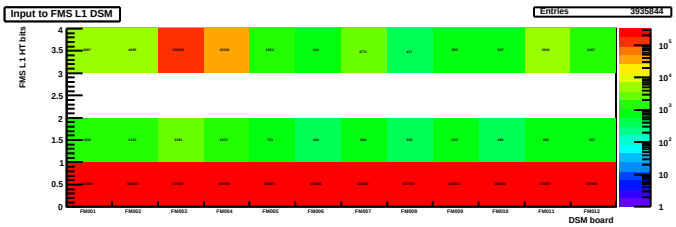

Input to FMS L1 DSM
Entries 12950
sumB - simulated
DSM board

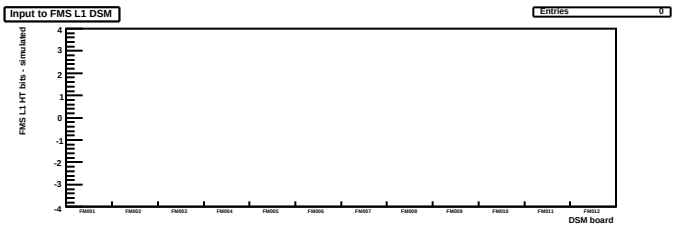

Input to FMS L1 DSM
Entries 3935844
sumAB
DSM board

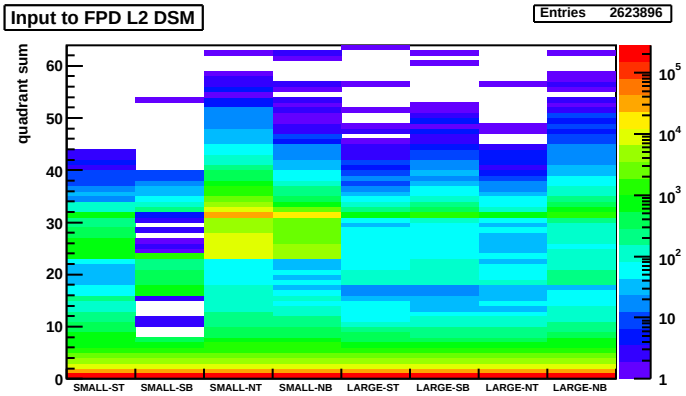

Input to FMS L1 DSM
Entries 3233
sumAB - simulated
DSM board

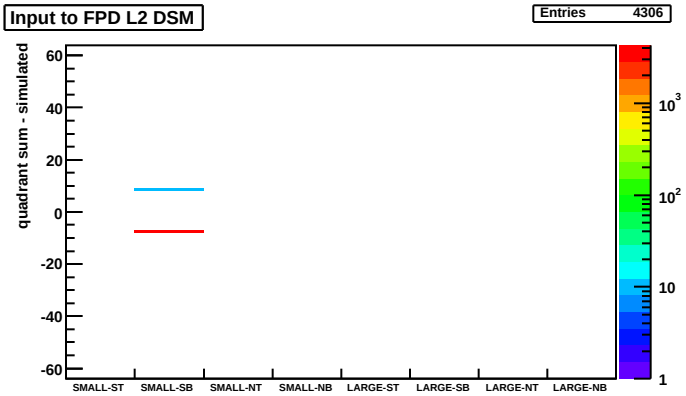

Input to FMS L1 DSM
Entries 3935844
sumA
DSM board

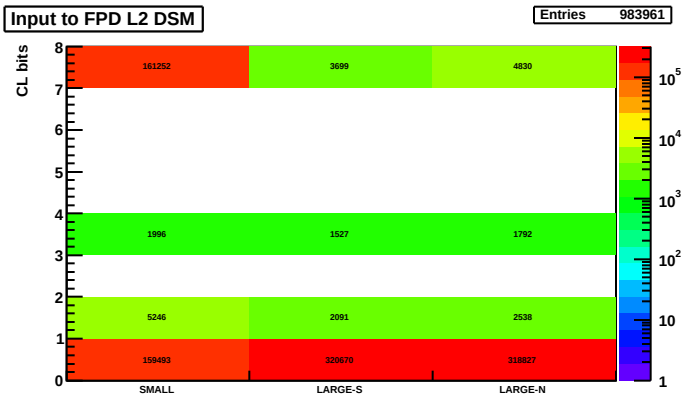

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

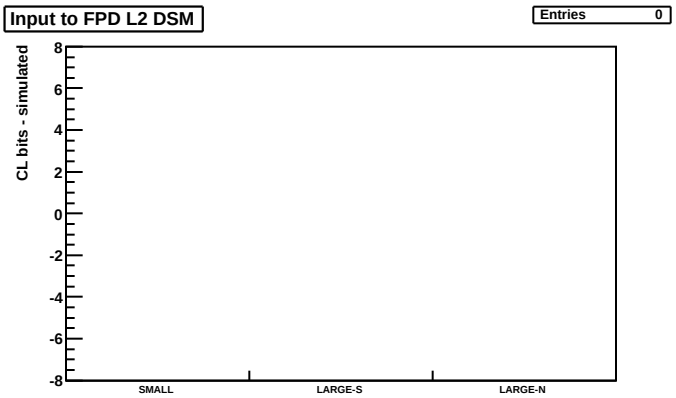

Input to FMS L1 DSM
Entries 3935844
FMS L1 HT bits
DSM board

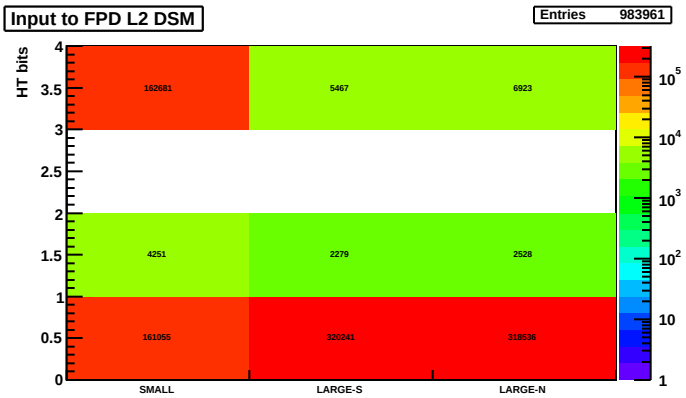

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

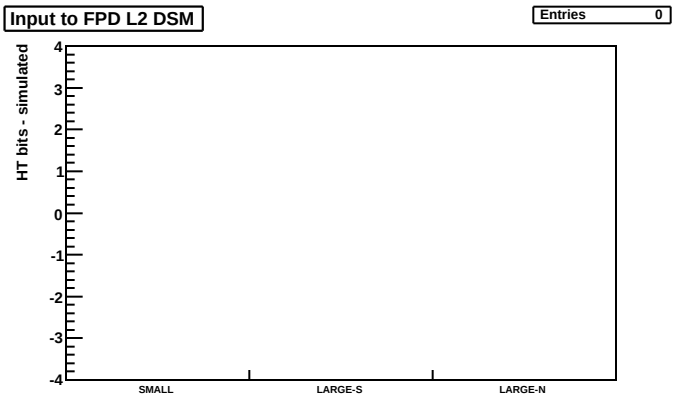

Input to FPD L2 DSM
Entries 2623896
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

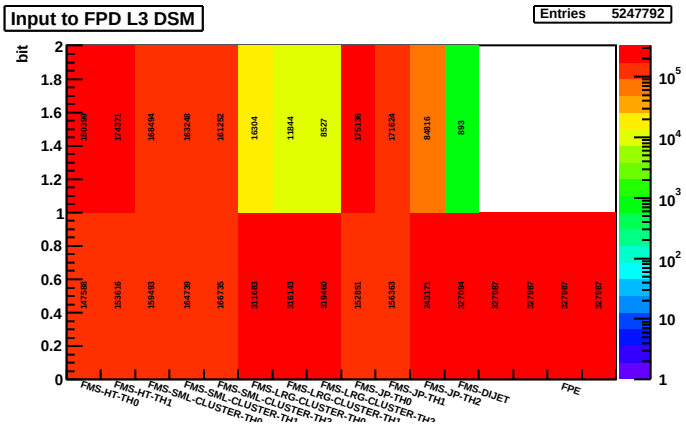

Input to FPD L2 DSM
Entries 4306
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

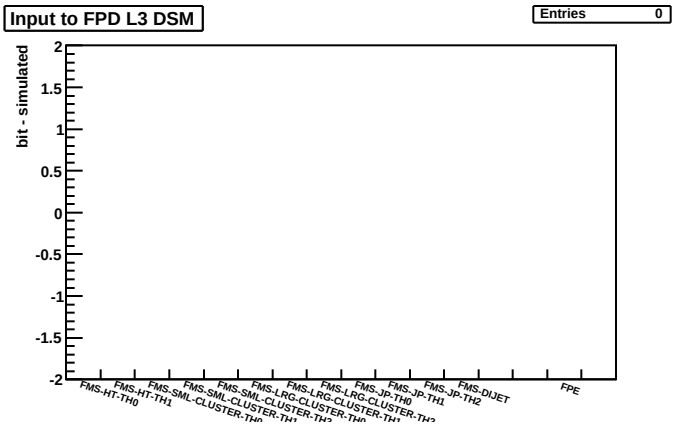

Input to FPD L2 DSM
Entries 983961
CL bits
161252
3699
4830
1996
1527
1792
5246
2091
2538
159493
320670
318827
SMALL
LARGE-S
LARGE-N

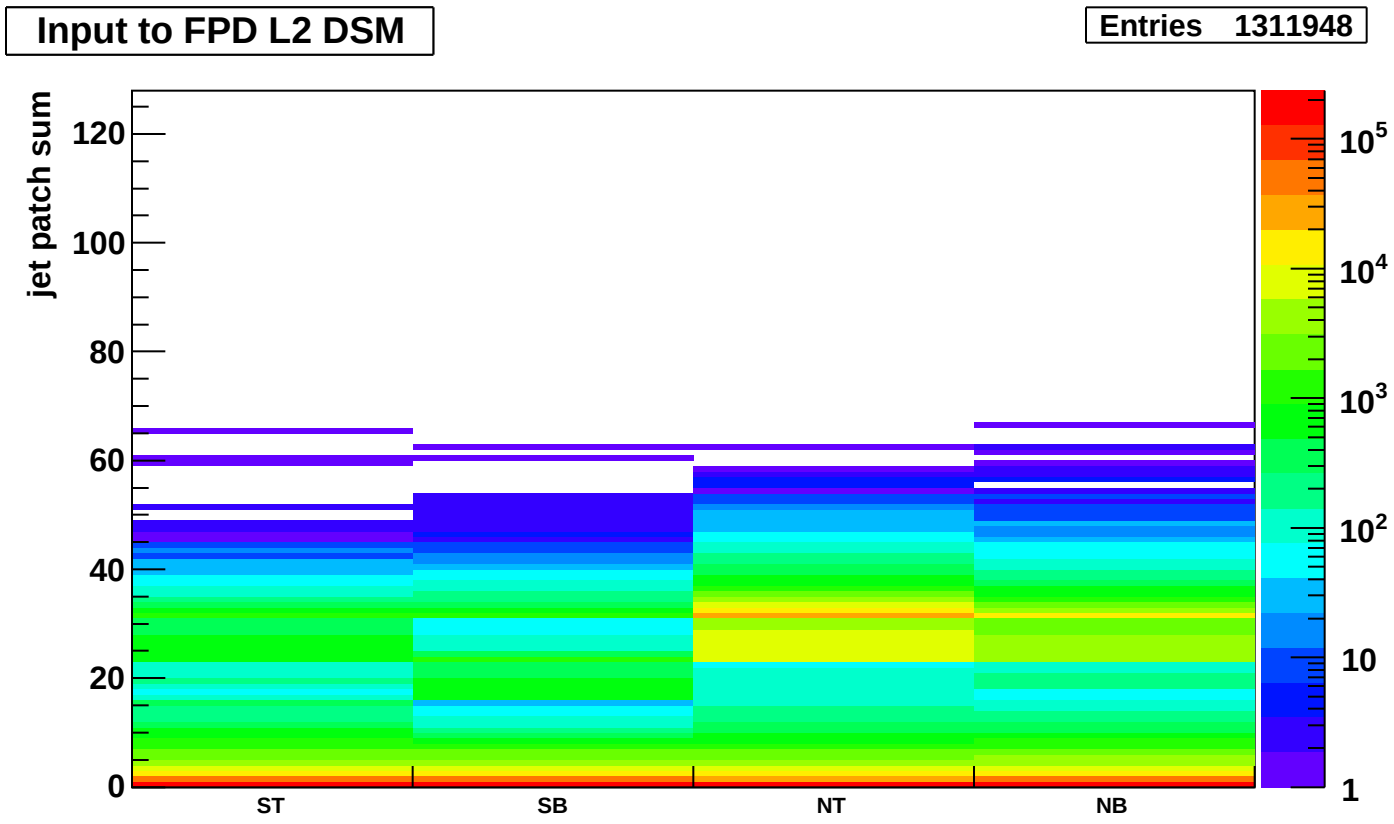

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

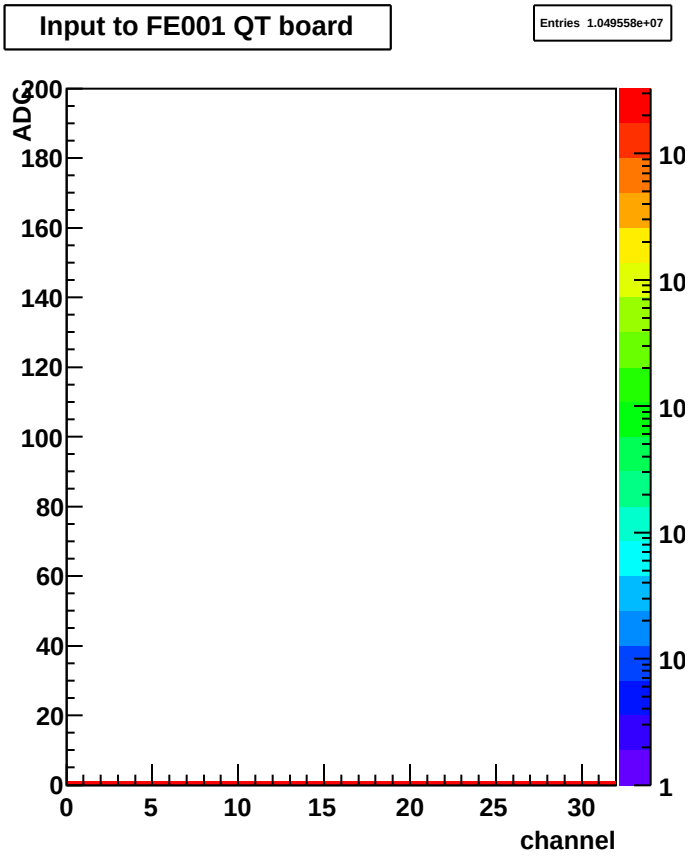

Input to FPD L2 DSM
Entries 983961
HT bits
162681
5467
6923
4251
2279
2528
161055
320241
318536
SMALL
LARGE-S
LARGE-N

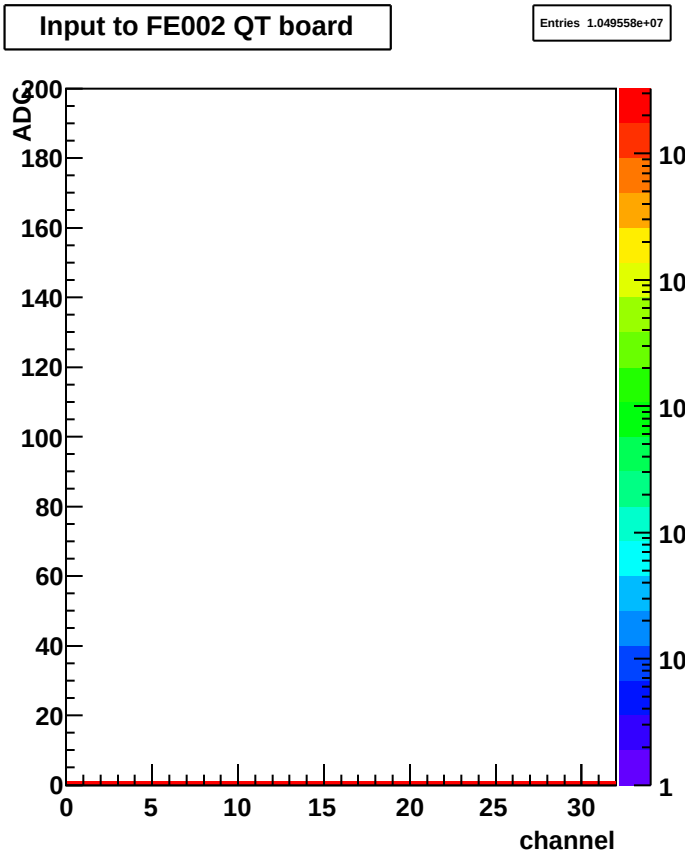

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

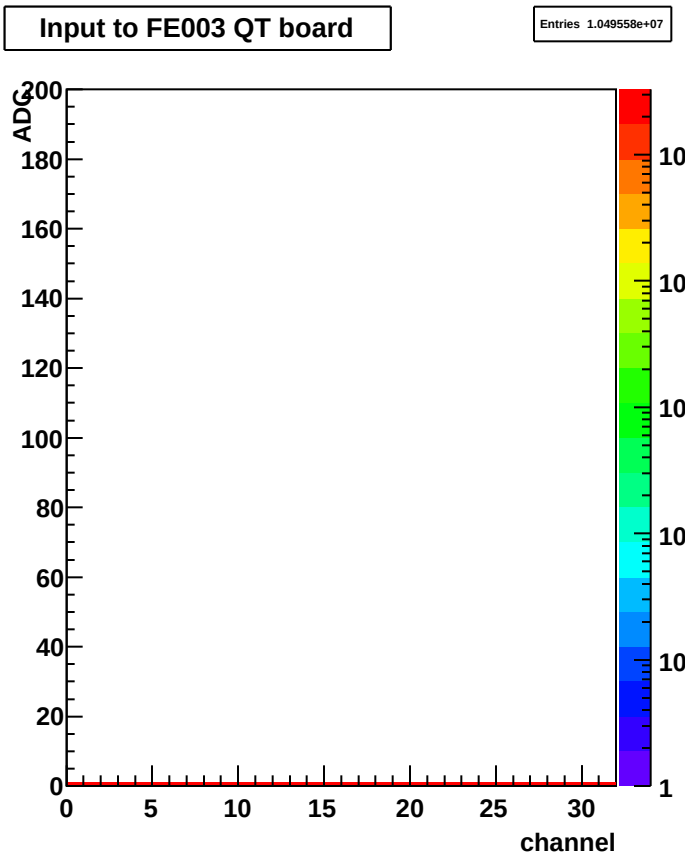

Input to FPD L3 DSM
Entries 5247792
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

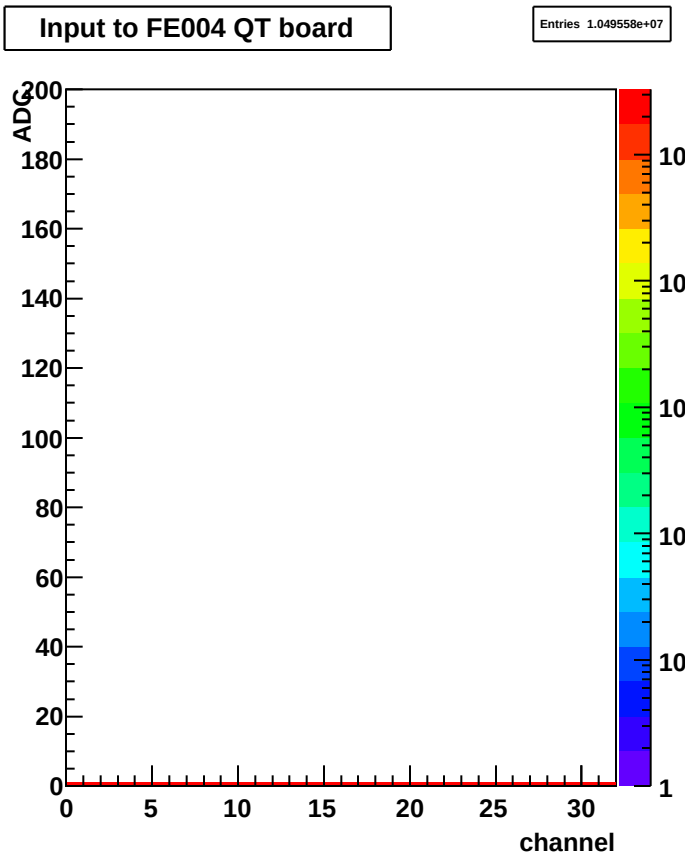

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

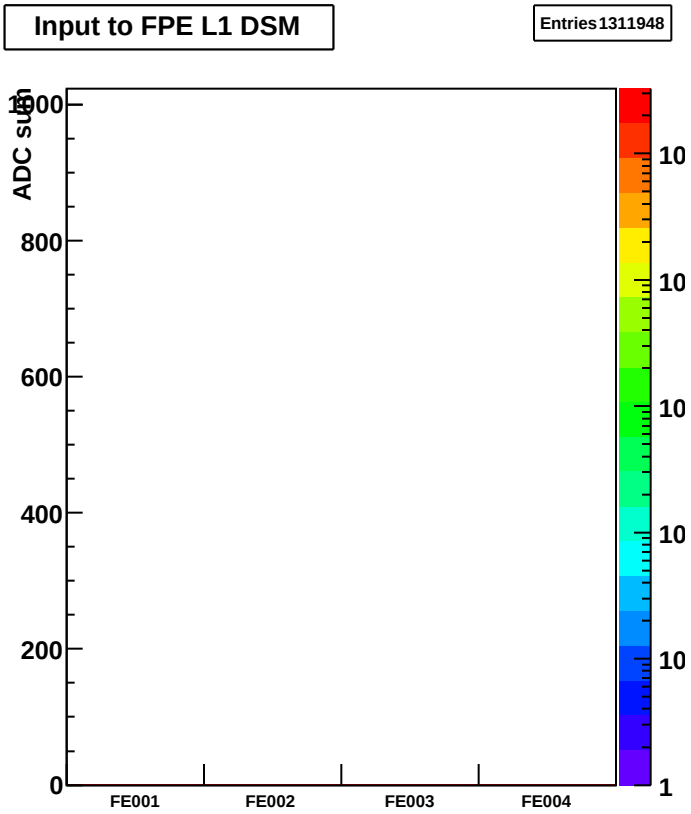

Input to FPD L2 DSM
Entries 1311948
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

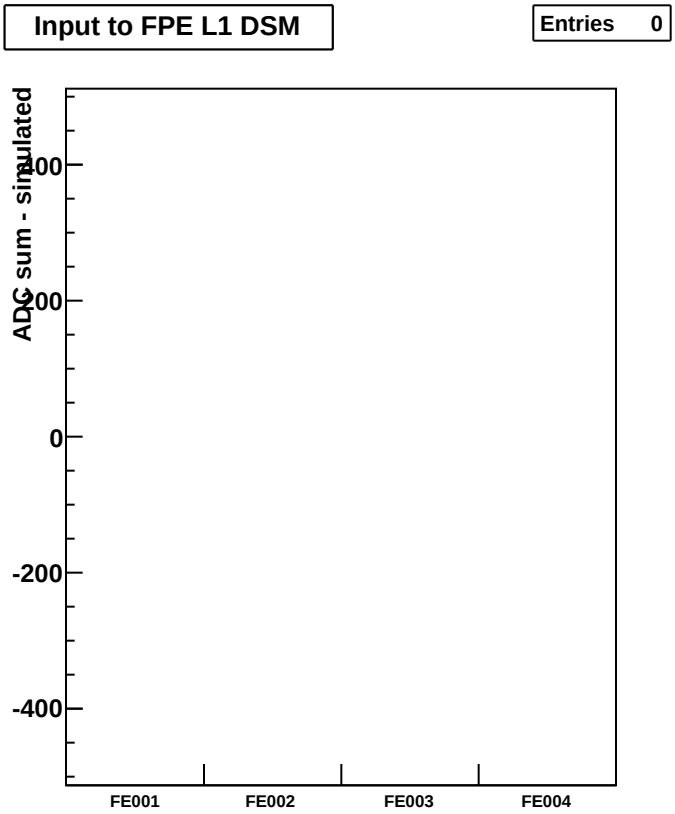

Input to FE001 QT board
Entries 1.049558e+07
ADC
channel

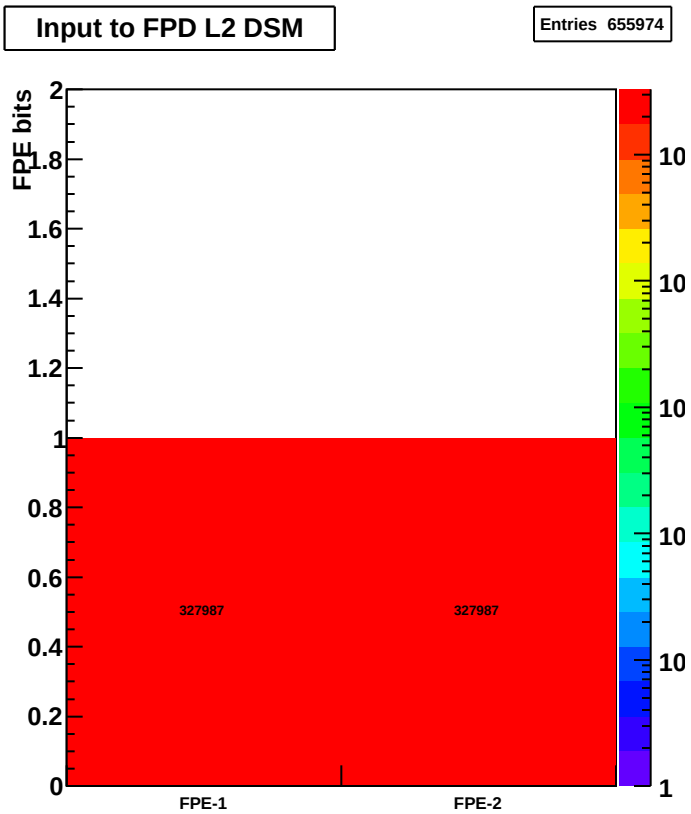

Input to FE002 QT board
Entries 1.049558e+07
ADC
channel

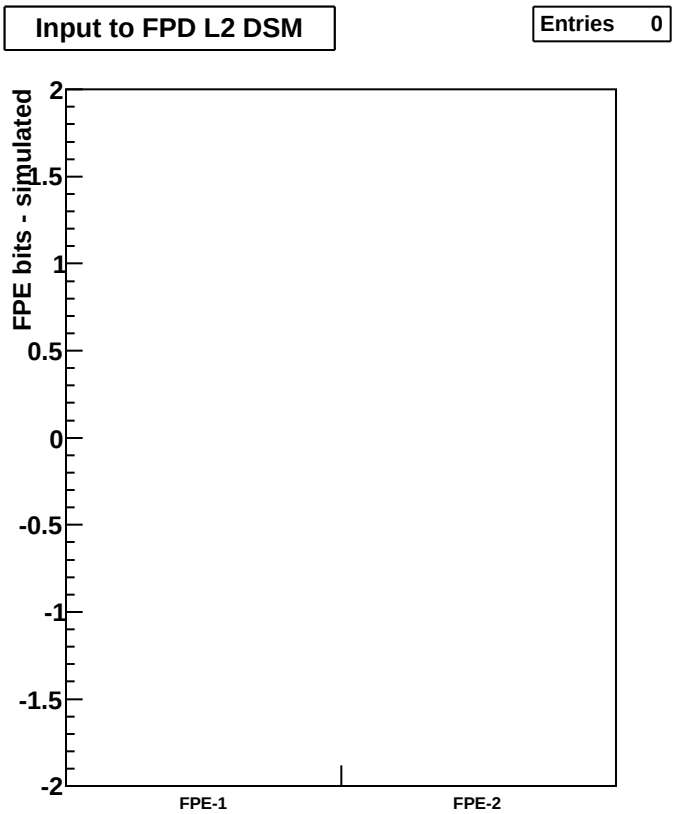

Input to FE003 QT board
Entries 1.049558e+07
ADC
channel


Input to FE004 QT board
Entries 1.049558e+07
ADC
channel


Input to FPE L1 DSM
Entries 1311948
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 655974
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
327987
327987
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2