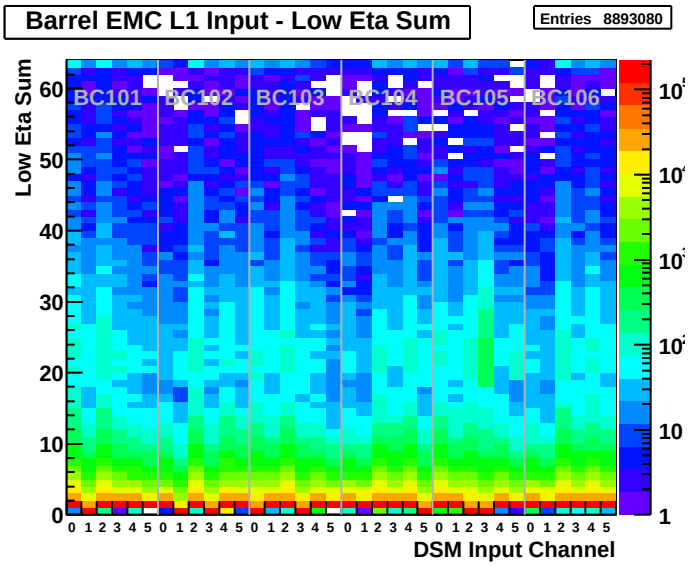
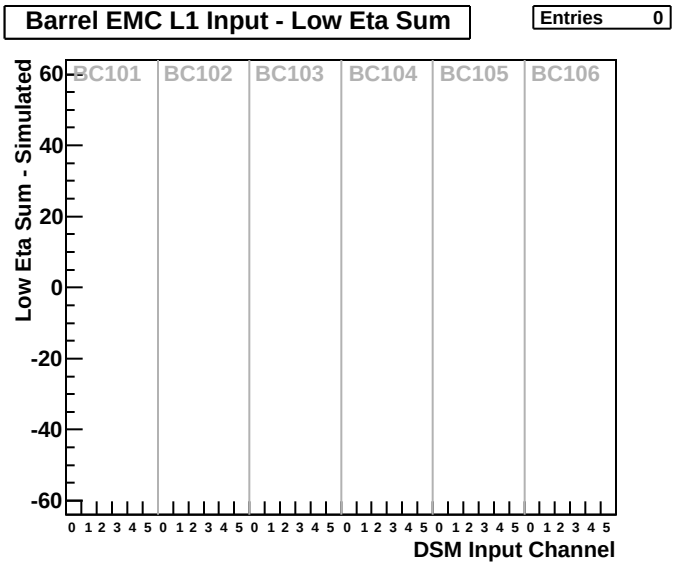
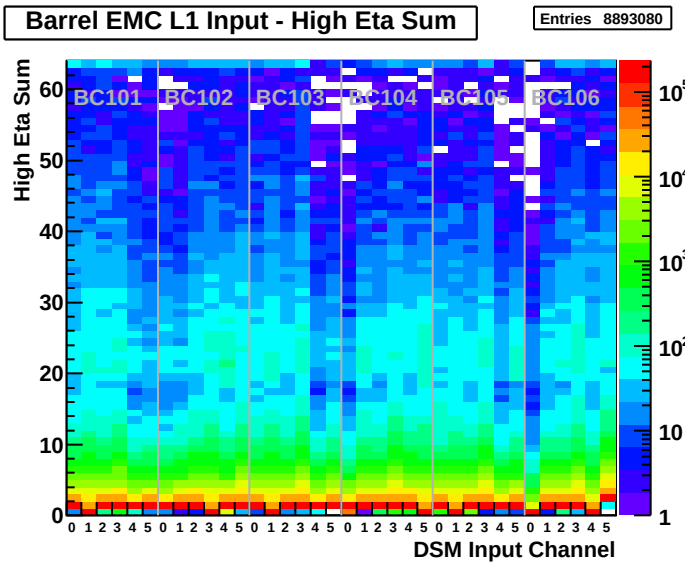
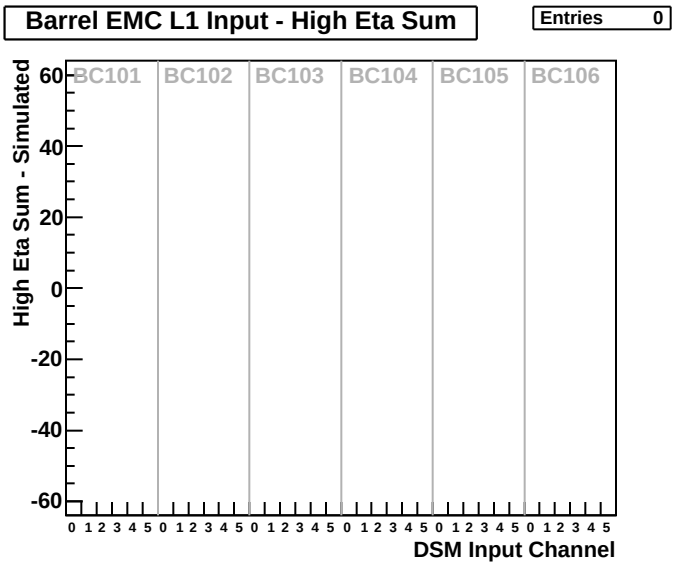
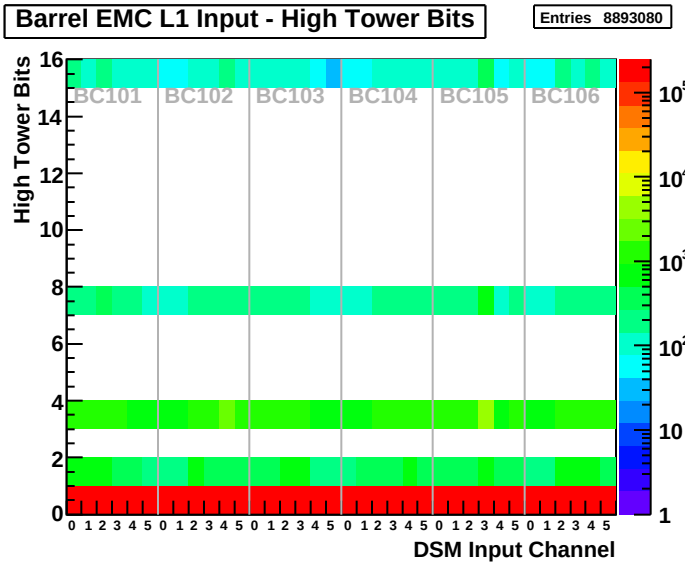
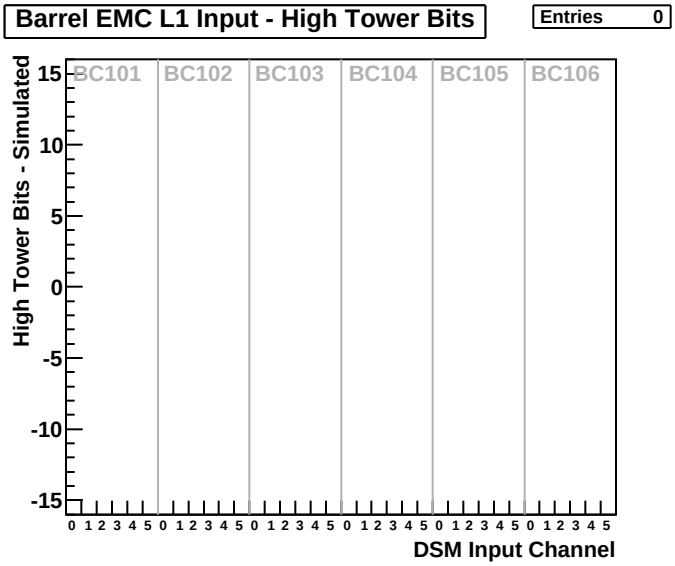

Barrel EMC L1 Input - Low Eta Sum
Entries 8893080
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

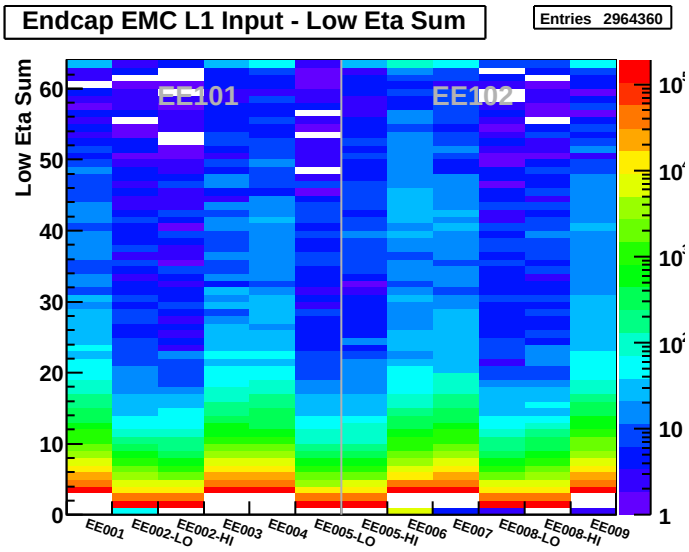

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

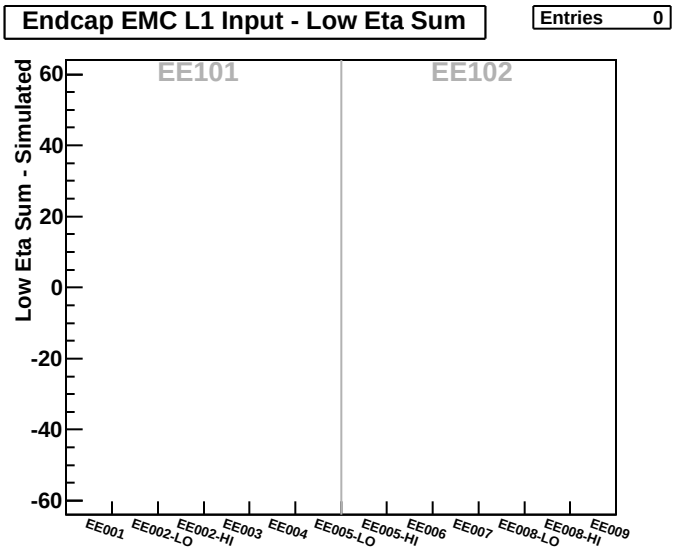

Barrel EMC L1 Input - High Eta Sum
Entries 8893080
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

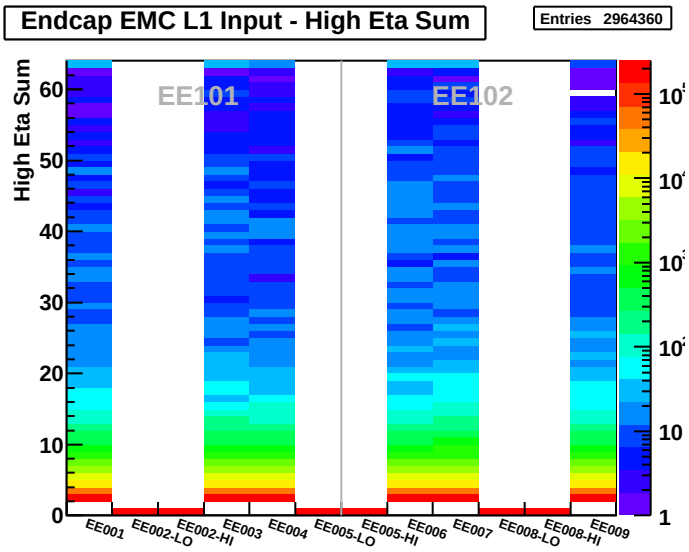

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

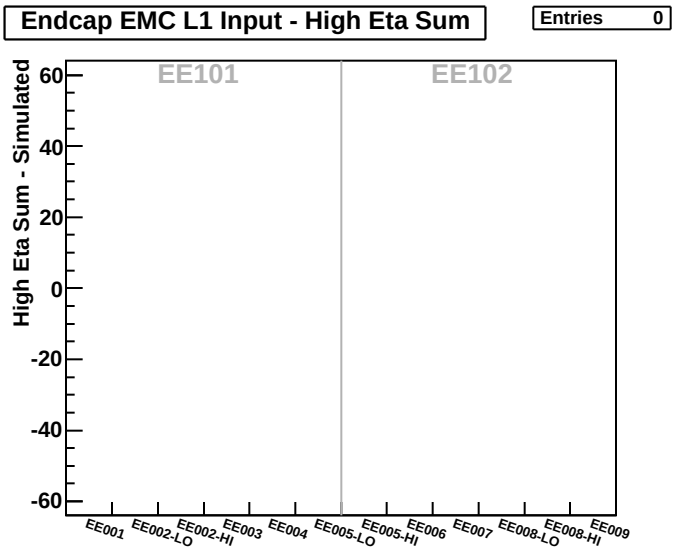

Barrel EMC L1 Input - High Tower Bits
Entries 8893080
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

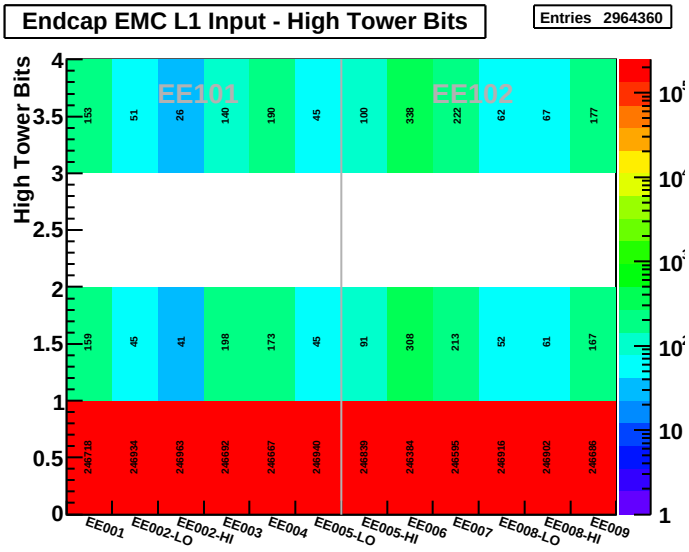

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

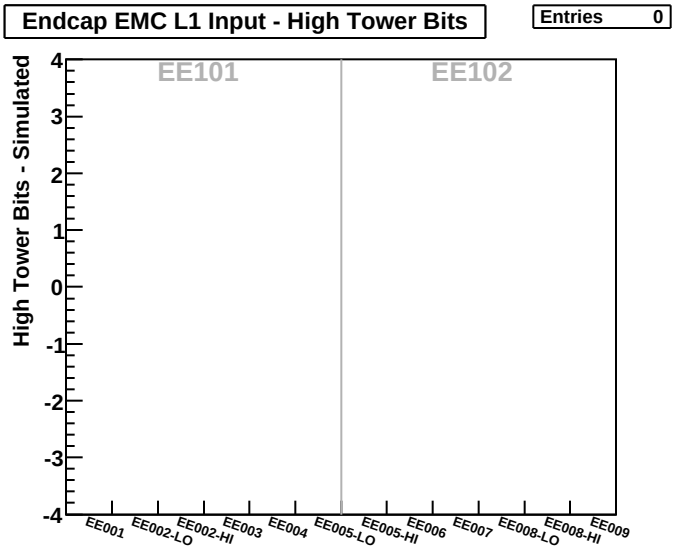

Endcap EMC L1 Input - Low Eta Sum
Entries 2964360
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

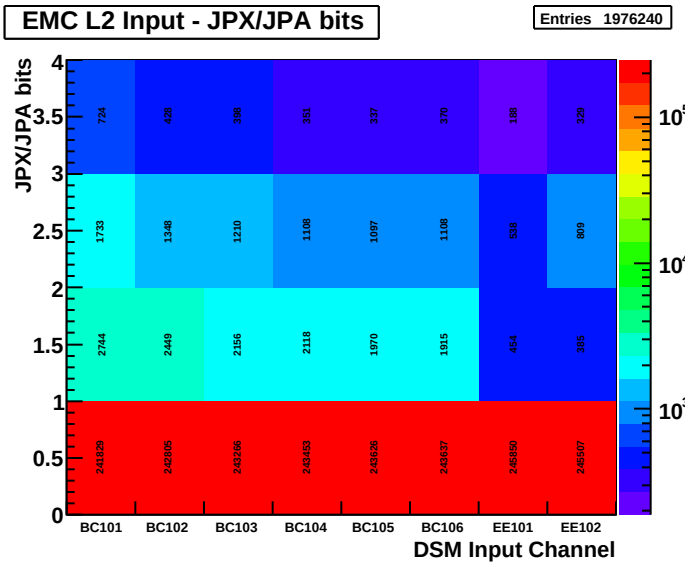

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

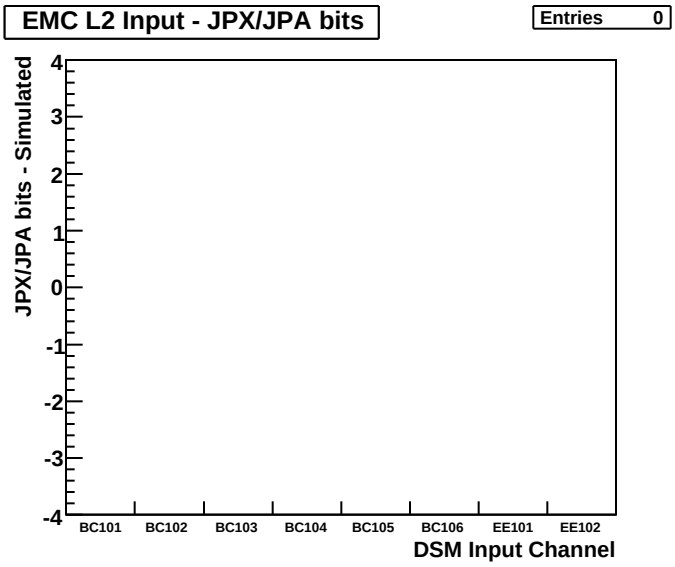

Endcap EMC L1 Input - High Eta Sum
Entries 2964360
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

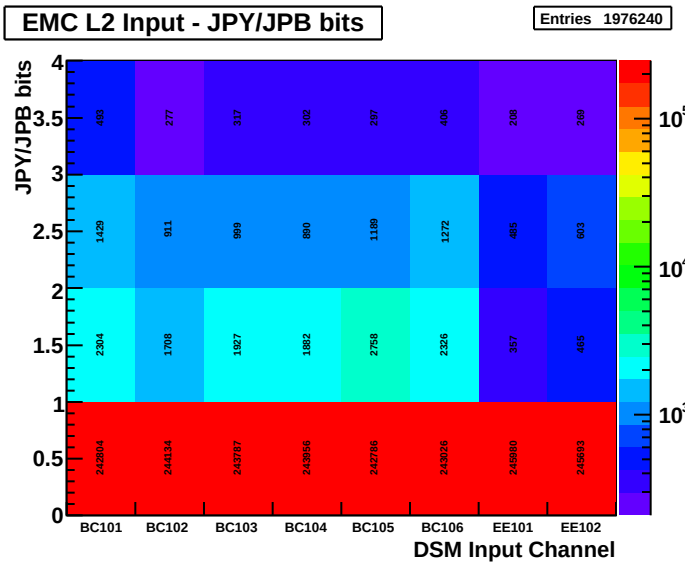

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

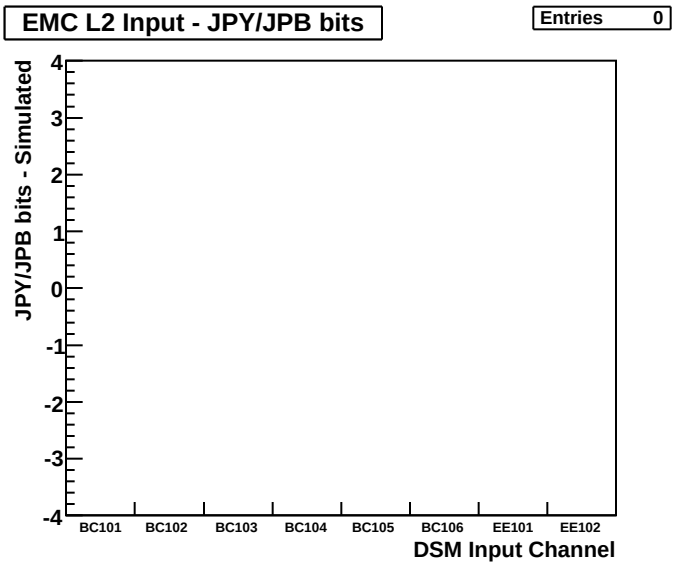

Endcap EMC L1 Input - High Tower Bits
Entries 2964360
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

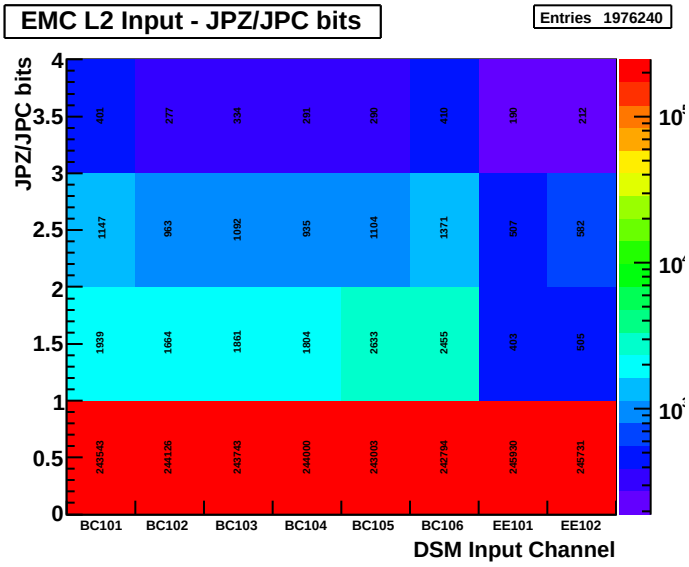

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

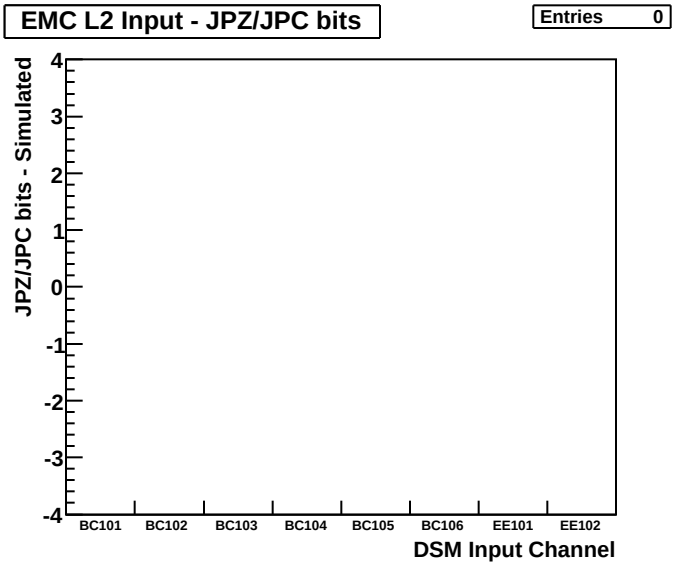

EMC L2 Input - JPX/JPA bits
Entries 1976240
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
724 428 398 351 337 370 188 329
1733 1348 1210 1108 1097 1108 538 809
2744 2449 2156 2118 1970 1915 454 385
241829 242805 242266 243453 243626 243637 245850 245507

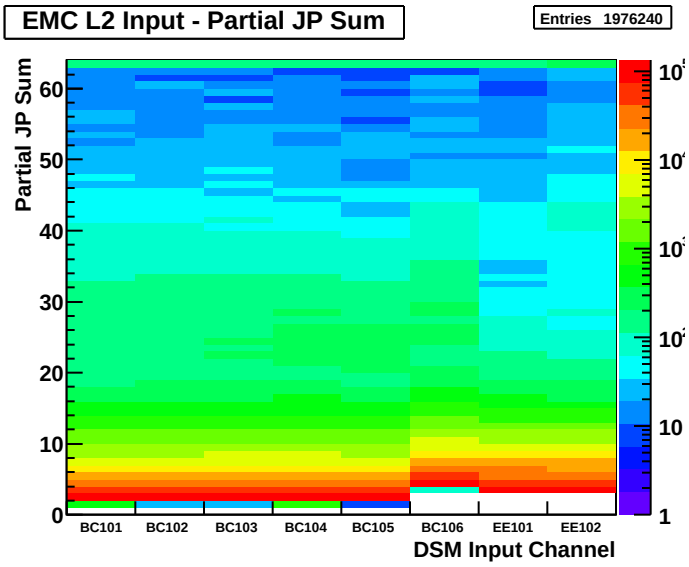

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

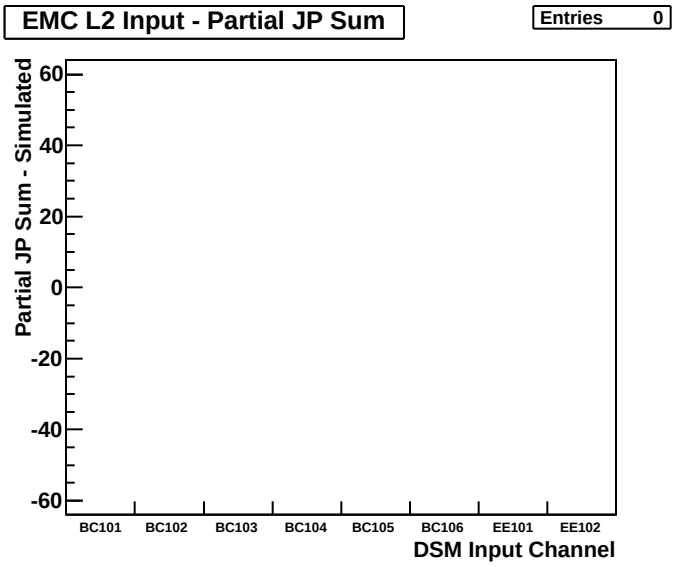

EMC L2 Input - JPY/JPB bits
Entries 1976240
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
486 277 317 302 297 406 208 289
1429 911 999 890 1189 1272 485 603
2304 1708 1927 1882 2758 2326 357 465
242804 244134 243787 243956 242786 243026 245980 245693

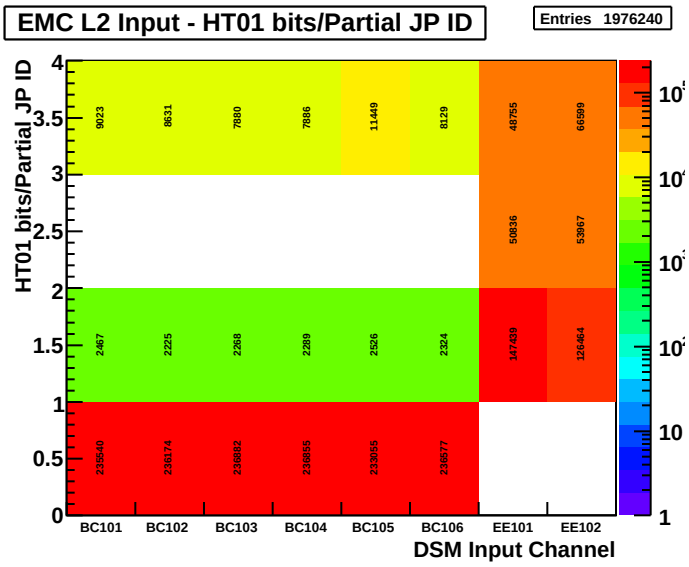

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

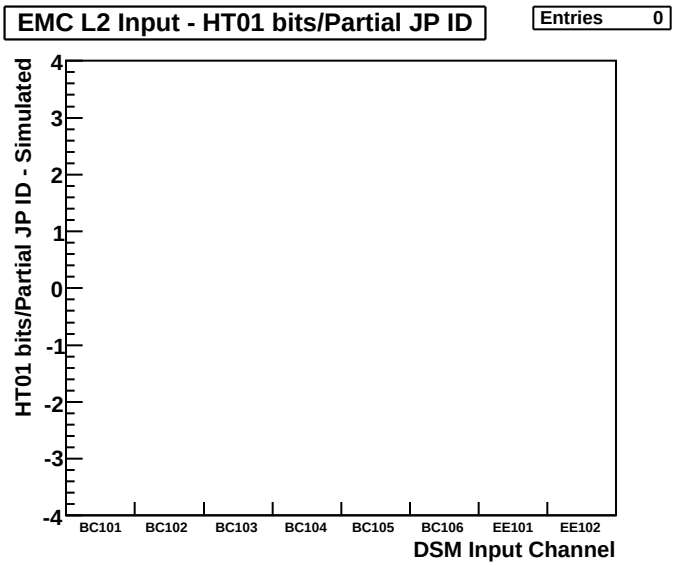

EMC L2 Input - JPZ/JPC bits
Entries 1976240
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
401 277 324 291 290 410 130 212
1147 963 1092 935 1104 1371 507 582
1939 1664 1861 1804 2633 2465 403 505
243543 244126 243743 244000 243003 242794 245930 245731

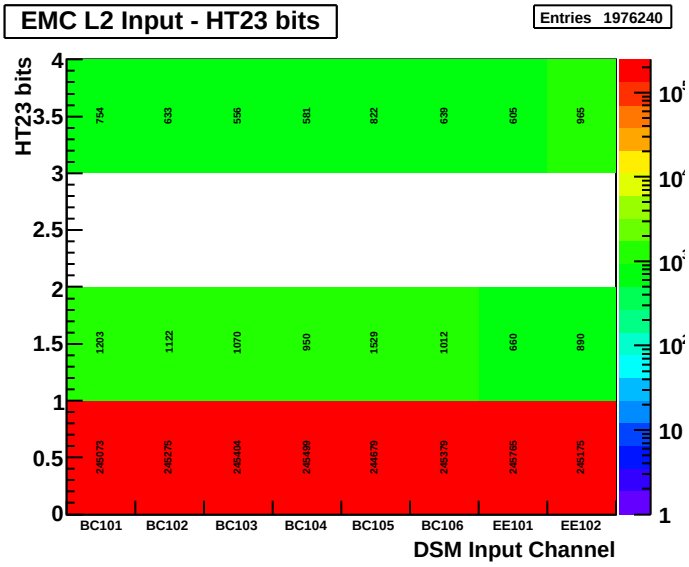

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

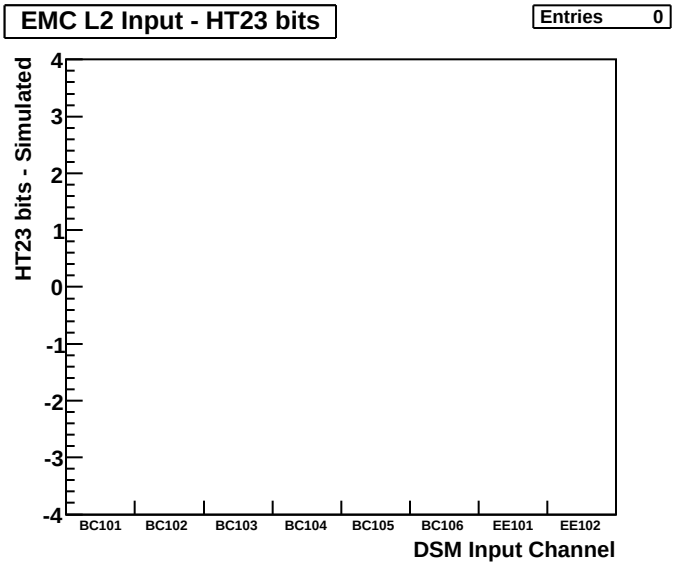

EMC L2 Input - Partial JP Sum
Entries 1976240
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

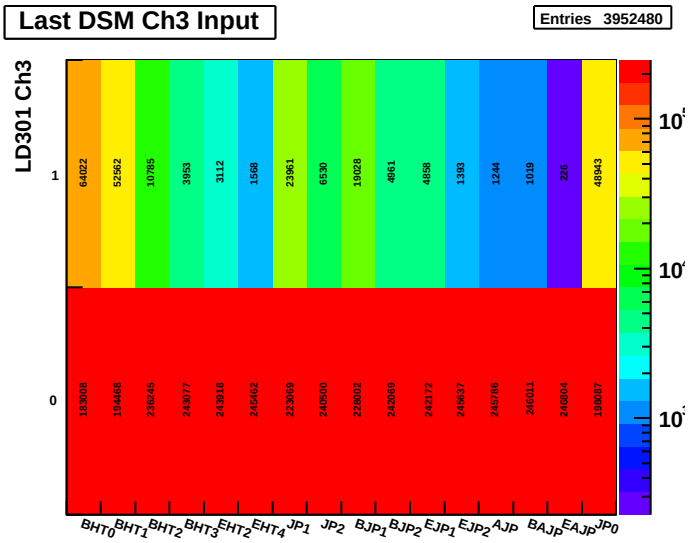

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

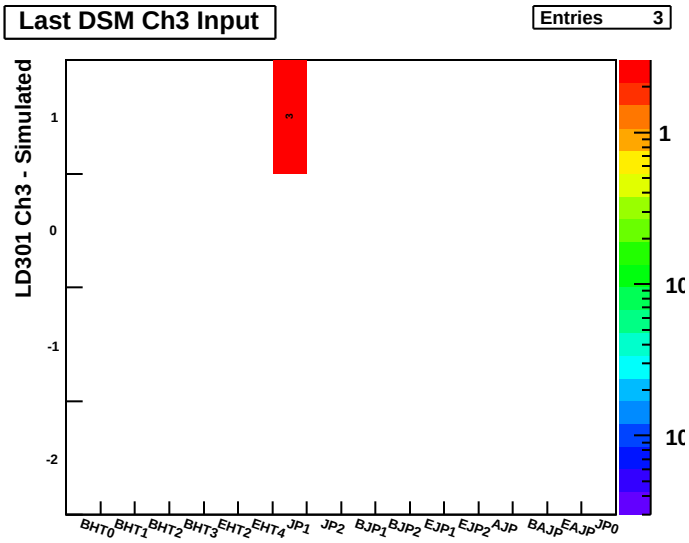

EMC L2 Input - HT01 bits/Partial JP ID
Entries 1976240
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

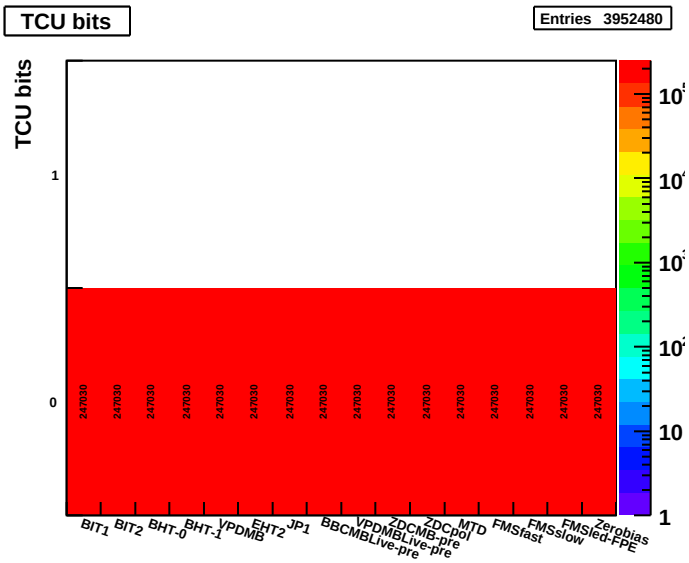

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

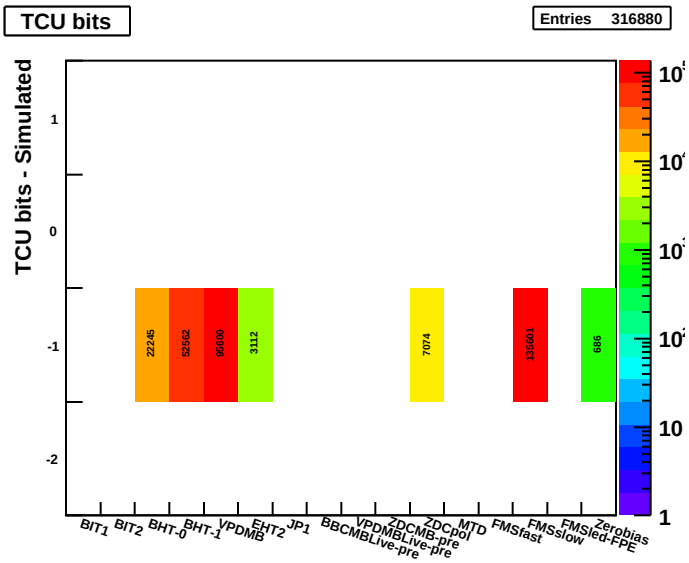

EMC L2 Input - HT23 bits
Entries 1976240
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

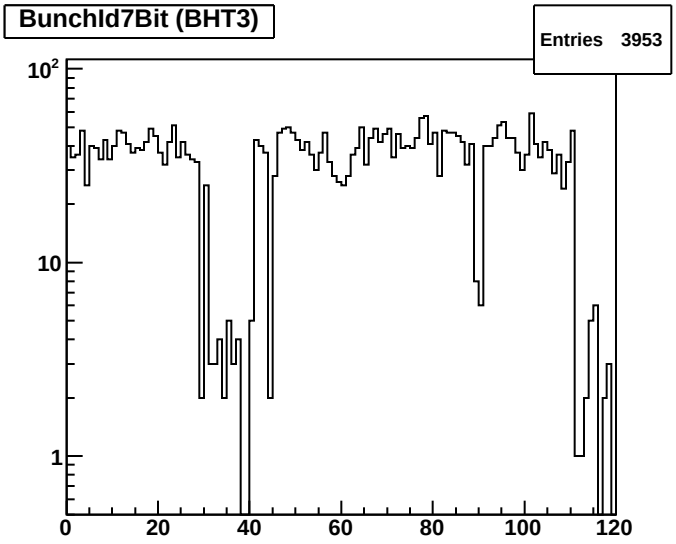

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

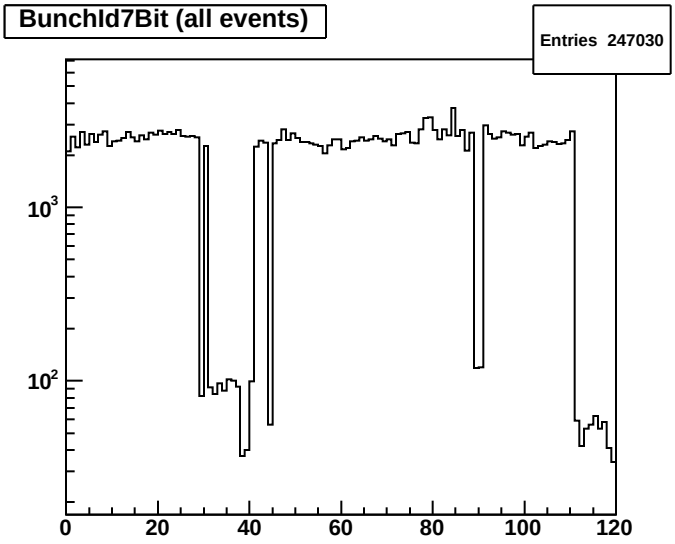

Last DSM Ch3 Input
Entries 3952480
LD301 Ch3
64022
52562
10785
3953
3112
1568
23961
6530
19028
4961
4958
1393
1244
1019
226
48943
183008
194468
236245
243077
243918
245462
223069
240500
228002
242069
242172
245537
245786
246011
246804
198087
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

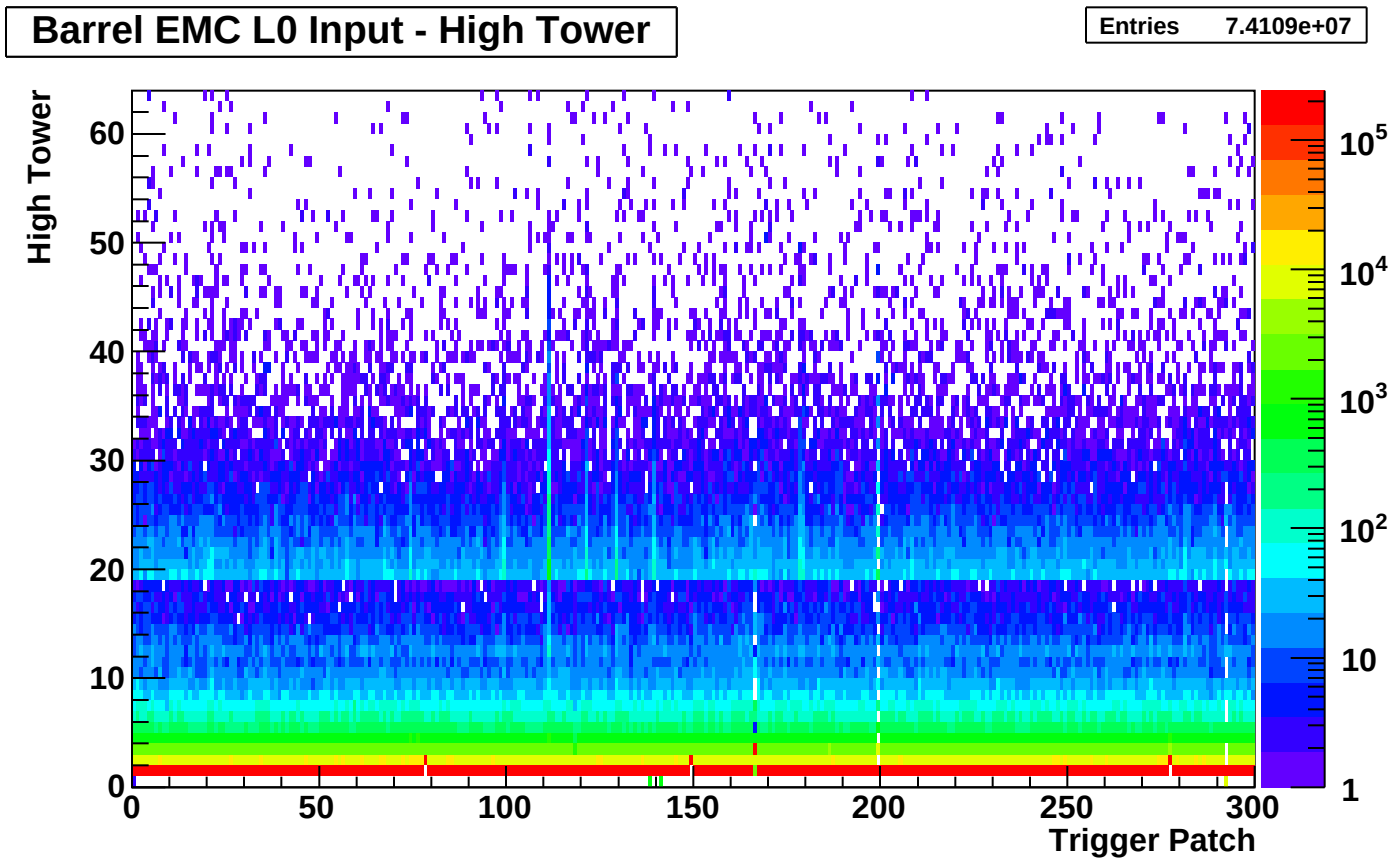

Last DSM Ch3 Input
Entries 3
LD301 Ch3 - Simulated
3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

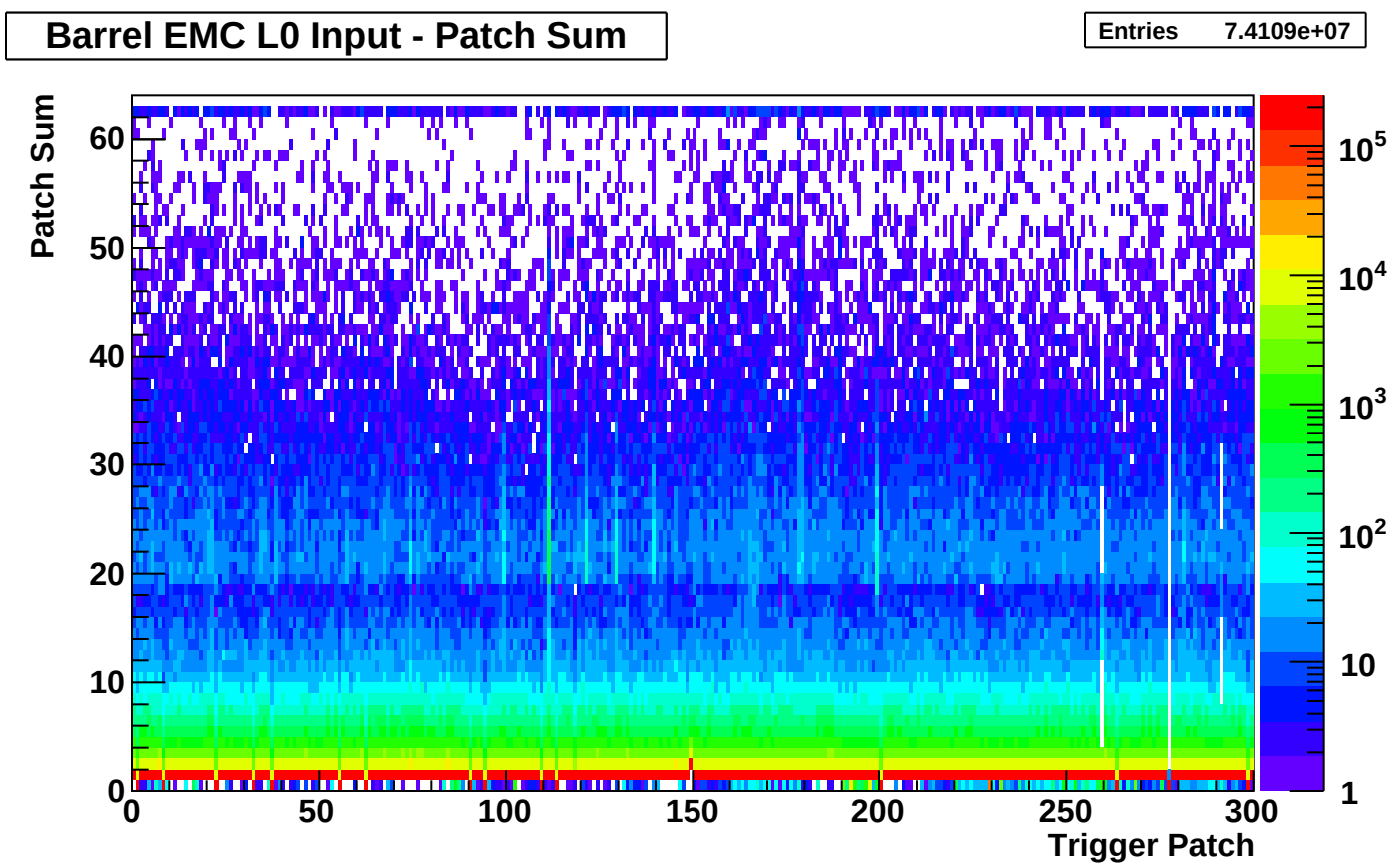

TCU bits
Entries 3952480
TCU bits
247030
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

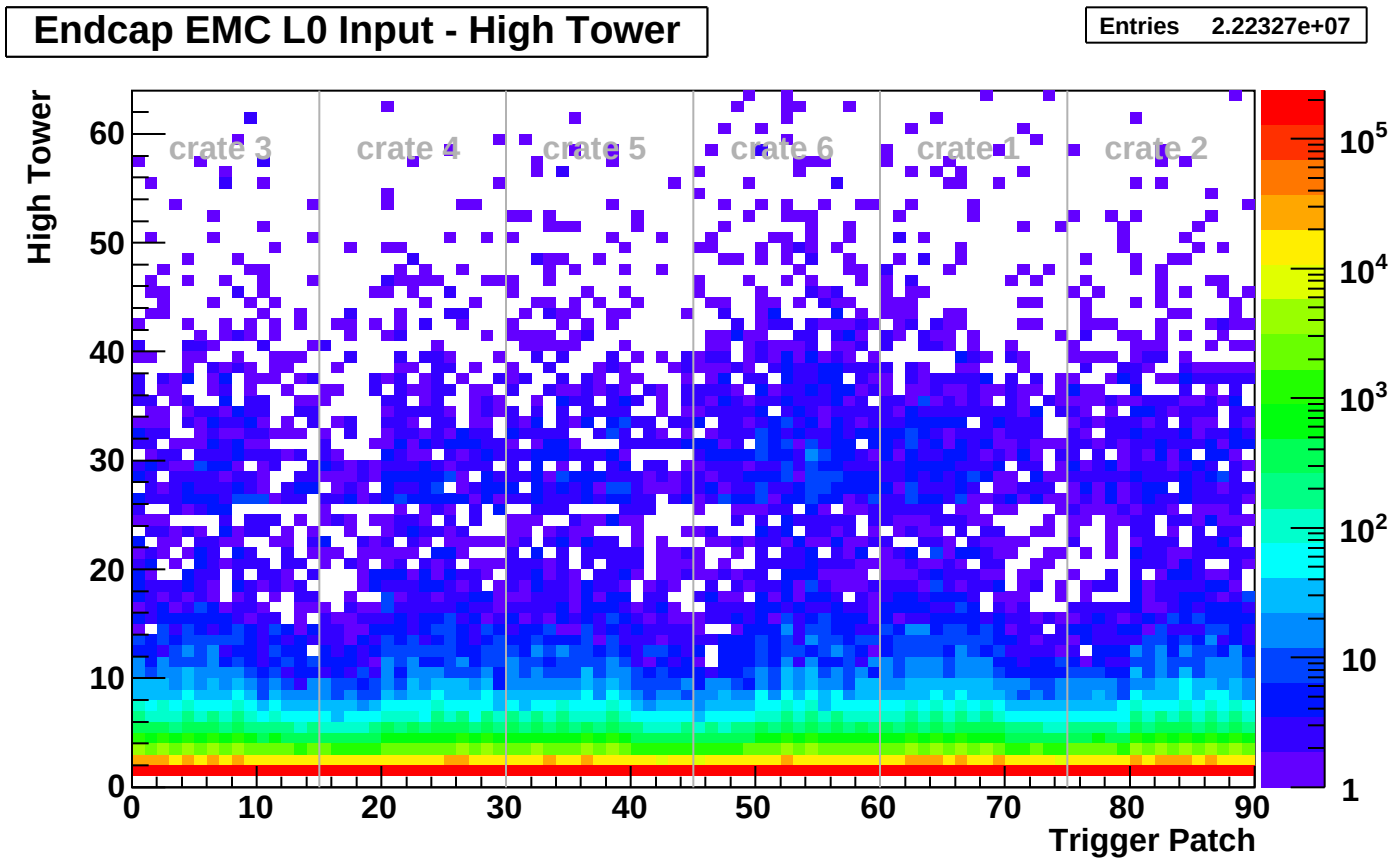

TCU bits
Entries 316880
TCU bits - Simulated
22245
52562
95600
3112
7074
135601
686
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

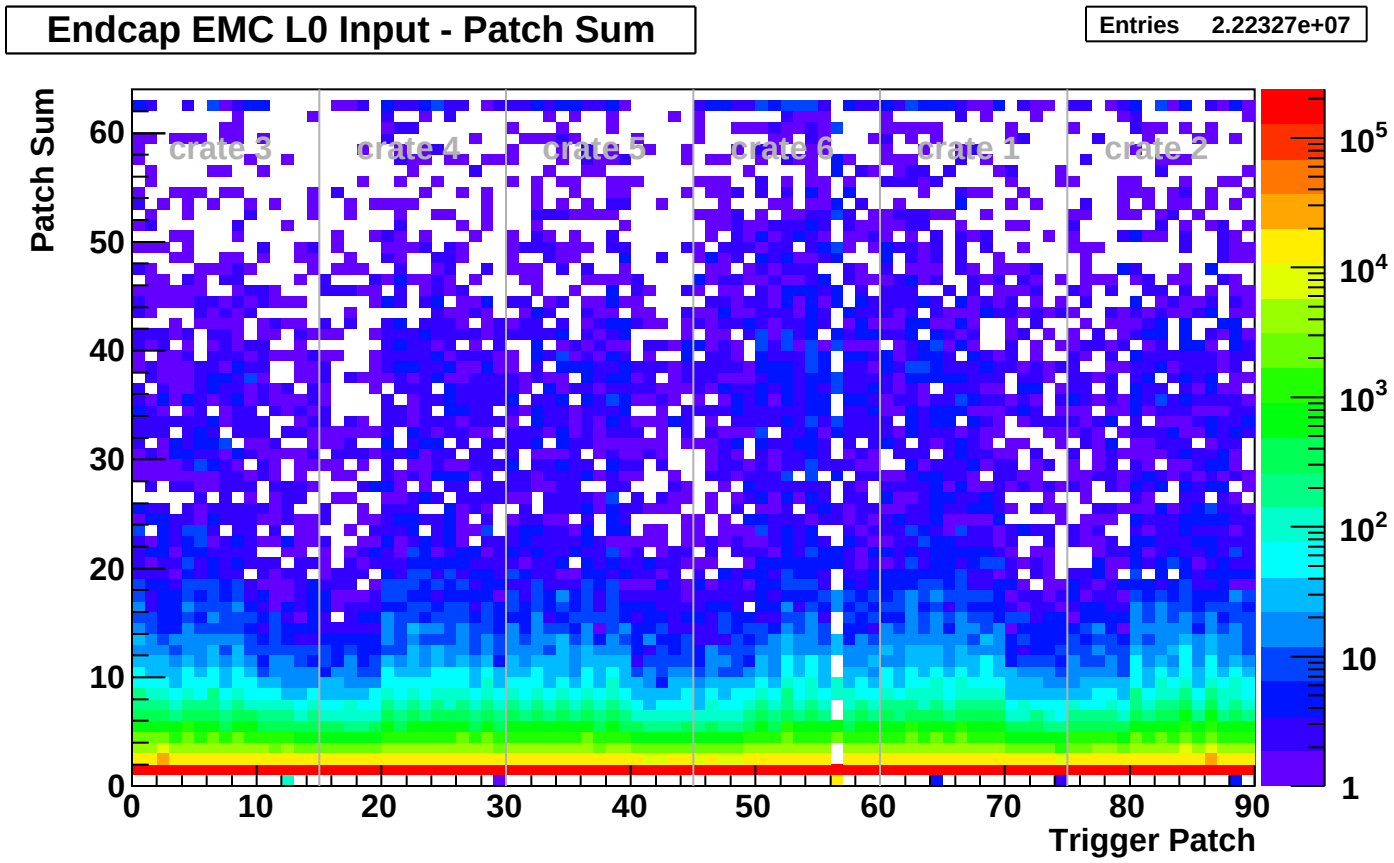

BunchId7Bit (BHT3)
Entries 3953

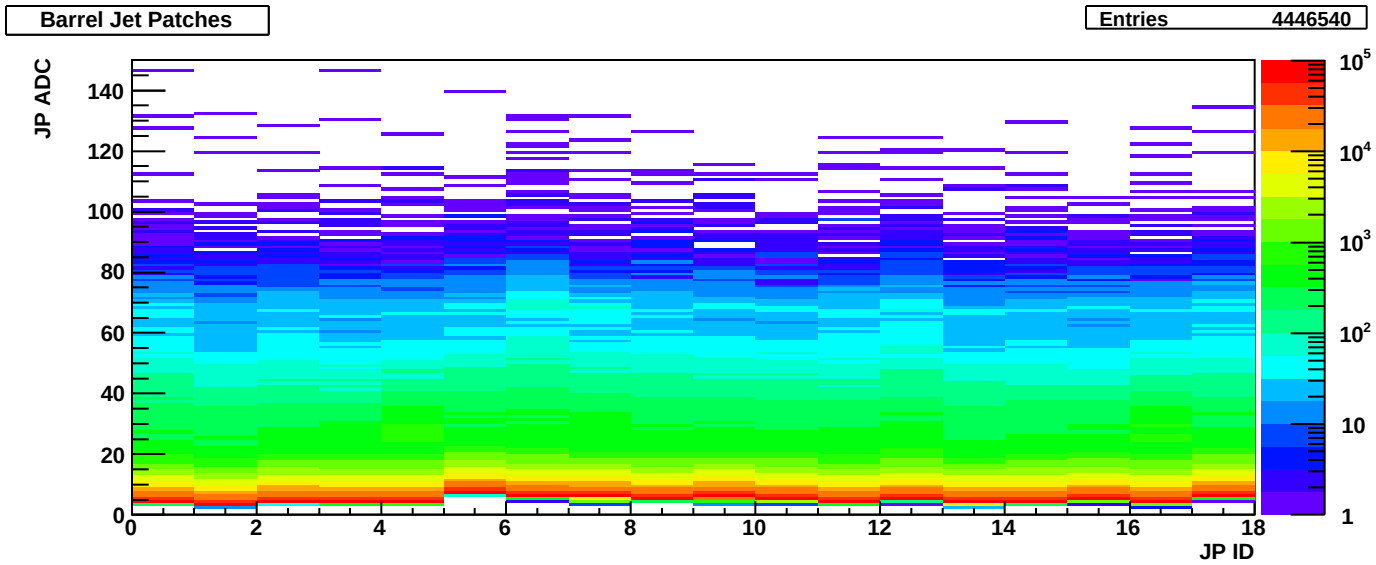

BunchId7Bit (all events)
Entries 247030

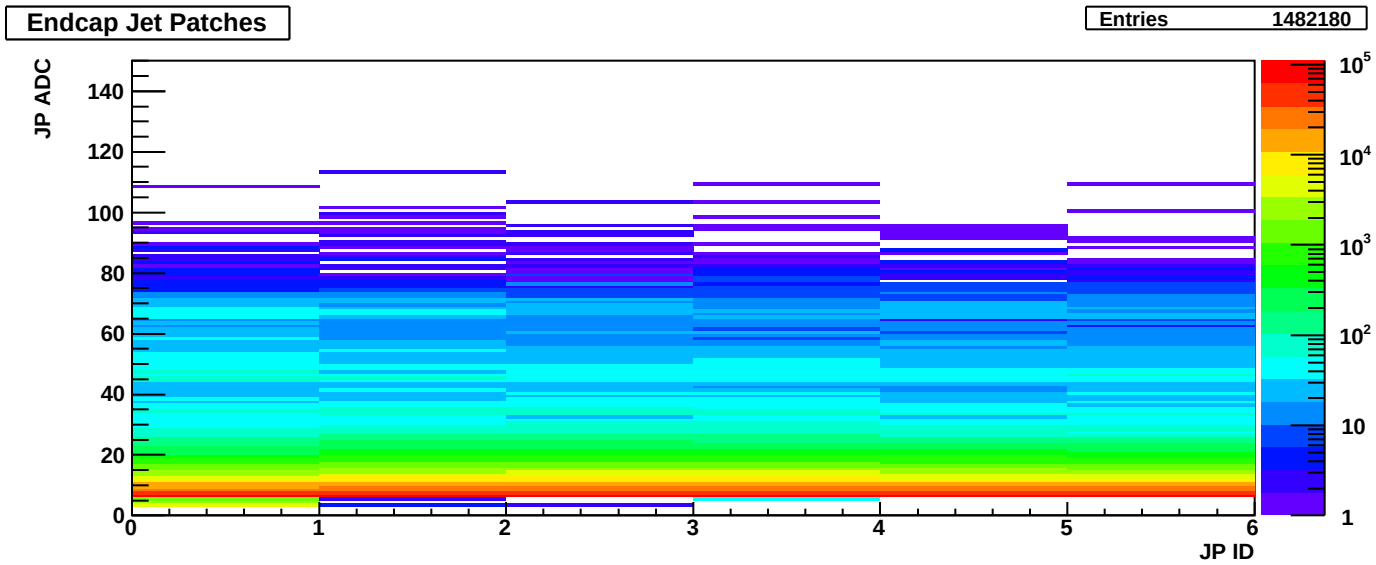

Barrel EMC L0 Input - High Tower
Entries 7.4109e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

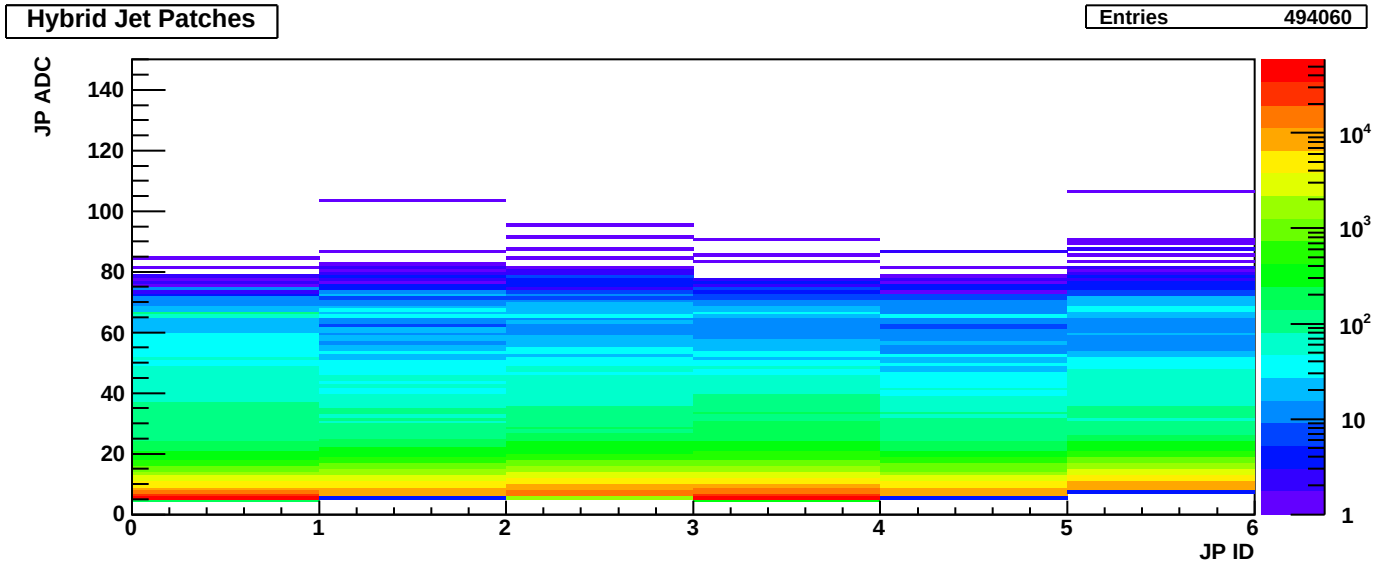

Barrel EMC L0 Input - Patch Sum
Entries 7.4109e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

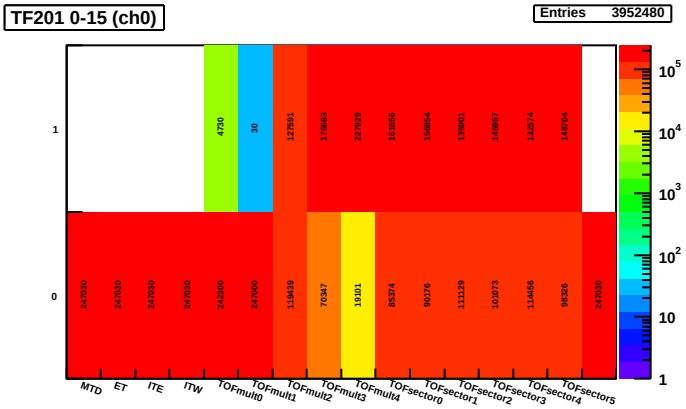

Endcap EMC L0 Input - High Tower
Entries 2.22327e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

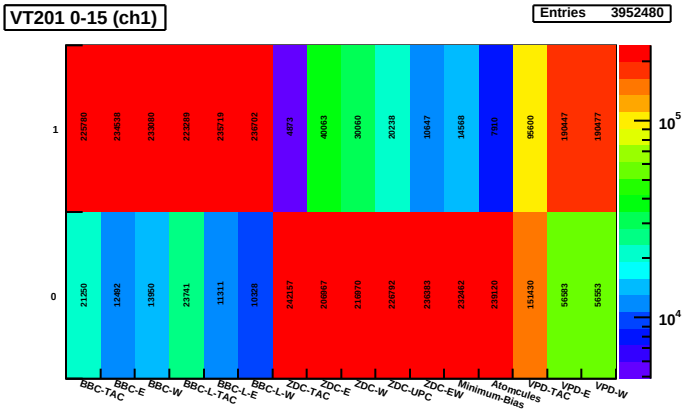

Endcap EMC L0 Input - Patch Sum
Entries 2.22327e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

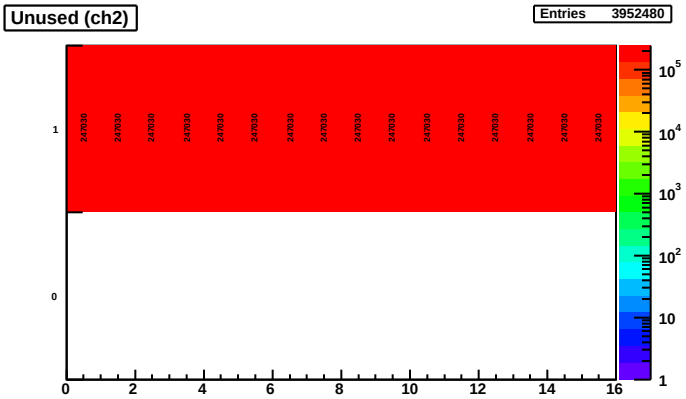

Barrel Jet Patches
Entries 4446540
JP ADC
JP ID

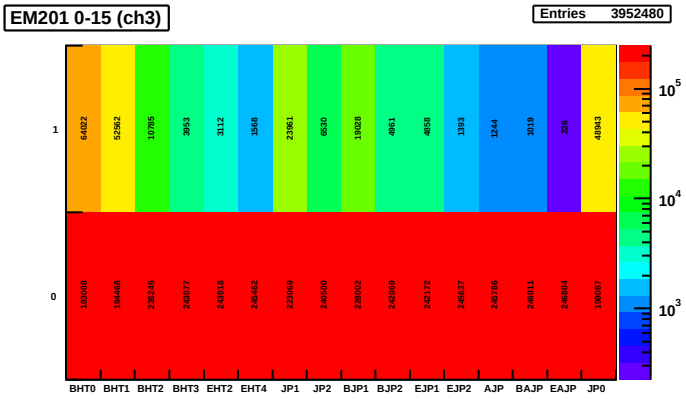

Endcap Jet Patches
Entries 1482180
JP ADC
JP ID

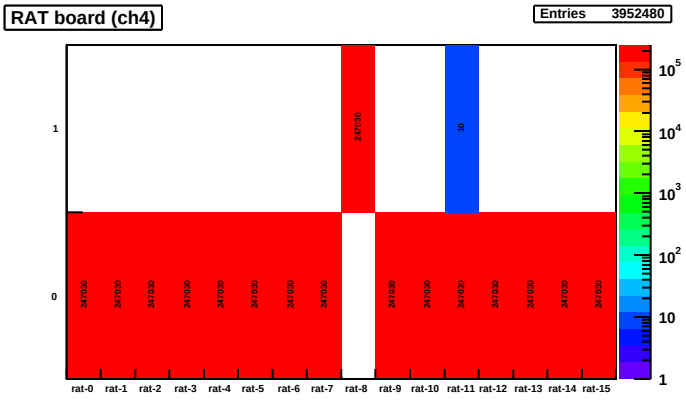

Hybrid Jet Patches
Entries 494060
JP ADC
JP ID

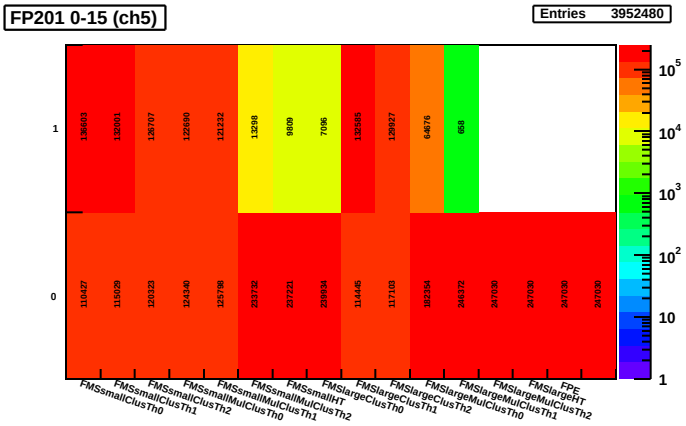

TF201 0-15 (ch0)
Entries 3952480
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

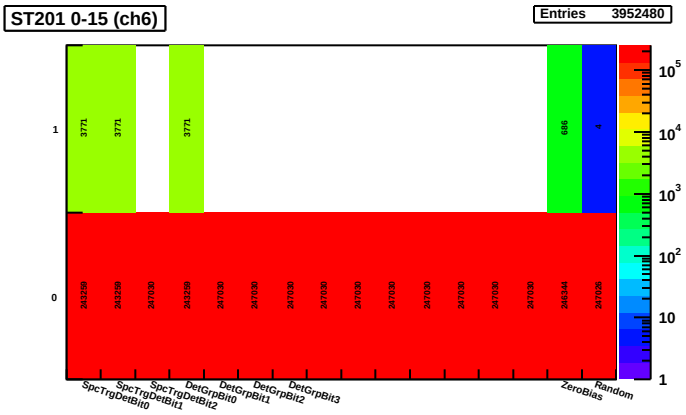

VT201 0-15 (ch1)
Entries 3952480
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

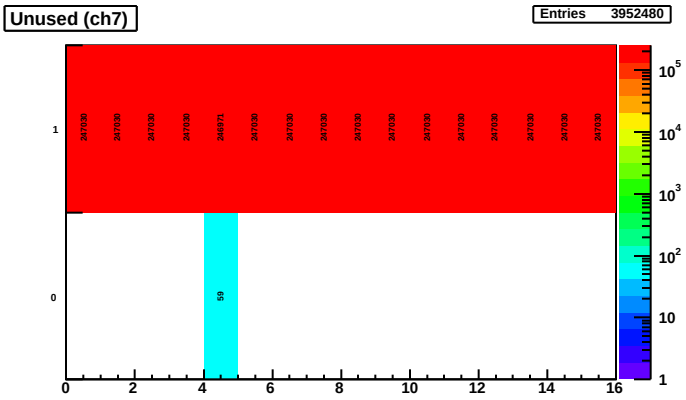

Unused (ch2)
Entries 3952480

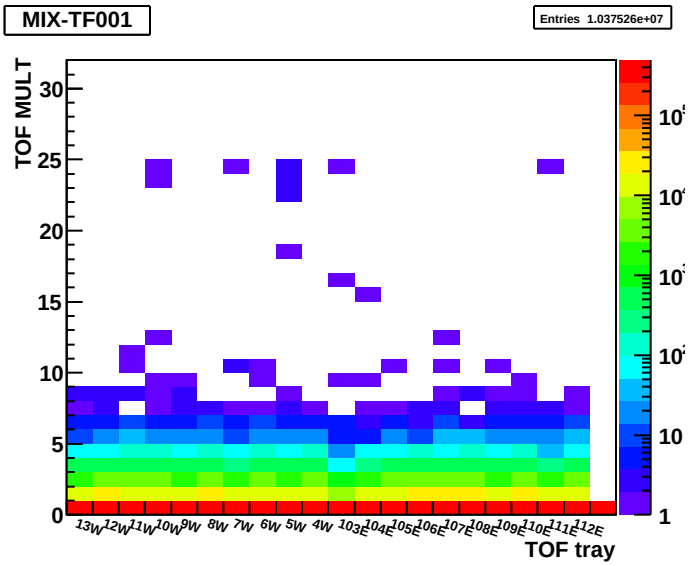

EM201 0-15 (ch3)
Entries 3952480
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

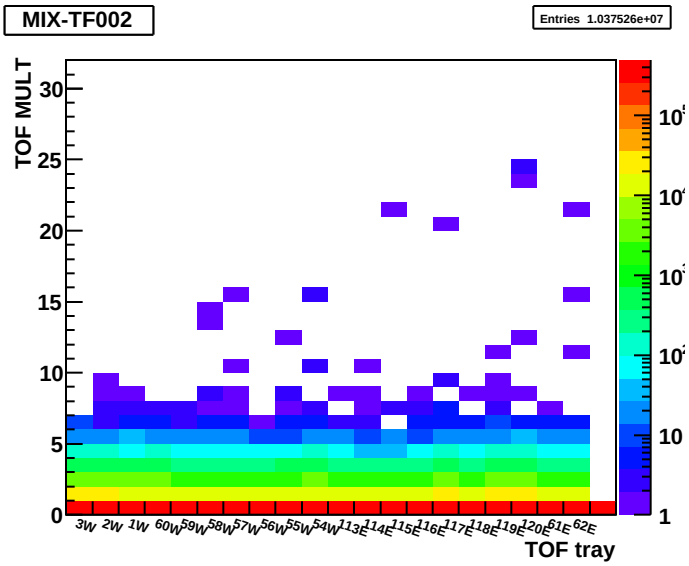

RAT board (ch4)
Entries 3952480
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

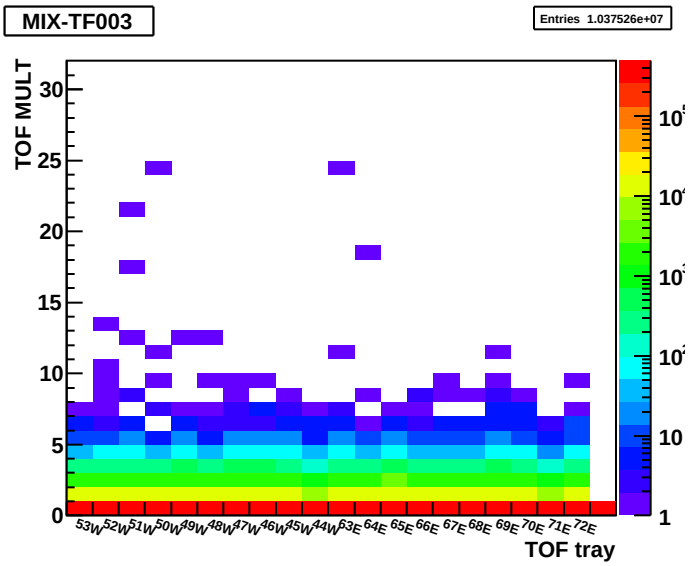

FP201 0-15 (ch5)
Entries 3952480
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

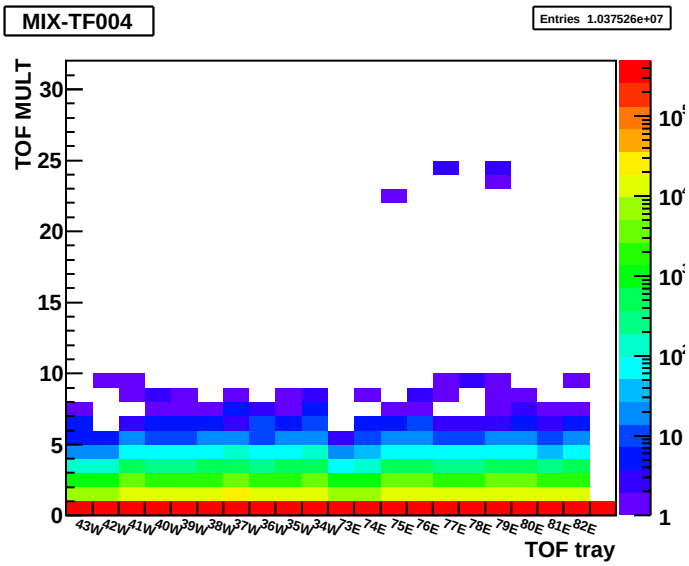

ST201 0-15 (ch6)
Entries 3952480
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

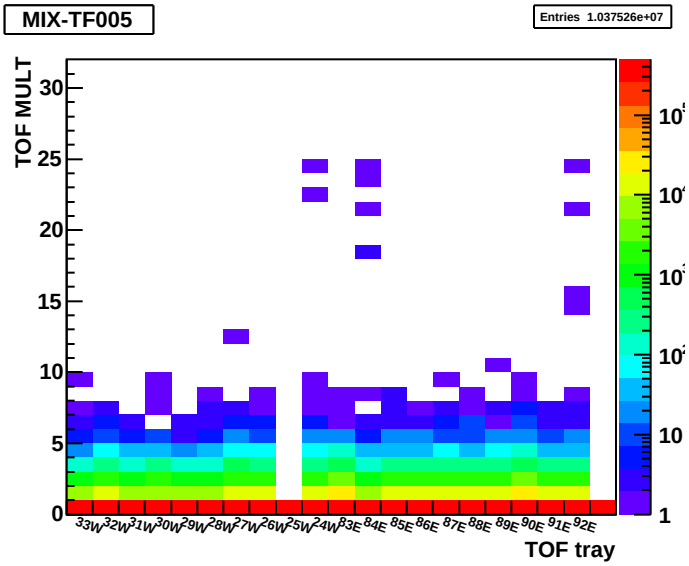

Unused (ch7)
Entries 3952480

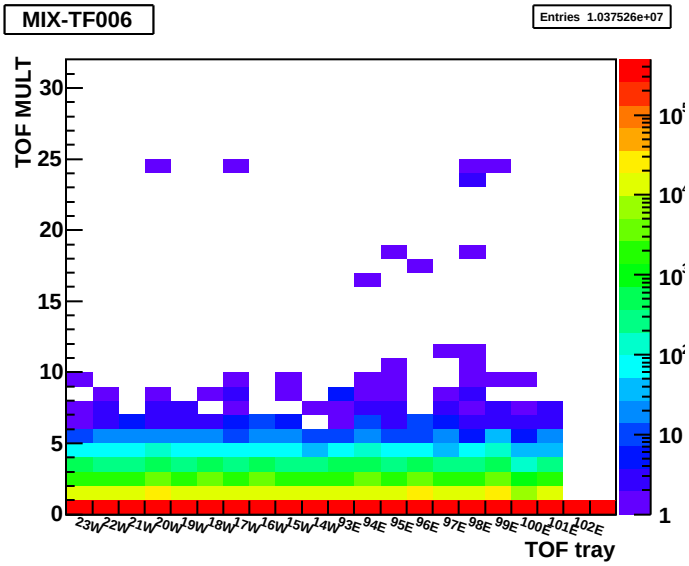

MIX-TF001
Entries 1.037526e+07
TOF MULT
TOF tray

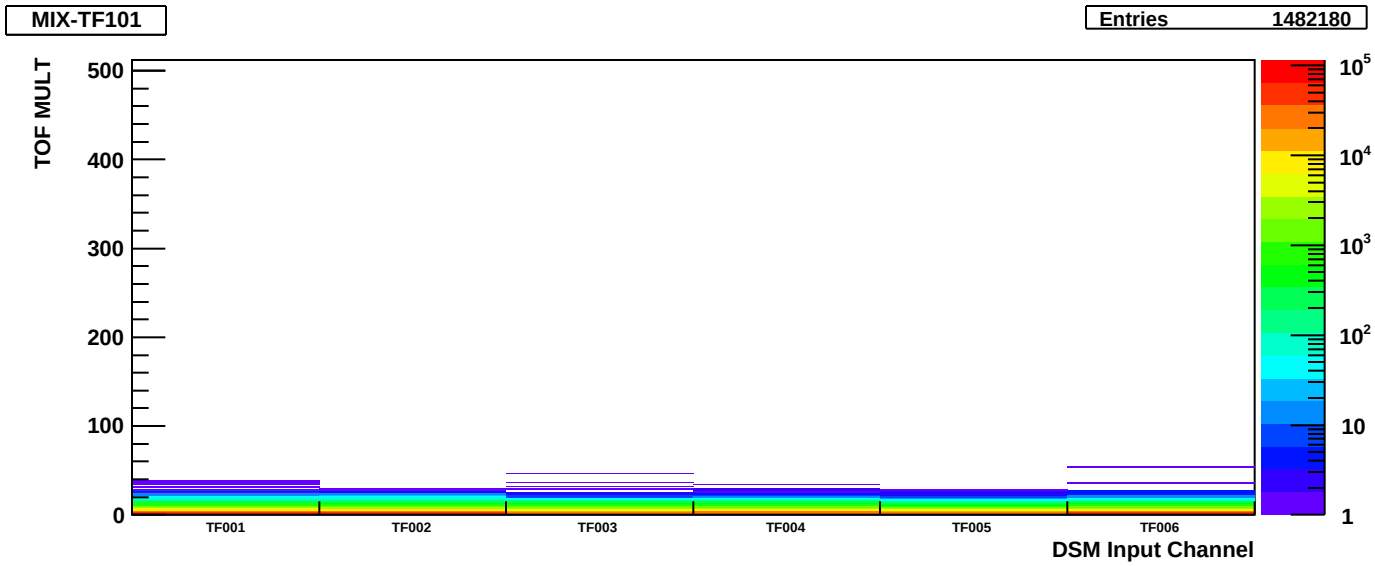

MIX-TF002
Entries 1.037526e+07
TOF MULT
TOF tray

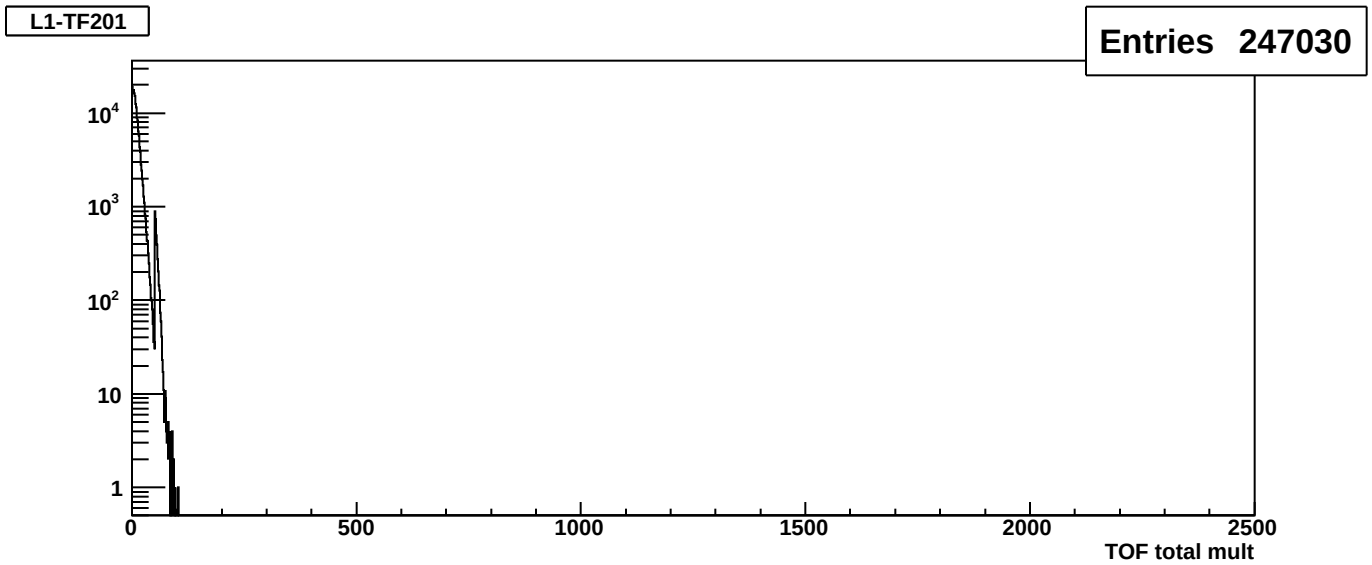

MIX-TF003
Entries 1.037526e+07
TOF MULT
TOF tray

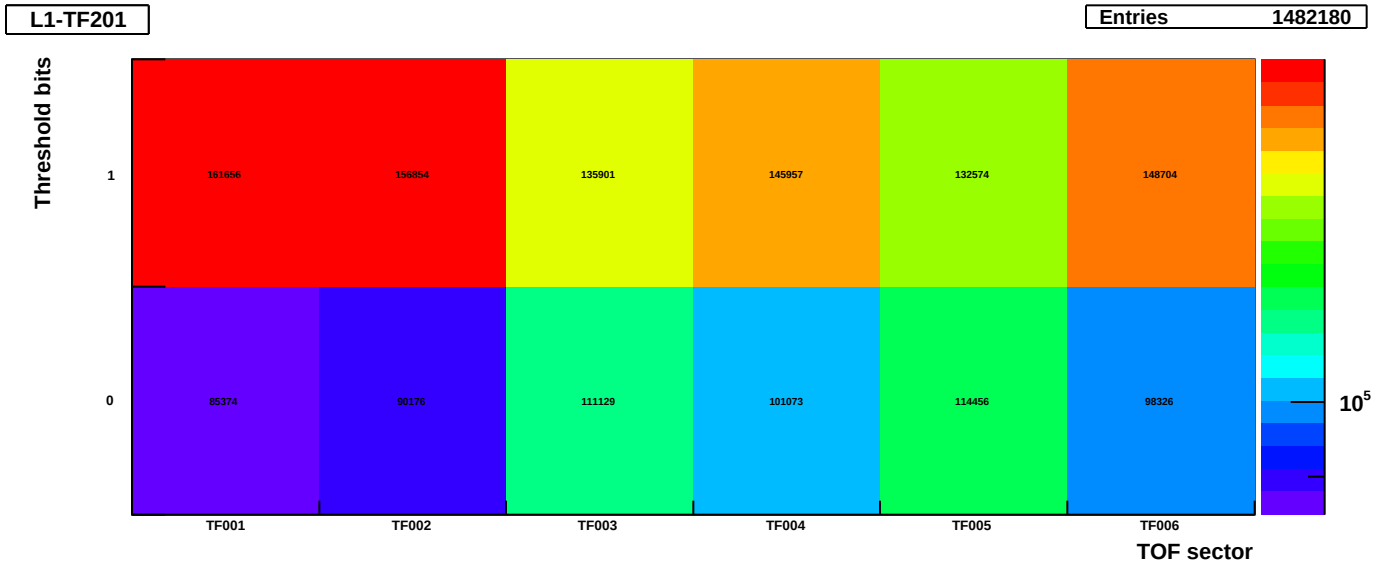

MIX-TF004
Entries 1.037526e+07
TOF MULT
TOF tray

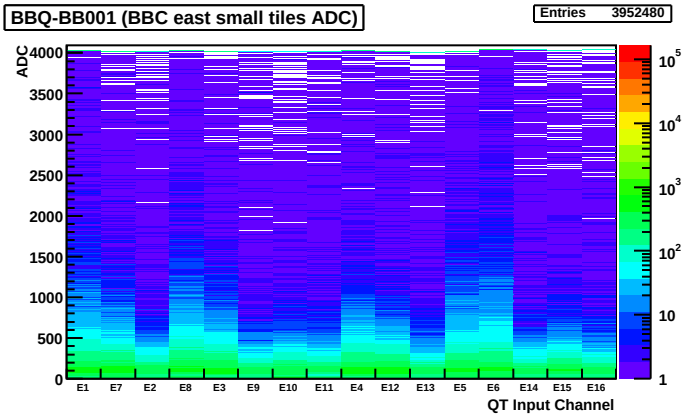

MIX-TF005
Entries 1.037526e+07
TOF MULT
TOF tray

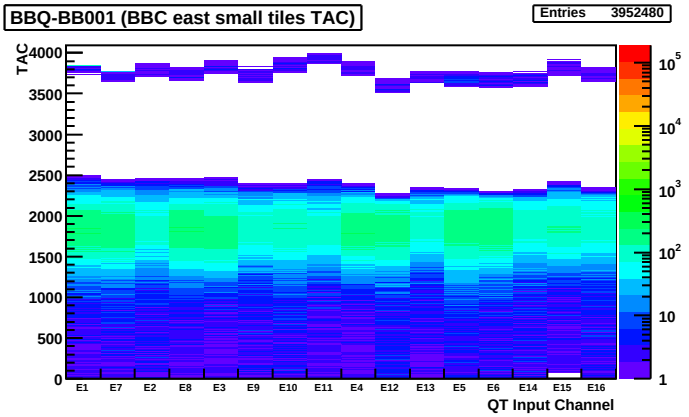

MIX-TF006
Entries 1.037526e+07
TOF MULT
TOF tray

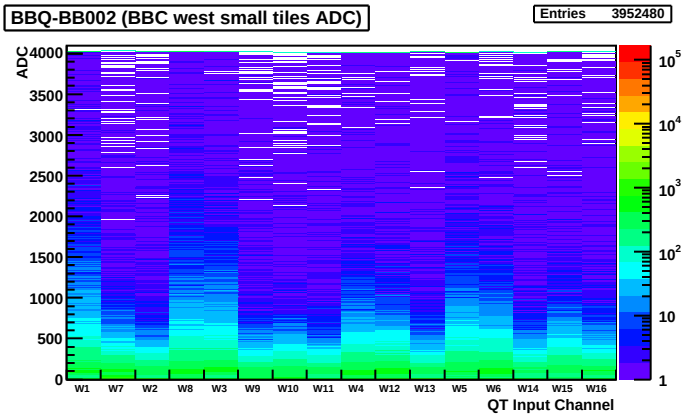

MIX-TF101
Entries 1482180
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

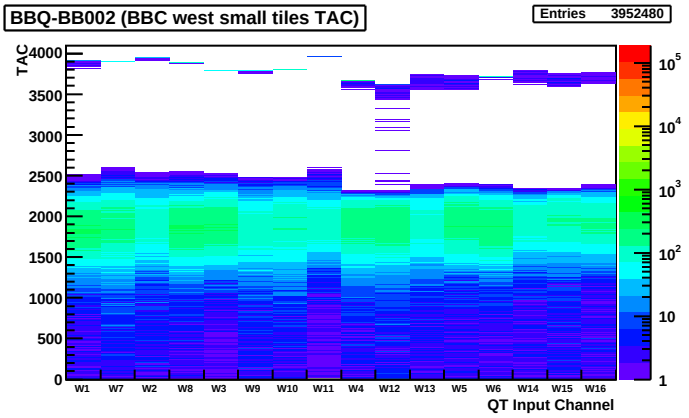

L1-TF201
Entries 247030
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

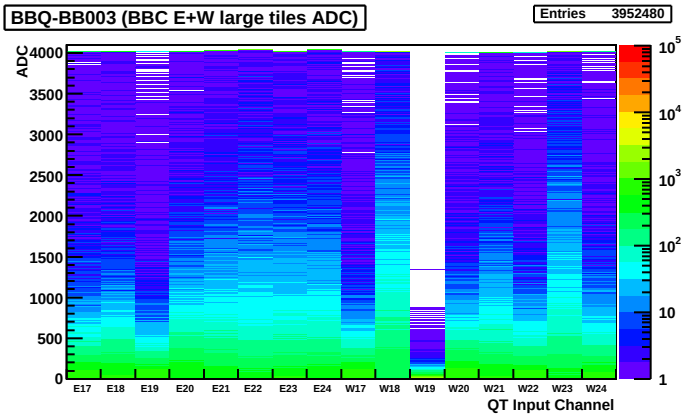

L1-TF201
Entries 1482180
Threshold bits
1
0
161656
156854
135901
145957
132574
148704
85374
90176
111129
101073
114456
98326
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

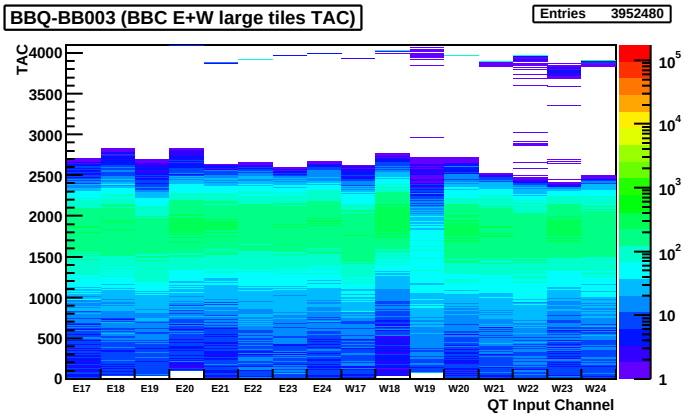

BBQ-BB001 (BBC east small tiles ADC)
Entries 3952480
ADC
QT Input Channel

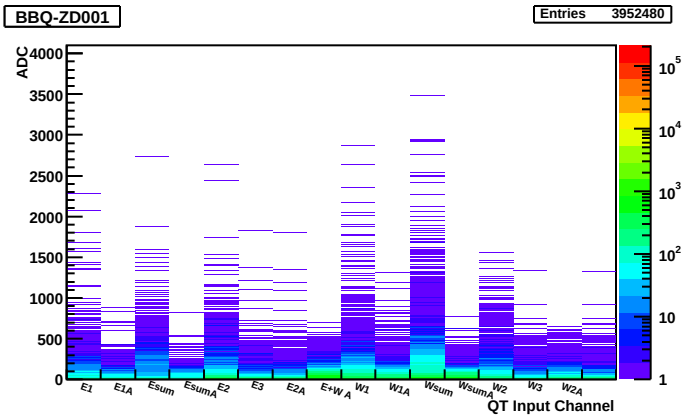

BBQ-BB001 (BBC east small tiles TAC)
Entries 3952480
TAC
QT Input Channel

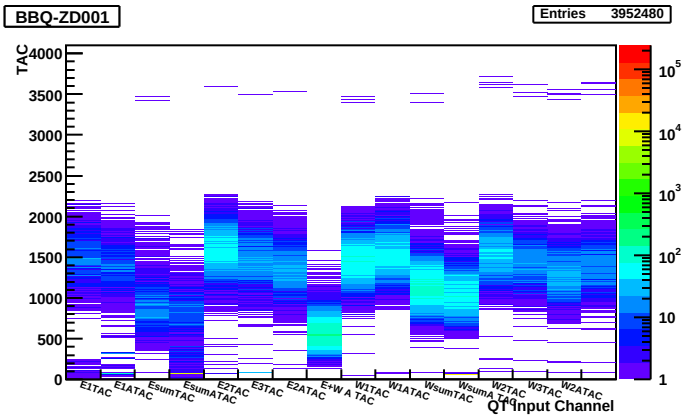

BBQ-BB002 (BBC west small tiles ADC)
Entries 3952480
ADC
QT Input Channel

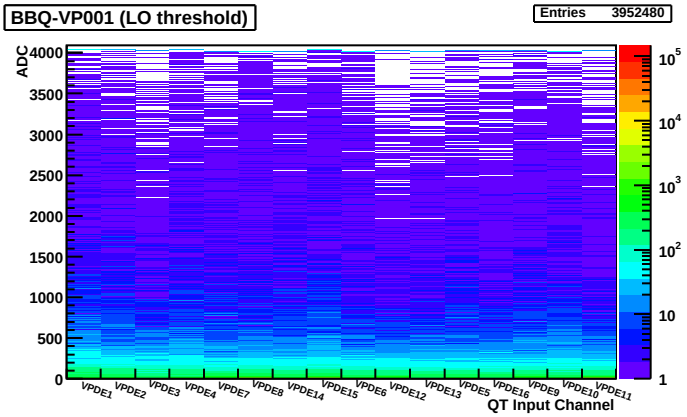

BBQ-BB002 (BBC west small tiles TAC)
Entries 3952480
TAC
QT Input Channel

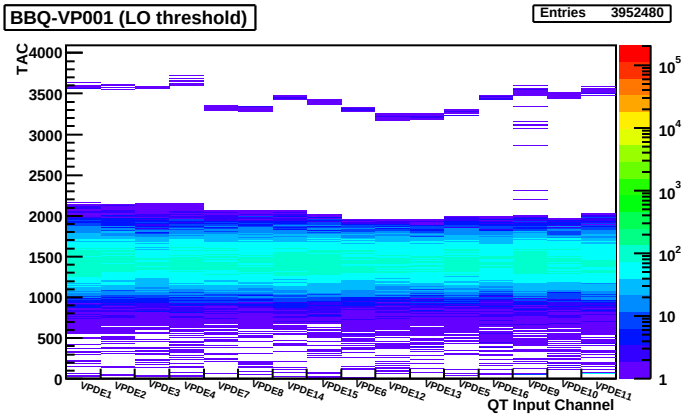

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 3952480
ADC
QT Input Channel

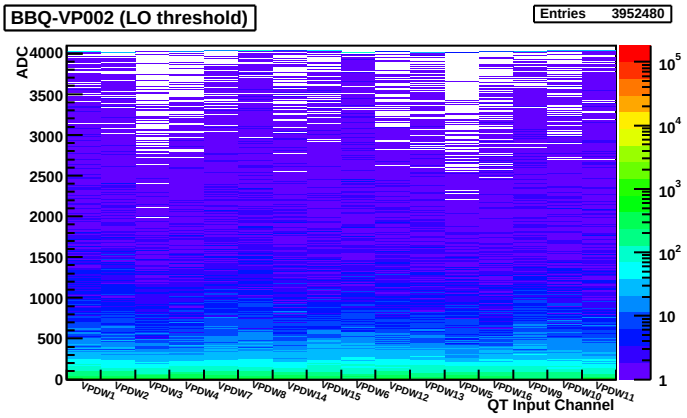

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 3952480
TAC
QT Input Channel

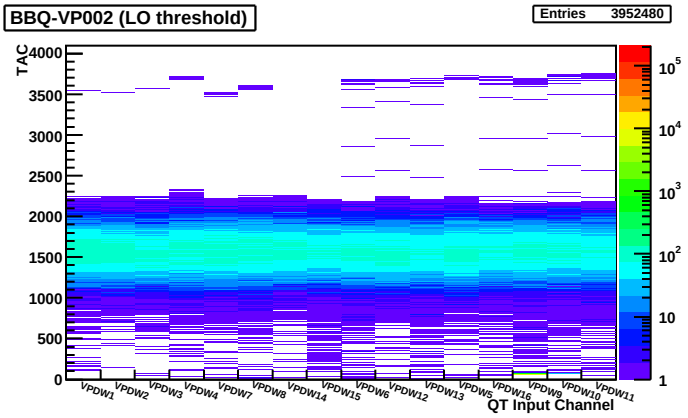

BBQ-ZD001
Entries 3952480
ADC
QT Input Channel

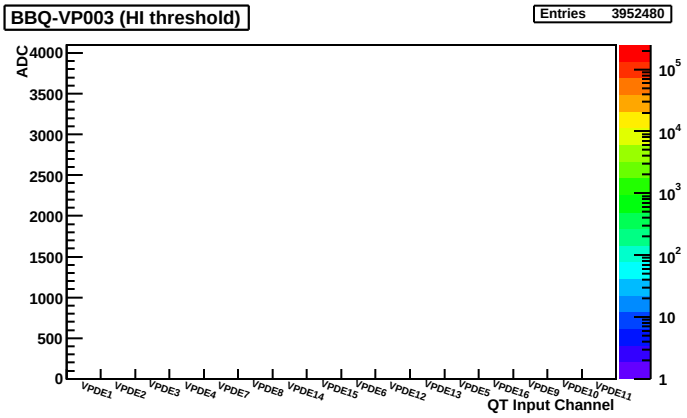

BBQ-ZD001
Entries 3952480
TAC
QT Input Channel

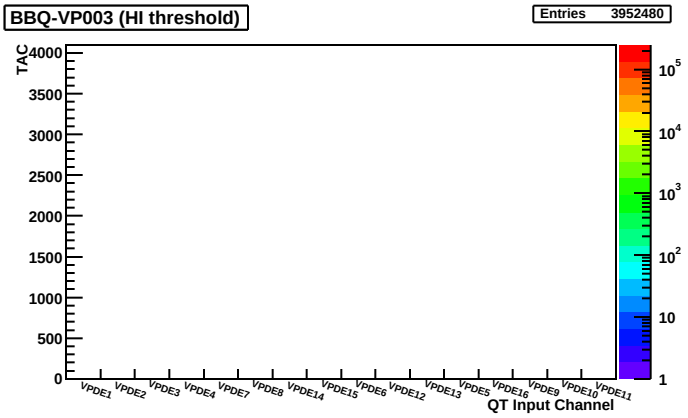

BBQ-VP001 (LO threshold)
Entries 3952480
ADC
QT Input Channel

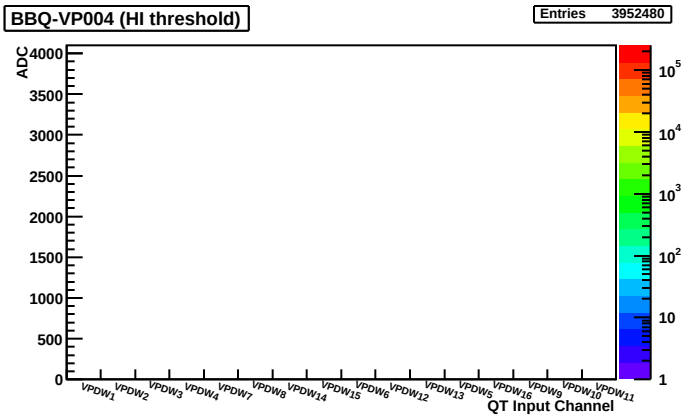

BBQ-VP001 (LO threshold)
Entries 3952480
TAC
QT Input Channel

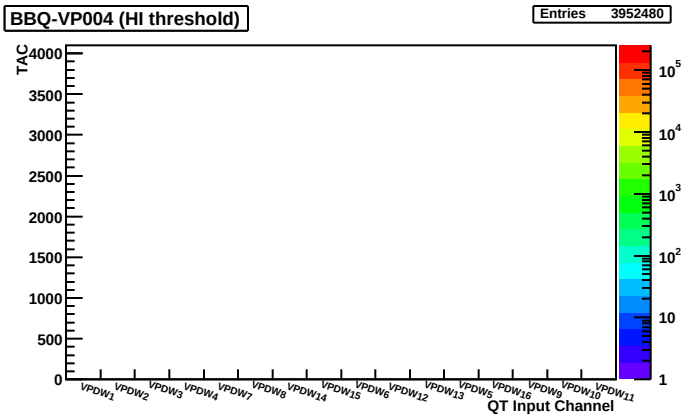

BBQ-VP002 (LO threshold)
Entries 3952480
ADC
QT Input Channel

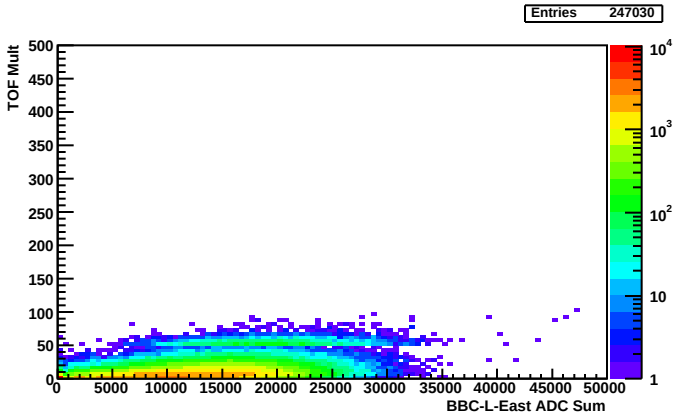

BBQ-VP002 (LO threshold)
Entries 3952480
TAC
QT Input Channel

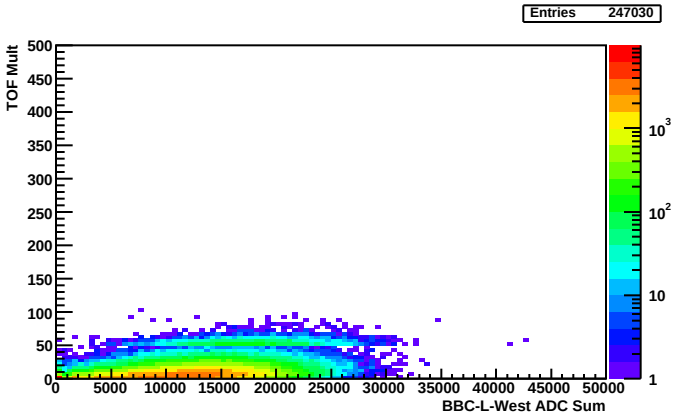

BBQ-VP003 (HI threshold)
Entries 3952480
ADC
QT Input Channel

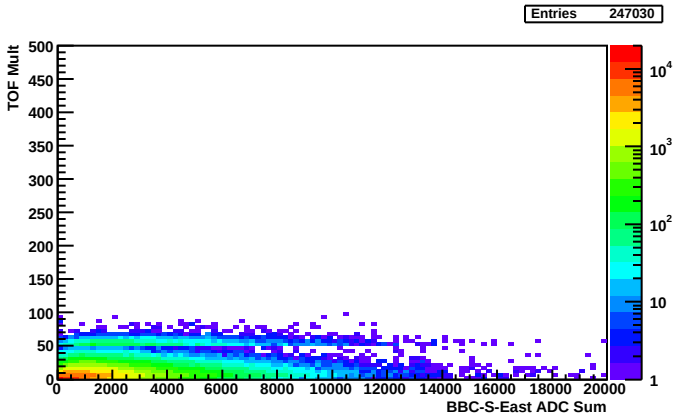

BBQ-VP003 (HI threshold)
Entries 3952480
TAC
QT Input Channel

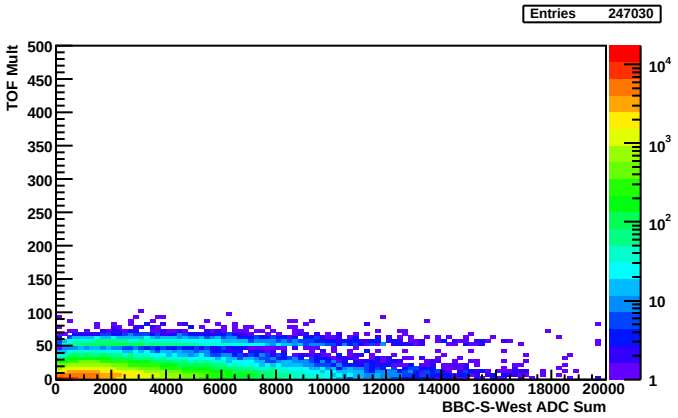

BBQ-VP004 (HI threshold)
Entries 3952480
ADC
QT Input Channel

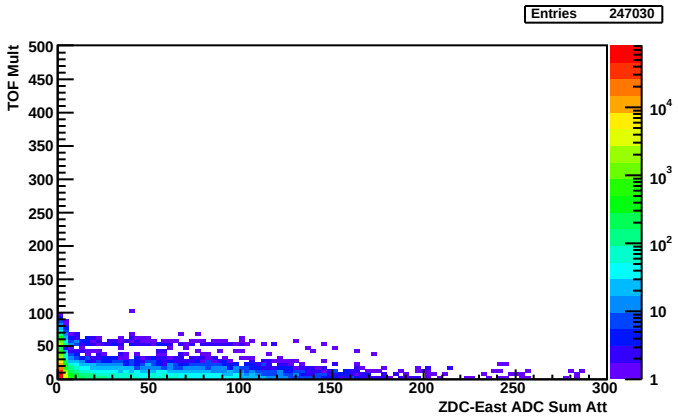

BBQ-VP004 (HI threshold)
Entries 3952480
TAC
QT Input Channel

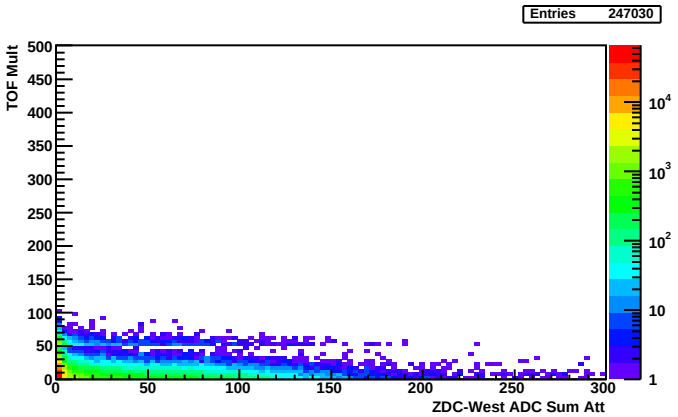

Entries 247030
TOF Mult
BBC-L-East ADC Sum

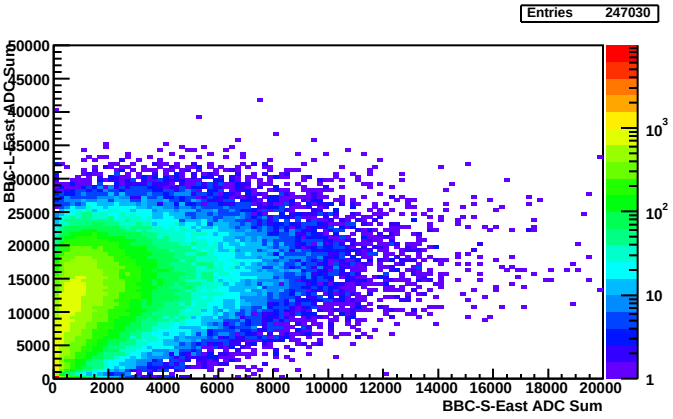

Entries 247030
TOF Mult
BBC-L-West ADC Sum

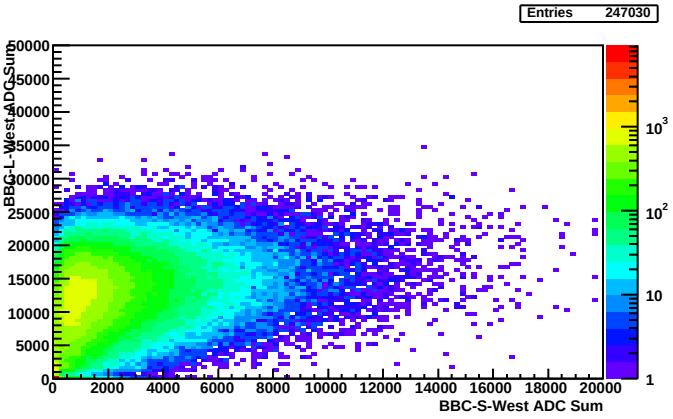

Entries 247030
TOF Mult
BBC-S-East ADC Sum

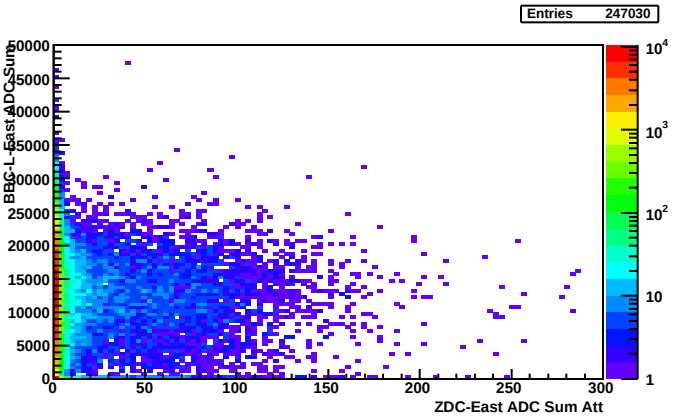

Entries 247030
TOF Mult
BBC-S-West ADC Sum

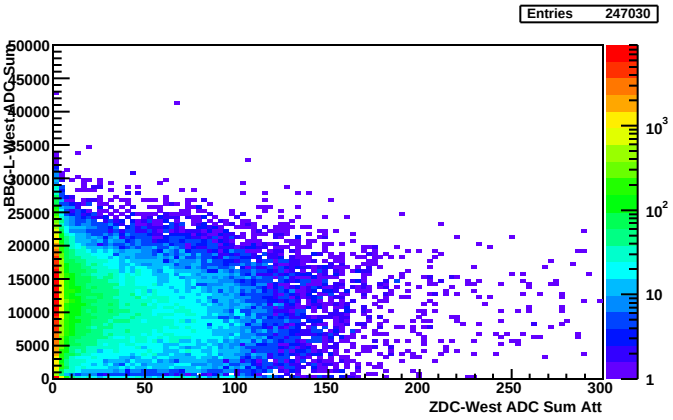

Entries 247030
TOF Mult
ZDC-East ADC Sum Att

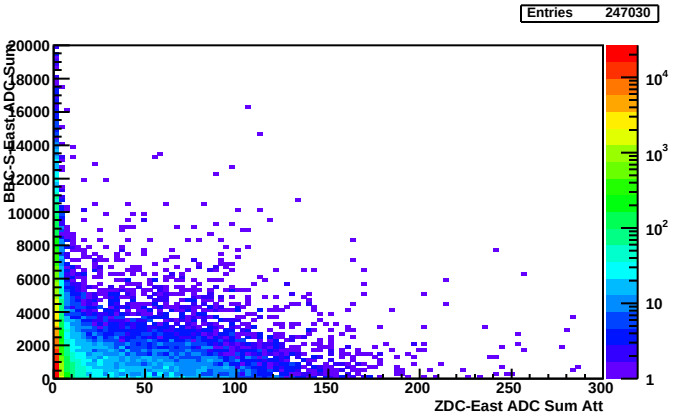

Entries 247030
TOF Mult
ZDC-West ADC Sum Att

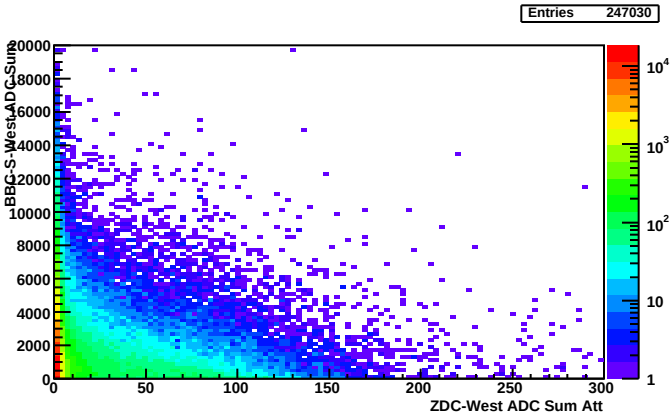

Entries 247030
BBC-L-East ADC Sum
BBC-S-East ADC Sum

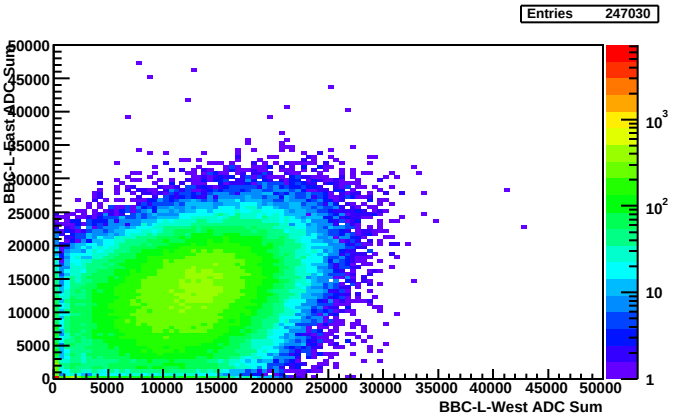

Entries 247030
BBC-L-West ADC Sum
BBC-S-West ADC Sum

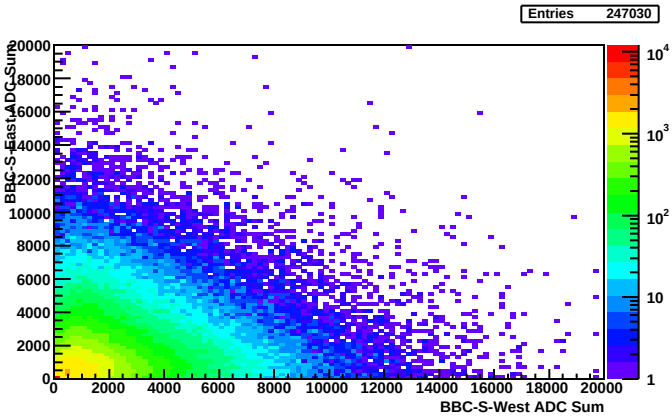

Entries 247030
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

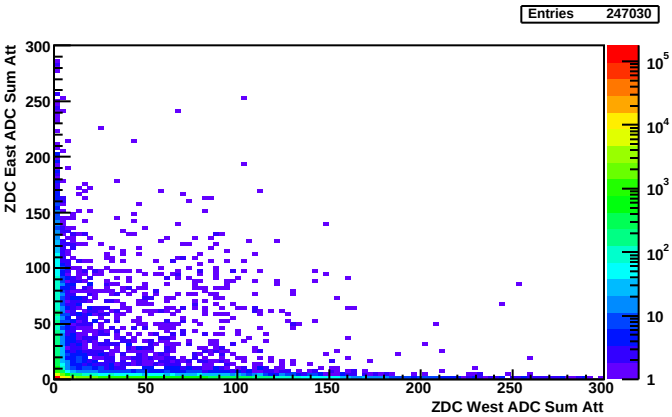

Entries 247030
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

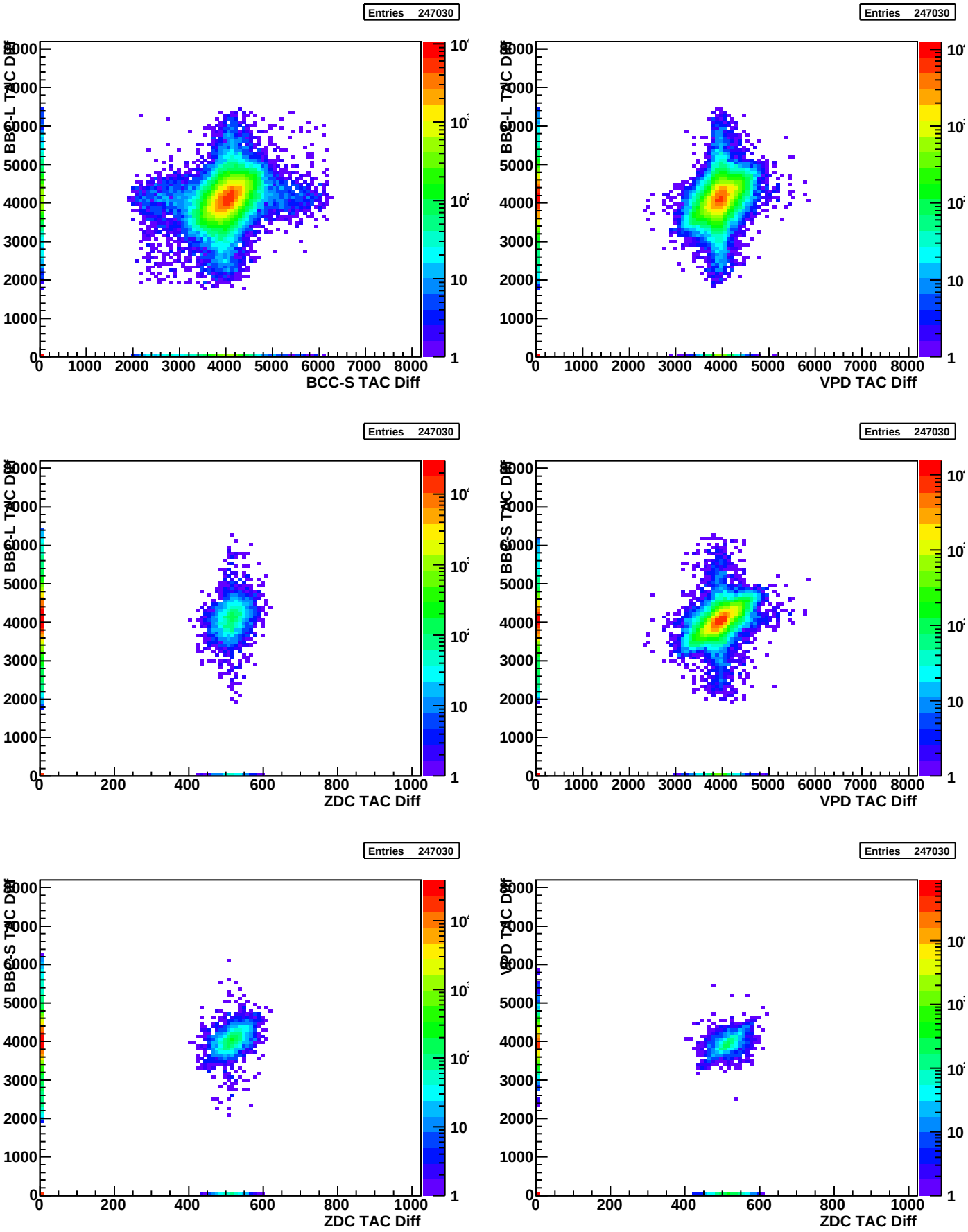

Entries 247030
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

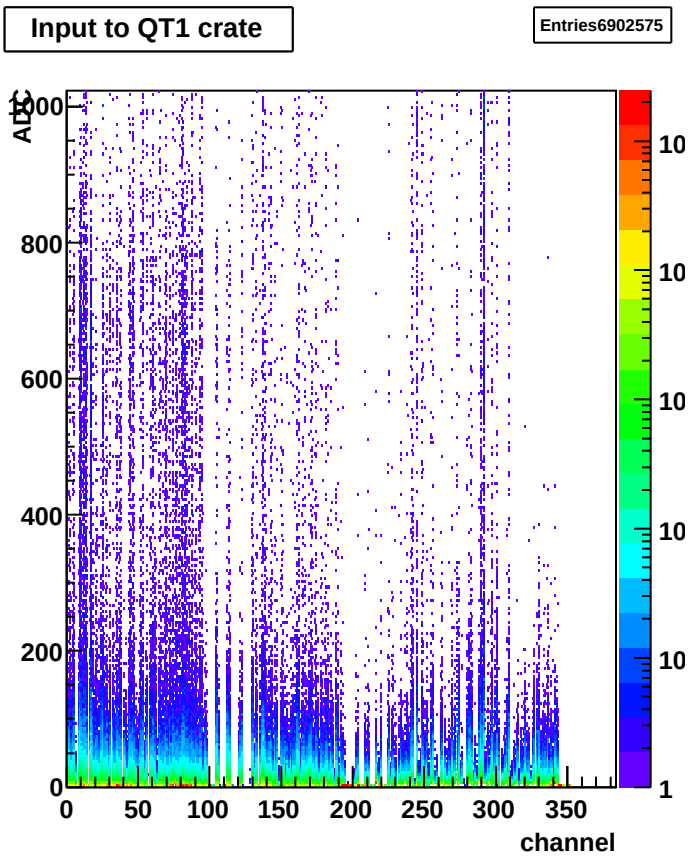

Entries 247030
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

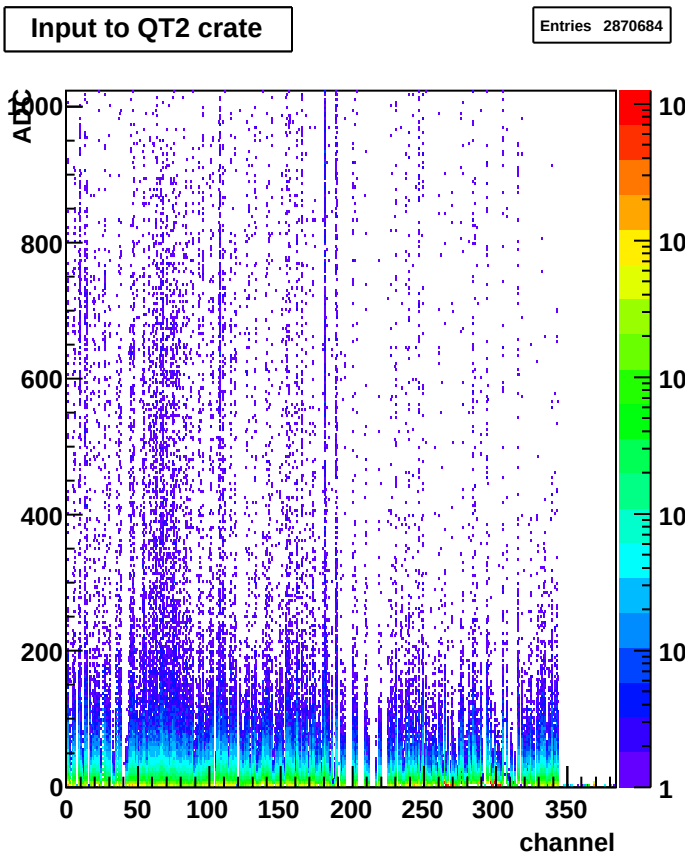

Entries 247030
BBC-L-East ADC Sum
BBC-L-West ADC Sum

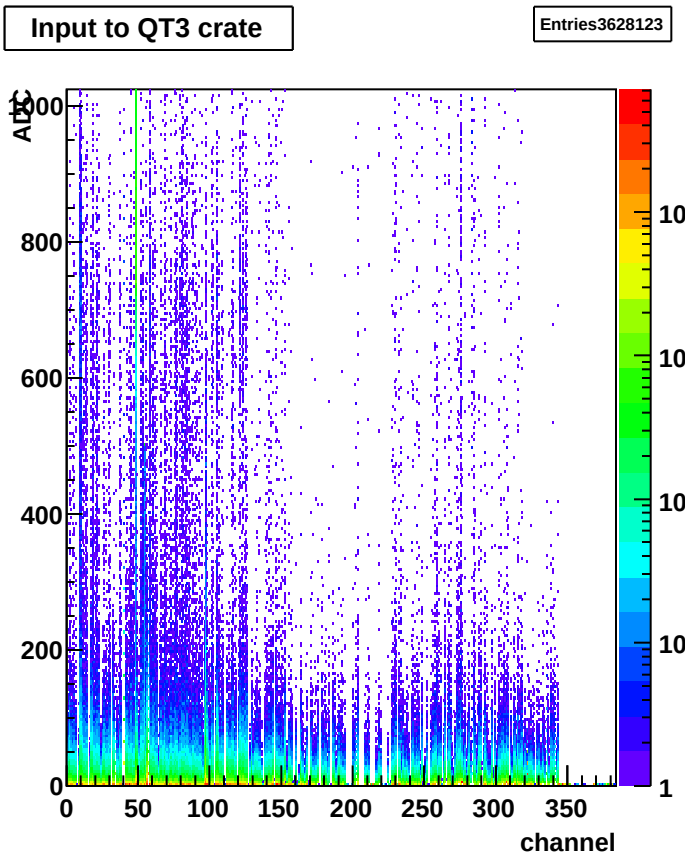

Entries 247030
BBC-S-East ADC Sum
BBC-S-West ADC Sum

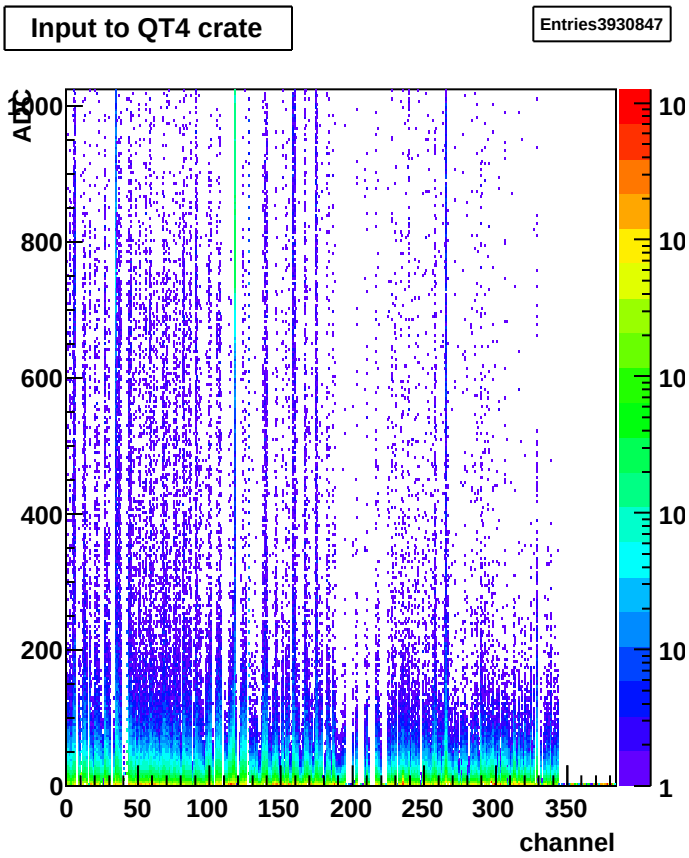

Entries 247030
ZDC East ADC Sum Att
ZDC West ADC Sum Att

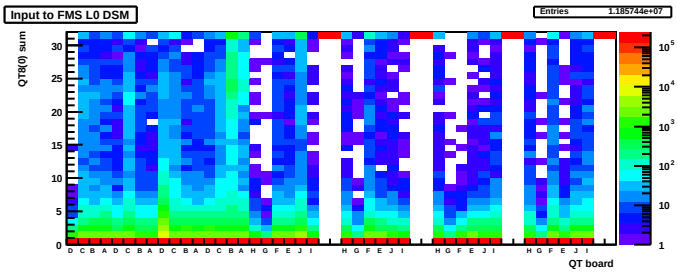

Entries 247030
BBC-L TAC Diff
BCC-S TAC Diff
Entries 247030
BBC-L TAC Diff
VPD TAC Diff
Entries 247030
BBC-L TAC Diff
ZDC TAC Diff
Entries 247030
BBC-S TAC Diff
VPD TAC Diff
Entries 247030
BBC-S TAC Diff
ZDC TAC Diff
Entries 247030
VPD TAC Diff
ZDC TAC Diff

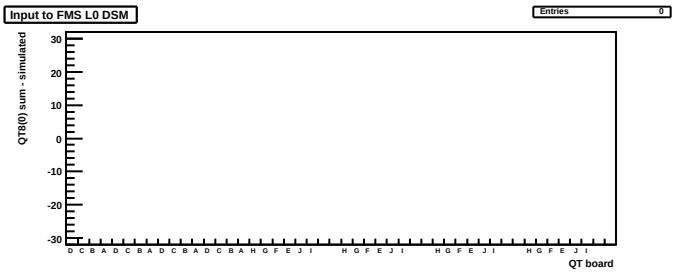

Input to QT1 crate
Entries6902575
ADC
channel

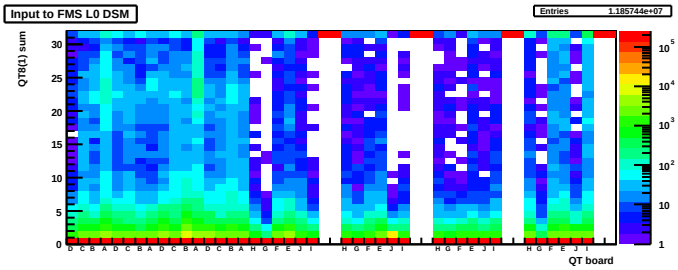

Input to QT2 crate
Entries 2870684
ADC
channel

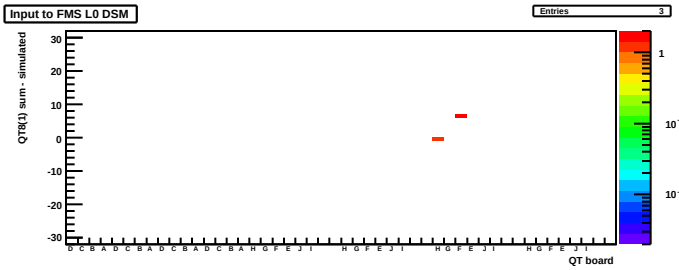

Input to QT3 crate
Entries3628123
ADC
channel

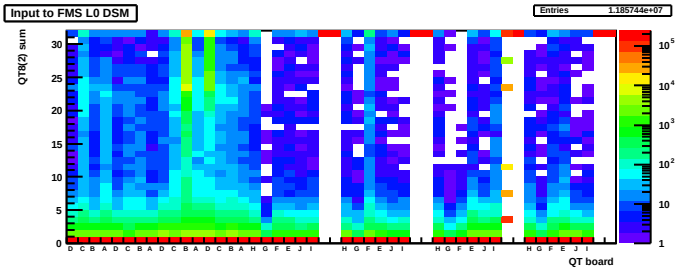

Input to QT4 crate
Entries3930847
ADC
channel

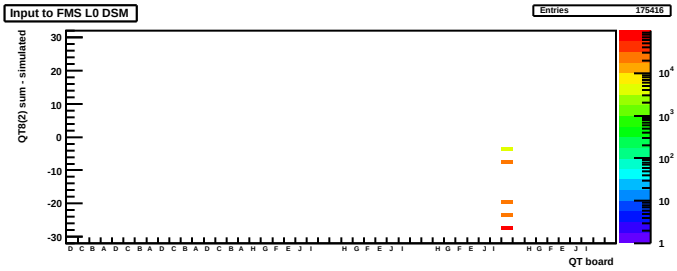

Input to FMS L0 DSM
Entries 1.185744e+07
QT8(0) sum
QT board

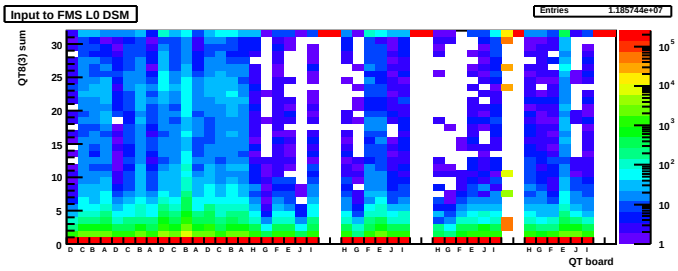

Input to FMS L0 DSM
Entries 0
QT8(0) sum - simulated
QT board

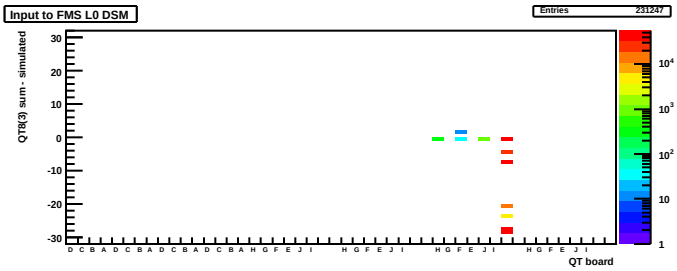

Input to FMS L0 DSM
Entries 1.185744e+07
QT8(1) sum
QT board

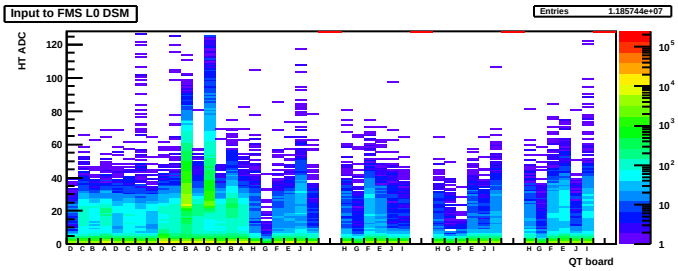

Input to FMS L0 DSM
Entries 3
QT8(1) sum - simulated
QT board

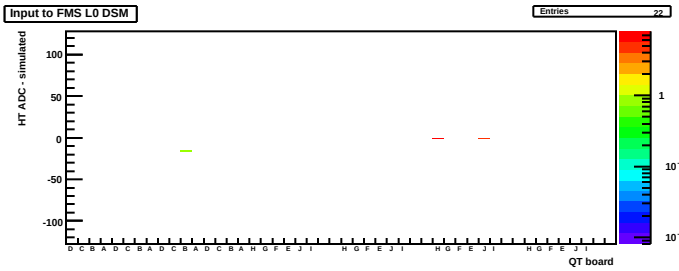

Input to FMS L0 DSM
Entries 1.185744e+07
QT8(2) sum
QT board

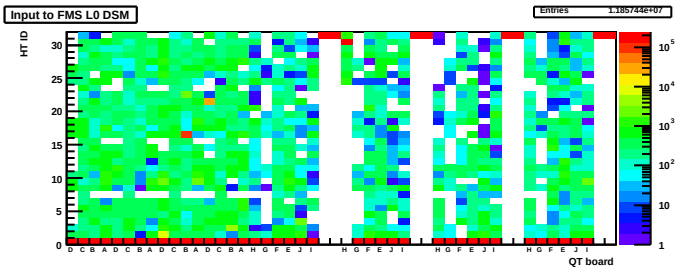

Input to FMS L0 DSM
Entries 175416
QT8(2) sum - simulated
QT board

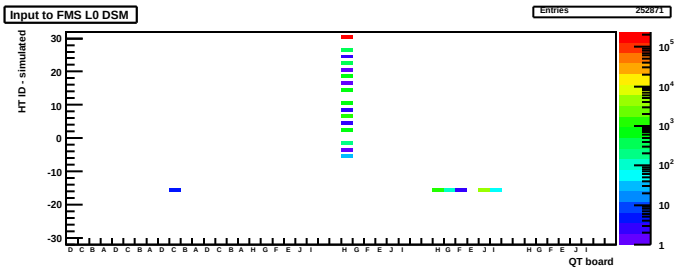

Input to FMS L0 DSM
Entries 1.185744e+07
QT8(3) sum
QT board

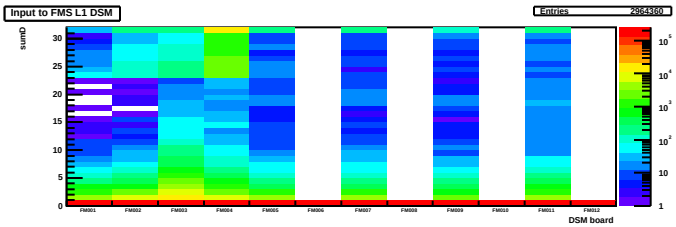

Input to FMS L0 DSM
Entries 231247
QT8(3) sum - simulated
QT board

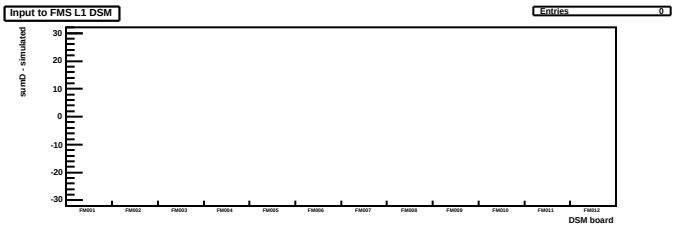

Input to FMS L0 DSM
Entries 1.185744e+07
HT ADC
QT board

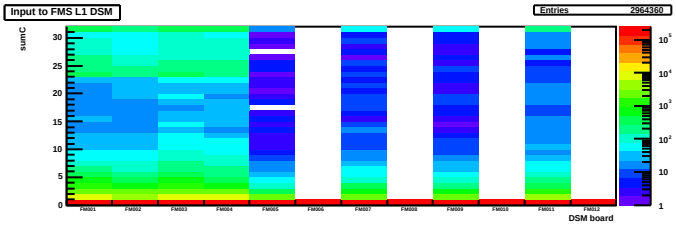

Input to FMS L0 DSM
Entries 22
HT ADC - simulated
QT board

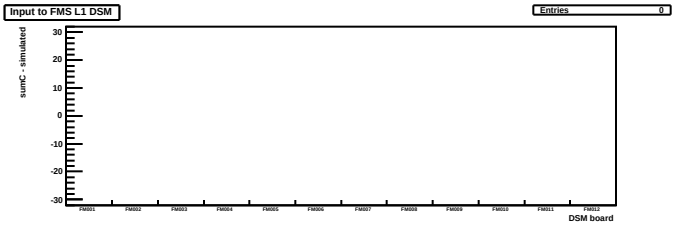

Input to FMS L0 DSM
Entries 1.185744e+07
HT ID
QT board

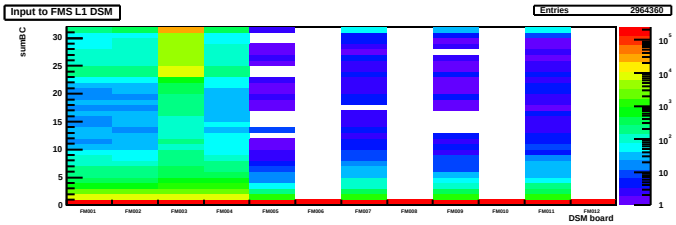

Input to FMS L0 DSM
Entries 252871
HT ID - simulated
QT board

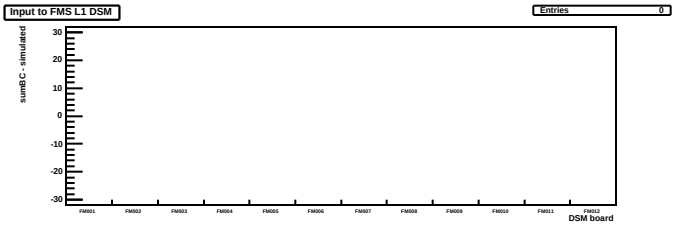

Input to FMS L1 DSM
Entries 2964360
sumD
DSM board

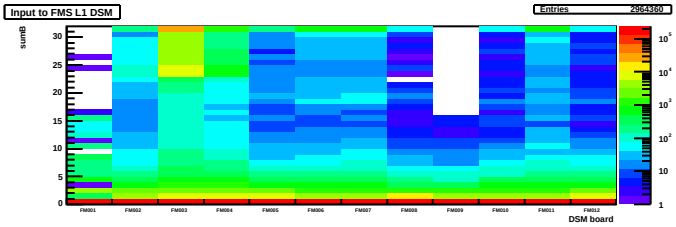

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

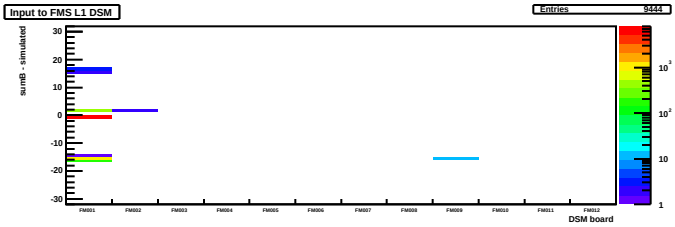

Input to FMS L1 DSM
Entries 2964360
sumC
DSM board

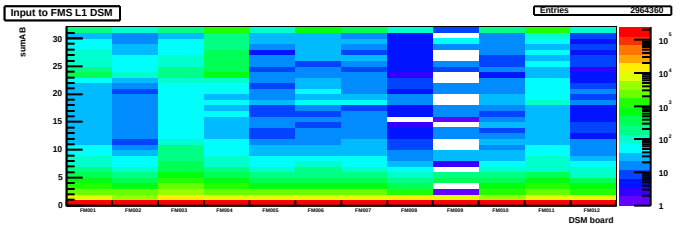

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

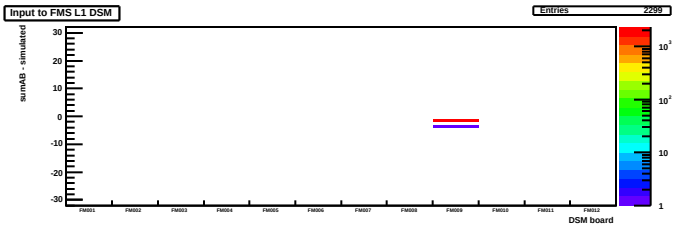

Input to FMS L1 DSM
Entries 2964360
sumBC
DSM board

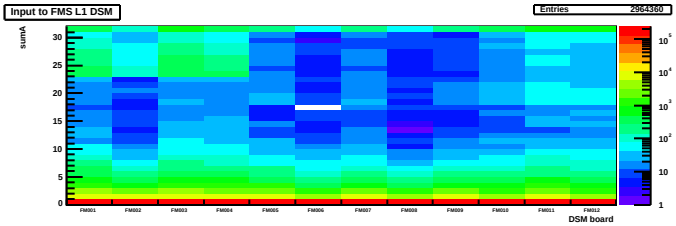

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

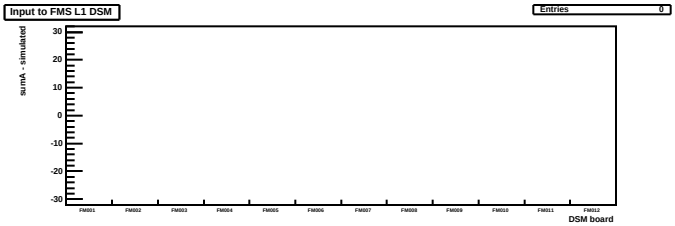

Input to FMS L1 DSM
Entries 2964360
sumB
DSM board

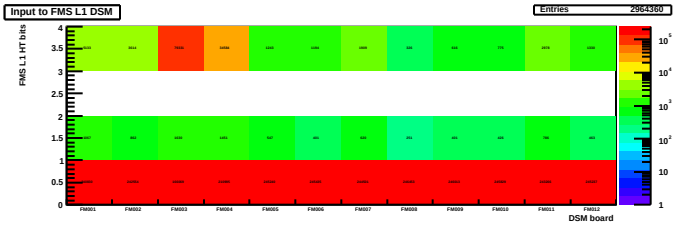

Input to FMS L1 DSM
Entries 9444
sumB - simulated
DSM board

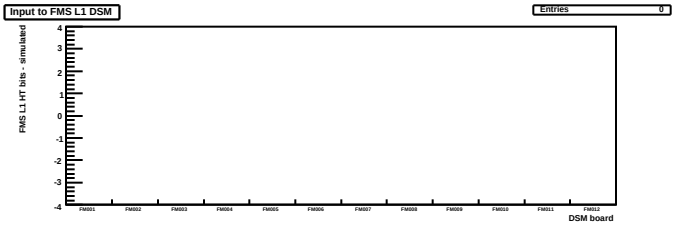

Input to FMS L1 DSM
Entries 2964360
sumAB
DSM board

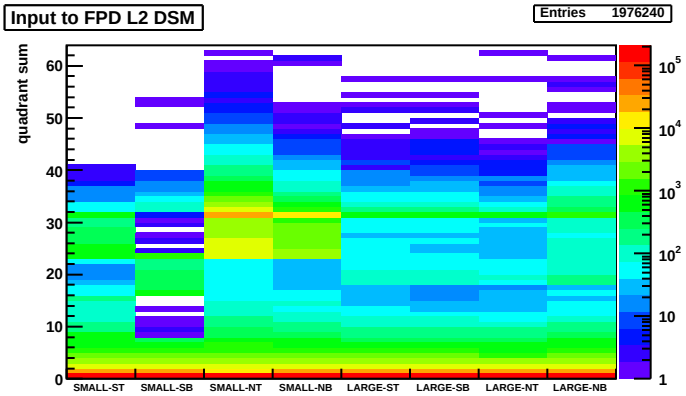

Input to FMS L1 DSM
Entries 2299
sumAB - simulated
DSM board

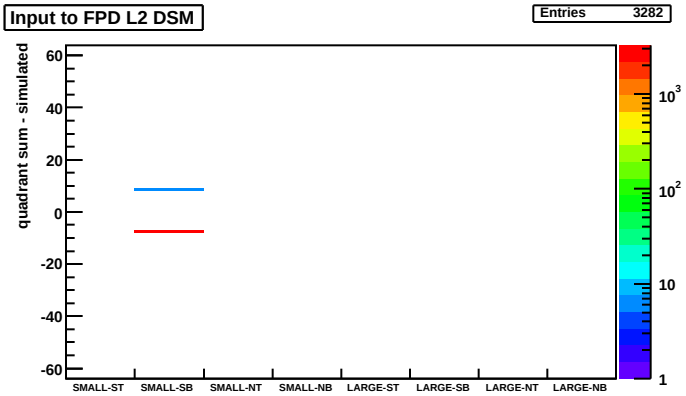

Input to FMS L1 DSM
Entries 2964360
sumA
DSM board

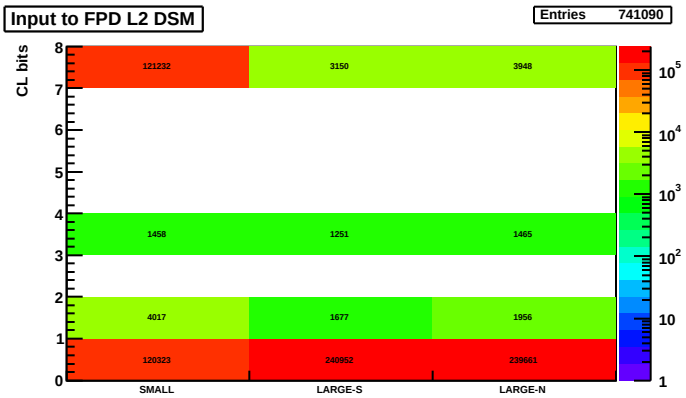

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

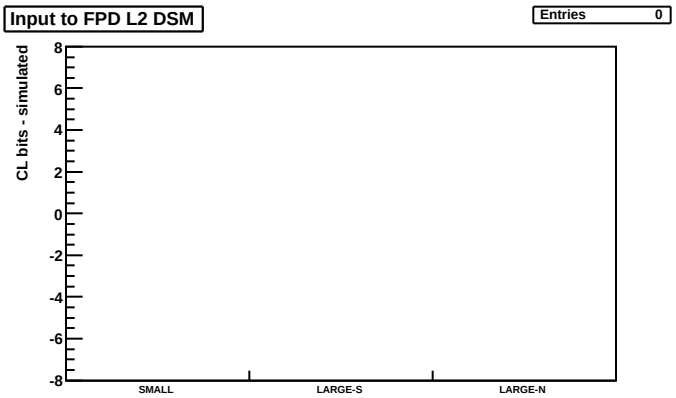

Input to FMS L1 DSM
Entries 2964360
FMS L1 HT bits
DSM board

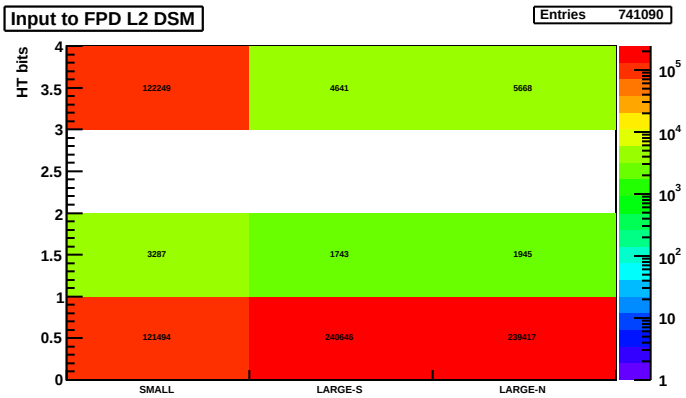

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

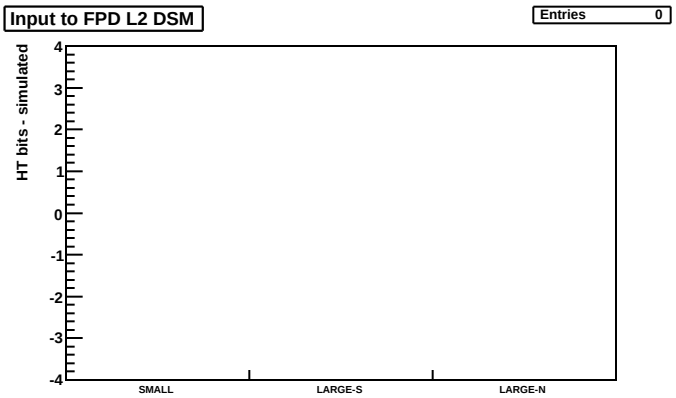

Input to FPD L2 DSM
Entries 1976240
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

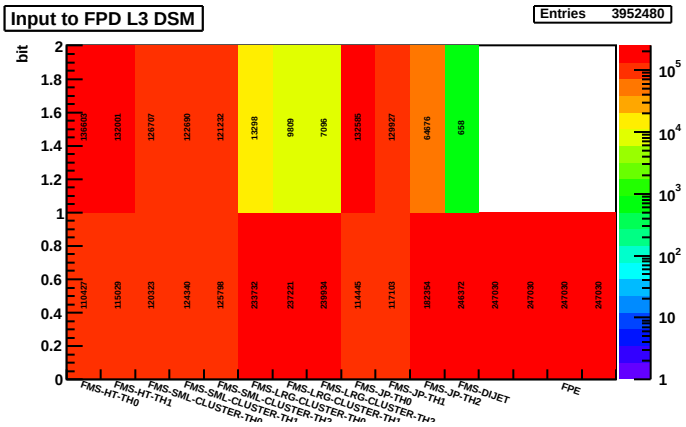

Input to FPD L2 DSM
Entries 3282
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

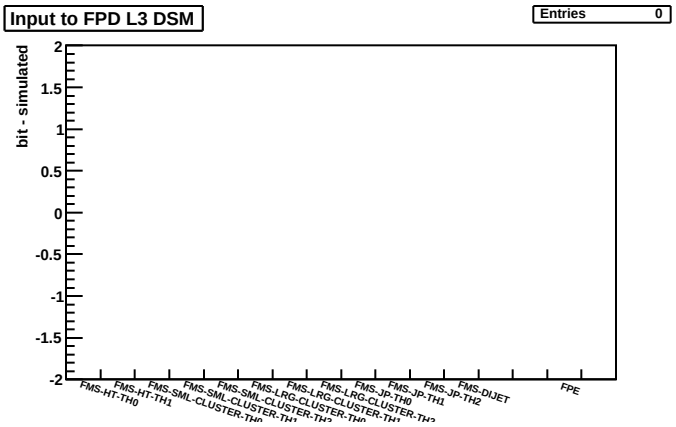

Input to FPD L2 DSM
Entries 741090
CL bits
121232
3150
3948
1458
1251
1465
4017
1677
1956
120323
240952
239661
SMALL
LARGE-S
LARGE-N

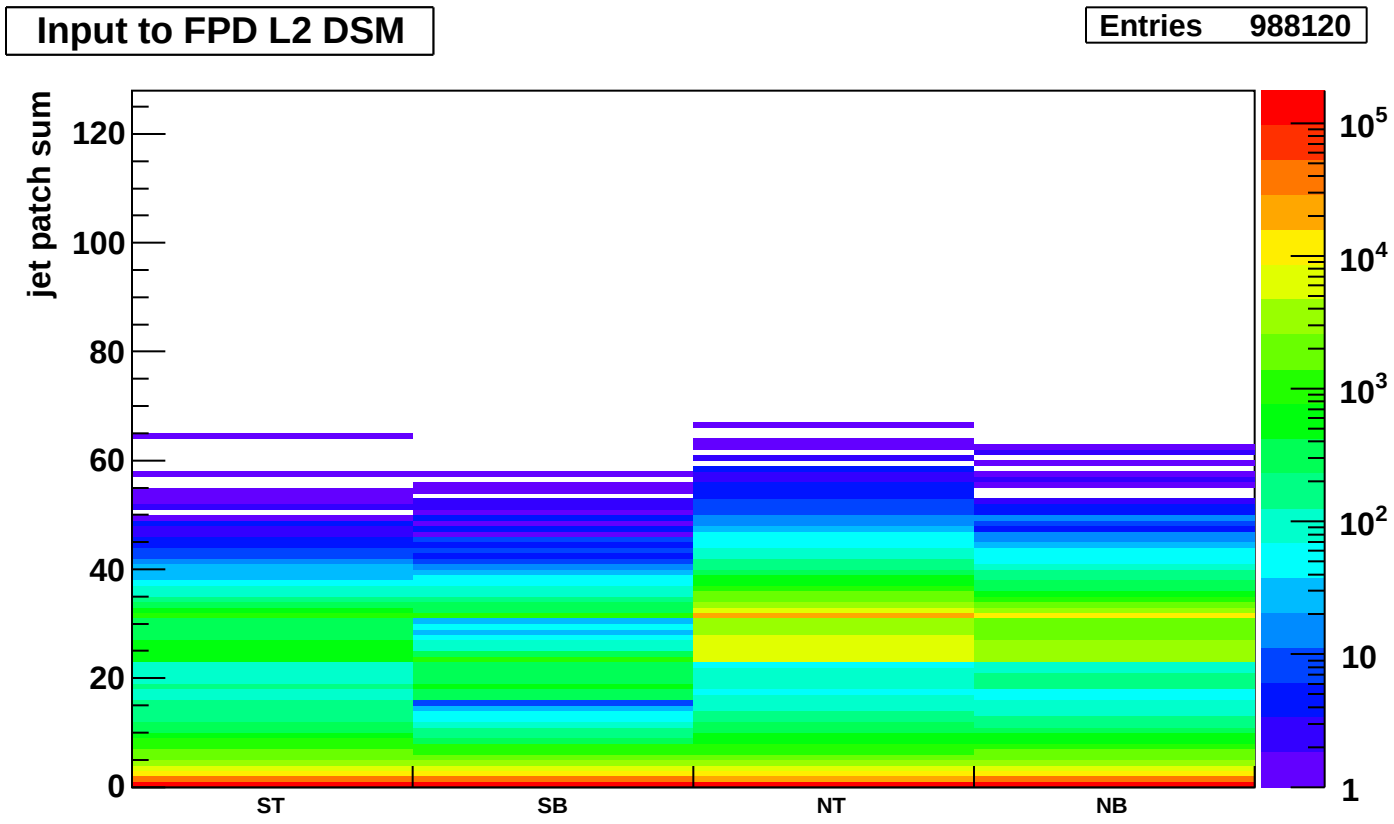

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

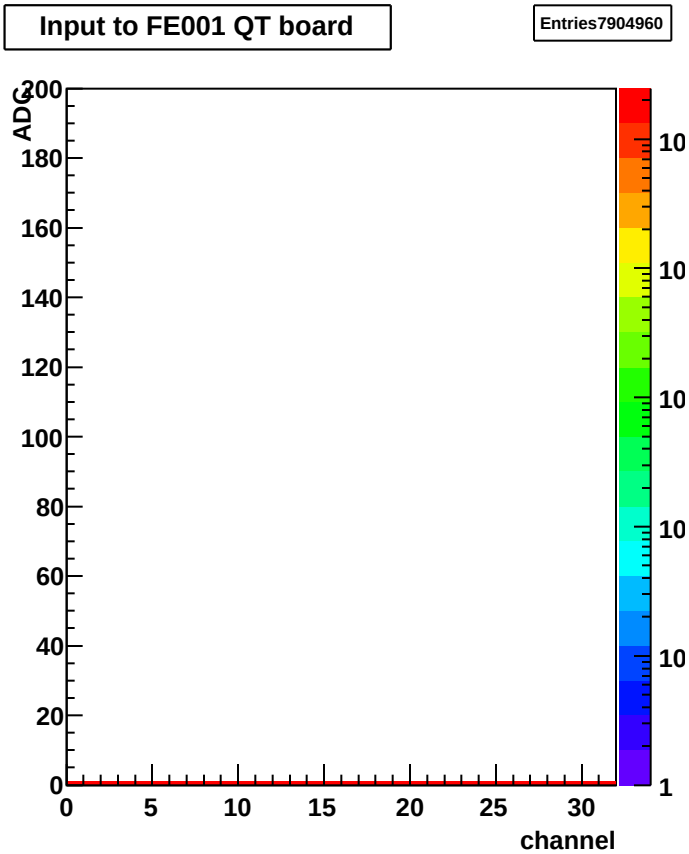

Input to FPD L2 DSM
Entries 741090
HT bits
122249
4641
5668
3287
1743
1945
121494
240646
239417
SMALL
LARGE-S
LARGE-N

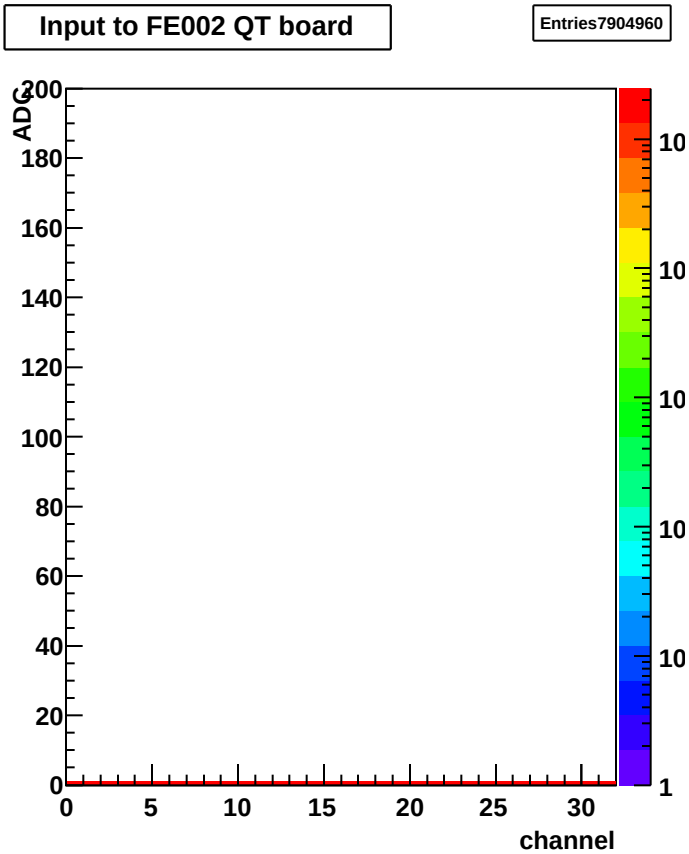

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

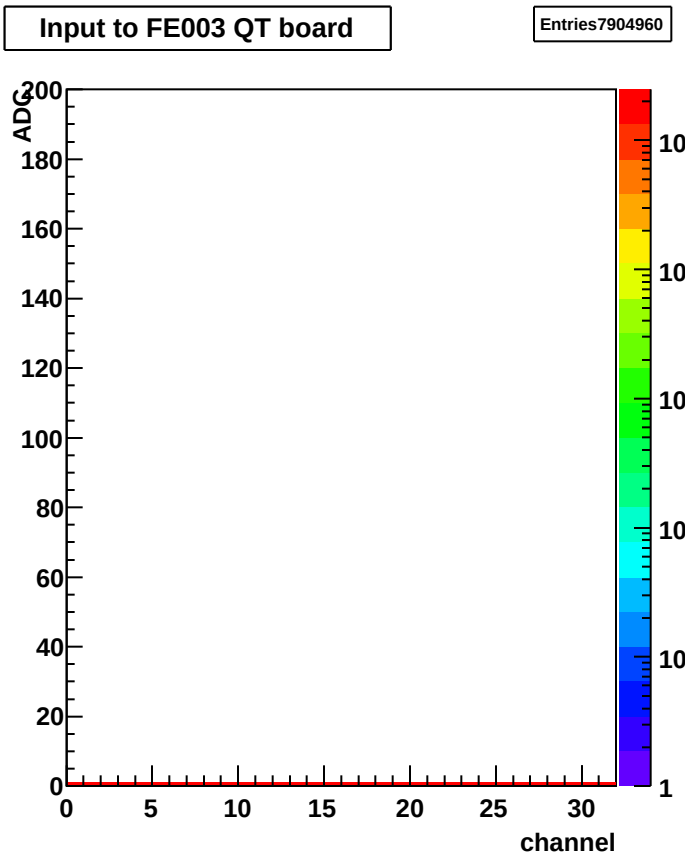

Input to FPD L3 DSM
Entries 3952480
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

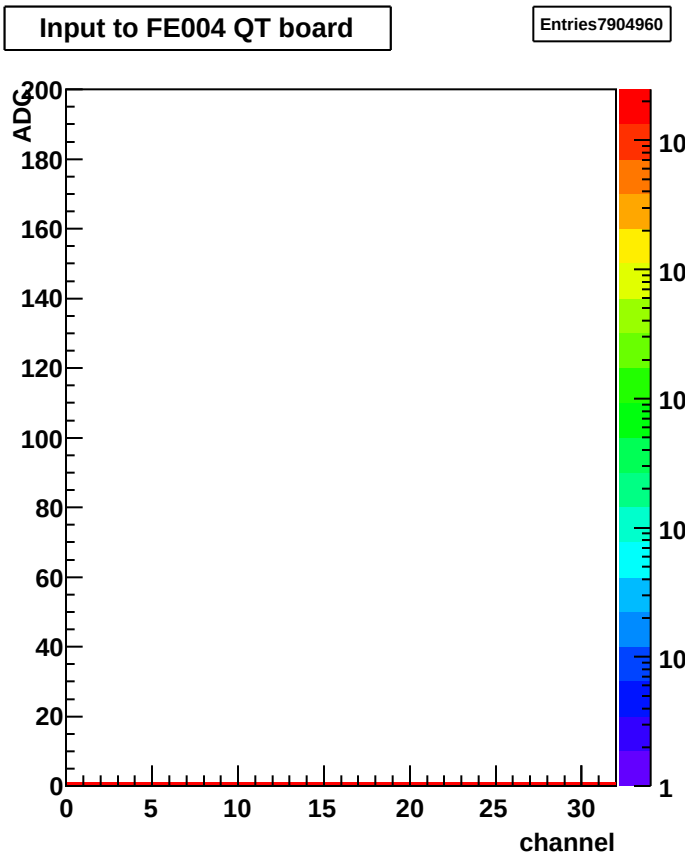

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

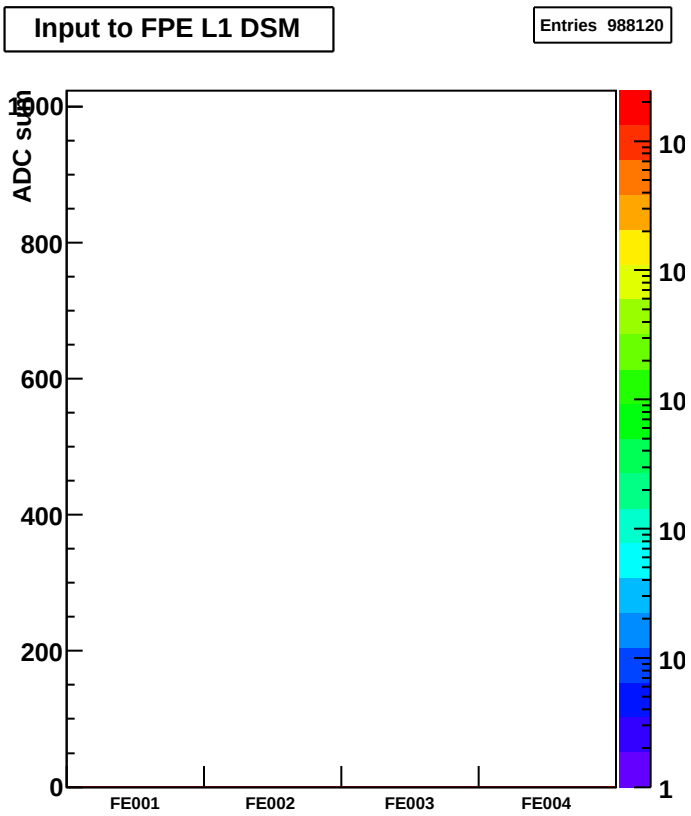

Input to FPD L2 DSM
Entries 988120
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

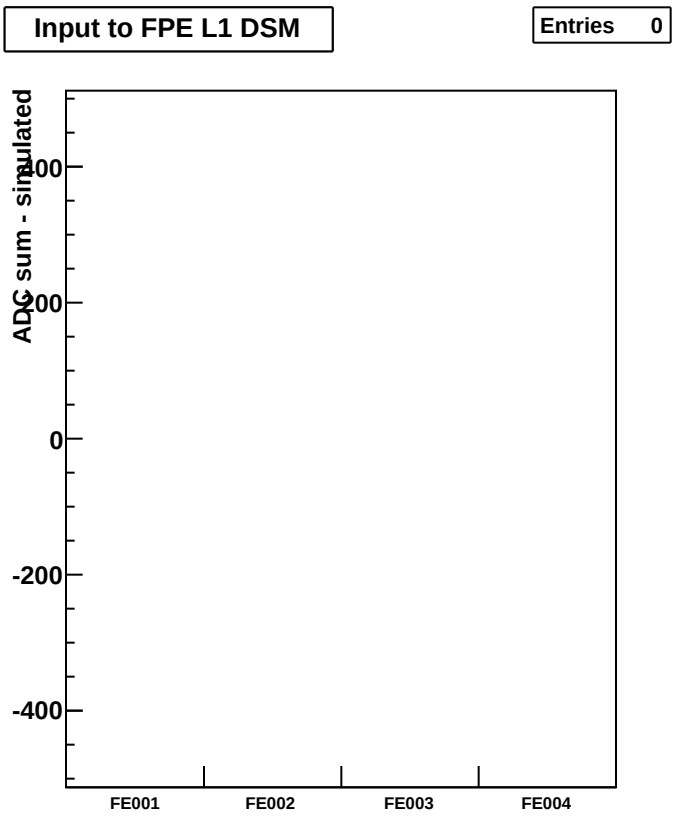

Input to FE001 QT board
Entries7904960
ADC
channel

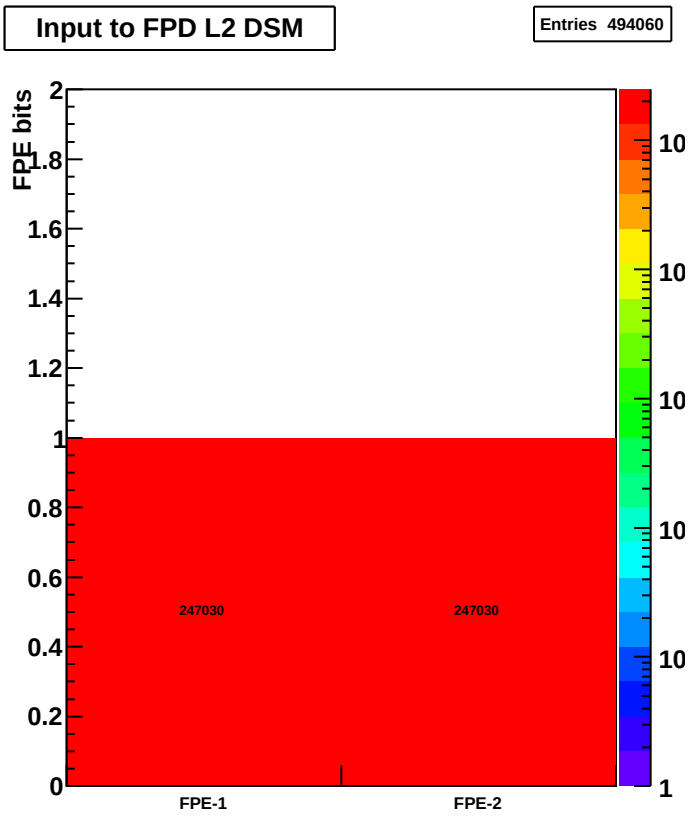

Input to FE002 QT board
Entries7904960
ADC
channel

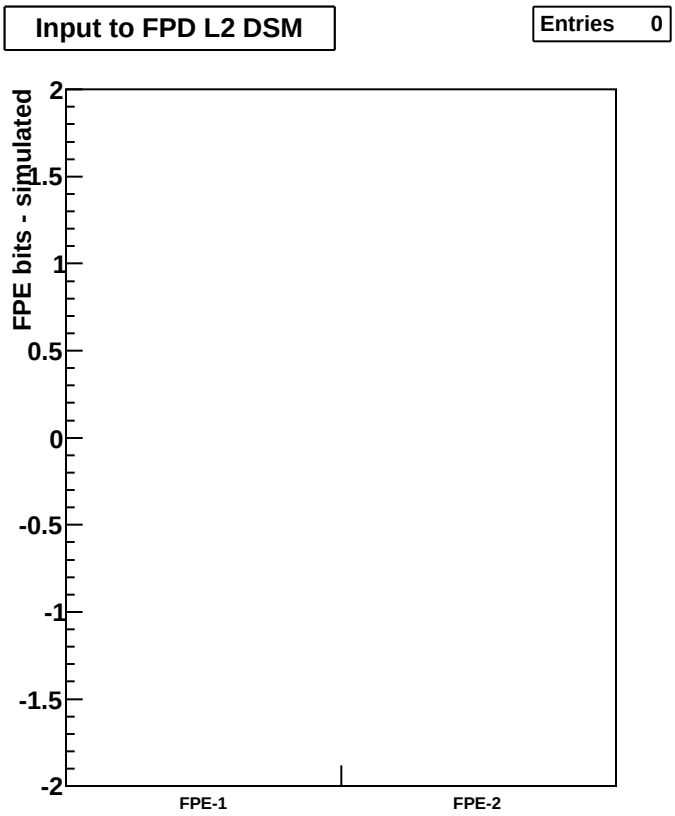

Input to FE003 QT board
Entries7904960
ADC
channel


Input to FE004 QT board
Entries7904960
ADC
channel


Input to FPE L1 DSM
Entries 988120
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 494060
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
247030
247030
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2