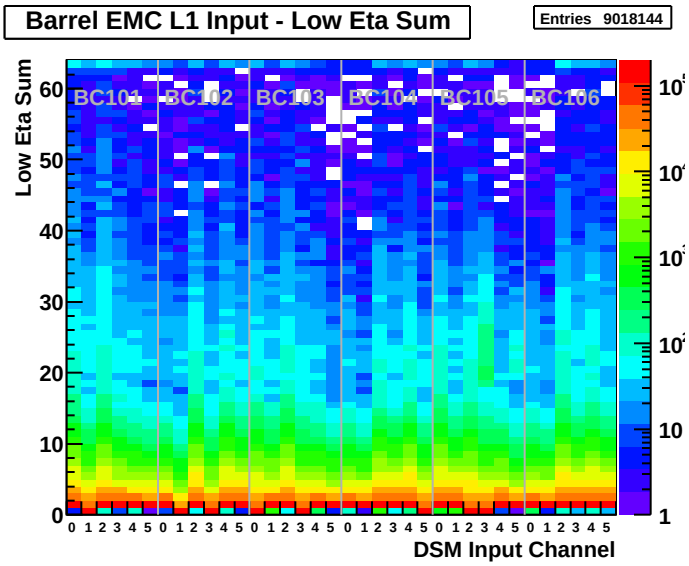
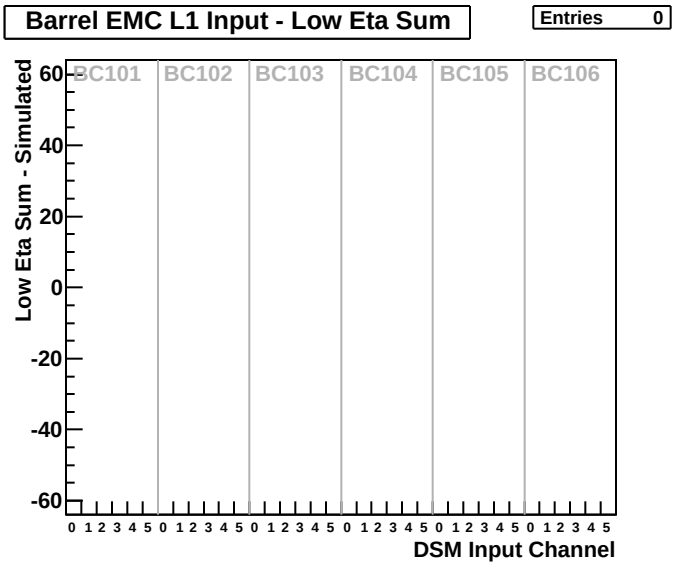
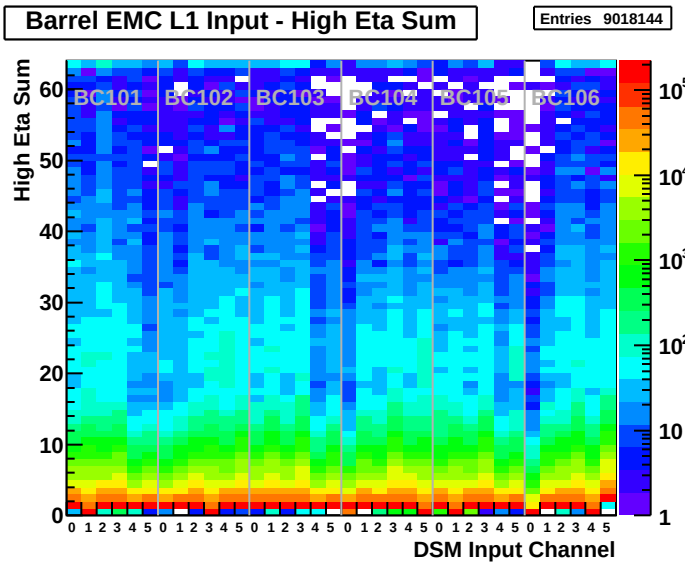
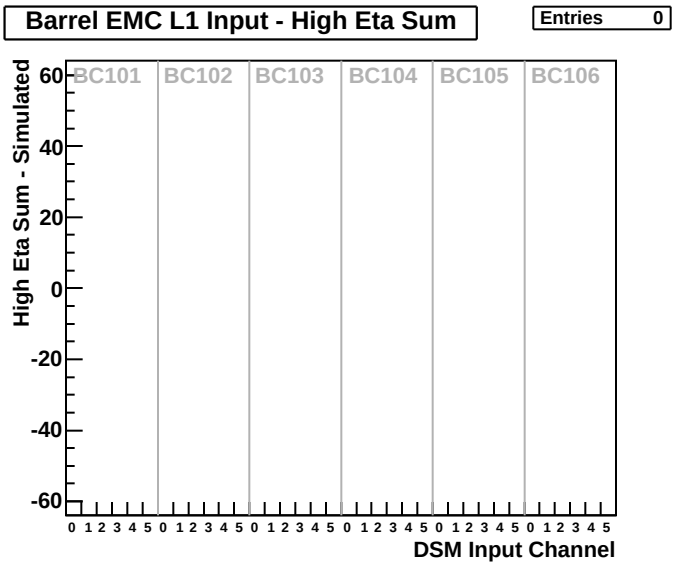
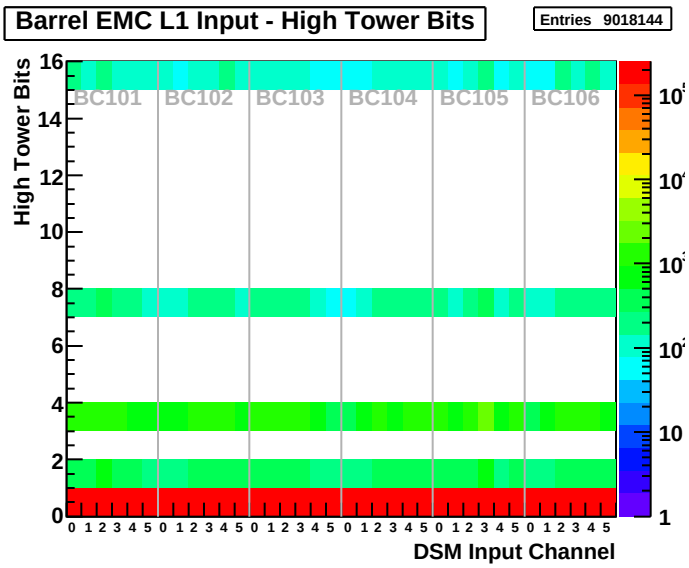
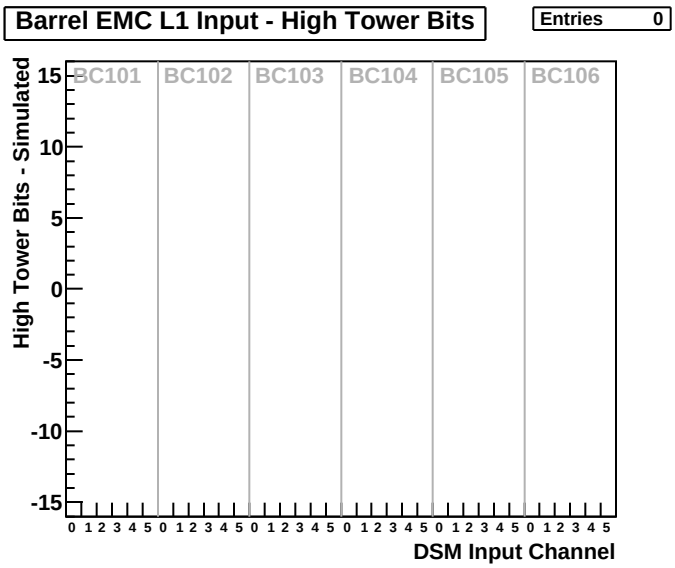

Barrel EMC L1 Input - Low Eta Sum
Entries 9018144
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

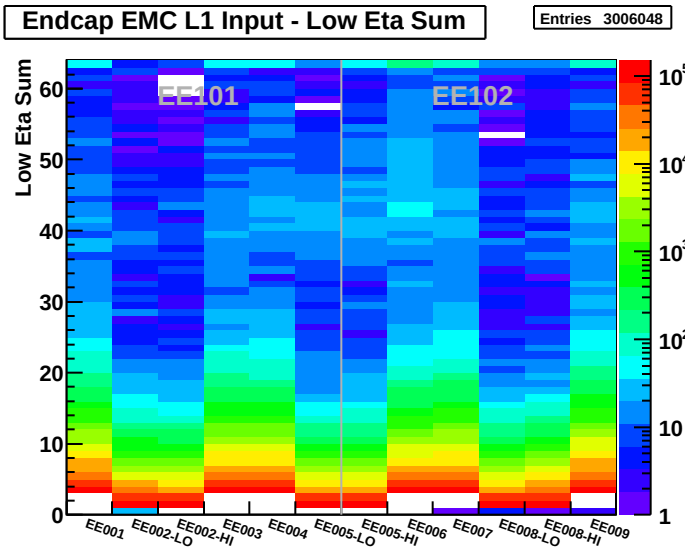

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

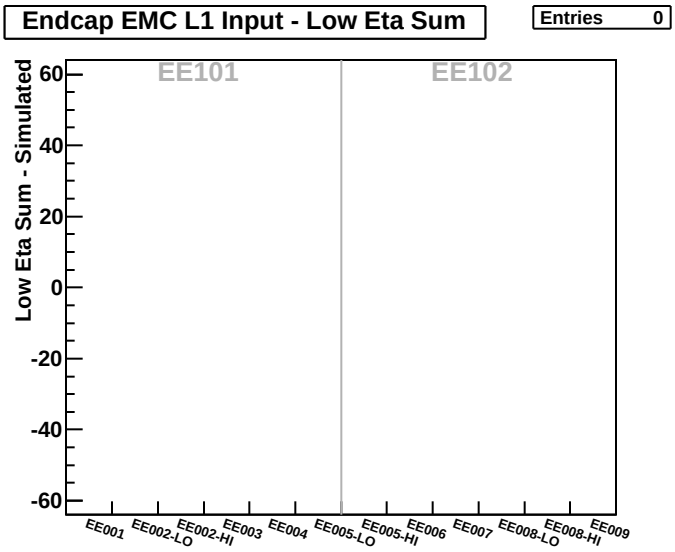

Barrel EMC L1 Input - High Eta Sum
Entries 9018144
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

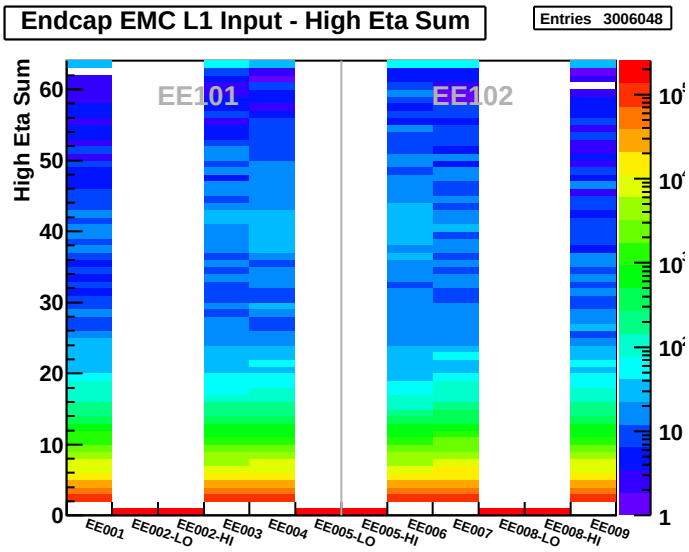

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

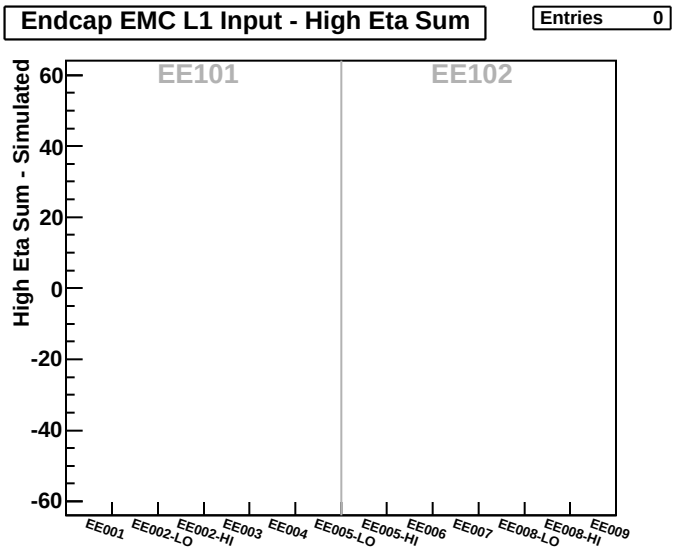

Barrel EMC L1 Input - High Tower Bits
Entries 9018144
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

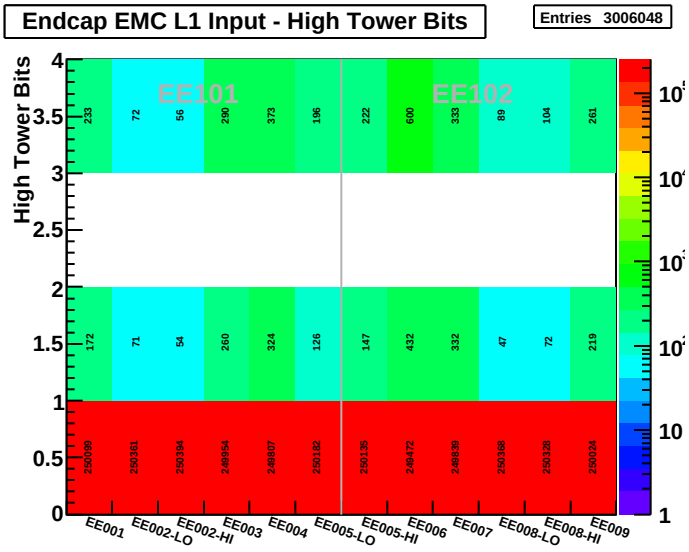

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

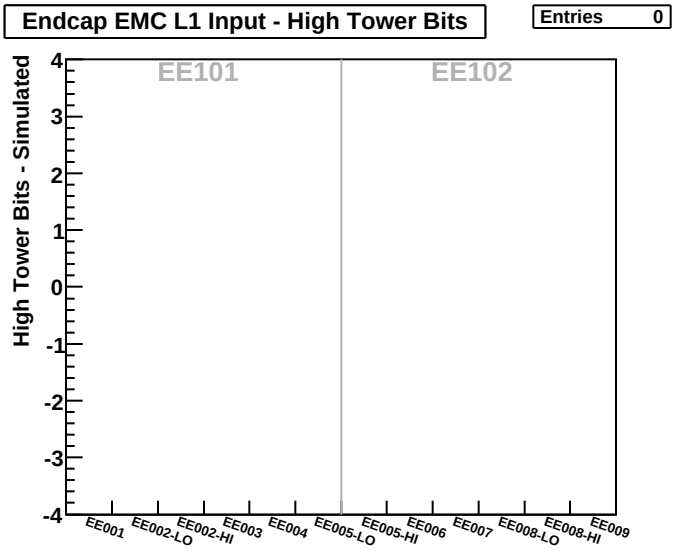

Endcap EMC L1 Input - Low Eta Sum
Entries 3006048
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

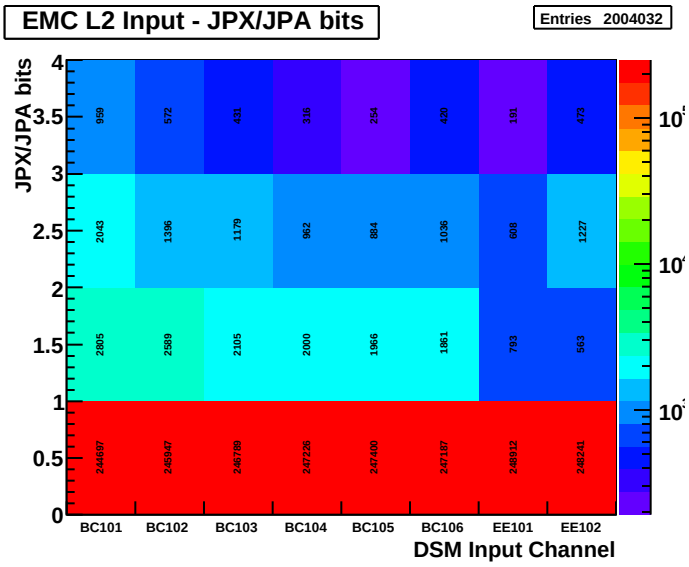

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

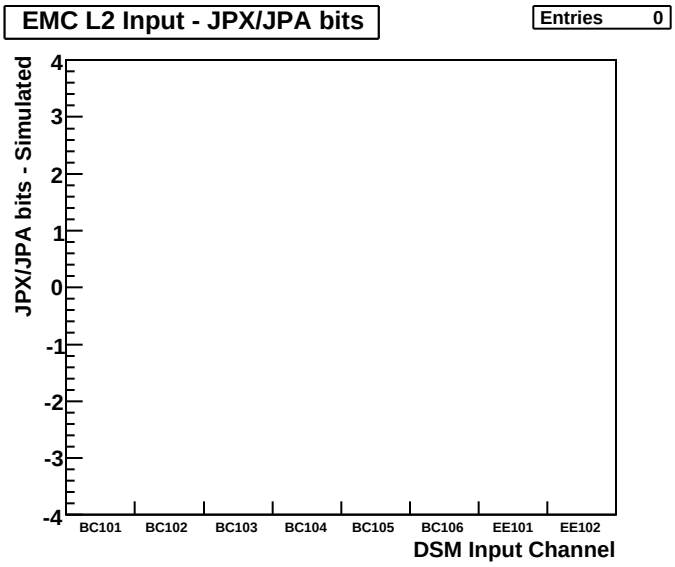

Endcap EMC L1 Input - High Eta Sum
Entries 3006048
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

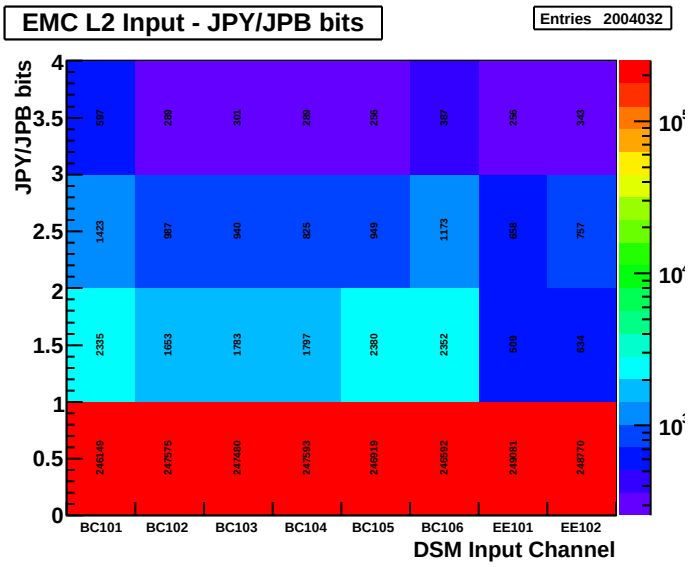

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

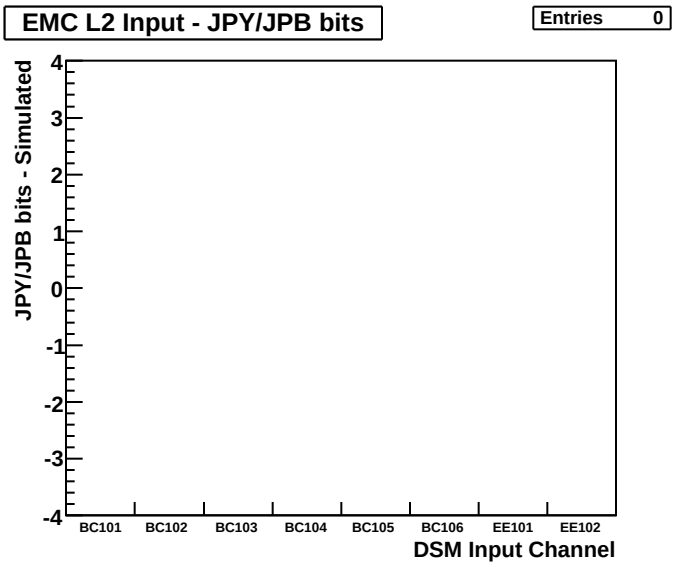

Endcap EMC L1 Input - High Tower Bits
Entries 3006048
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

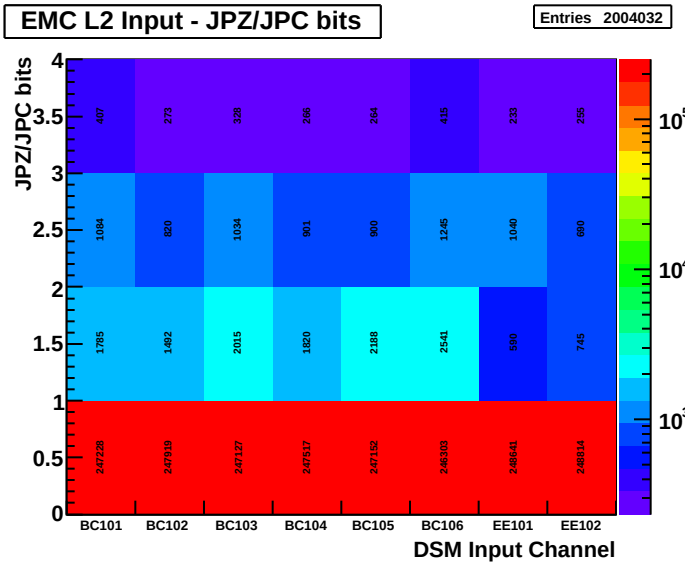

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

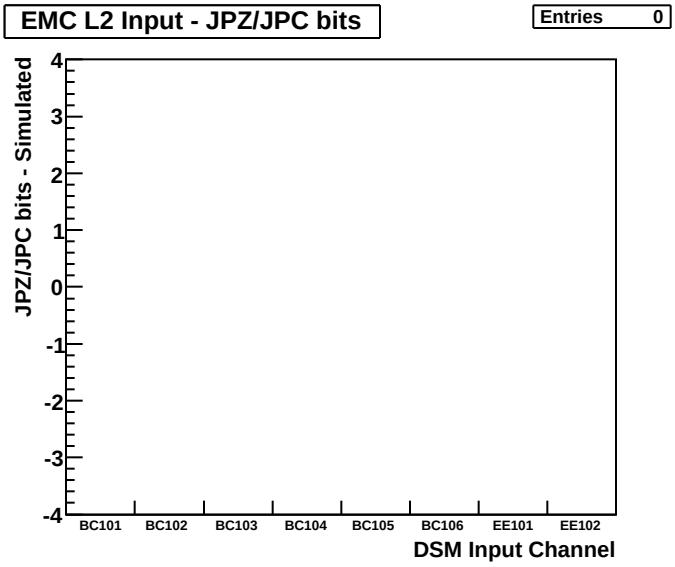

EMC L2 Input - JPX/JPA bits
Entries 2004032
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
999 572 431 316 254 420 191 473
2043 1396 1179 962 884 1036 608 1227
2805 2589 2105 2000 1966 1861 793 563
244697 245947 246789 247226 247400 247187 248912 248241

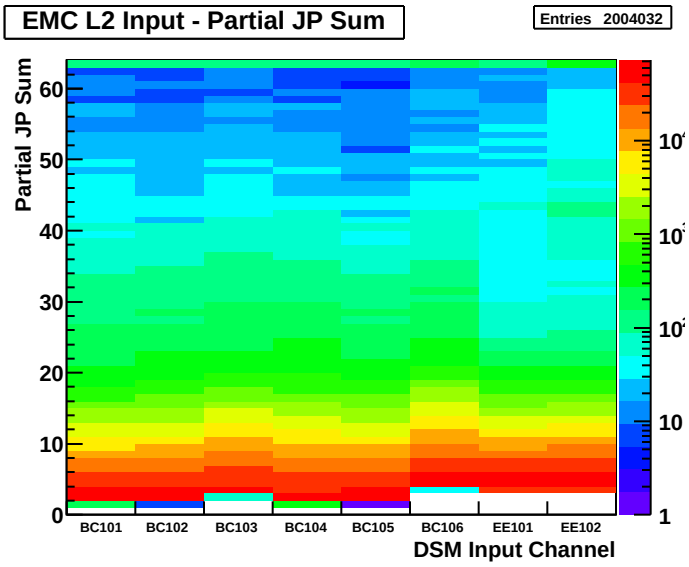

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

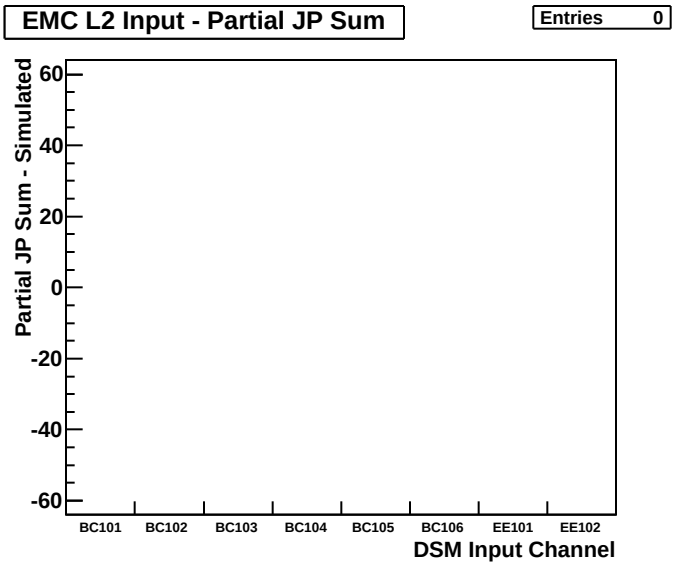

EMC L2 Input - JPY/JPB bits
Entries 2004032
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
597 289 301 288 256 387 256 343
1423 987 940 825 949 1173 658 757
2335 1653 1783 1797 2380 2352 509 634
246149 247575 247480 247593 248919 246592 249081 249770

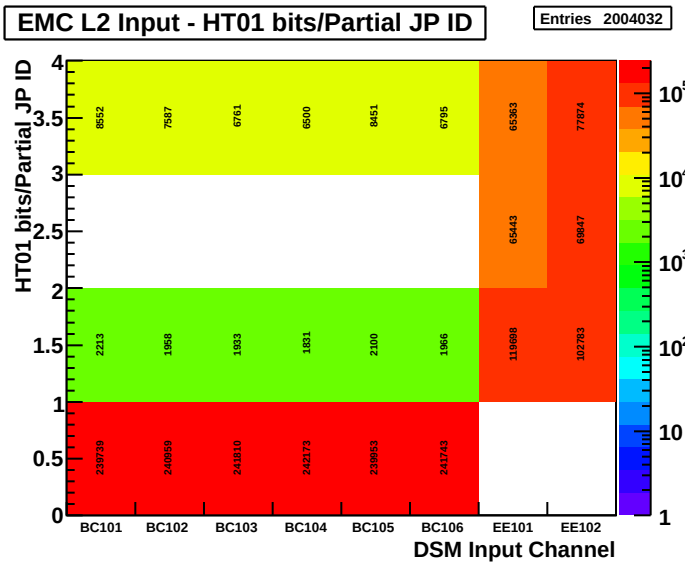

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

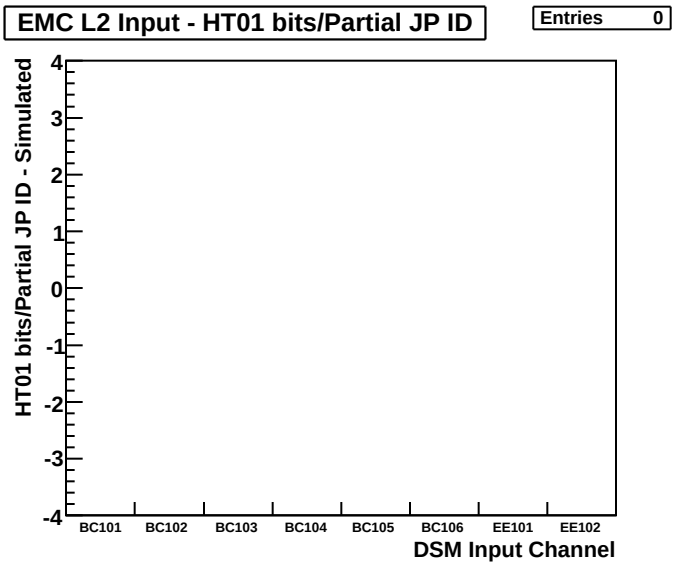

EMC L2 Input - JPZ/JPC bits
Entries 2004032
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
407 273 328 266 264 415 233 255
1084 820 1034 901 900 1245 1040 690
1785 1492 2015 1820 2188 2541 590 745
247228 247919 247127 247517 247152 246303 248641 248814

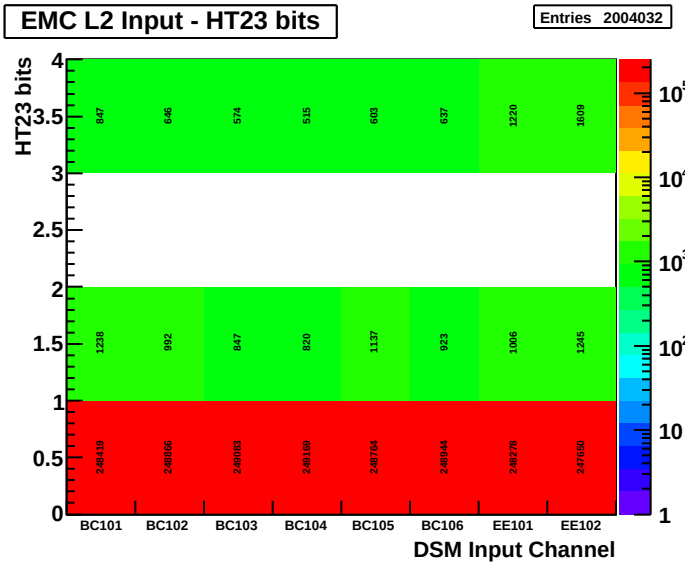

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

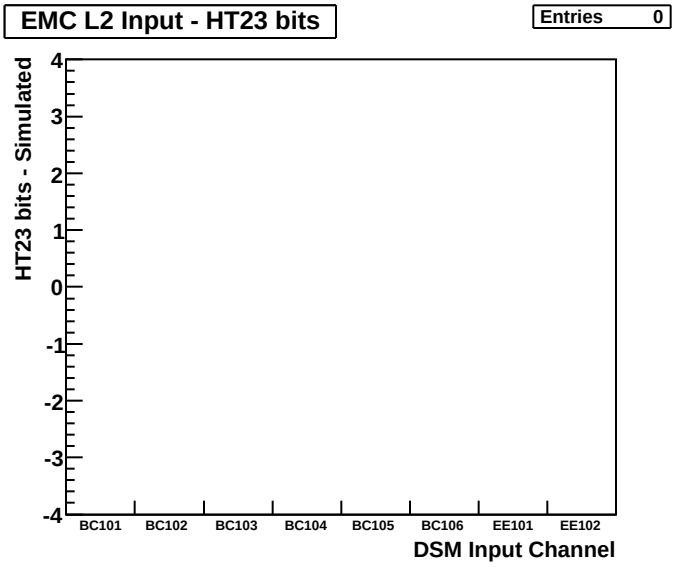

EMC L2 Input - Partial JP Sum
Entries 2004032
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

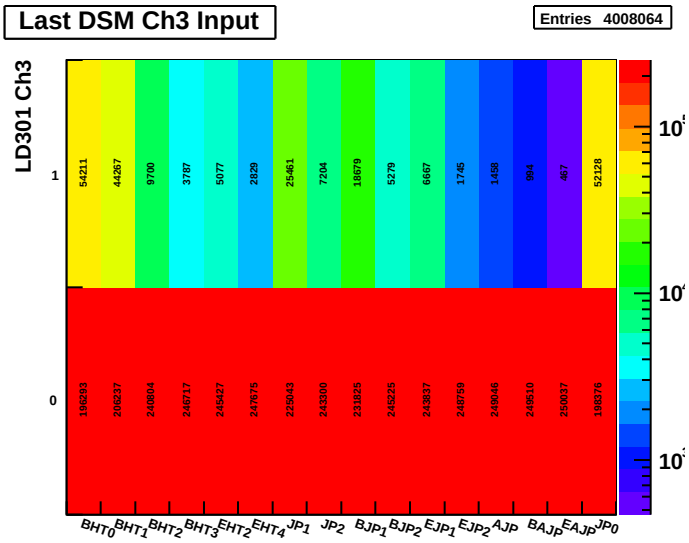

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

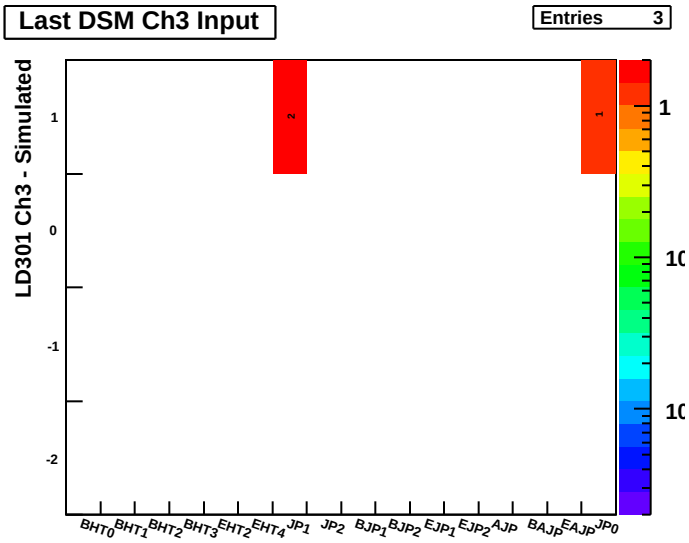

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2004032
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

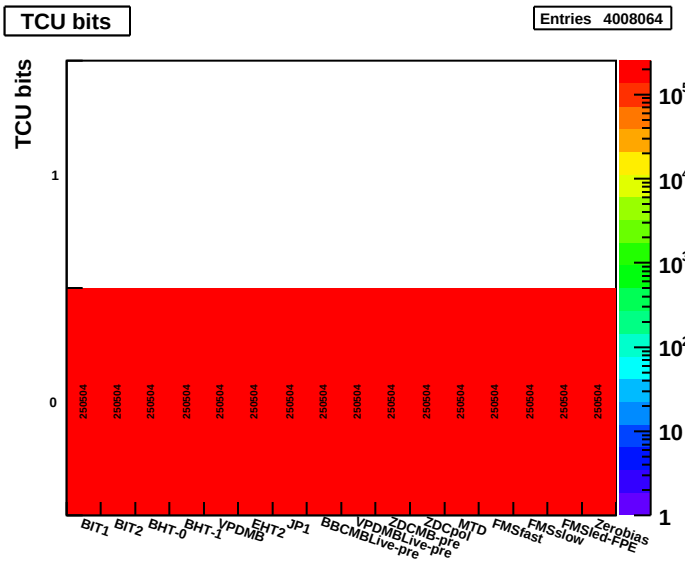

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

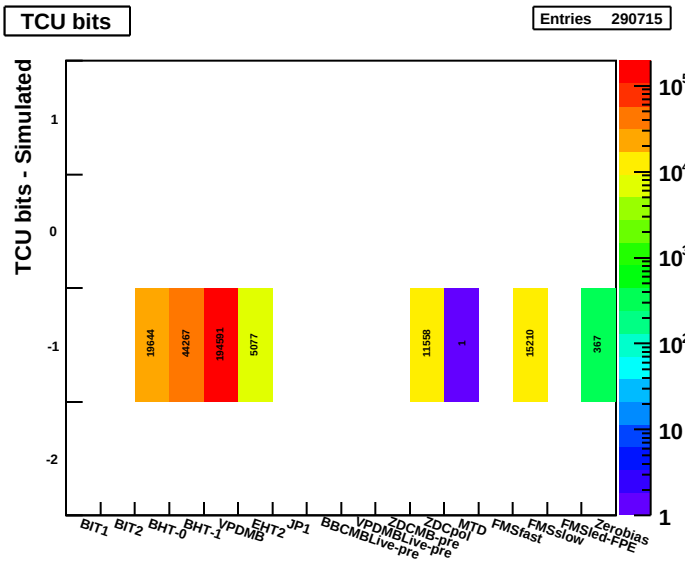

EMC L2 Input - HT23 bits
Entries 2004032
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

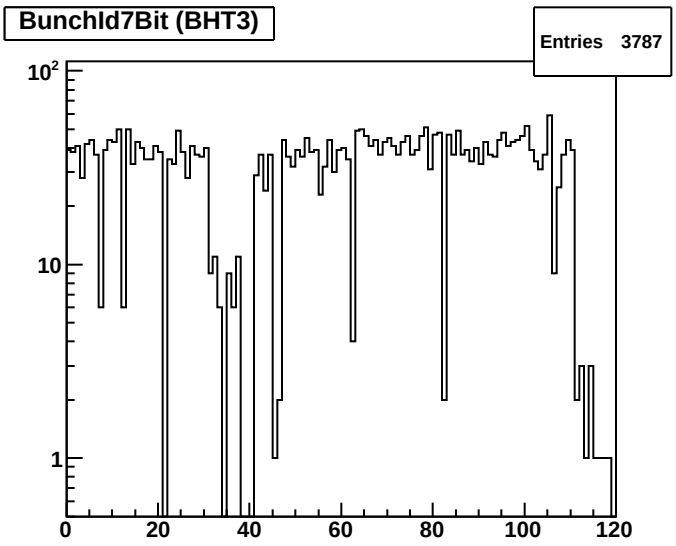

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

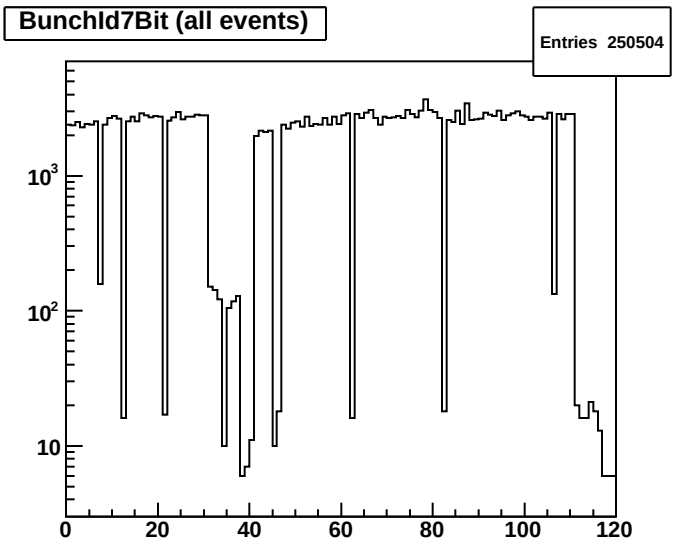

Last DSM Ch3 Input
Entries 4008064
LD301 Ch3
54211 44267 9700 3787 5077 2829 25461 7204 18679 5279 6667 1745 1458 894 467 52128
196283 206237 240804 246717 245427 247675 225043 243300 231825 245225 243837 248769 249046 249510 250037 198376
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

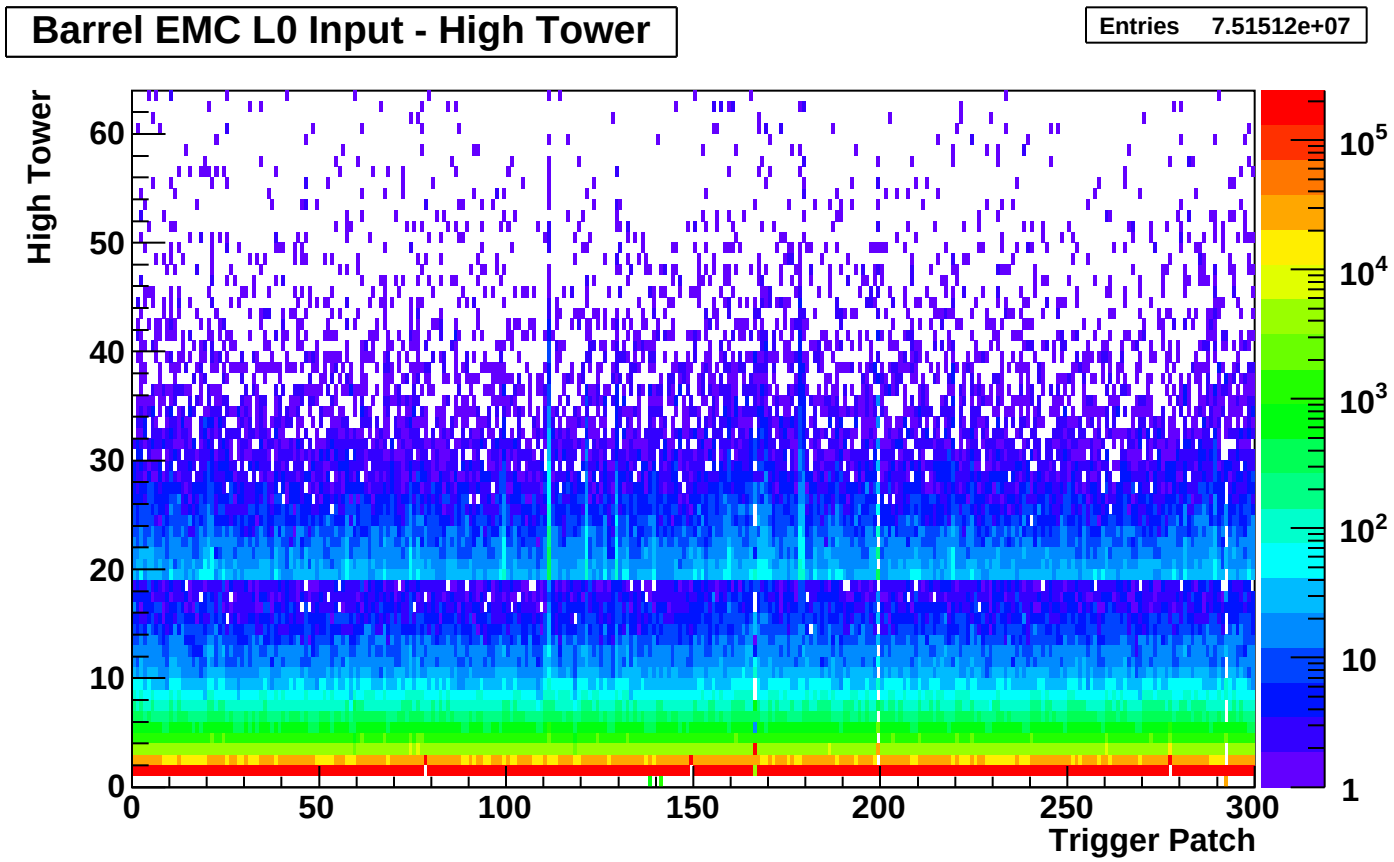

Last DSM Ch3 Input
Entries 3
LD301 Ch3 - Simulated
2
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

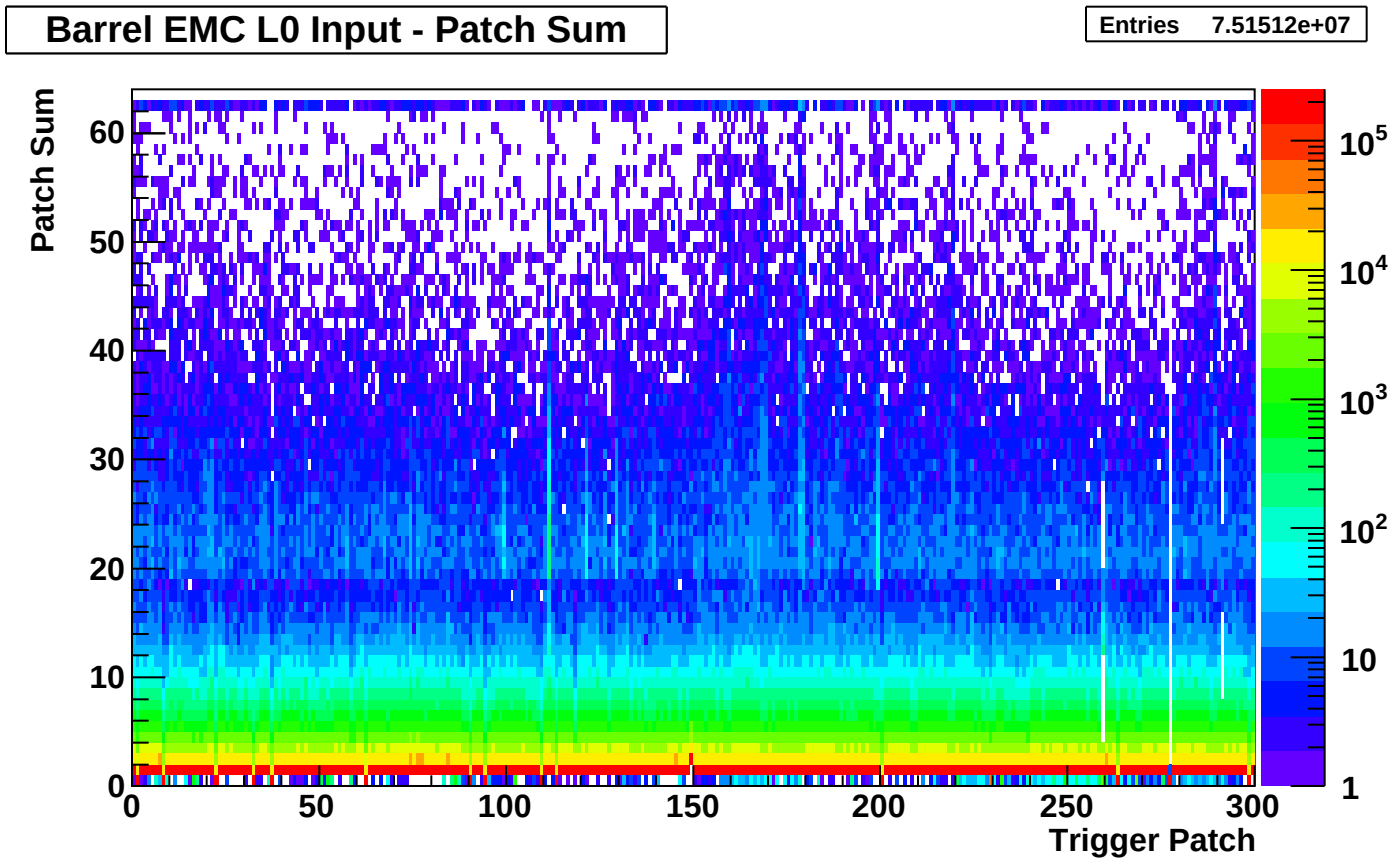

TCU bits
Entries 4008064
TCU bits
250504 250504 250504 250504 250504 250504 250504 250504 250504 250504 250504 250504 250504 250504 250504 250504
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

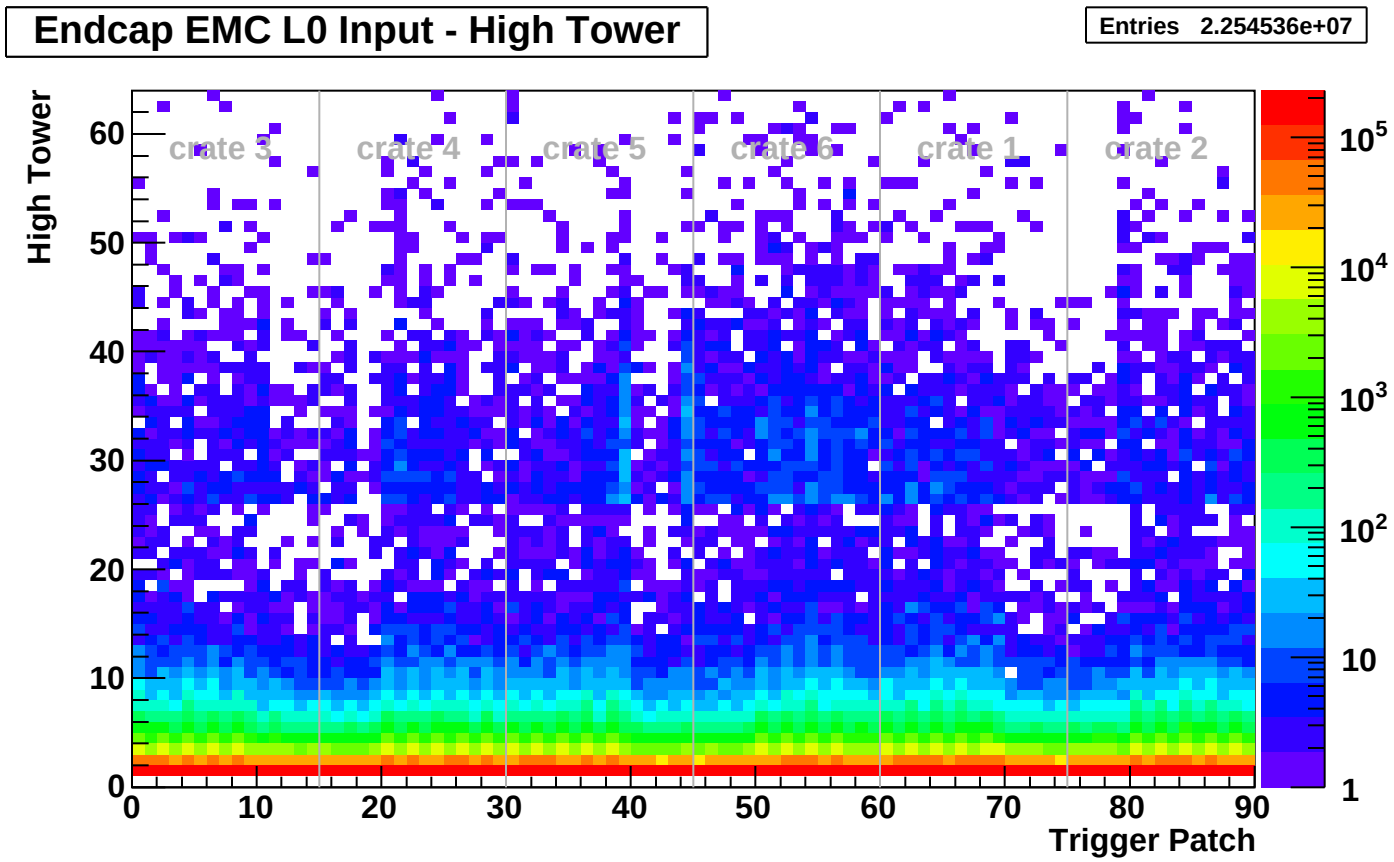

TCU bits
Entries 290715
TCU bits - Simulated
19644 44267 194591 5077 11558 1 15210 367
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

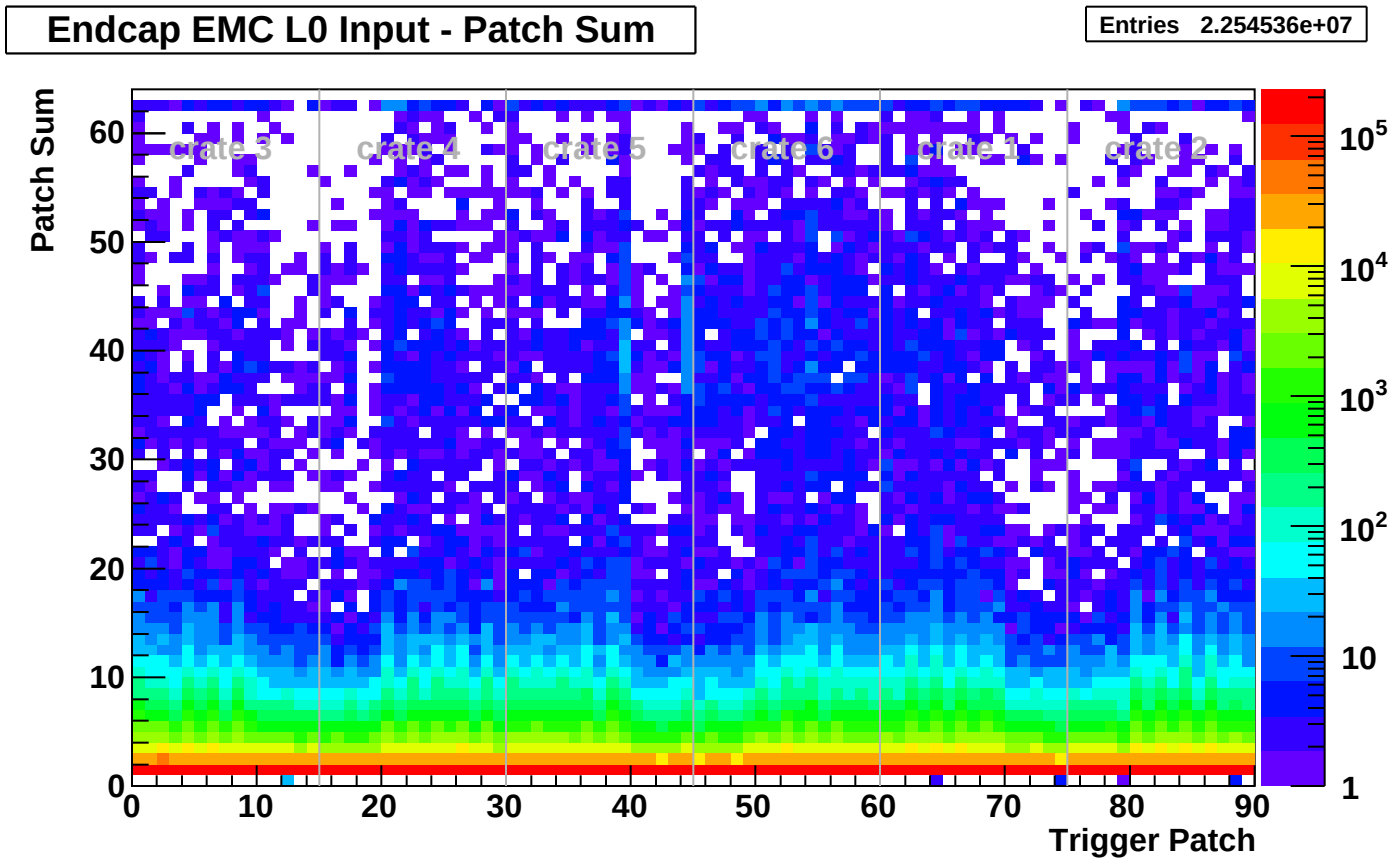

BunchId7Bit (BHT3)
Entries 3787

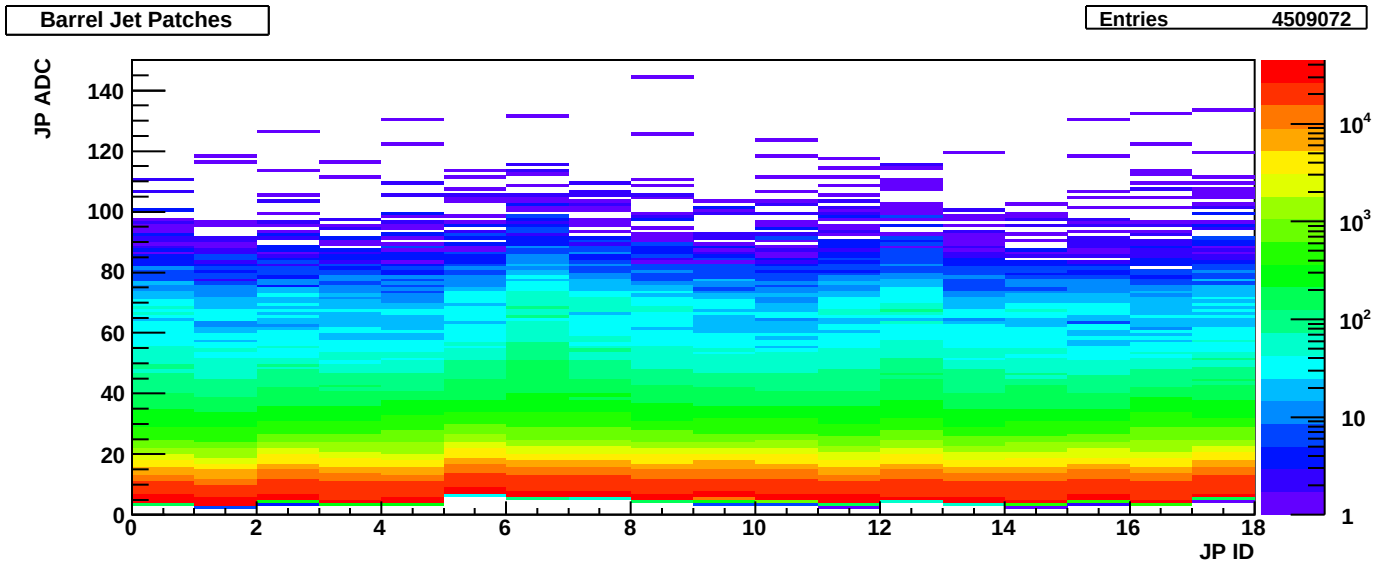

BunchId7Bit (all events)
Entries 250504

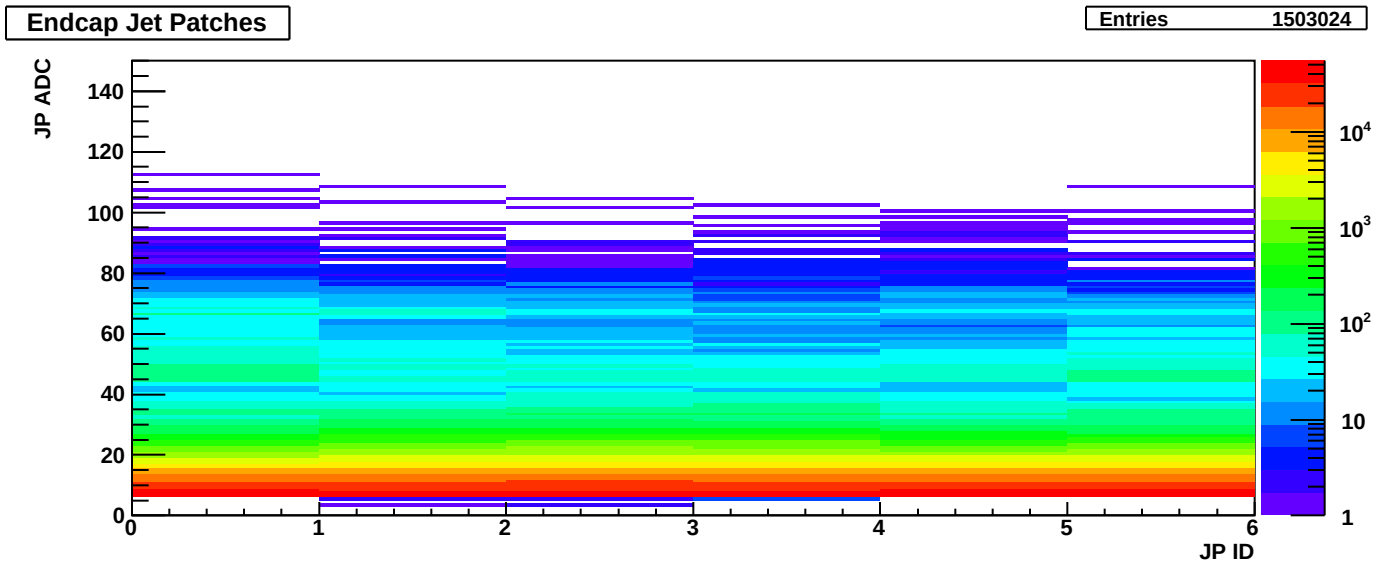

Barrel EMC L0 Input - High Tower
Entries 7.51512e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

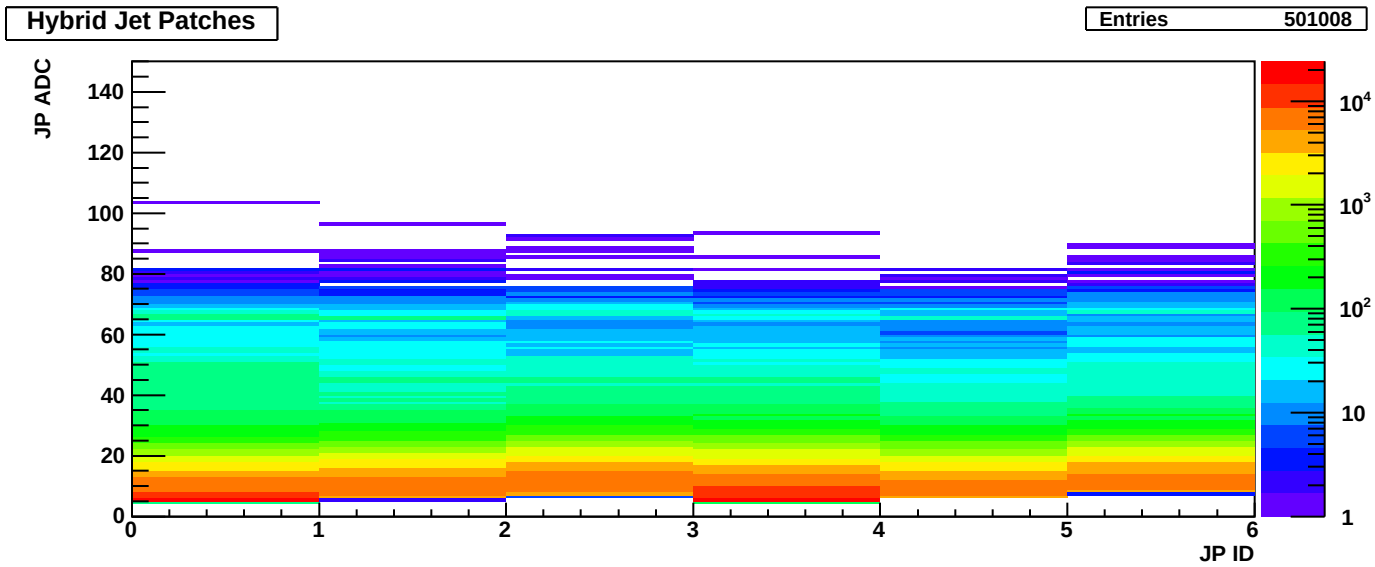

Barrel EMC L0 Input - Patch Sum
Entries 7.51512e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 2.254536e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Endcap EMC L0 Input - Patch Sum
Entries 2.254536e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

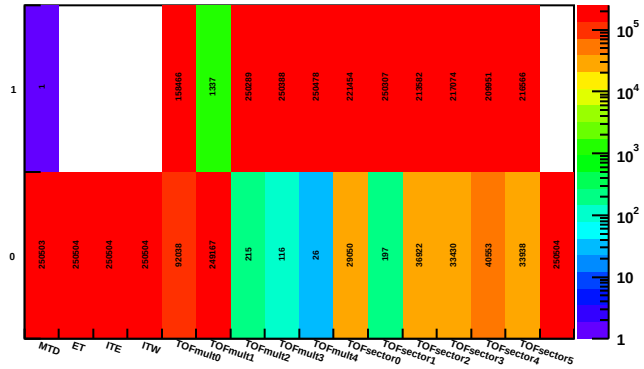

Barrel Jet Patches
Entries 4509072
JP ADC
JP ID


Endcap Jet Patches
Entries 1503024
JP ADC
JP ID


Hybrid Jet Patches
Entries 501008
JP ADC
JP ID

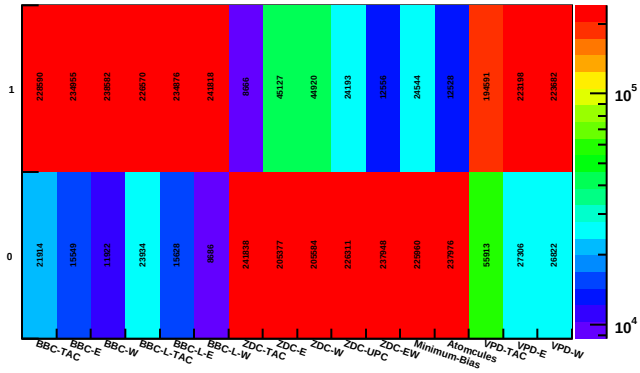
TF201 0-15 (ch0)

Entries 4008064



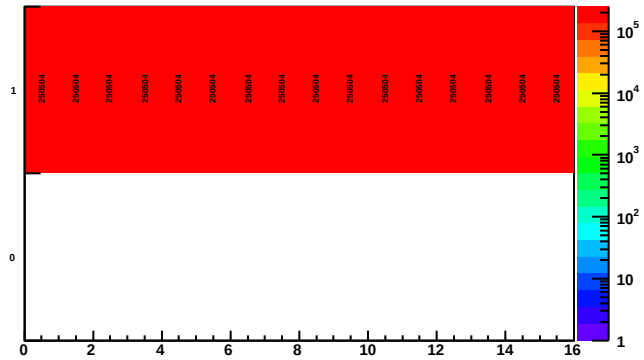
VT201 0-15 (ch1)

Entries 4008064



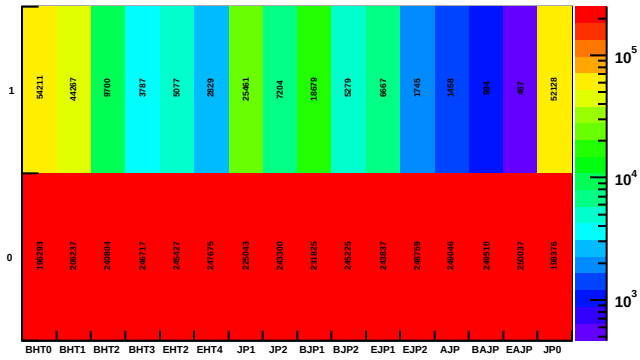
Unused (ch2)

Entries 4008064



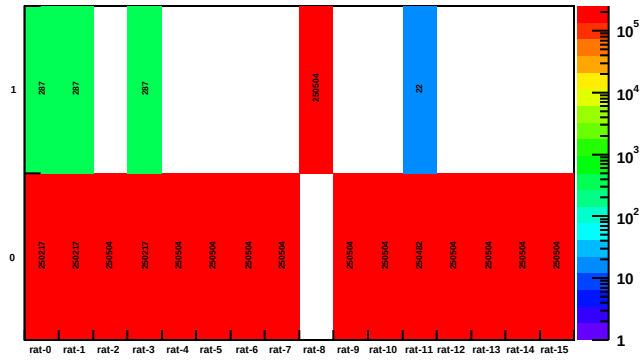
EM201 0-15 (ch3)

Entries 4008064



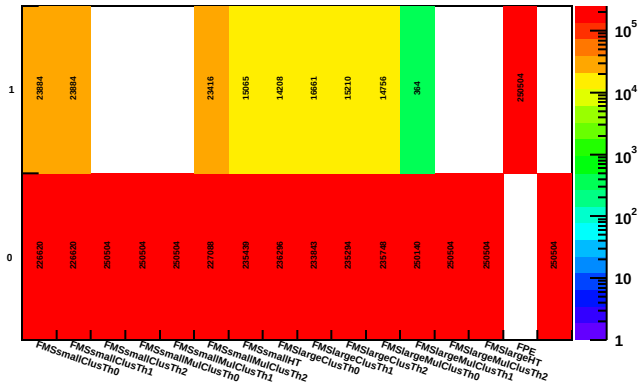
RAT board (ch4)

Entries 4008064



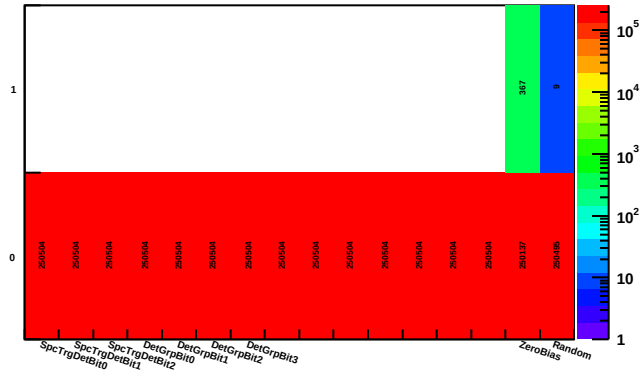
FP201 0-15 (ch5)

Entries 4008064



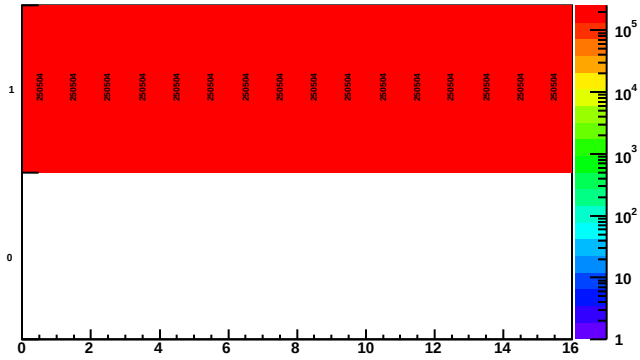
ST201 0-15 (ch6)

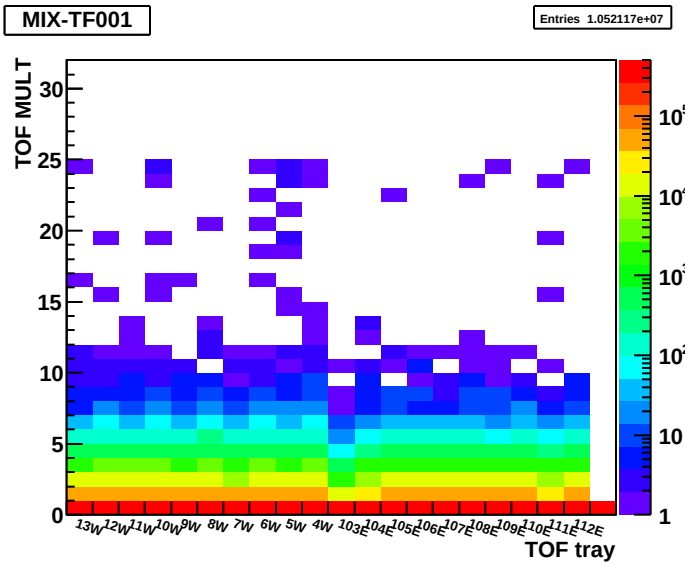
Entries 4008064

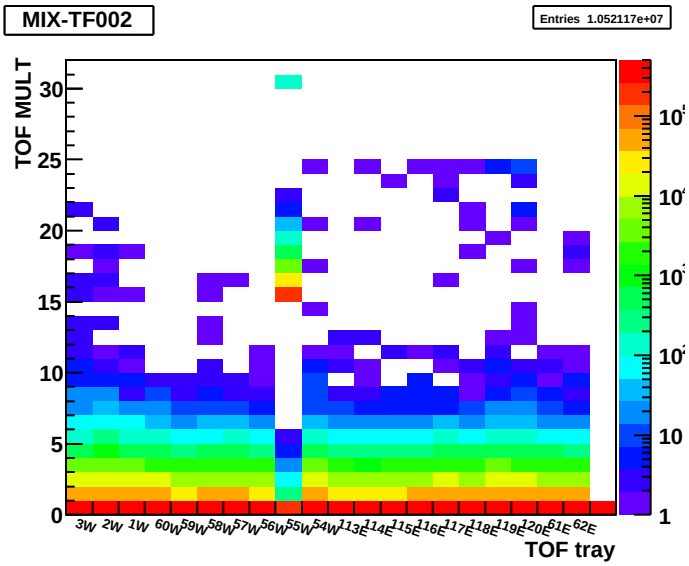


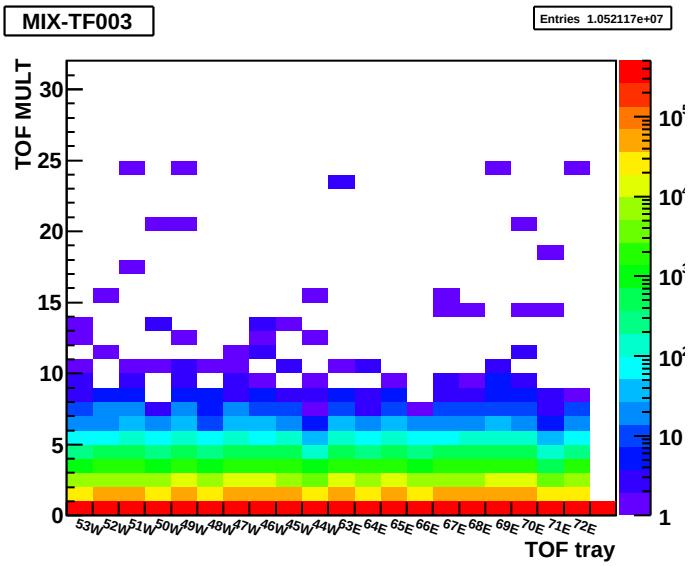
Unused (ch7)

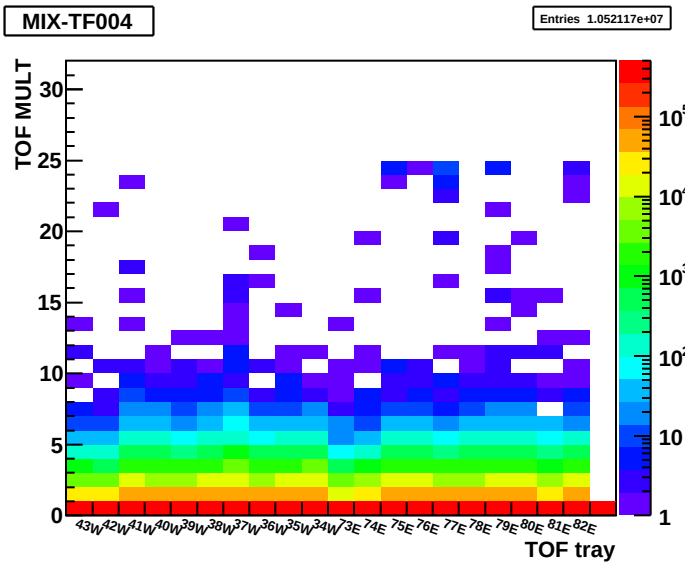
Entries 4008064

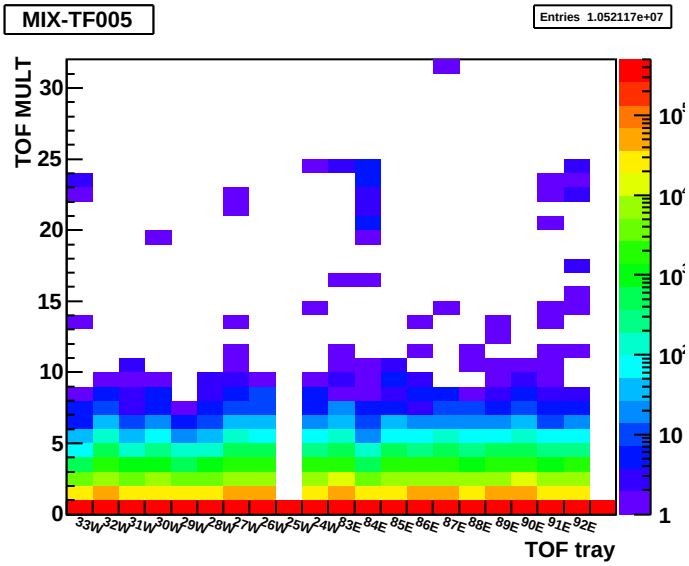


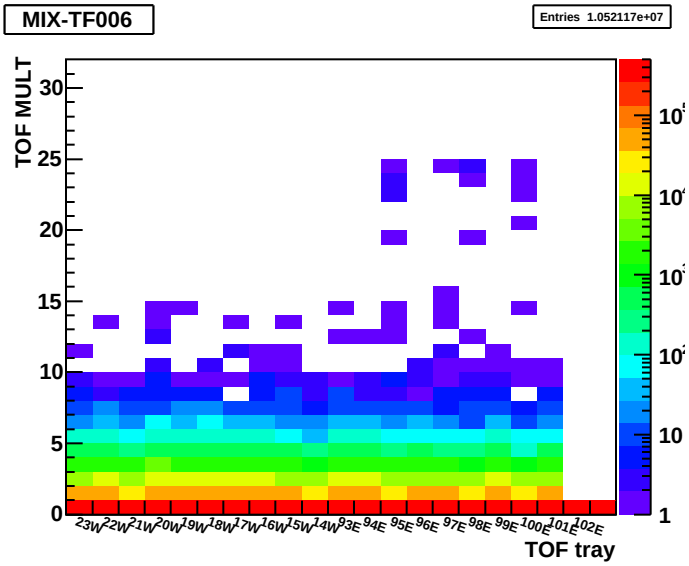

MIX-TF001
Entries 1.052117e+07
TOF MULT
TOF tray

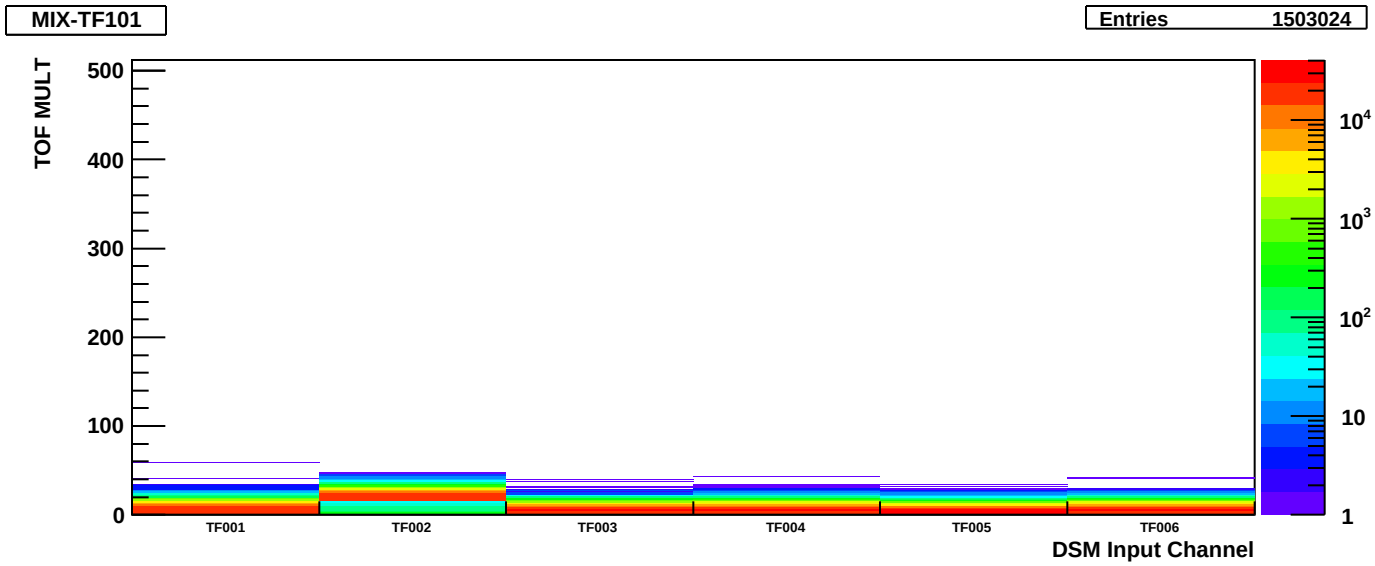

MIX-TF002
Entries 1.052117e+07
TOF MULT
TOF tray

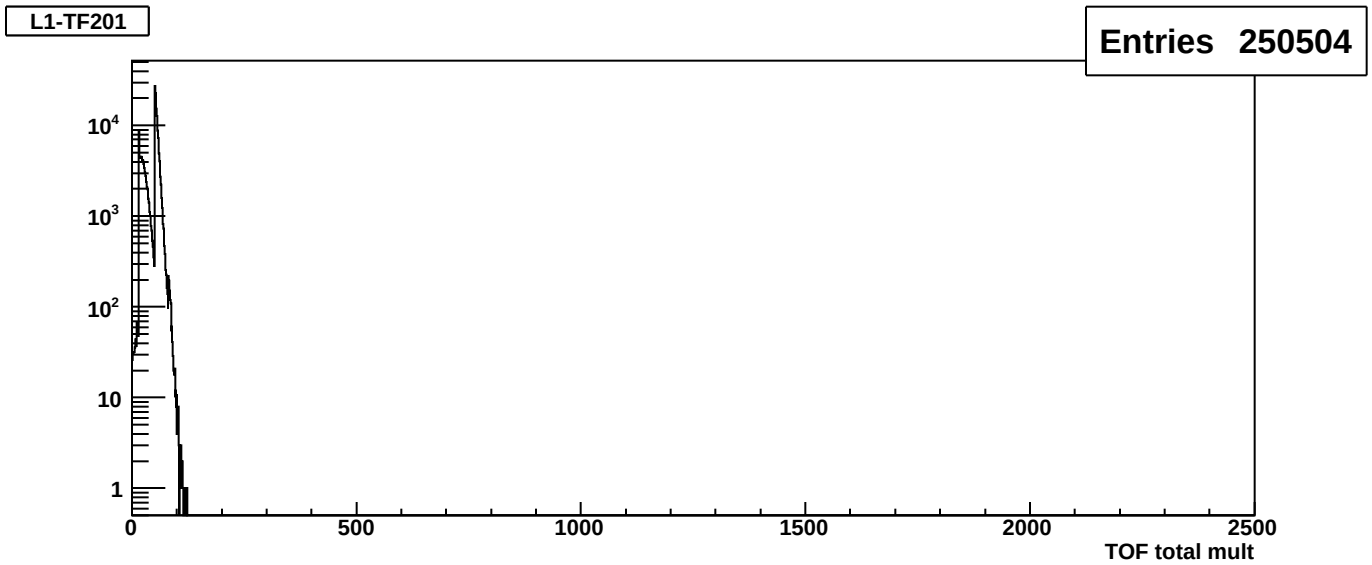

MIX-TF003
Entries 1.052117e+07
TOF MULT
TOF tray

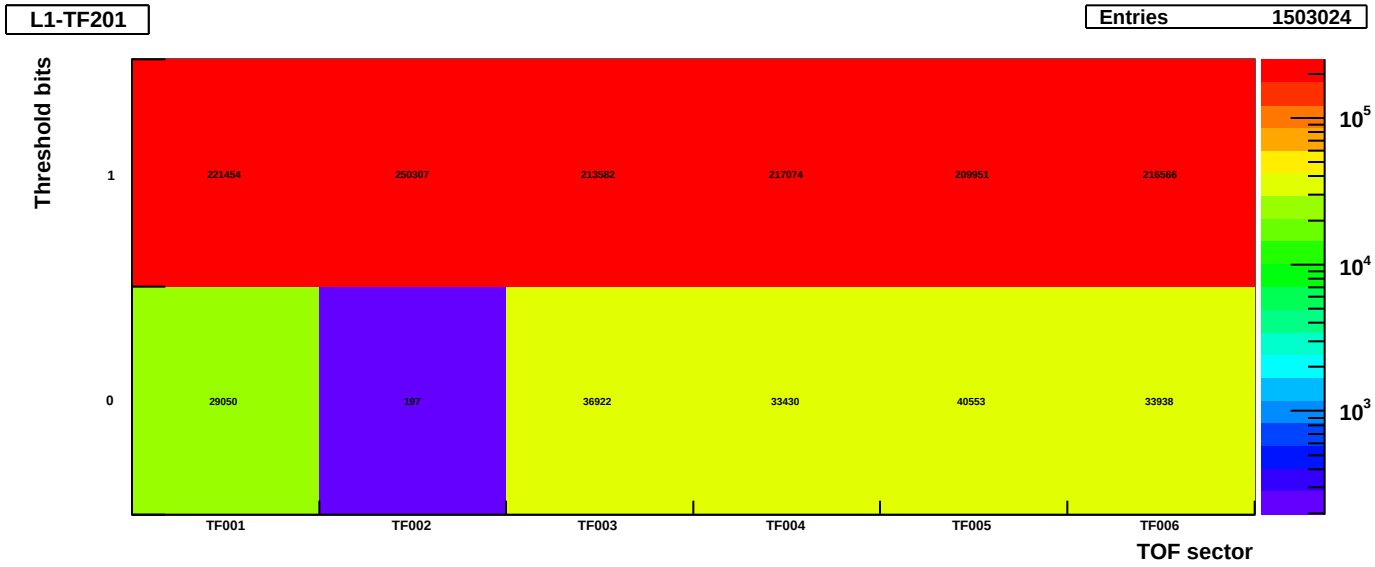

MIX-TF004
Entries 1.052117e+07
TOF MULT
TOF tray

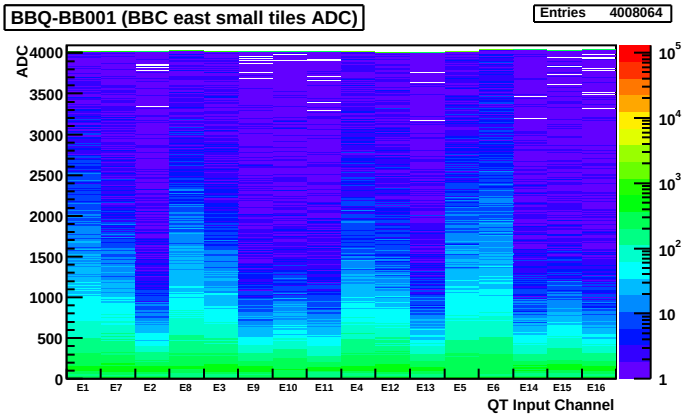

MIX-TF005
Entries 1.052117e+07
TOF MULT
TOF tray

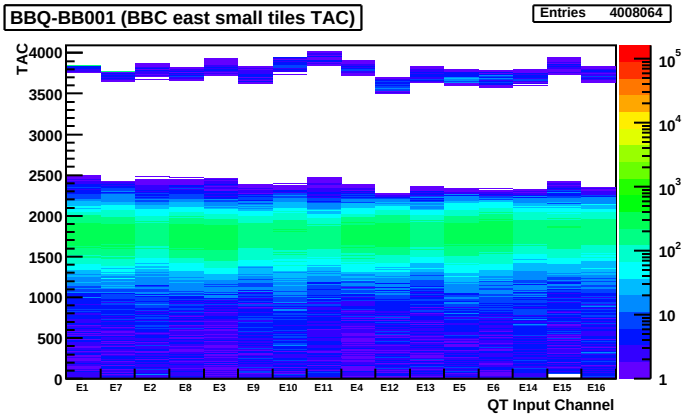

MIX-TF006
Entries 1.052117e+07
TOF MULT
TOF tray

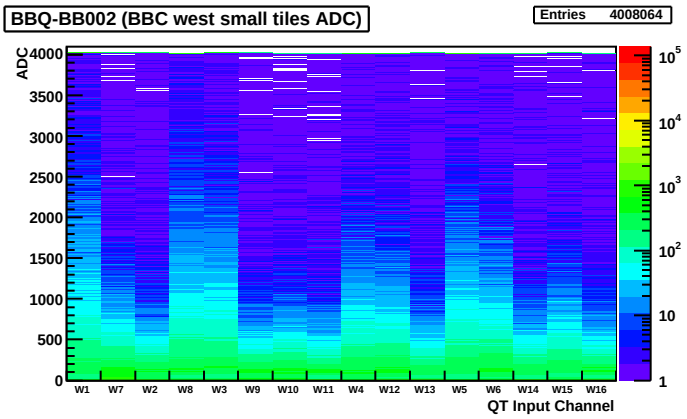

MIX-TF101
Entries 1503024
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^4
10^3
10^2
10
1

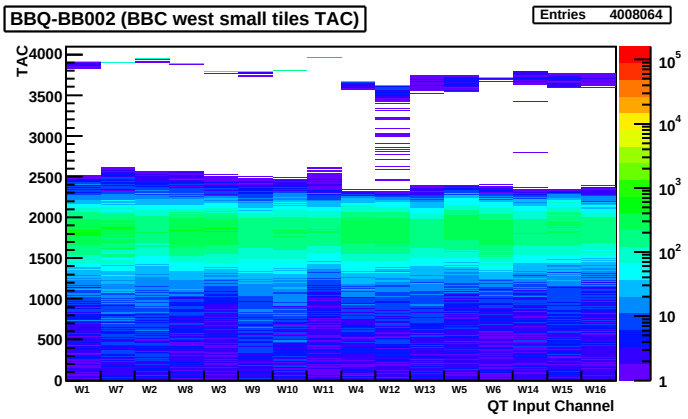

L1-TF201
Entries 250504
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

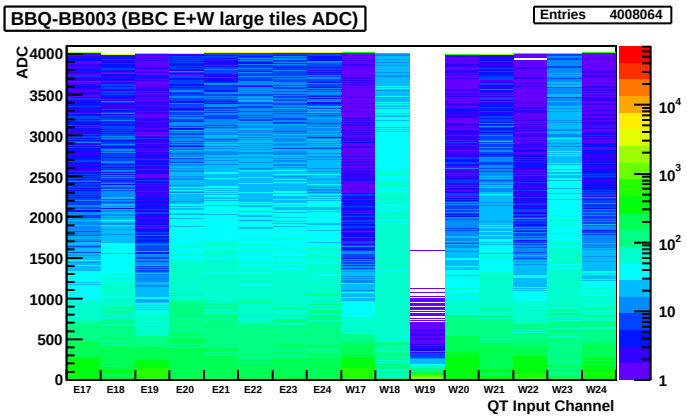

L1-TF201
Entries 1503024
Threshold bits
1
0
221454
250307
213582
217074
209951
216566
29050
197
36922
33430
40553
33938
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5
10^4
10^3

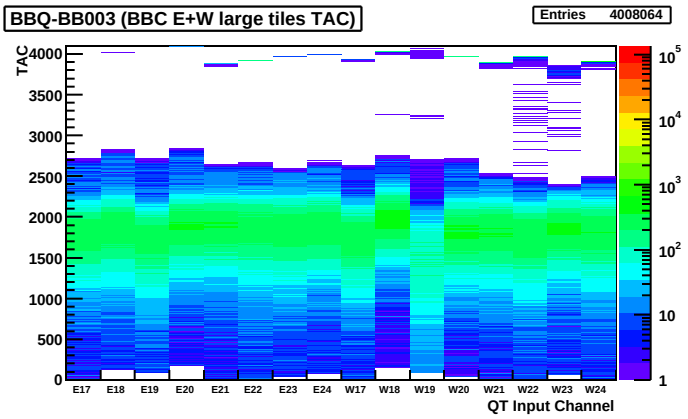

BBQ-BB001 (BBC east small tiles ADC)
Entries 4008064
ADC
QT Input Channel

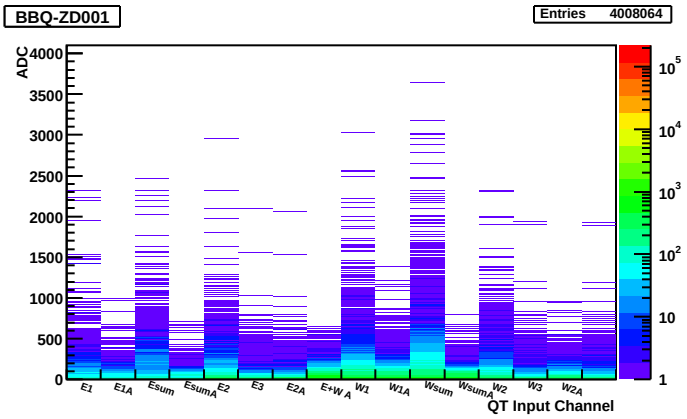

BBQ-BB001 (BBC east small tiles TAC)
Entries 4008064
TAC
QT Input Channel

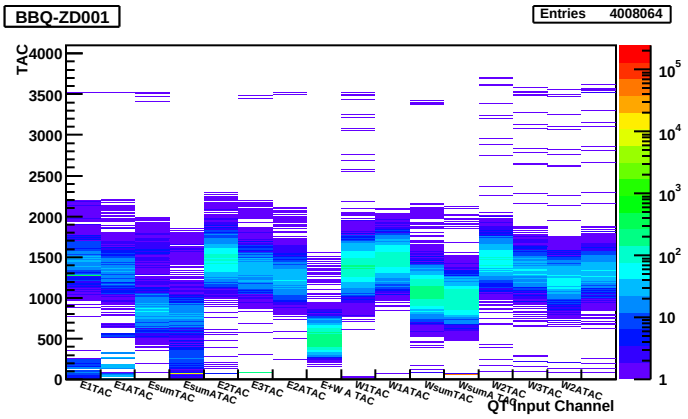

BBQ-BB002 (BBC west small tiles ADC)
Entries 4008064
ADC
QT Input Channel

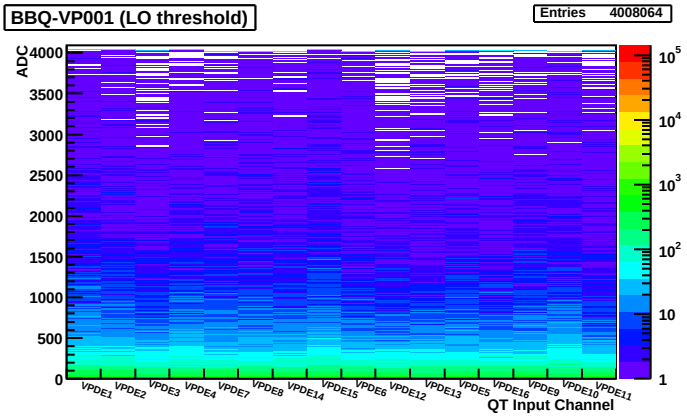

BBQ-BB002 (BBC west small tiles TAC)
Entries 4008064
TAC
QT Input Channel

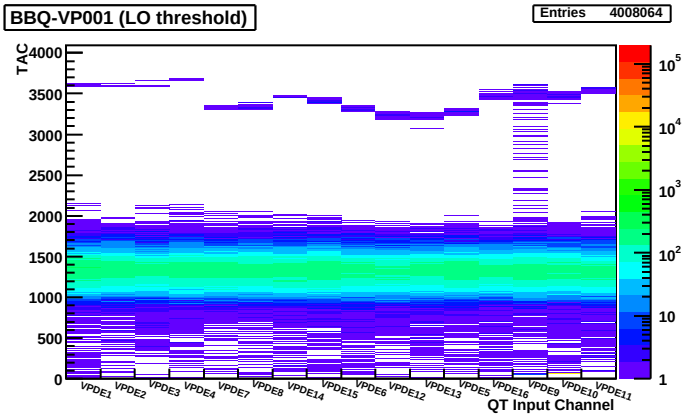

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4008064
ADC
QT Input Channel

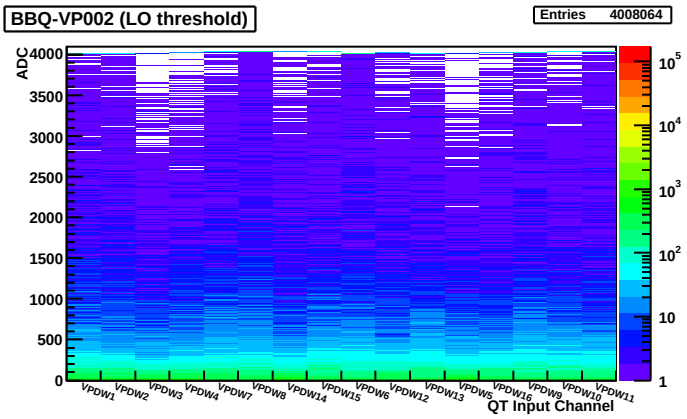

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4008064
TAC
QT Input Channel

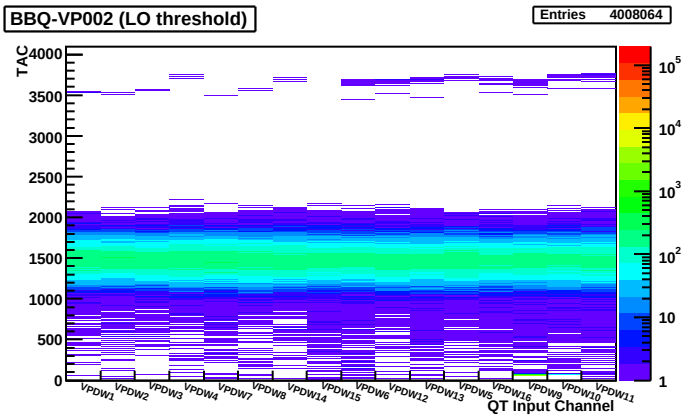

BBQ-ZD001
Entries 4008064
ADC
QT Input Channel

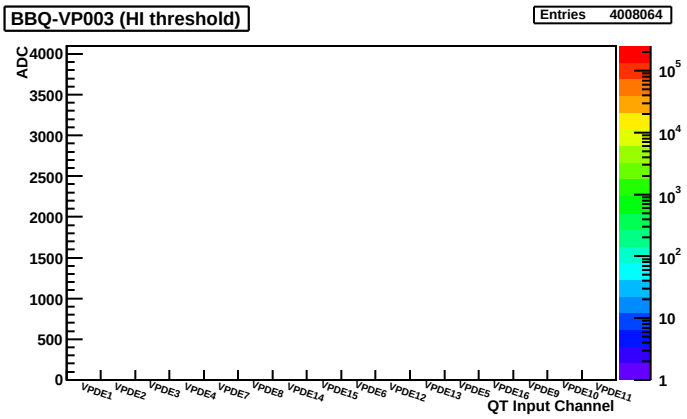

BBQ-ZD001
Entries 4008064
TAC
QT Input Channel

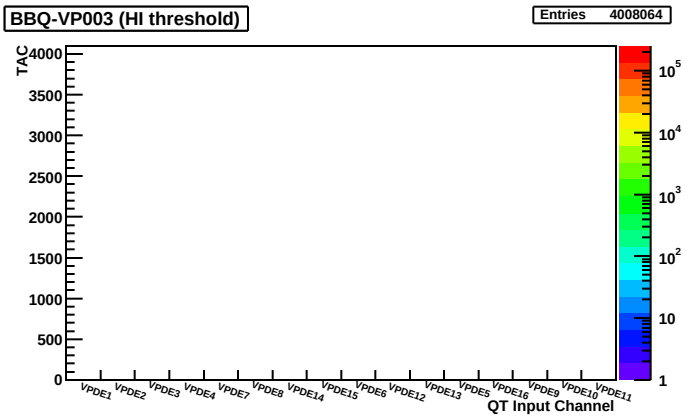

BBQ-VP001 (LO threshold)
Entries 4008064
ADC
QT Input Channel

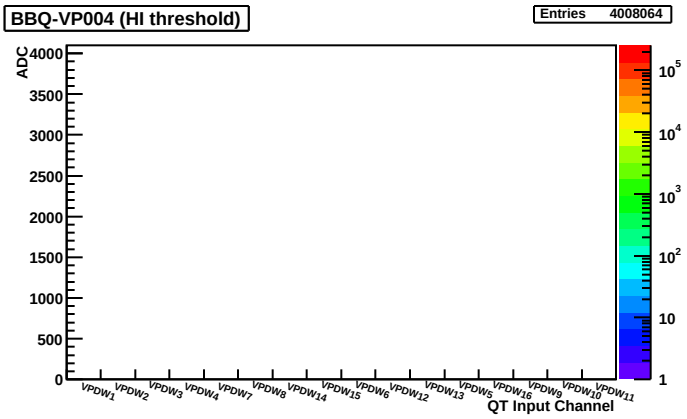

BBQ-VP001 (LO threshold)
Entries 4008064
TAC
QT Input Channel

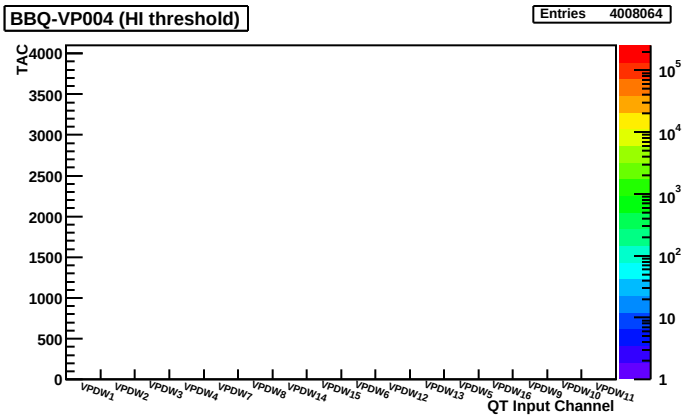

BBQ-VP002 (LO threshold)
Entries 4008064
ADC
QT Input Channel

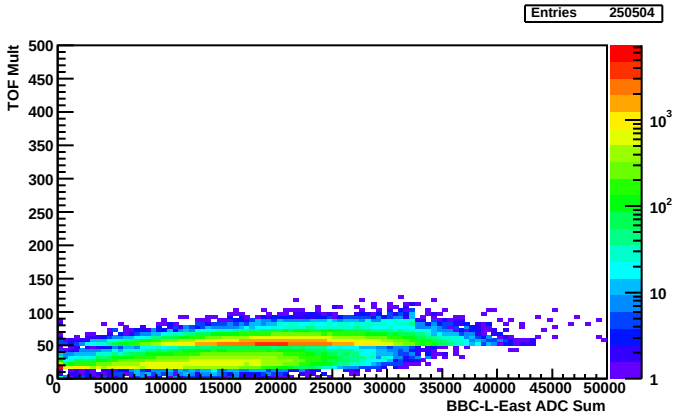

BBQ-VP002 (LO threshold)
Entries 4008064
TAC
QT Input Channel

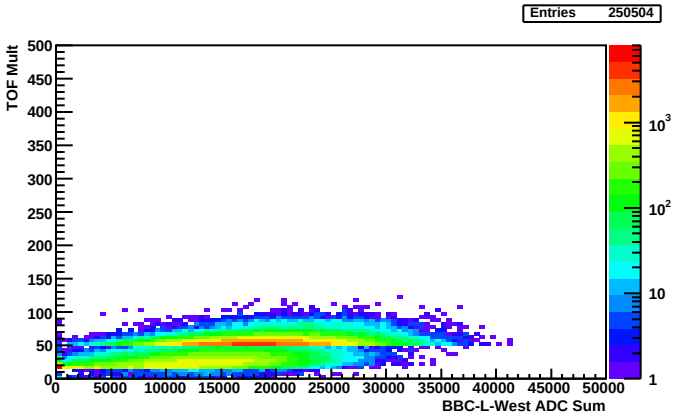

BBQ-VP003 (HI threshold)
Entries 4008064
ADC
QT Input Channel

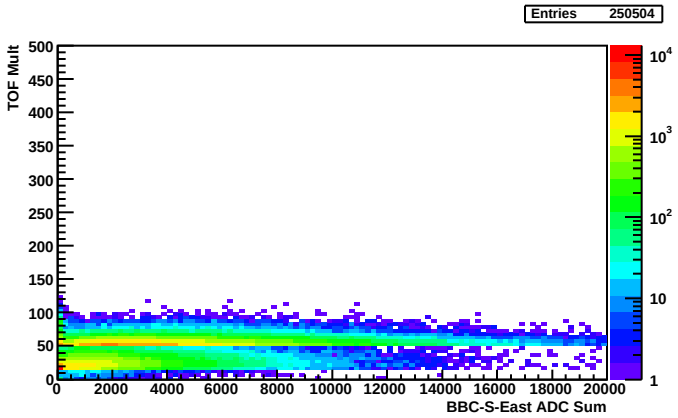

BBQ-VP003 (HI threshold)
Entries 4008064
TAC
QT Input Channel

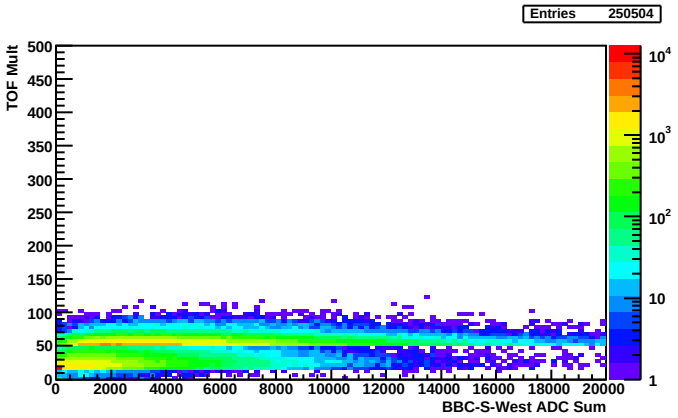

BBQ-VP004 (HI threshold)
Entries 4008064
ADC
QT Input Channel

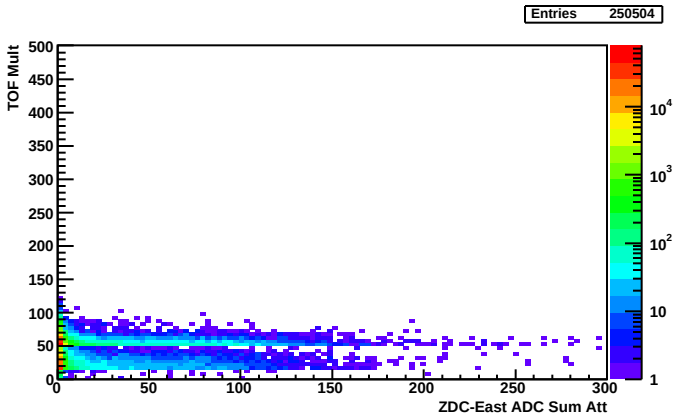

BBQ-VP004 (HI threshold)
Entries 4008064
TAC
QT Input Channel

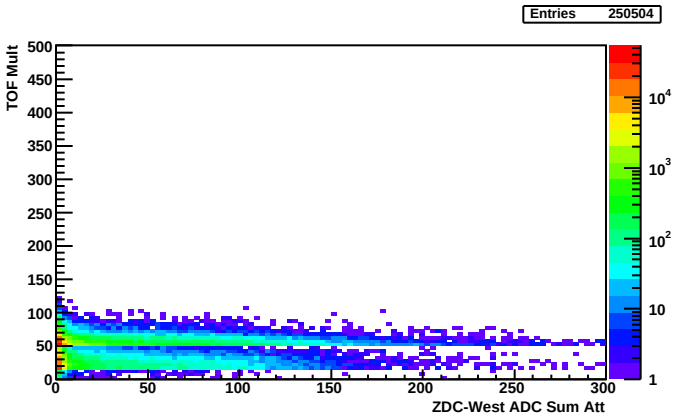

Entries 250504
TOF Mult
BBC-L-East ADC Sum

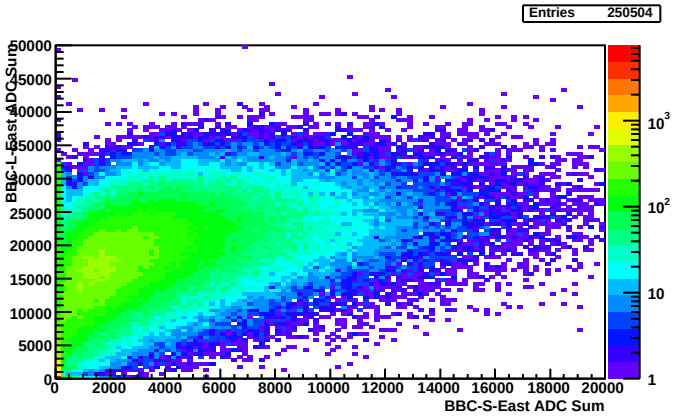

Entries 250504
TOF Mult
BBC-L-West ADC Sum

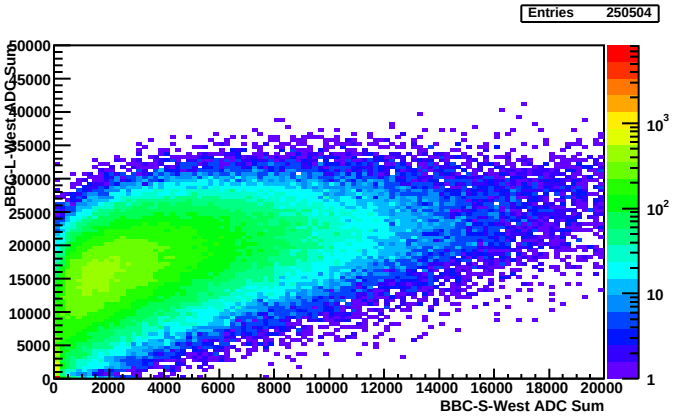

Entries 250504
TOF Mult
BBC-S-East ADC Sum

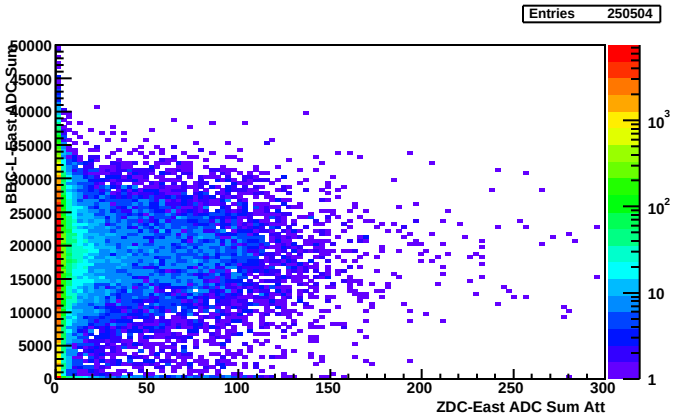

Entries 250504
TOF Mult
BBC-S-West ADC Sum

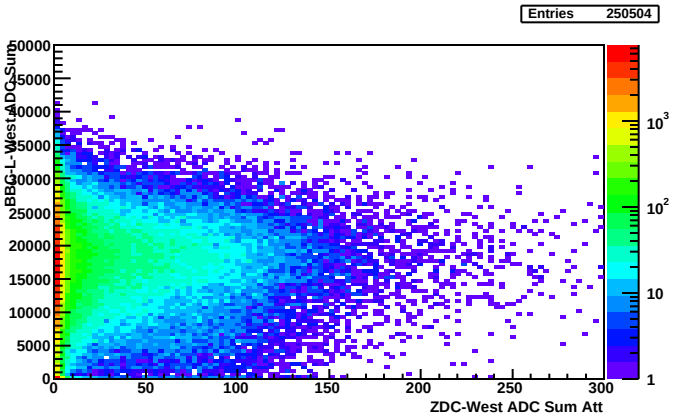

Entries 250504
TOF Mult
ZDC-East ADC Sum Att

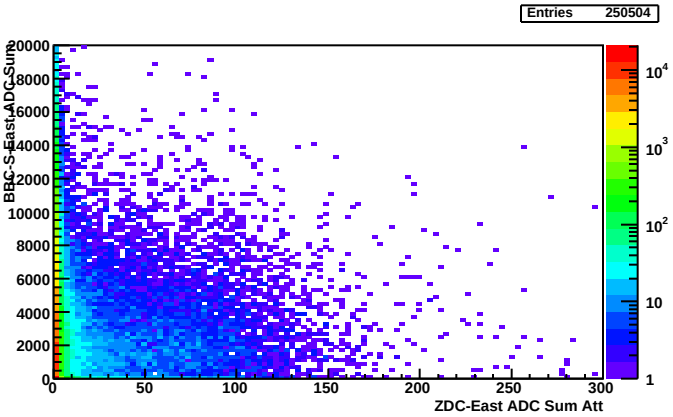

Entries 250504
TOF Mult
ZDC-West ADC Sum Att

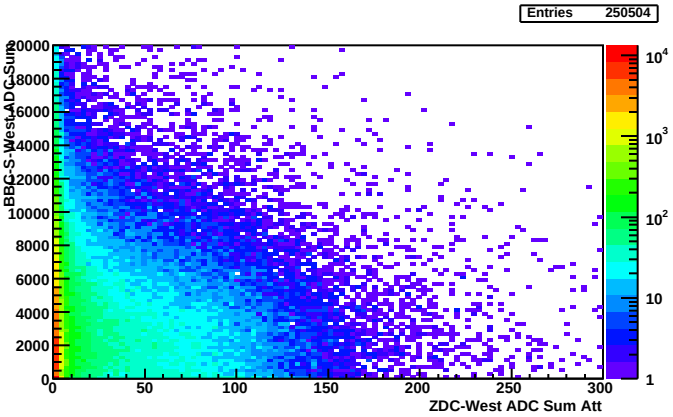

Entries 250504
BBC-L-East ADC Sum
BBC-S-East ADC Sum

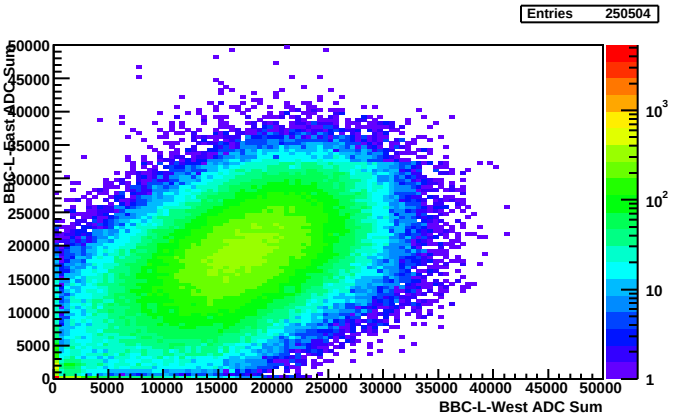

Entries 250504
BBC-L-West ADC Sum
BBC-S-West ADC Sum

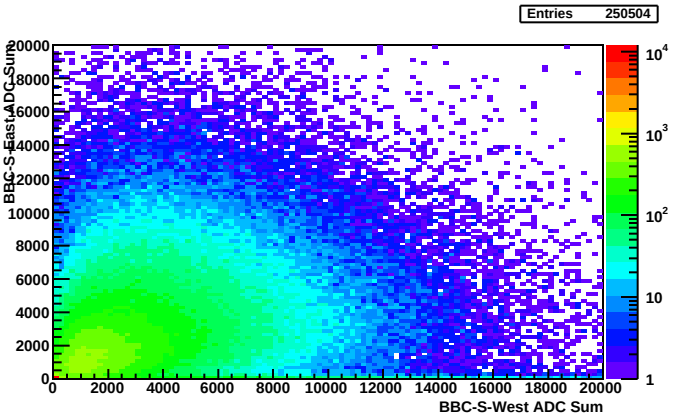

Entries 250504
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

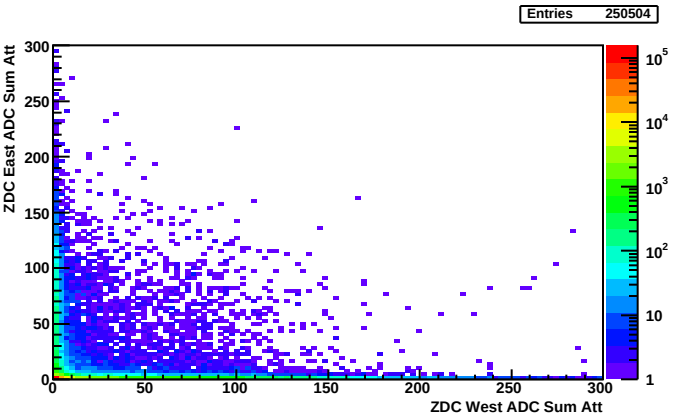

Entries 250504
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

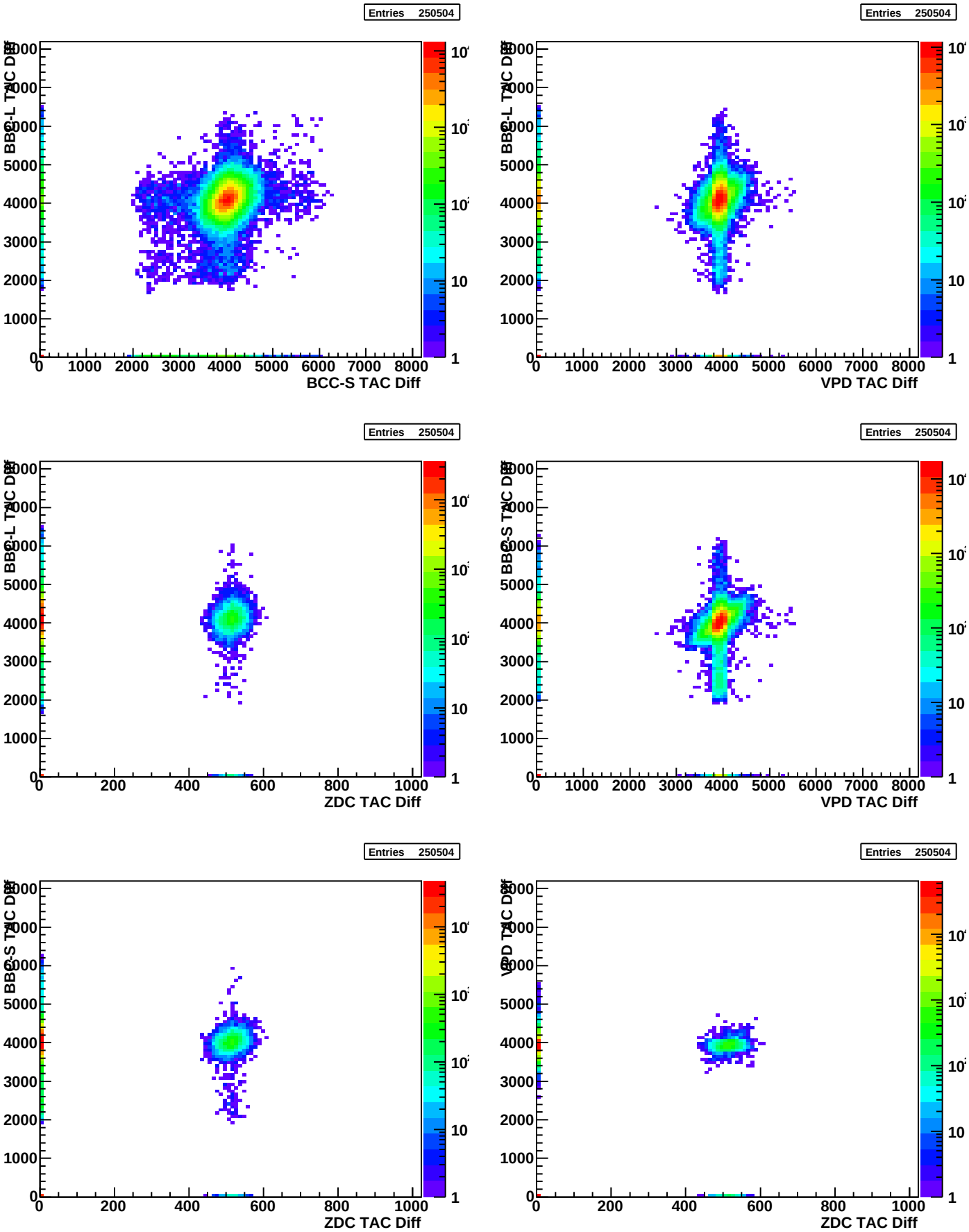

Entries 250504
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

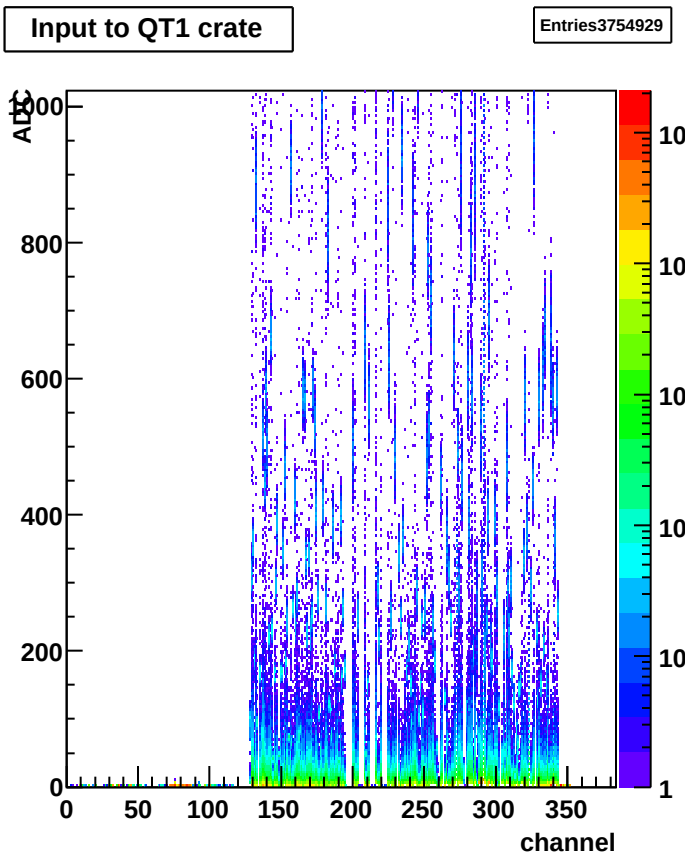

Entries 250504
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

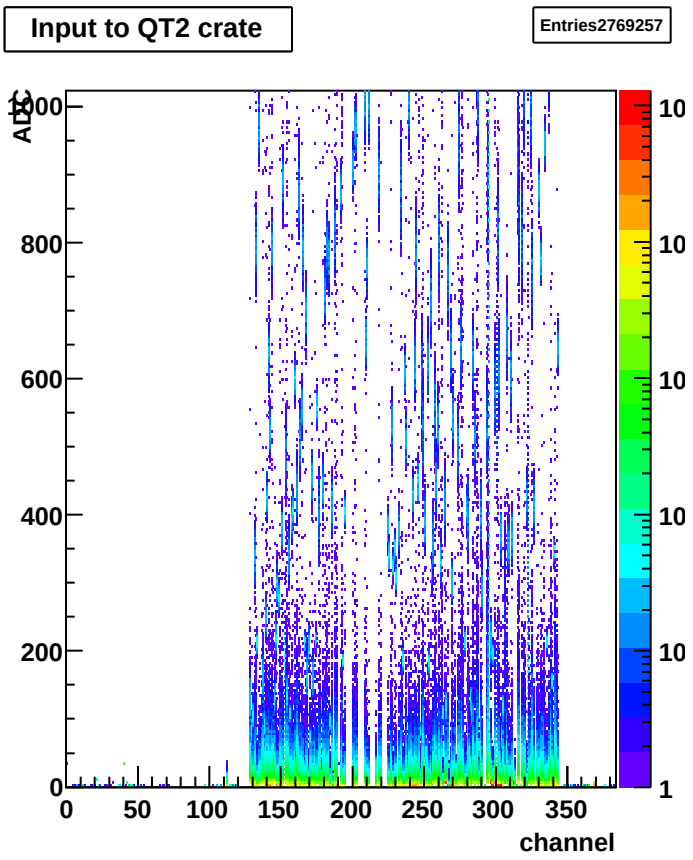

Entries 250504
BBC-L-East ADC Sum
BBC-L-West ADC Sum

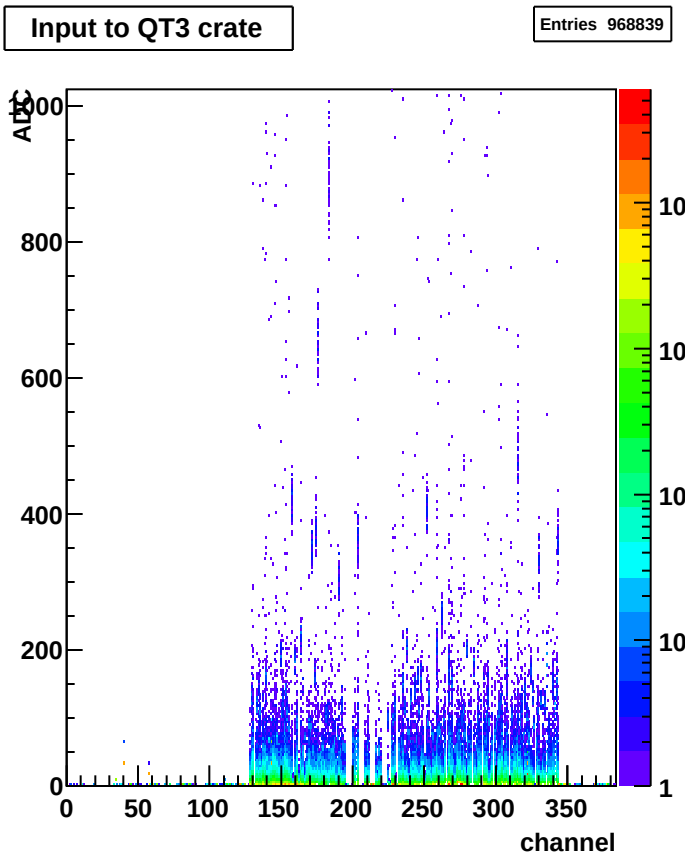

Entries 250504
BBC-S-East ADC Sum
BBC-S-West ADC Sum

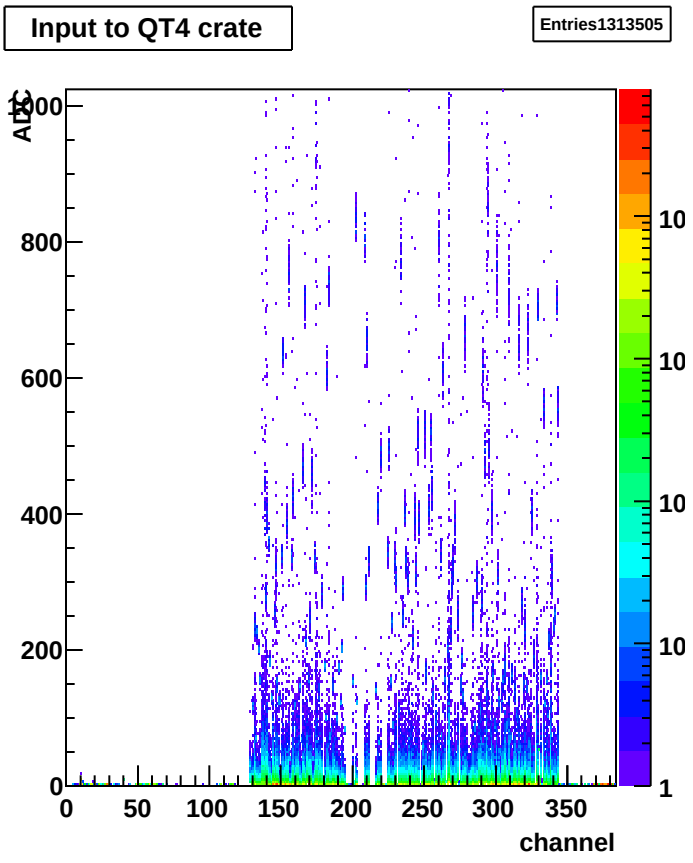

Entries 250504
ZDC East ADC Sum Att
ZDC West ADC Sum Att

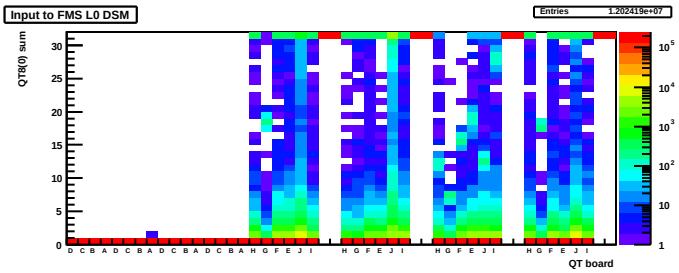

Entries 250504
BBC-L TAC Diff
BCC-S TAC Diff
Entries 250504
BBC-L TAC Diff
VPD TAC Diff
Entries 250504
BBC-L TAC Diff
ZDC TAC Diff
Entries 250504
BBC-S TAC Diff
VPD TAC Diff
Entries 250504
BBC-S TAC Diff
ZDC TAC Diff
Entries 250504
VPD TAC Diff
ZDC TAC Diff

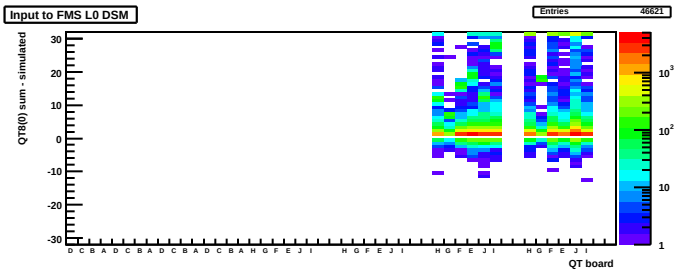

Input to QT1 crate
Entries3754929
ADC
channel

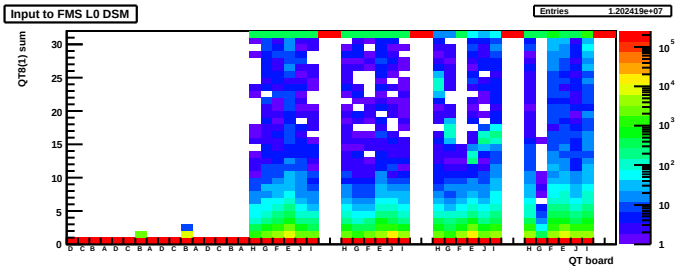

Input to QT2 crate
Entries2769257
ADC
channel

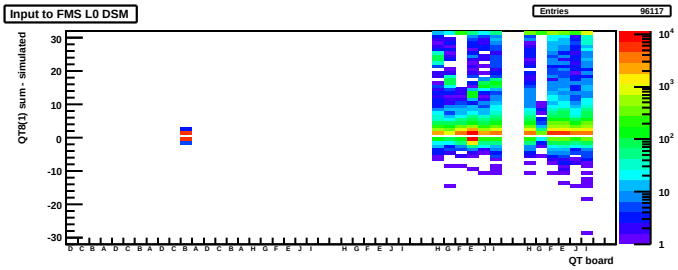

Input to QT3 crate
Entries 968839
ADC
channel

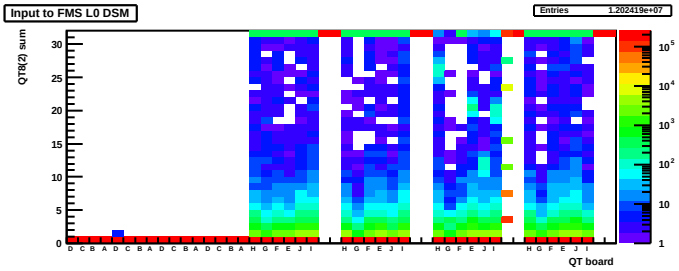

Input to QT4 crate
Entries1313505
ADC
channel

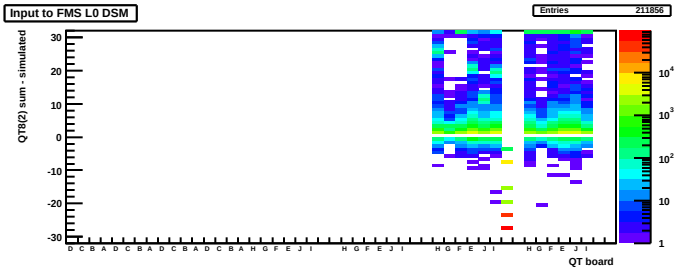

Input to FMS L0 DSM
Entries 1.202419e+07
QT8(0) sum
QT board

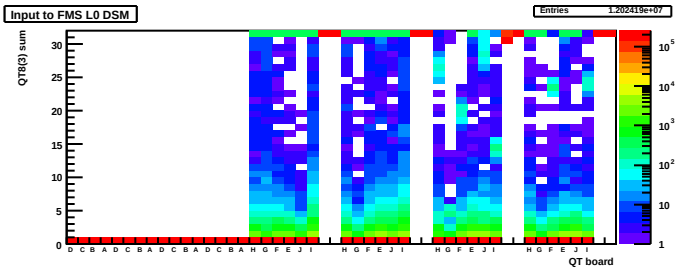

Input to FMS L0 DSM
Entries 46621
QT8(0) sum - simulated
QT board

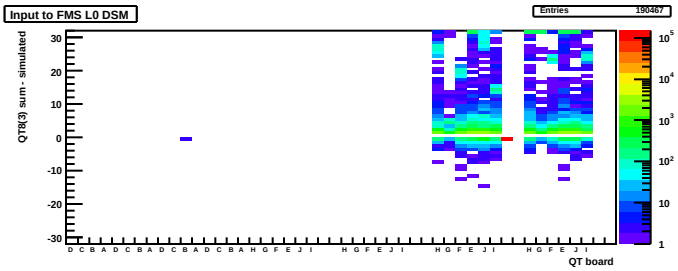

Input to FMS L0 DSM
Entries 1.202419e+07
QT8(1) sum
QT board

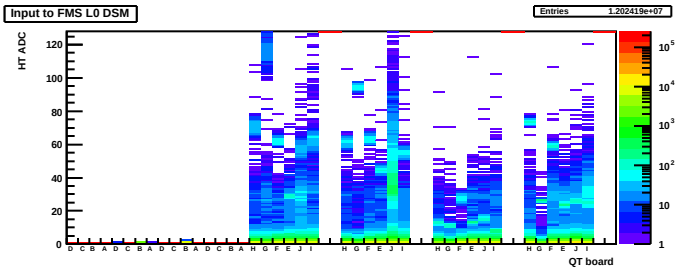

Input to FMS L0 DSM
Entries 96117
QT8(1) sum - simulated
QT board

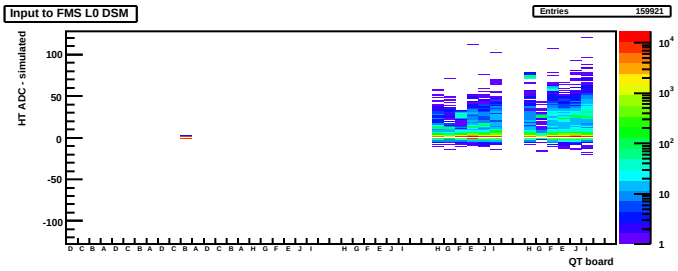

Input to FMS L0 DSM
Entries 1.202419e+07
QT8(2) sum
QT board

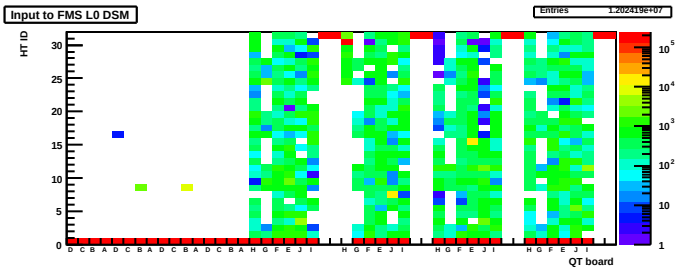

Input to FMS L0 DSM
Entries 211856
QT8(2) sum - simulated
QT board

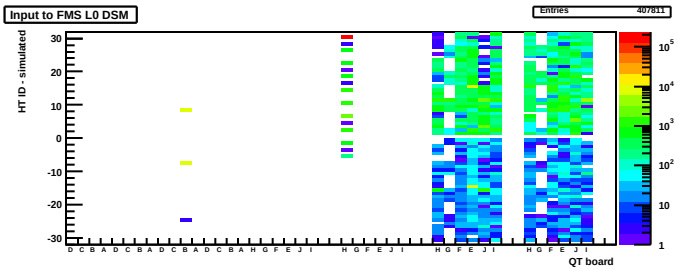

Input to FMS L0 DSM
Entries 1.202419e+07
QT8(3) sum
QT board

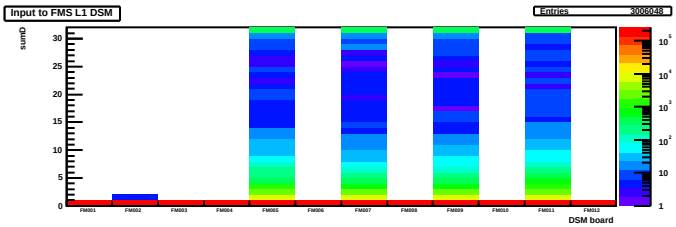

Input to FMS L0 DSM
Entries 190467
QT8(3) sum - simulated
QT board

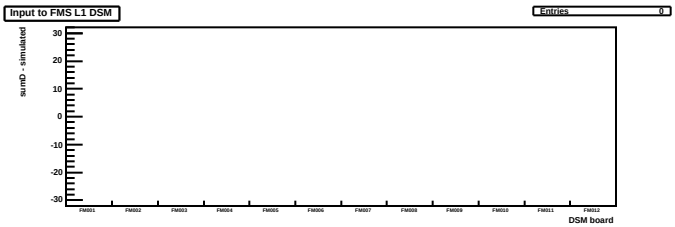

Input to FMS L0 DSM
Entries 1.202419e+07
HT ADC
QT board

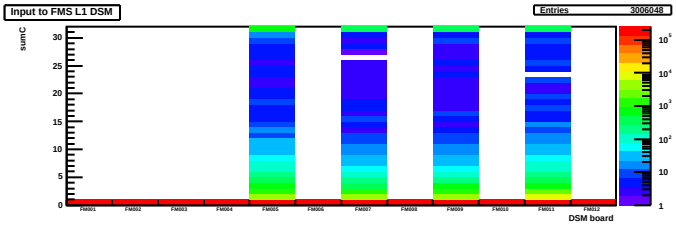

Input to FMS L0 DSM
Entries 159921
HT ADC - simulated
QT board

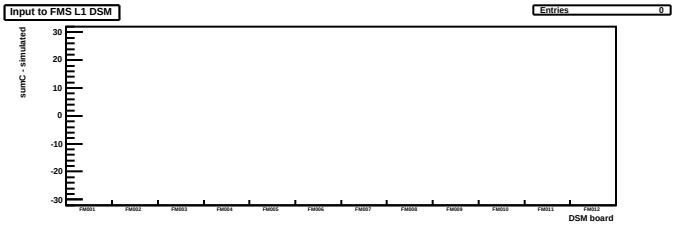

Input to FMS L0 DSM
Entries 1.202419e+07
HT ID
QT board

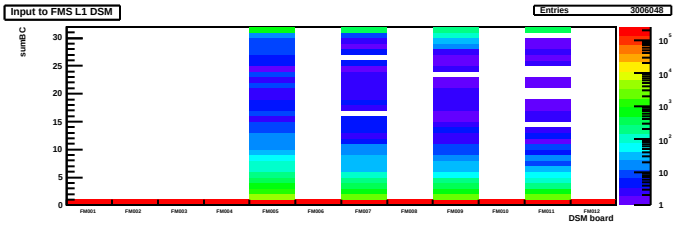

Input to FMS L0 DSM
Entries 407811
HT ID - simulated
QT board

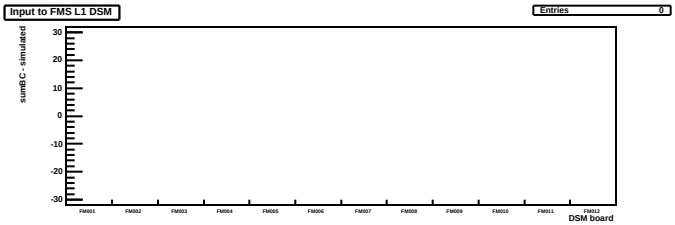

Input to FMS L1 DSM
Entries 3006048
sumD
DSM board

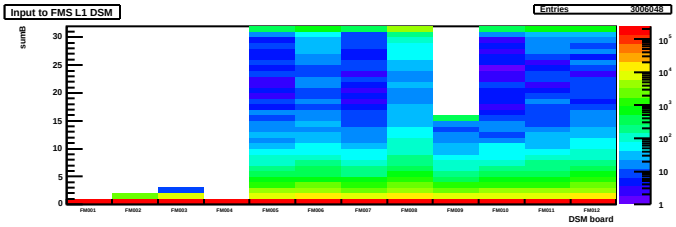

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

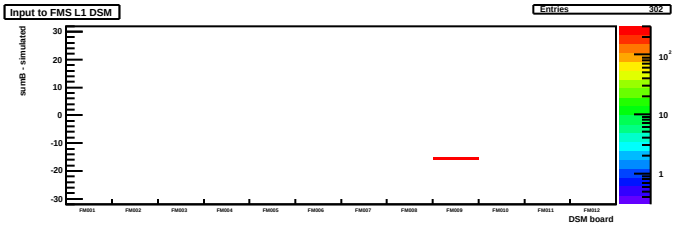

Input to FMS L1 DSM
Entries 3006048
sumC
DSM board

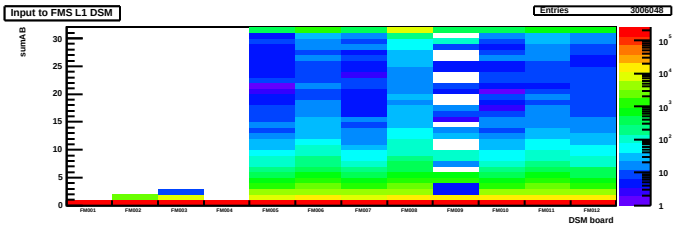

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

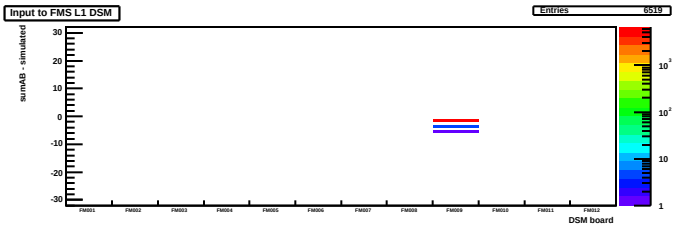

Input to FMS L1 DSM
Entries 3006048
sumBC
DSM board

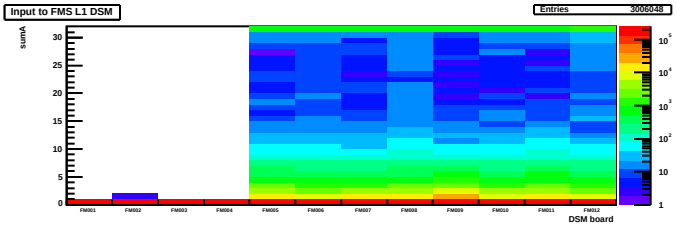

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

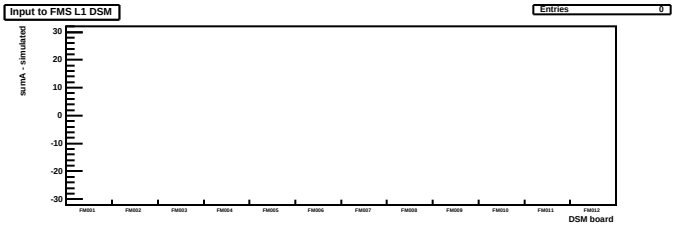

Input to FMS L1 DSM
Entries 3006048
sumB
DSM board

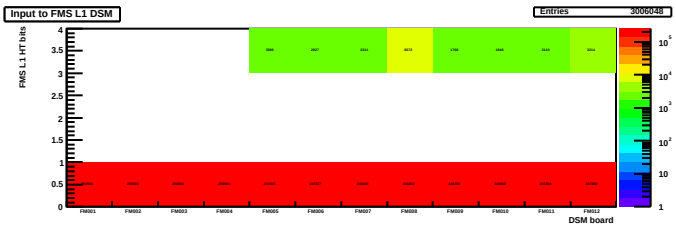

Input to FMS L1 DSM
Entries 302
sumB - simulated
DSM board

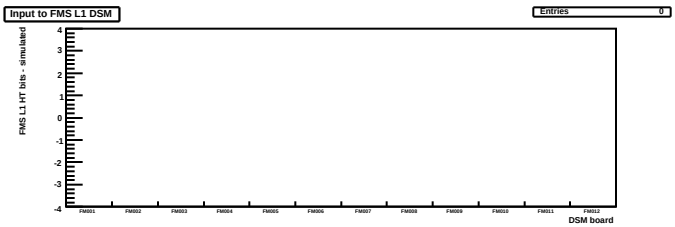

Input to FMS L1 DSM
Entries 3006048
sumAB
DSM board

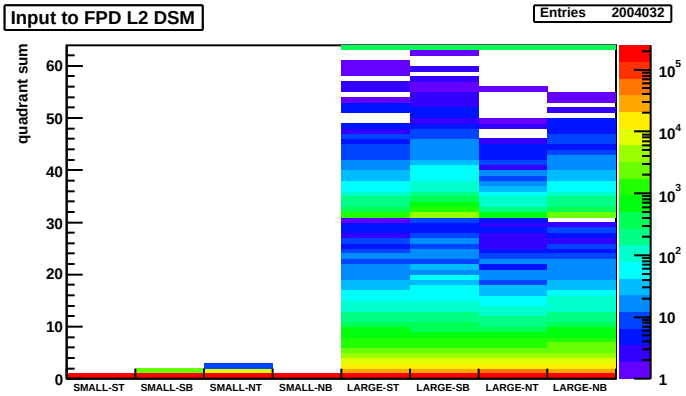

Input to FMS L1 DSM
Entries 6519
sumAB - simulated
DSM board

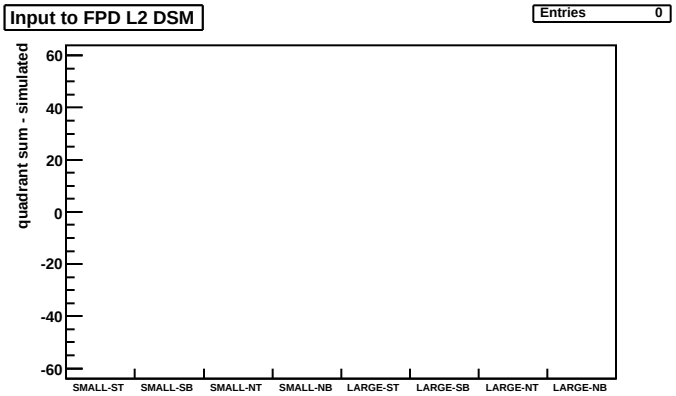

Input to FMS L1 DSM
Entries 3006048
sumA
DSM board

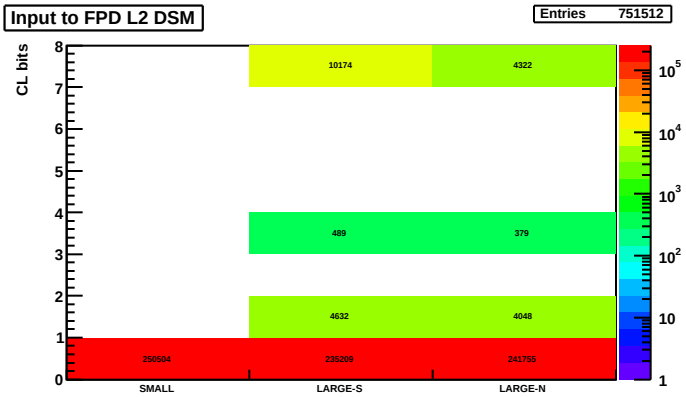

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

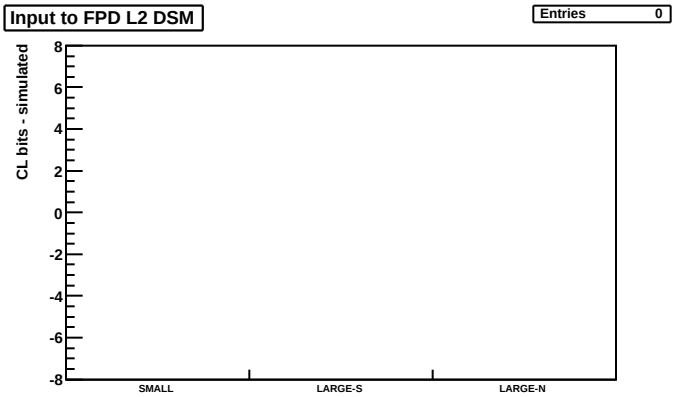

Input to FMS L1 DSM
Entries 3006048
FMS L1 HT bits
DSM board

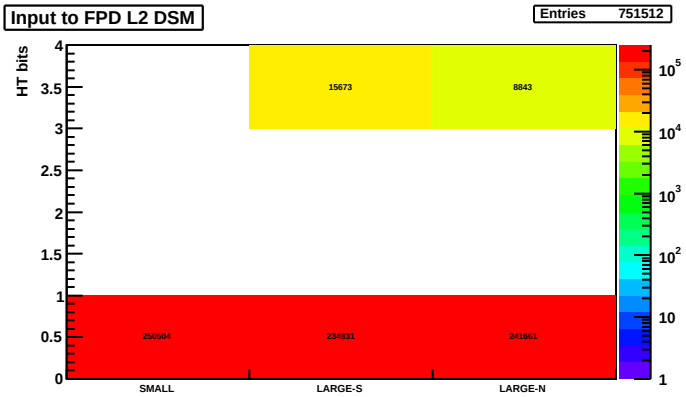

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

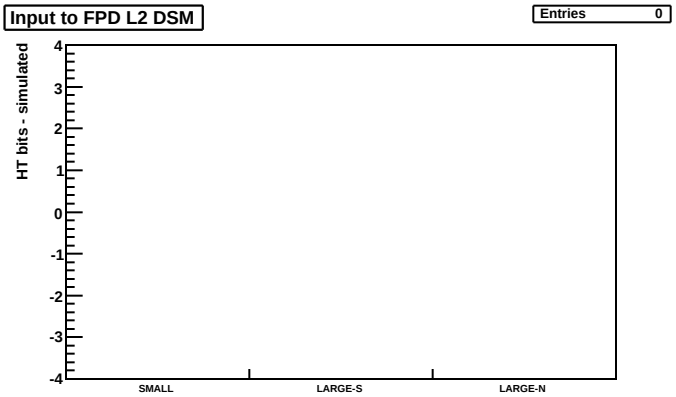

Input to FPD L2 DSM
Entries 2004032
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

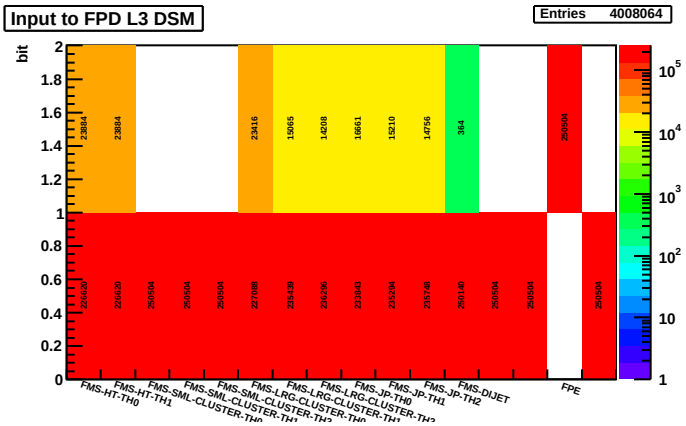

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

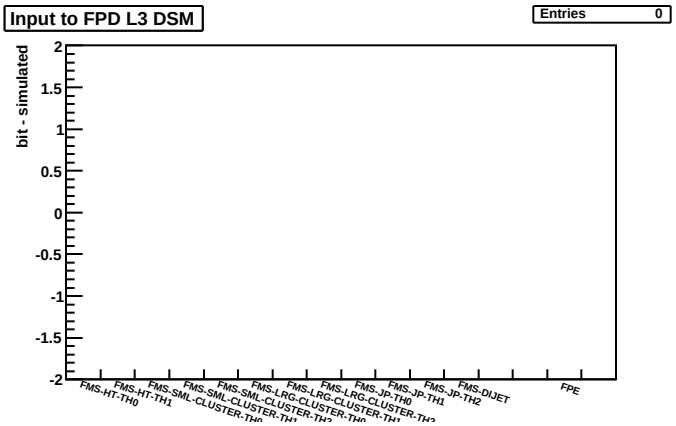

Input to FPD L2 DSM
Entries 751512
CL bits
10174
4322
489
379
4632
4048
250504
235209
241755
SMALL
LARGE-S
LARGE-N

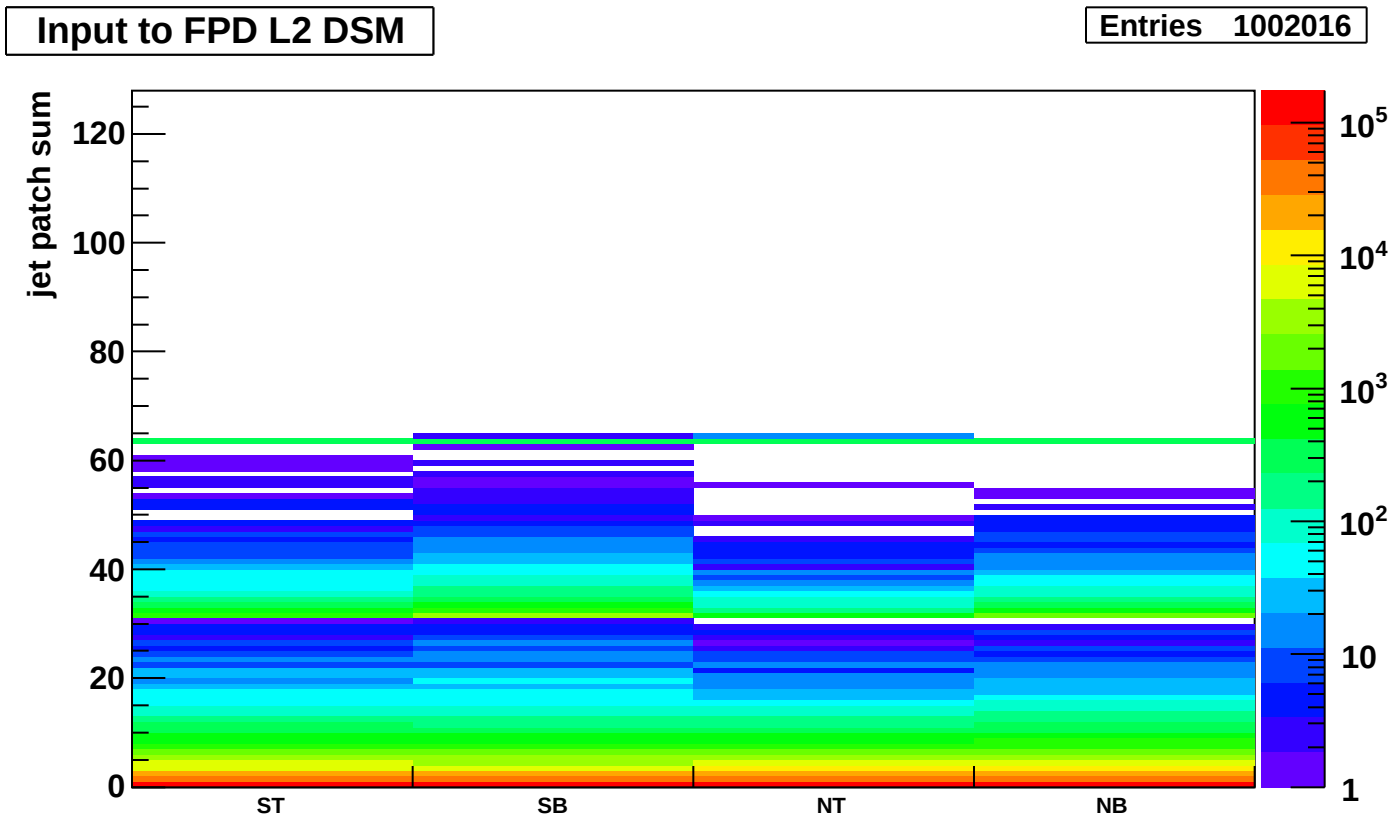

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

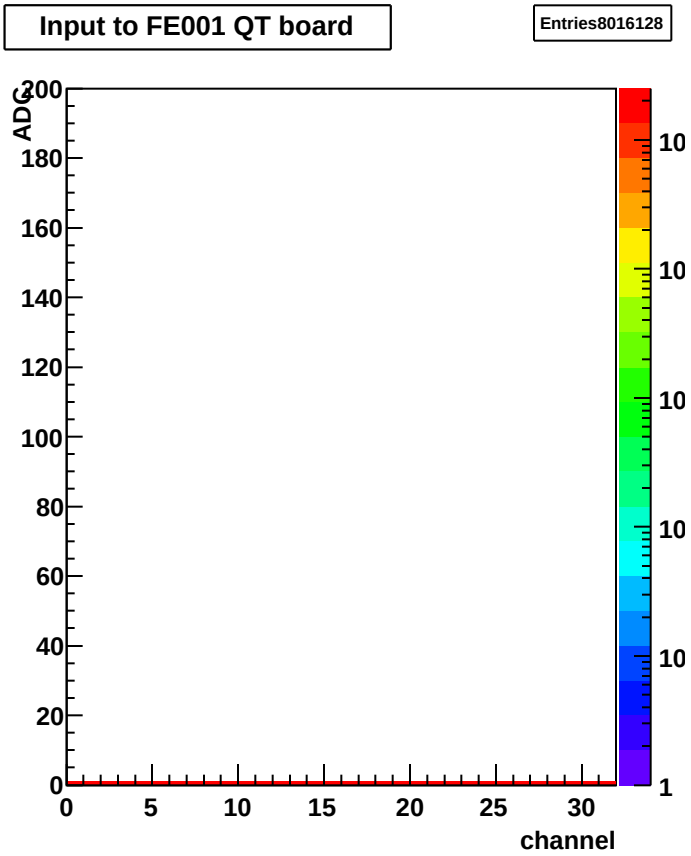

Input to FPD L2 DSM
Entries 751512
HT bits
15673
8843
250504
234831
241661
SMALL
LARGE-S
LARGE-N

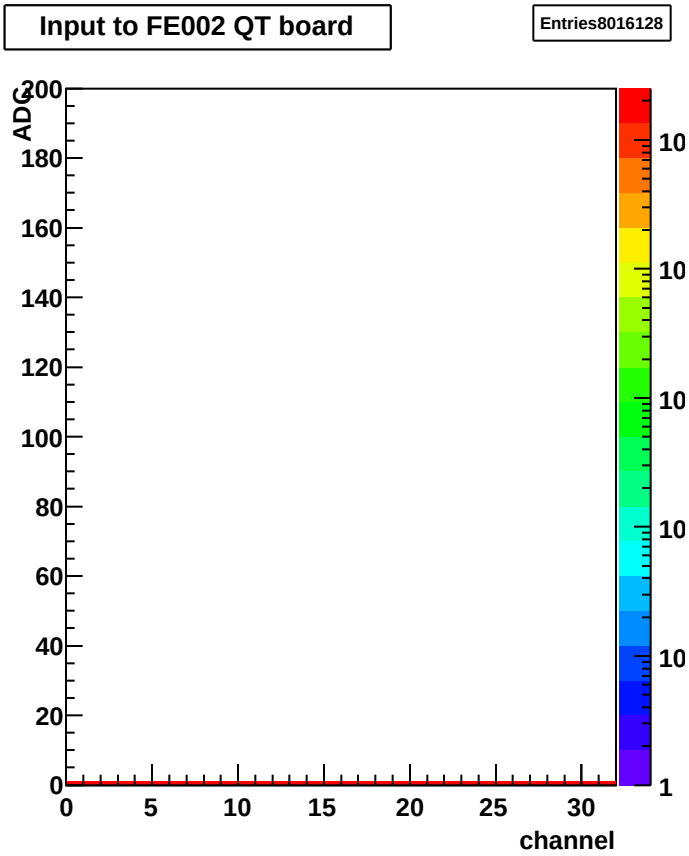

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

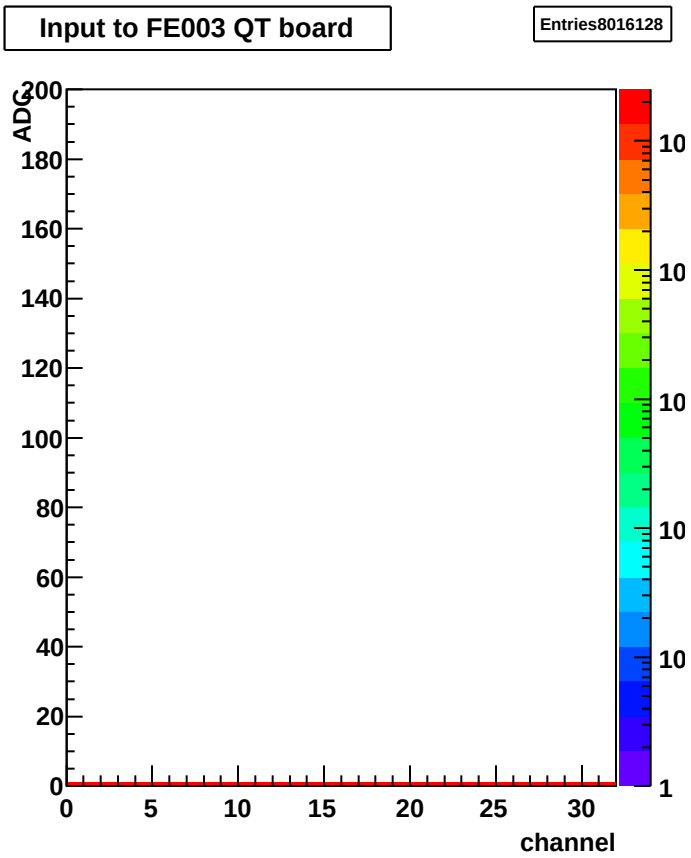

Input to FPD L3 DSM
Entries 4008064
bit
23804
23884
23416
19065
14208
16661
15210
14756
364
250504
226620
226630
250504
250504
250504
227088
235439
236286
233843
235294
235748
250140
250504
250504
250504
250504
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

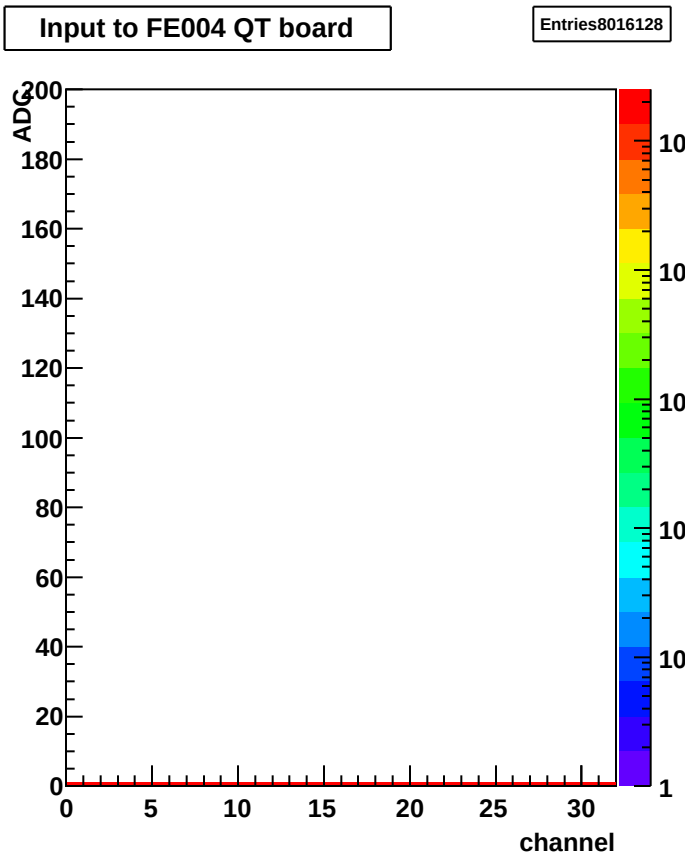

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

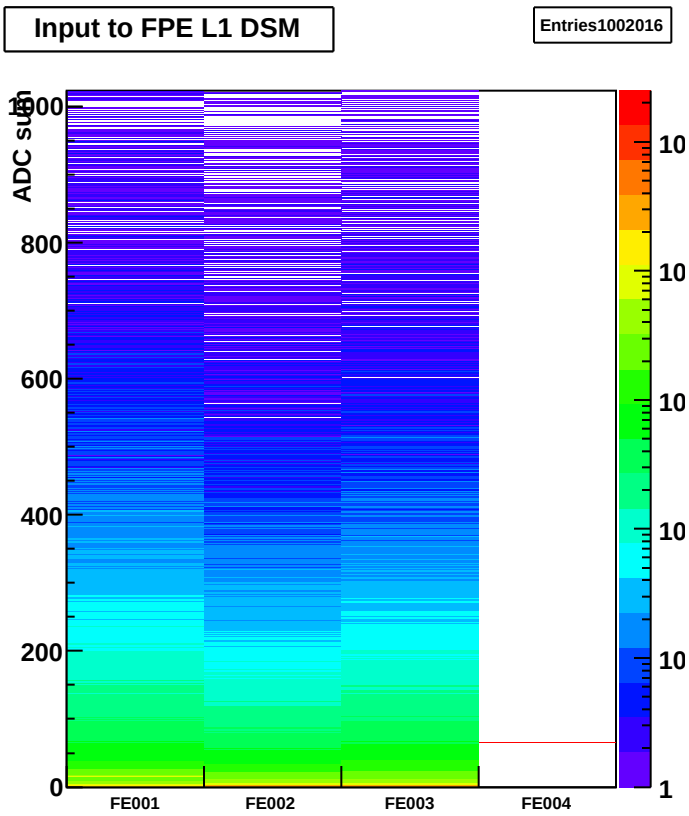

Input to FPD L2 DSM
Entries 1002016
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

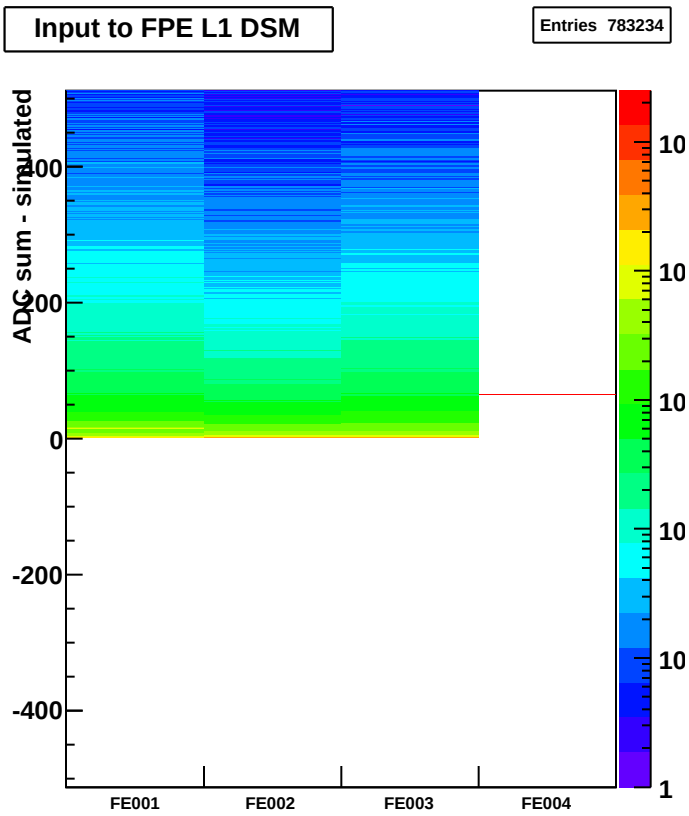

Input to FE001 QT board
Entries8016128
ADC
channel

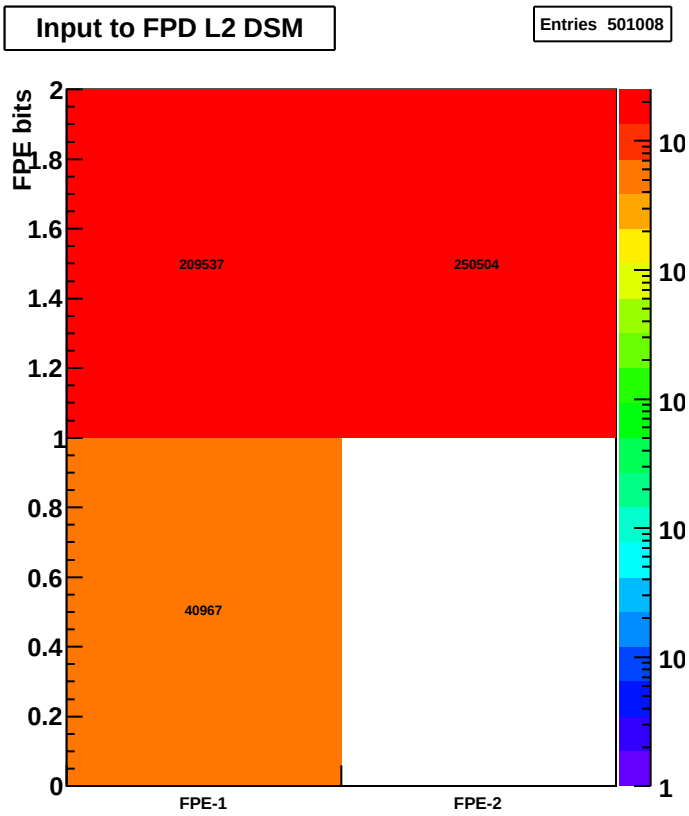

Input to FE002 QT board
Entries8016128
ADC
channel

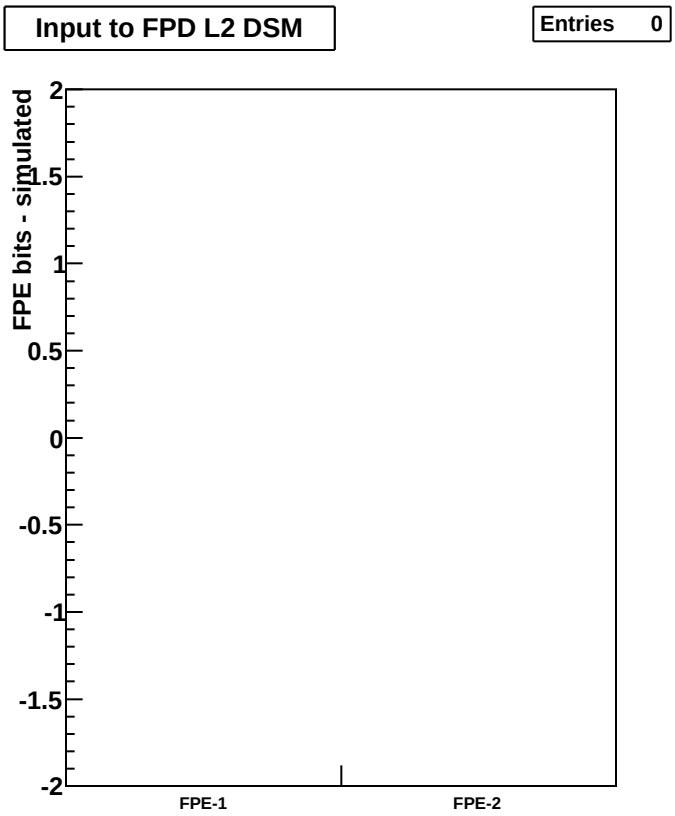

Input to FE003 QT board
Entries8016128
ADC
channel


Input to FE004 QT board
Entries8016128
ADC
channel


Input to FPE L1 DSM
Entries1002016
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 783234
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 501008
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
209537
250504
40967
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2