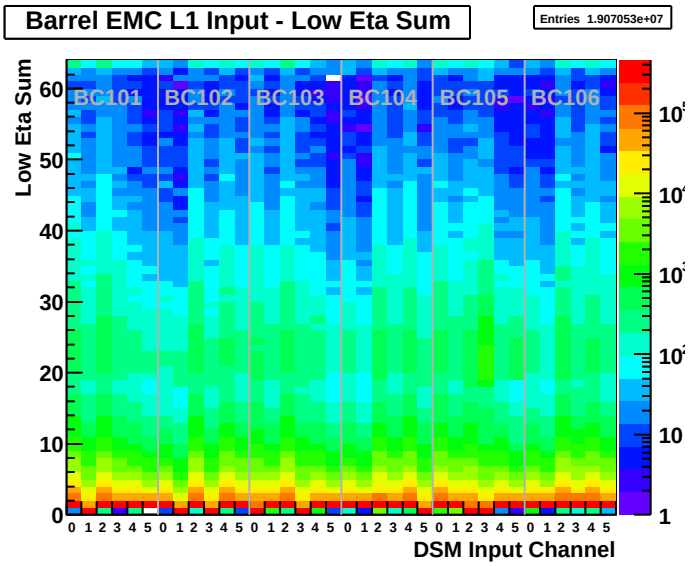
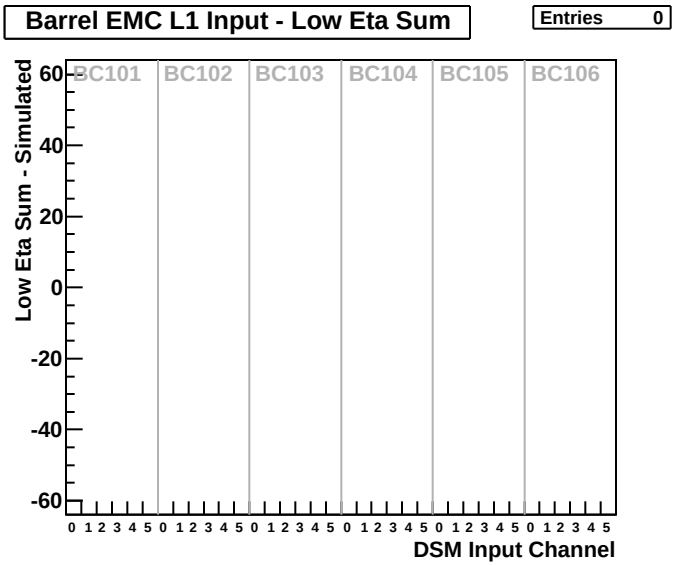
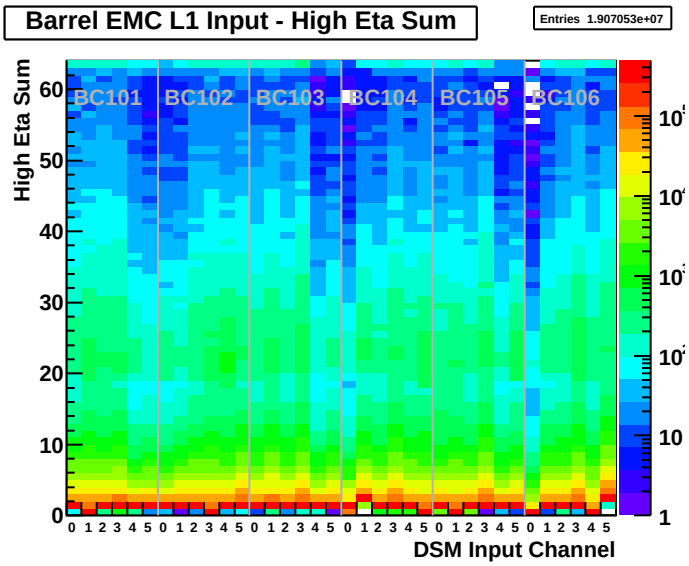
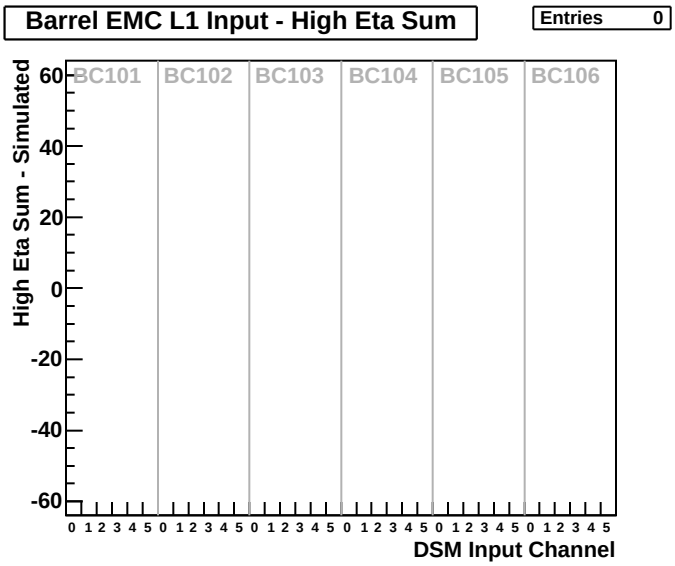
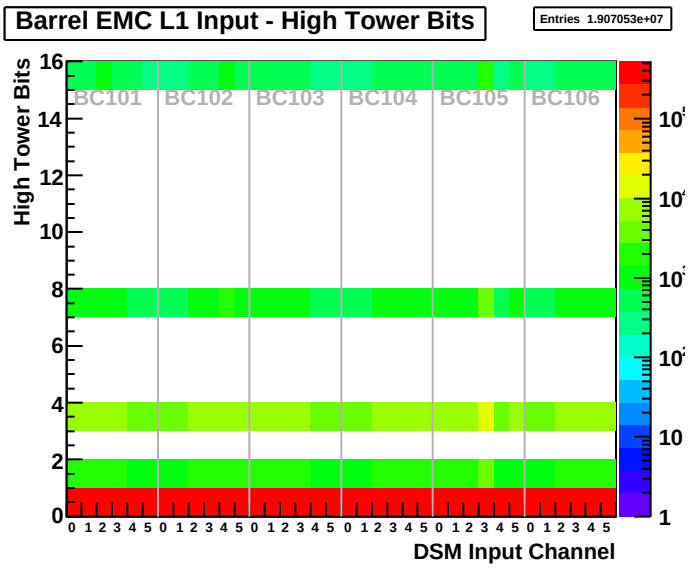
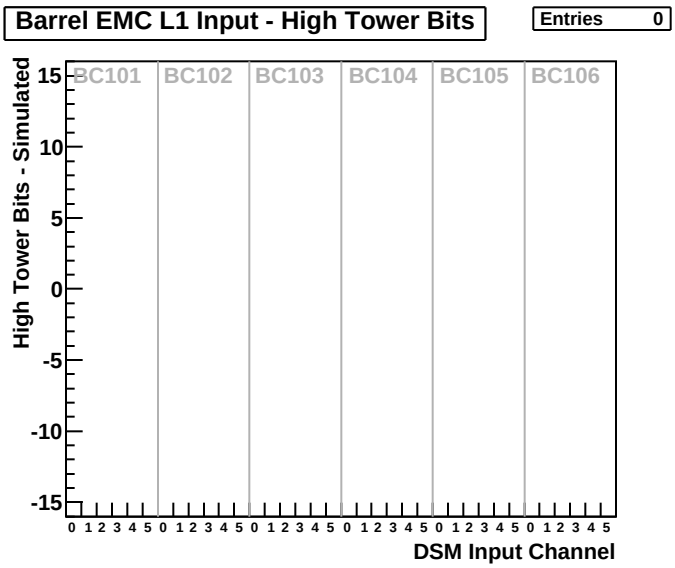

Barrel EMC L1 Input - Low Eta Sum
Entries 1.907053e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

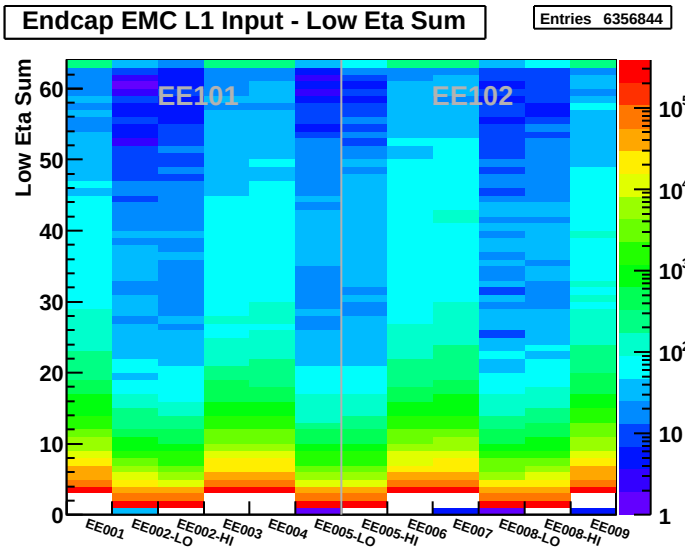

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

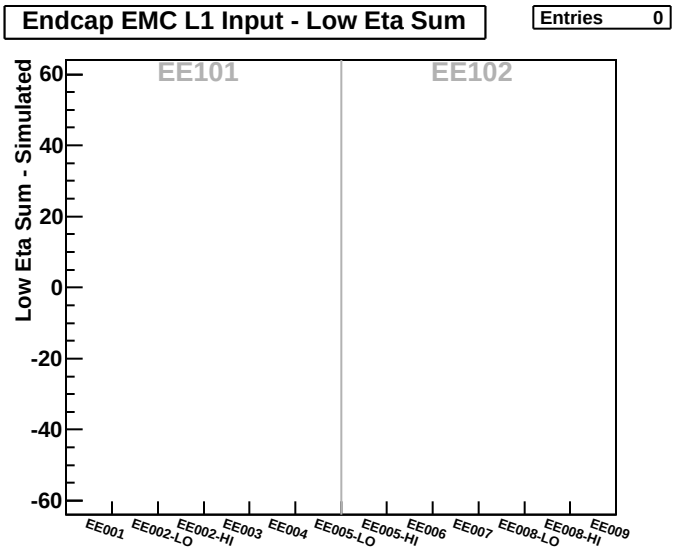

Barrel EMC L1 Input - High Eta Sum
Entries 1.907053e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

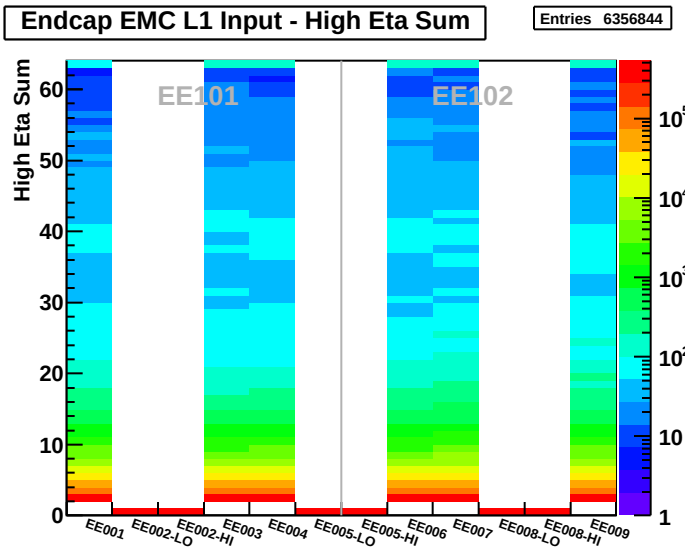

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

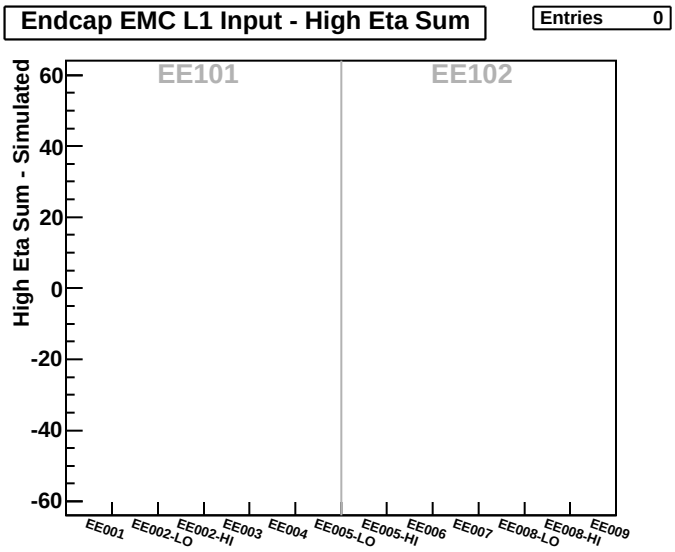

Barrel EMC L1 Input - High Tower Bits
Entries 1.907053e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

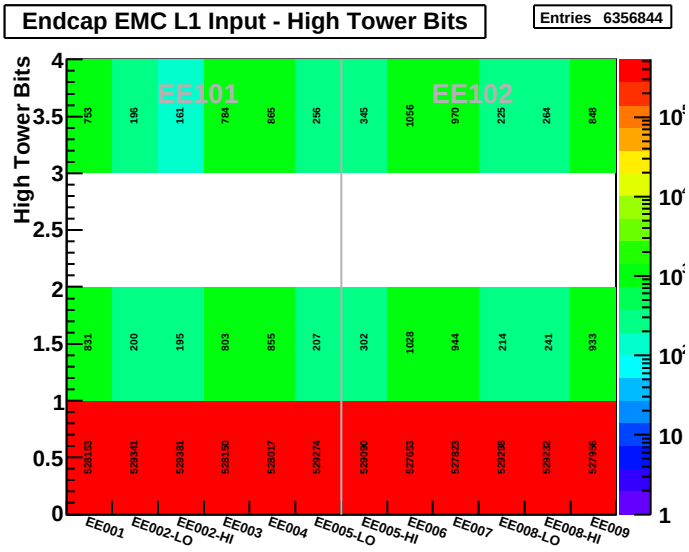

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

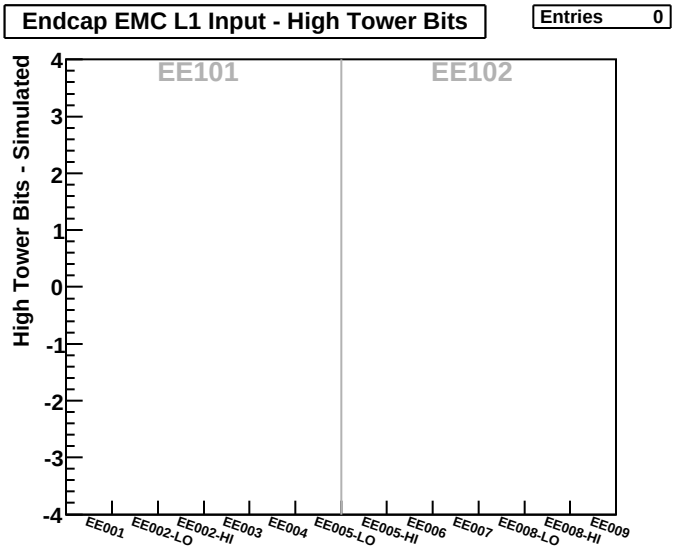

Endcap EMC L1 Input - Low Eta Sum
Entries 6356844
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

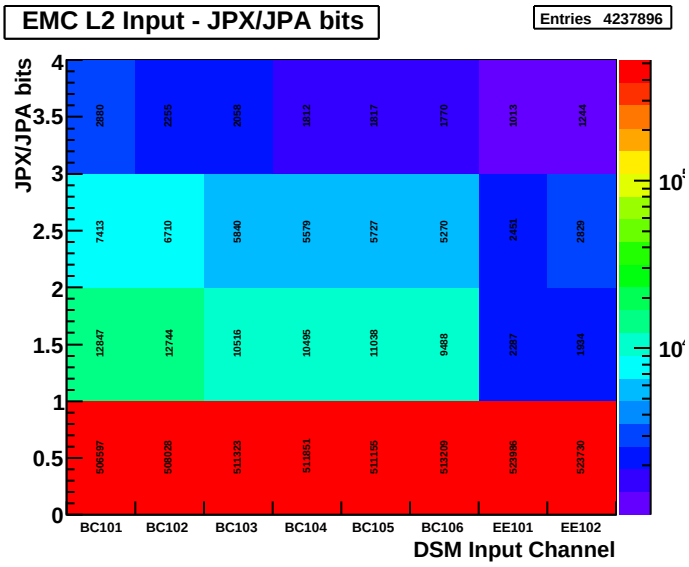

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

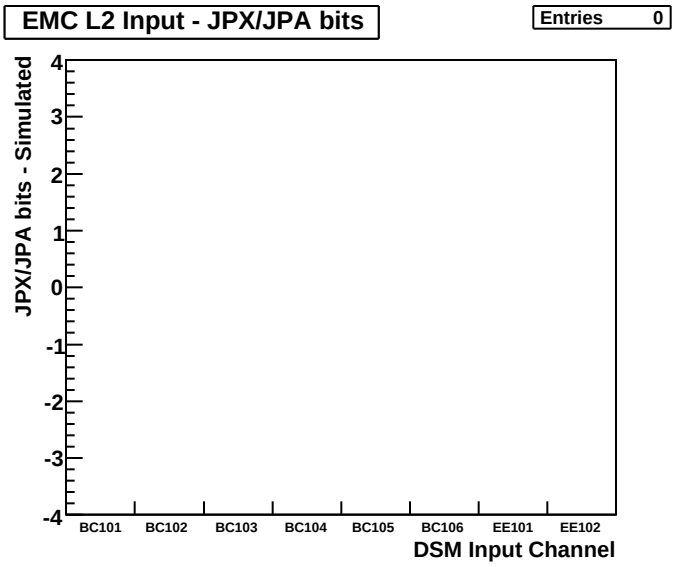

Endcap EMC L1 Input - High Eta Sum
Entries 6356844
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

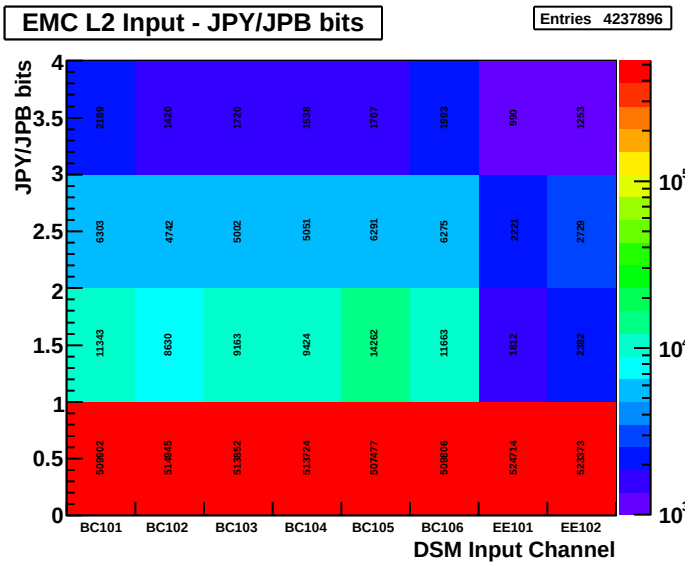

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

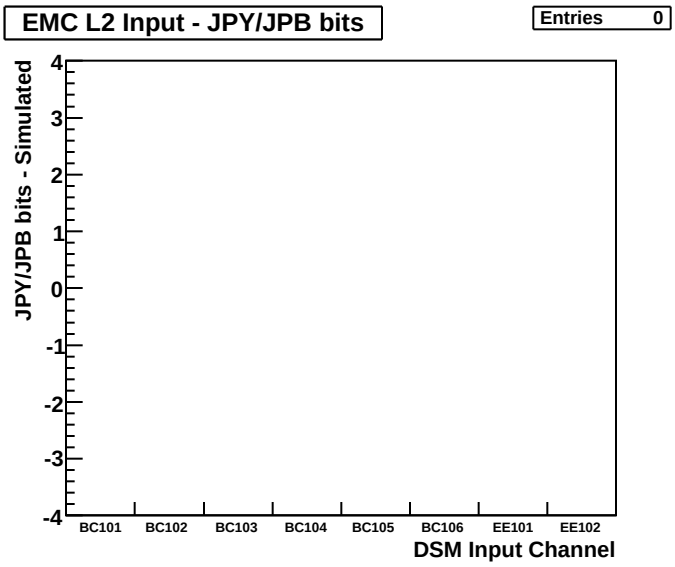

Endcap EMC L1 Input - High Tower Bits
Entries 6356844
High Tower Bits
EE101
EE102
753 196 161 784 865 256 345 1056 970 225 264 848
831 200 195 803 855 207 302 1028 944 214 241 933
528153 529341 529381 528150 528017 529274 529090 527653 527823 529298 529232 527956
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

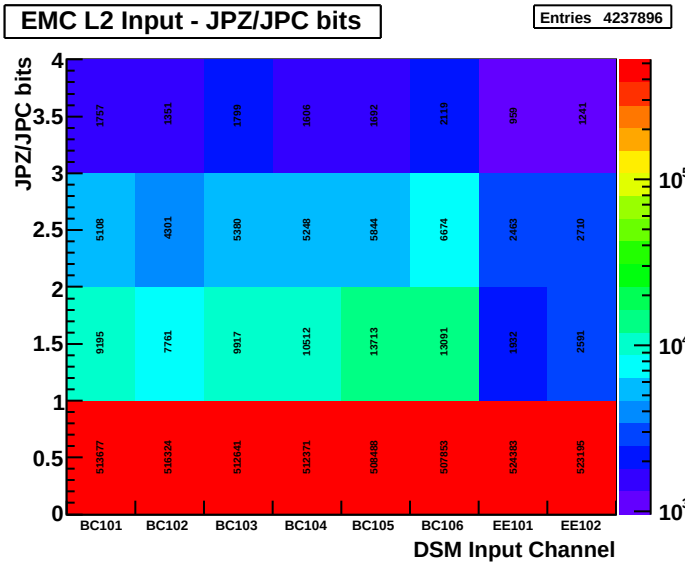

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

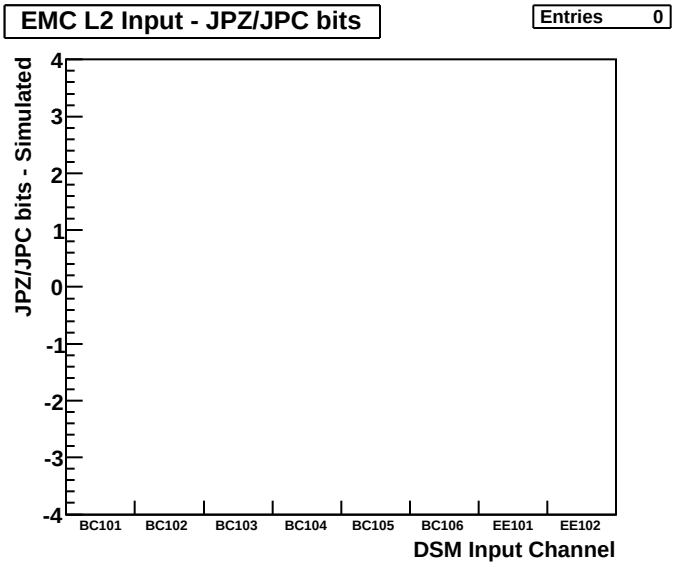

EMC L2 Input - JPX/JPA bits
Entries 4237896
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2880 2255 2058 1812 1817 1770 1013 1244
7413 6710 5840 5579 5727 5270 2451 2829
12847 12744 10516 10495 11038 9488 2287 1934
506597 508028 511323 511851 511155 513209 523986 523730

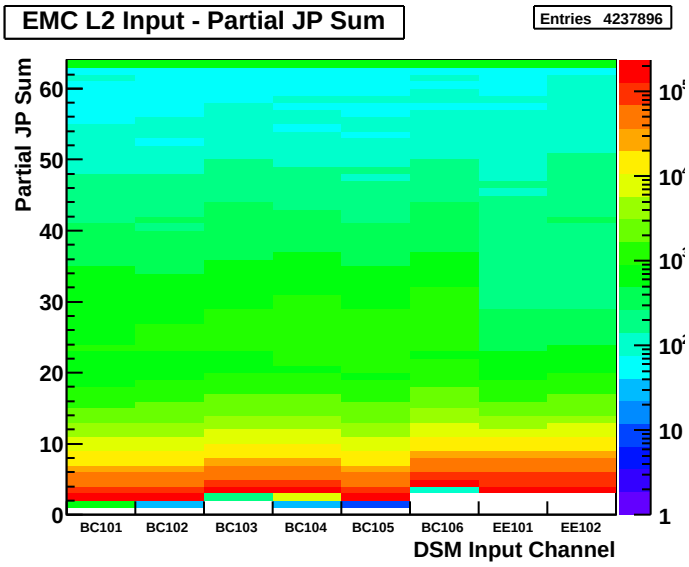

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

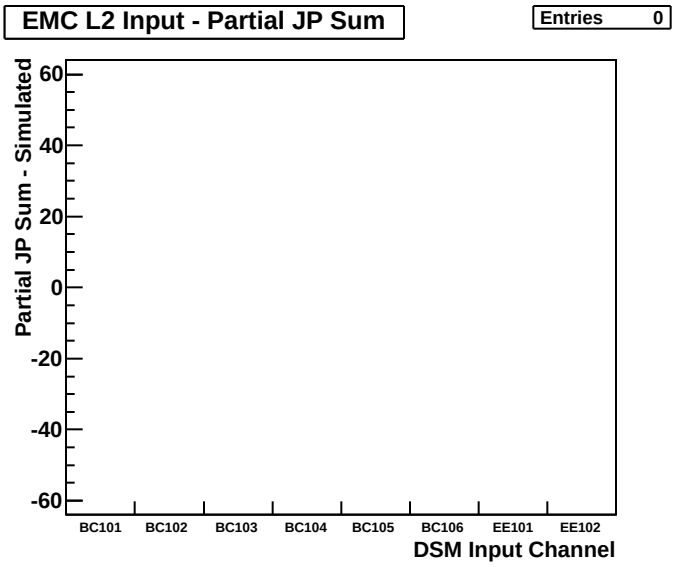

EMC L2 Input - JPY/JPB bits
Entries 4237896
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2189 1420 1720 1538 1707 1993 990 1253
6303 4742 5002 5051 6291 6275 2221 2729
11343 8630 9163 9424 14262 11663 1612 2382
509802 514945 513852 513724 507477 509806 524714 523373

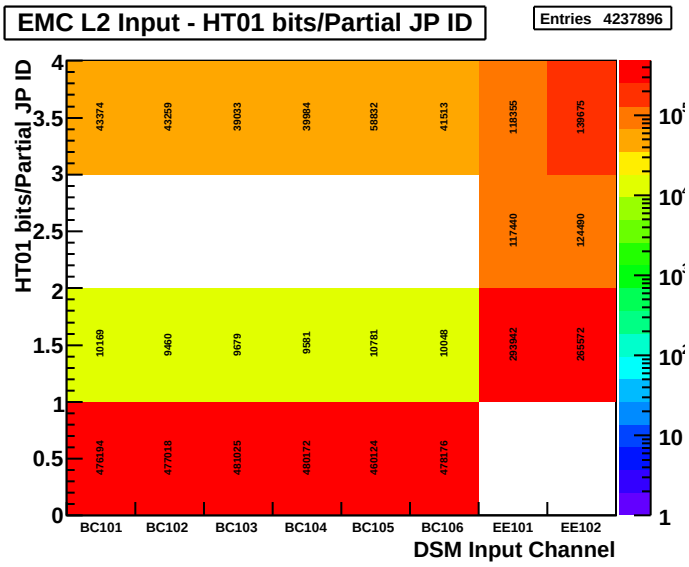

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

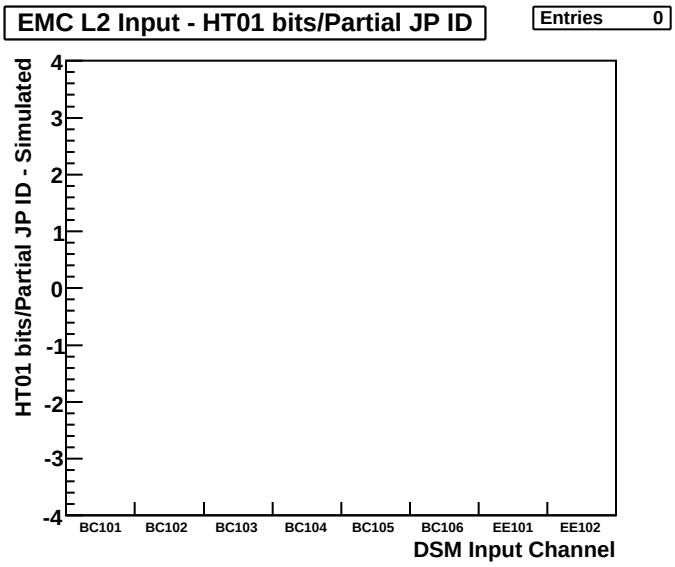

EMC L2 Input - JPZ/JPC bits
Entries 4237896
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1757 1351 1799 1606 1692 2119 959 1241
5108 4301 5380 5248 5844 6674 2463 2710
9395 7761 9817 10512 13713 13091 1932 2591
513677 516324 512641 512371 508488 507853 524383 523195

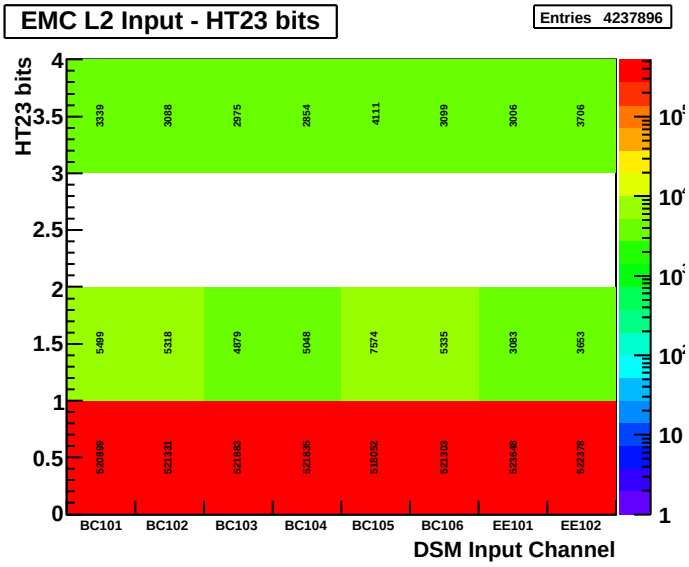

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

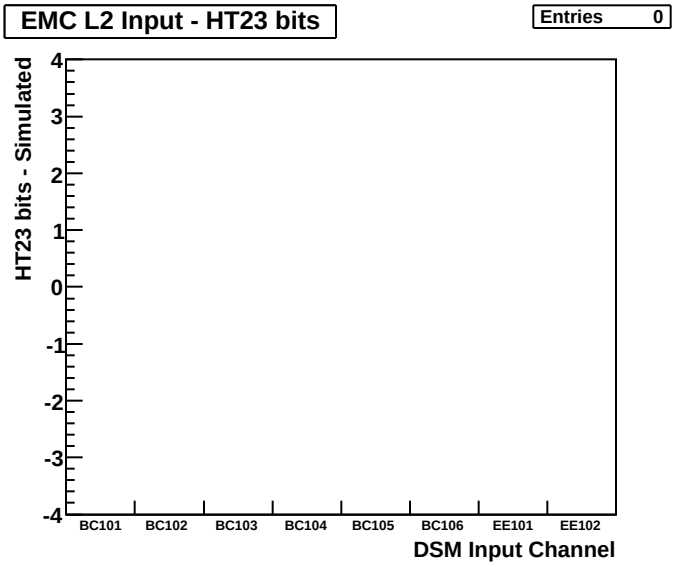

EMC L2 Input - Partial JP Sum
Entries 4237896
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

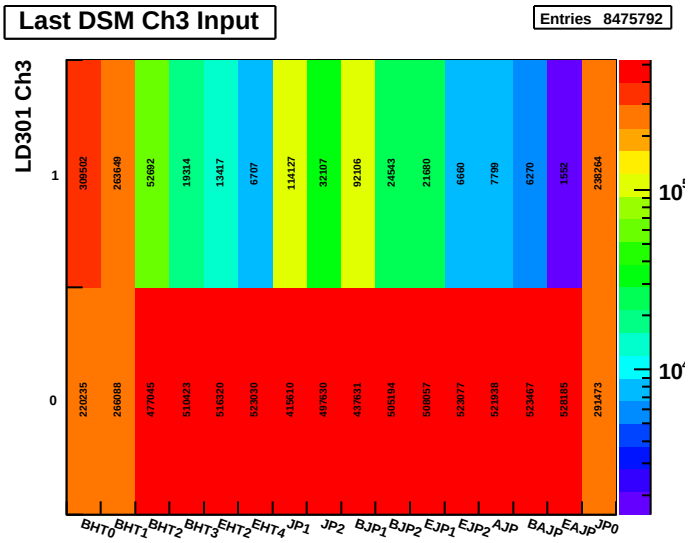

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

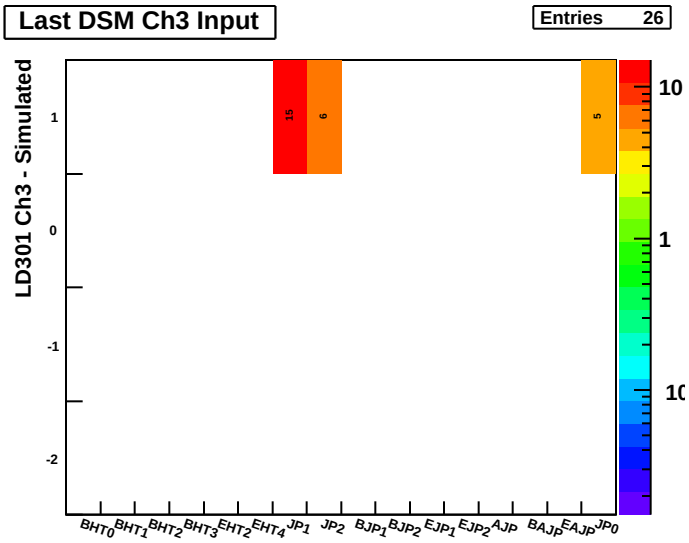

EMC L2 Input - HT01 bits/Partial JP ID
Entries 4237896
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

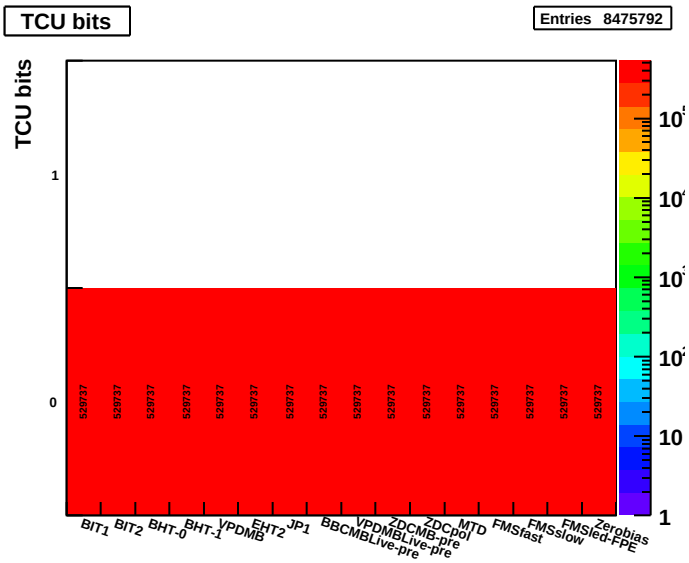

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

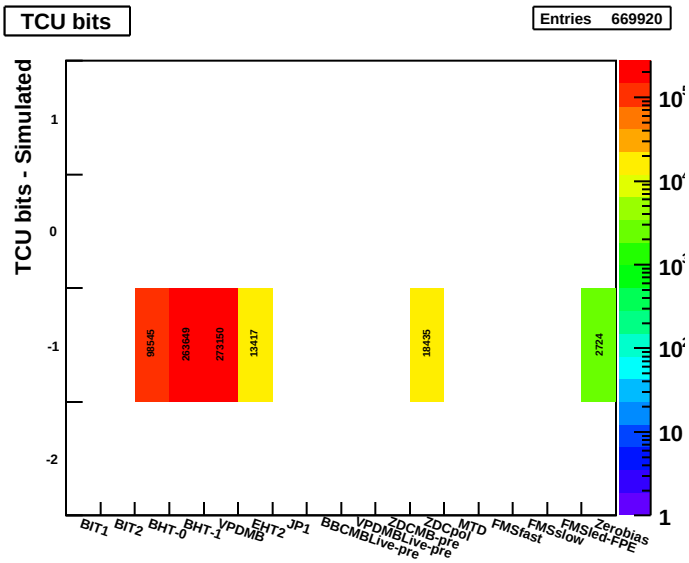

EMC L2 Input - HT23 bits
Entries 4237896
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

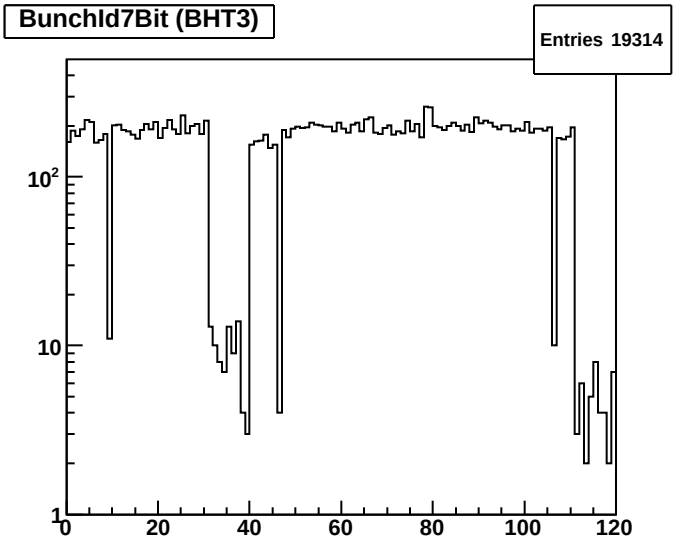

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

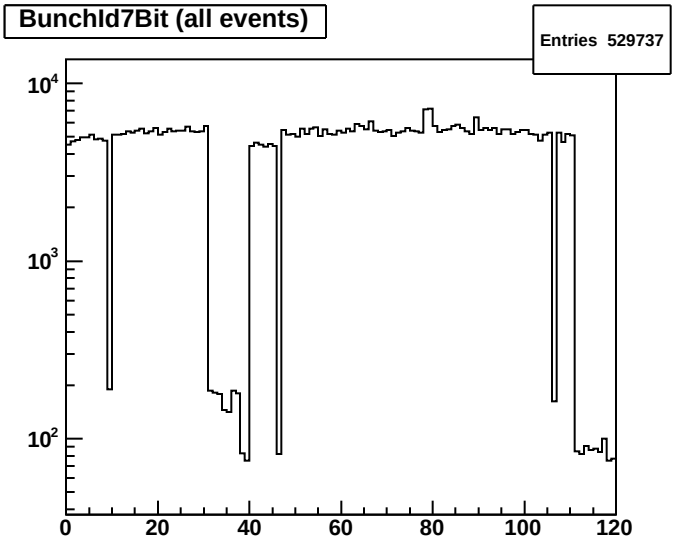

Last DSM Ch3 Input
Entries 8475792
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

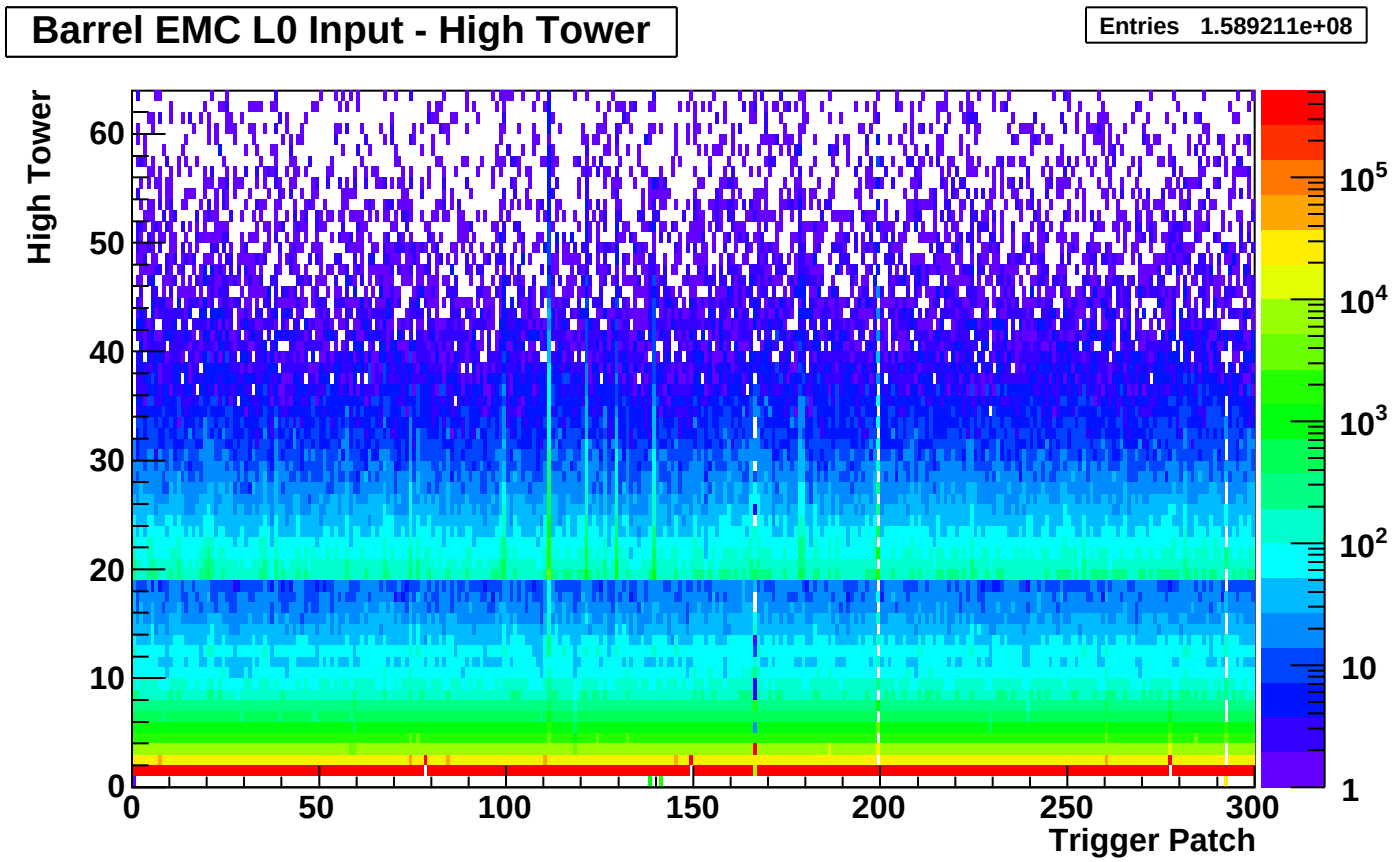

Last DSM Ch3 Input
Entries 26
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

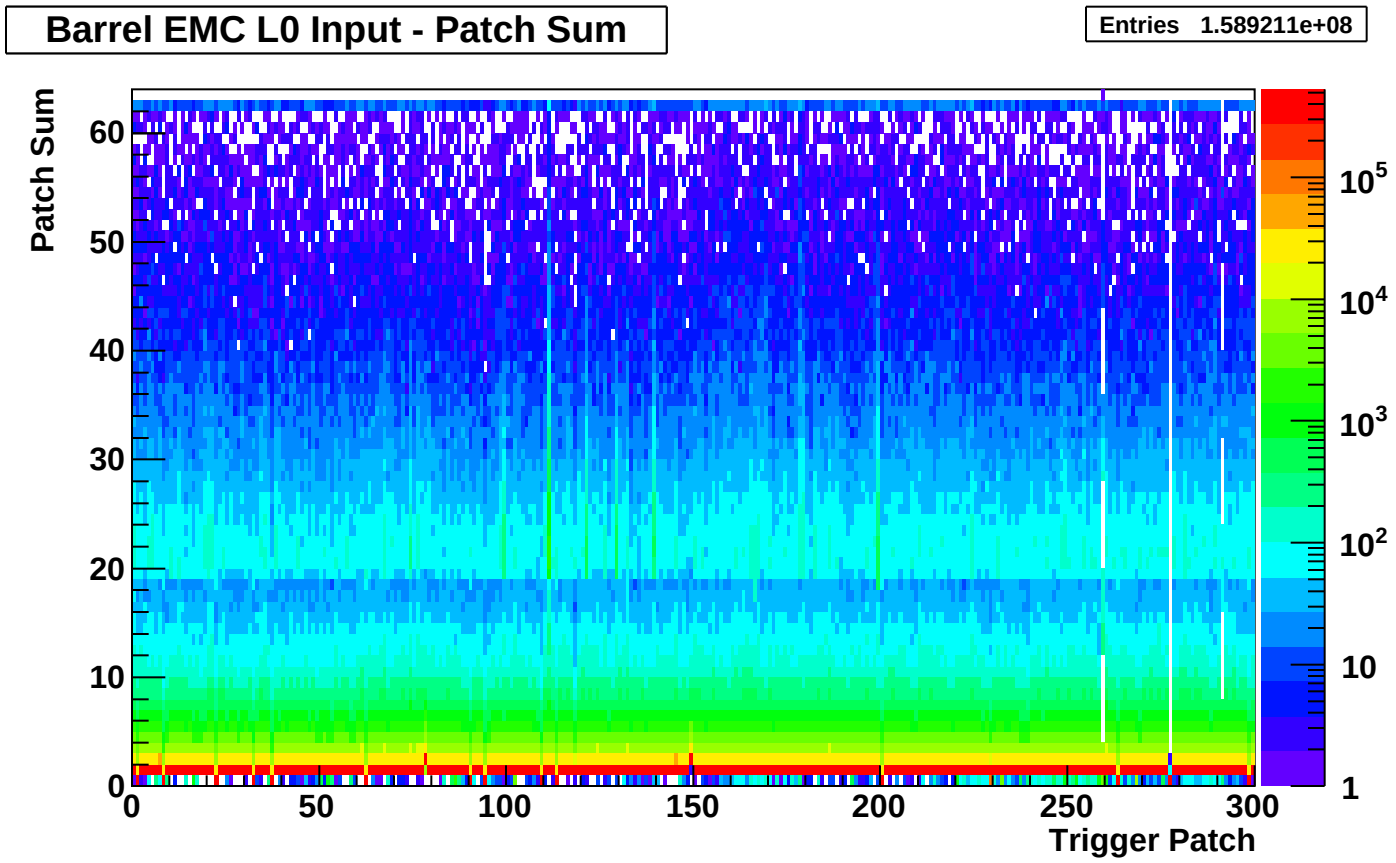

TCU bits
Entries 8475792
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

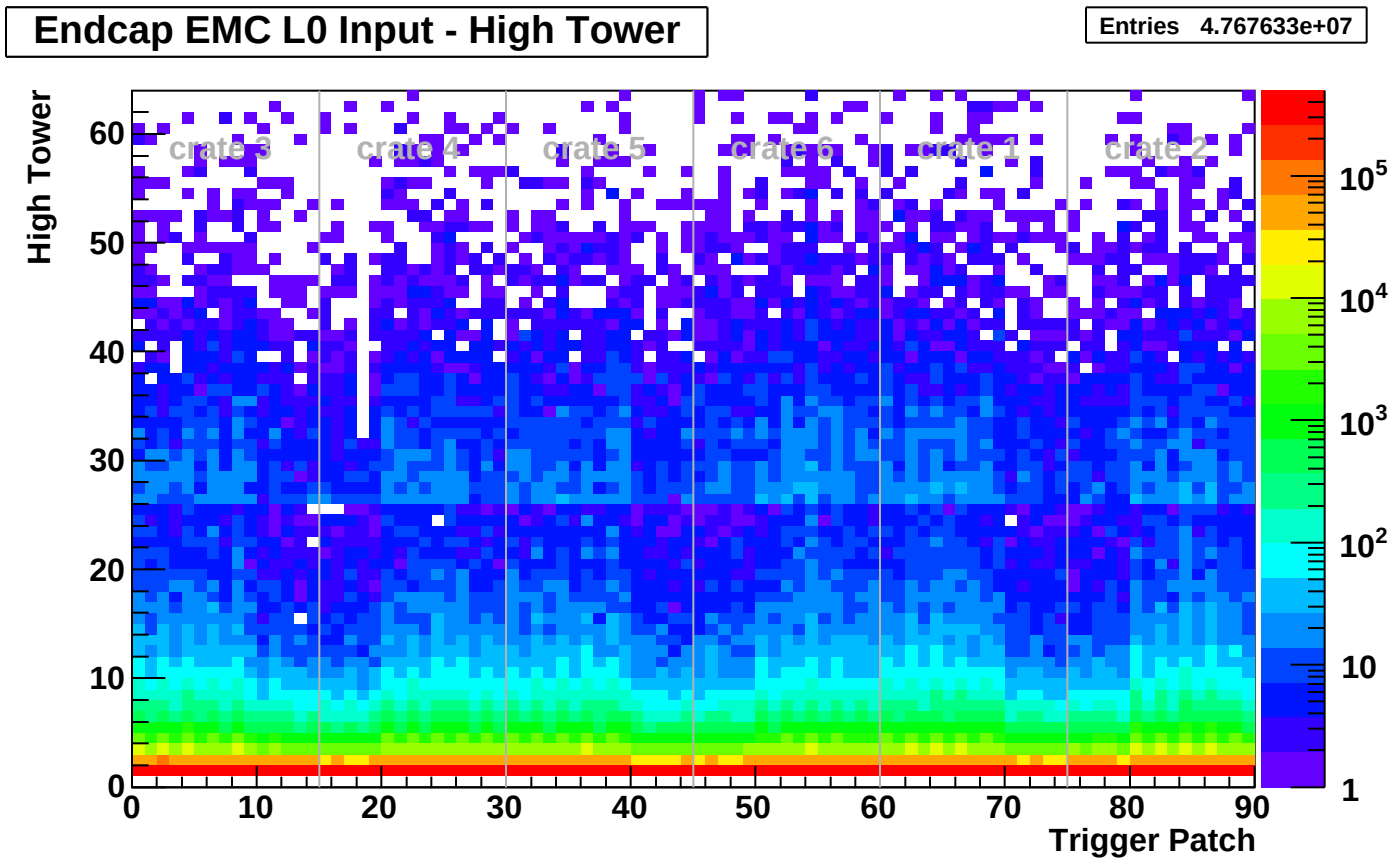

TCU bits
Entries 669920
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

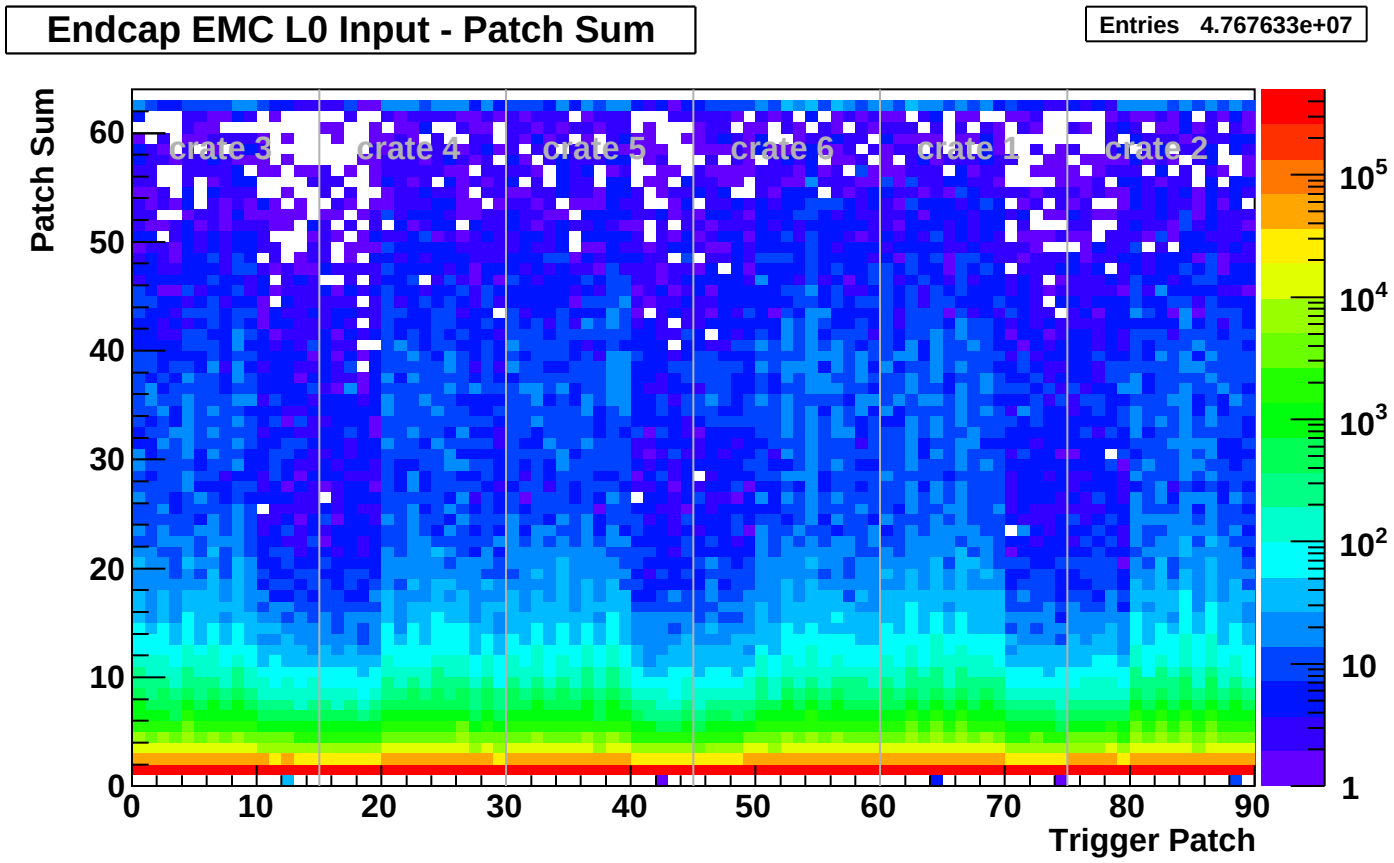

BunchId7Bit (BHT3)
Entries 19314

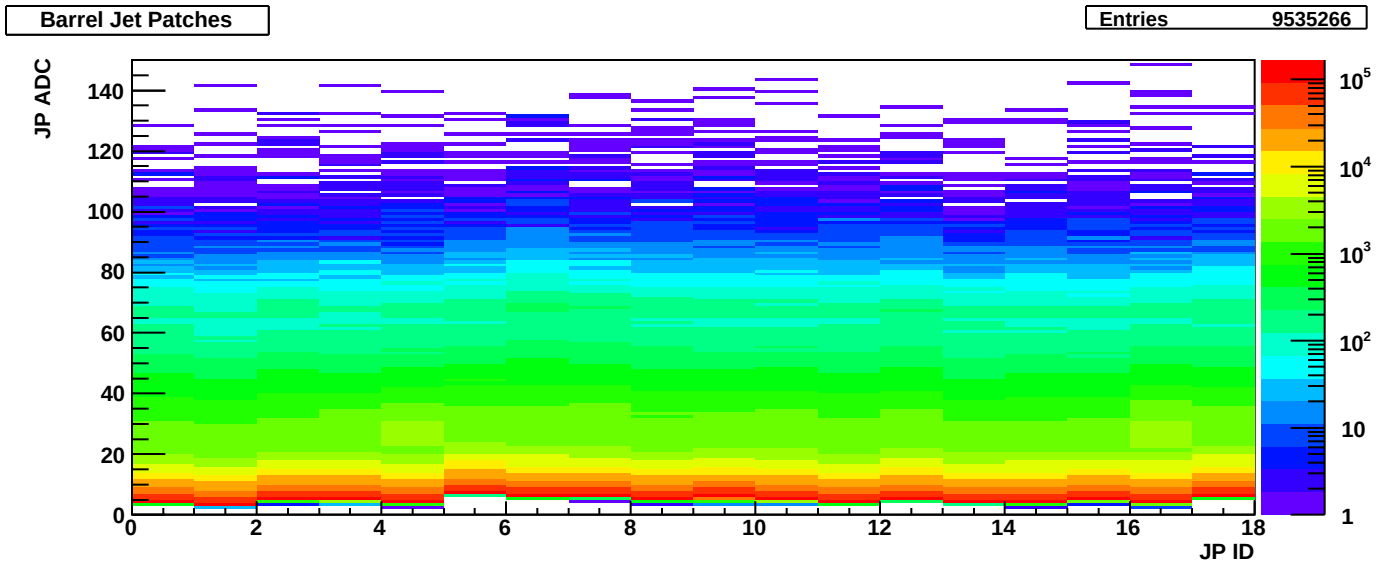

BunchId7Bit (all events)
Entries 529737

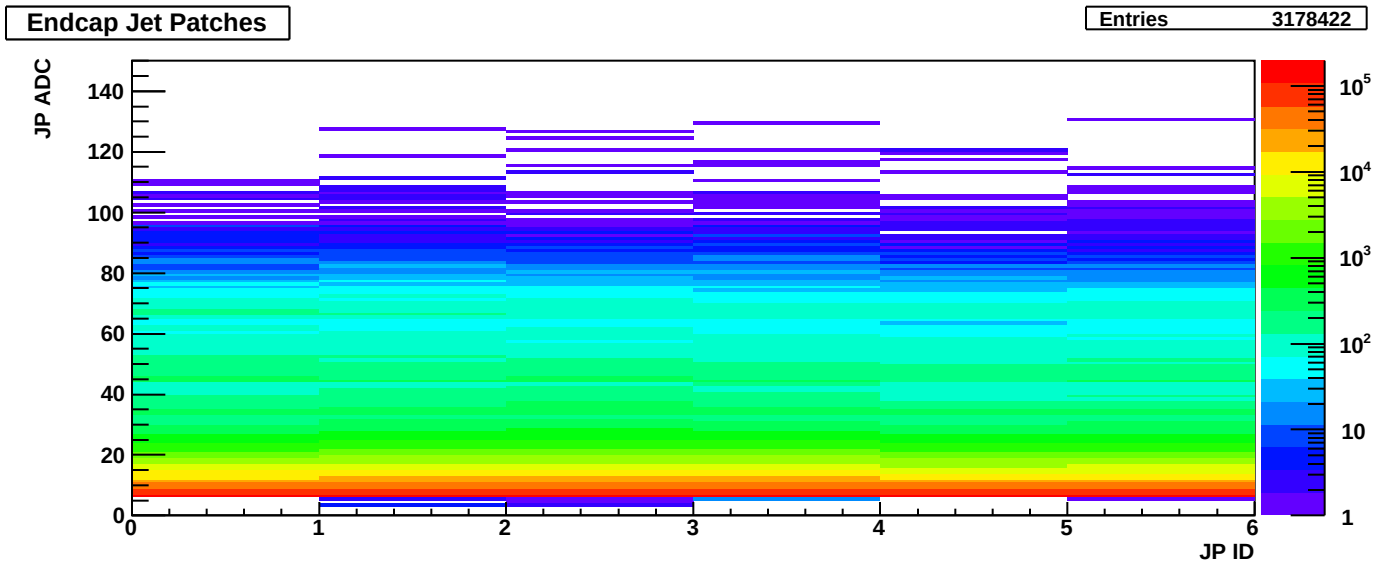

Barrel EMC L0 Input - High Tower
Entries 1.589211e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

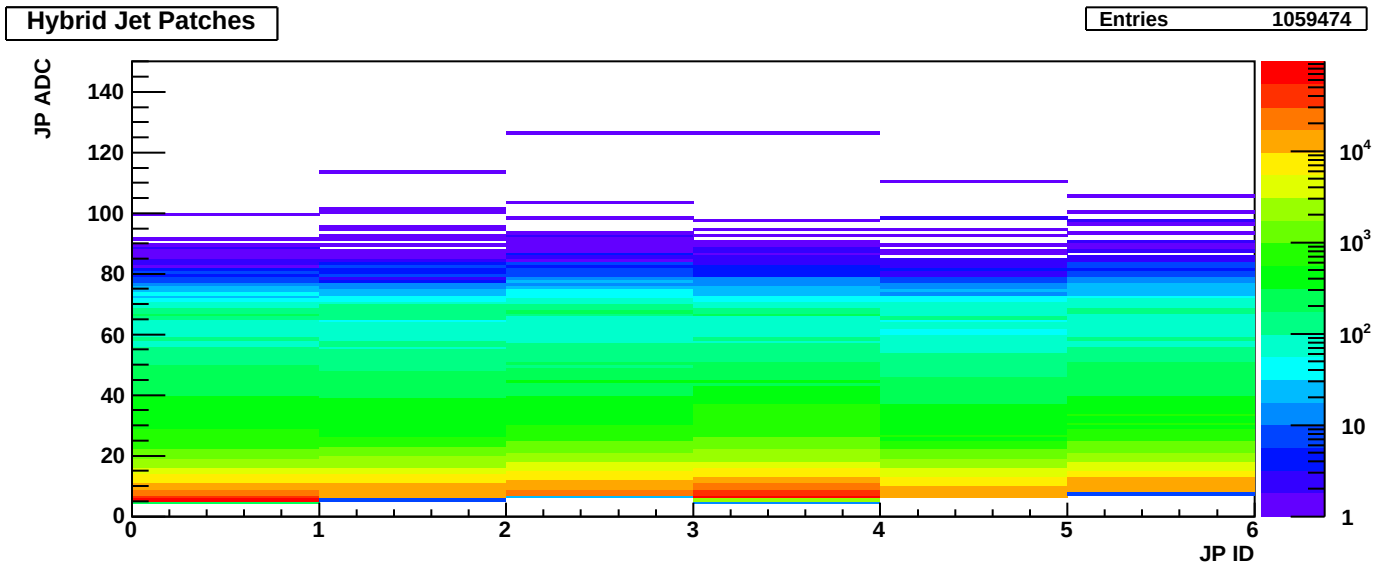

Barrel EMC L0 Input - Patch Sum
Entries 1.589211e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

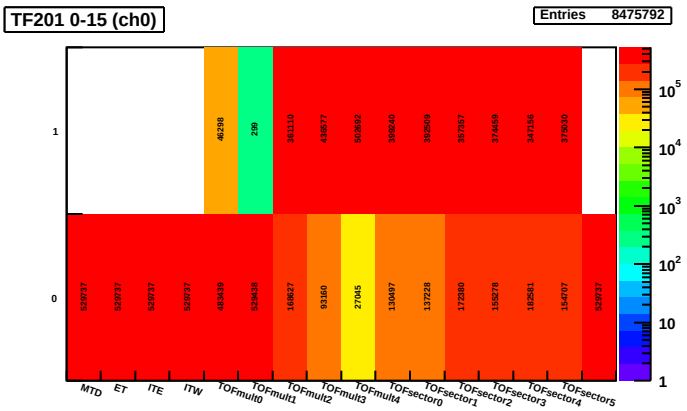

Endcap EMC L0 Input - High Tower
Entries 4.767633e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

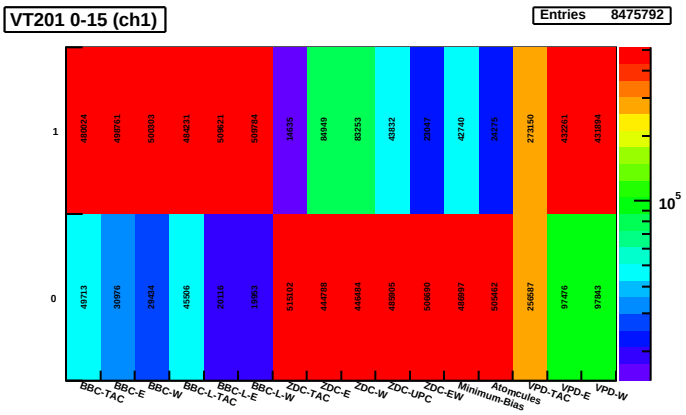

Endcap EMC L0 Input - Patch Sum
Entries 4.767633e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

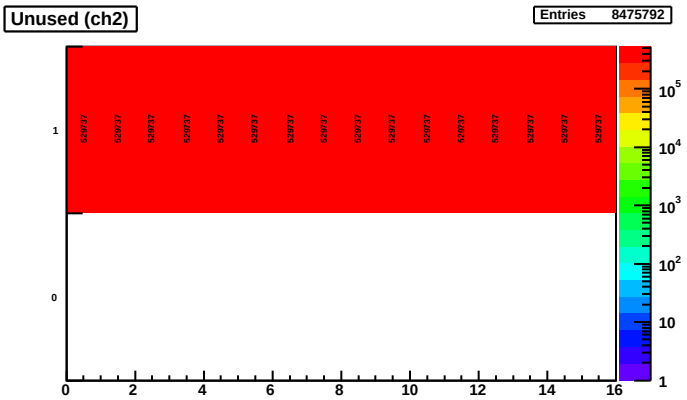

Barrel Jet Patches
Entries 9535266
JP ADC
JP ID

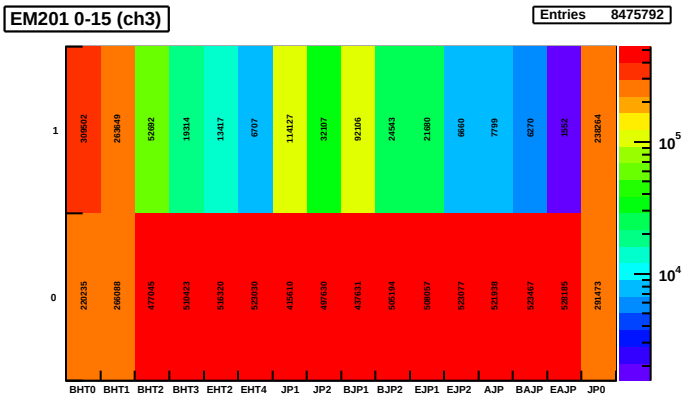

Endcap Jet Patches
Entries 3178422
JP ADC
JP ID

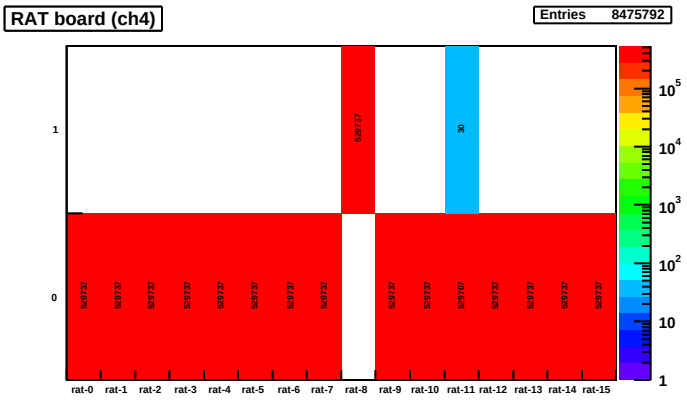

Hybrid Jet Patches
Entries 1059474
JP ADC
JP ID

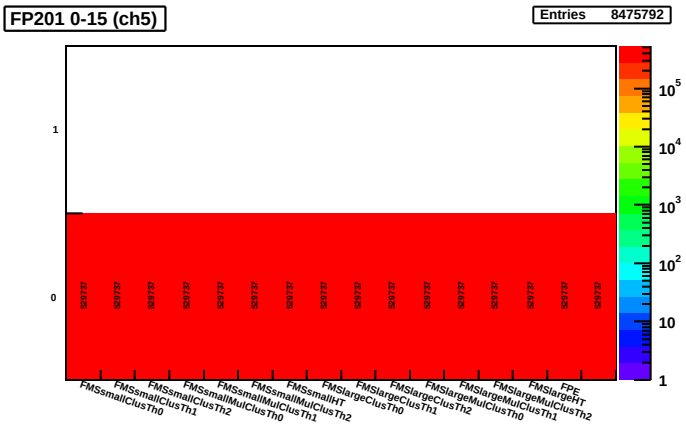

TF201 0-15 (ch0)
Entries 8475792
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

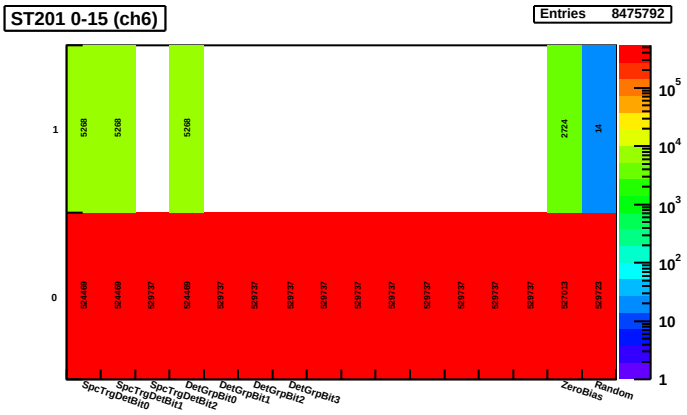

VT201 0-15 (ch1)
Entries 8475792
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

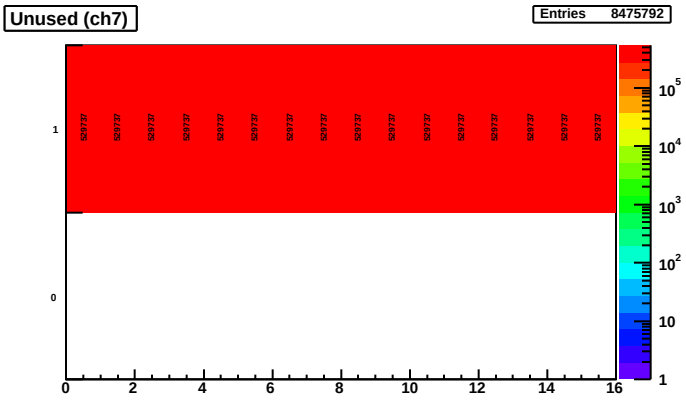

Unused (ch2)
Entries 8475792

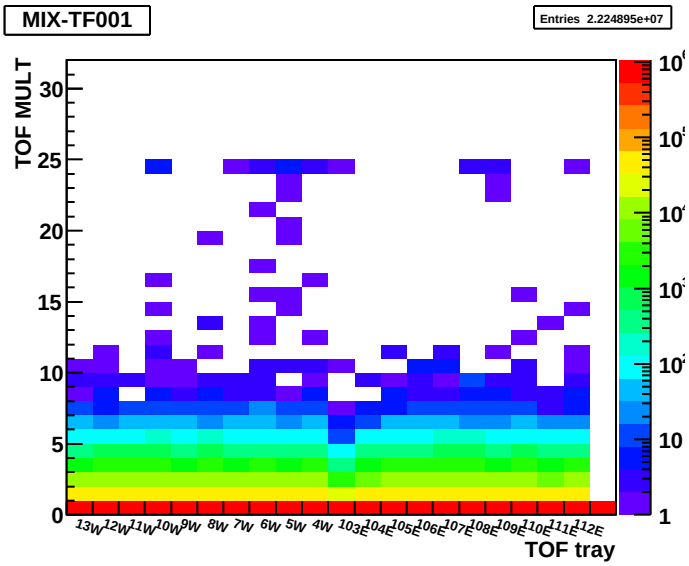

EM201 0-15 (ch3)
Entries 8475792
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

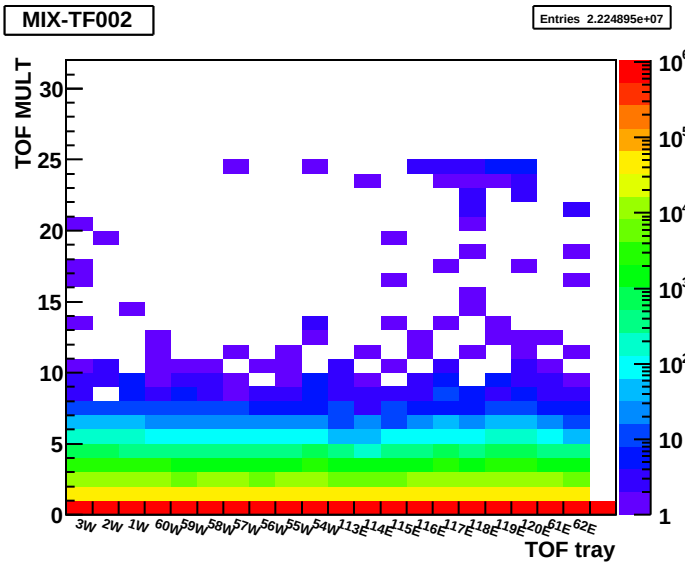

RAT board (ch4)
Entries 8475792
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

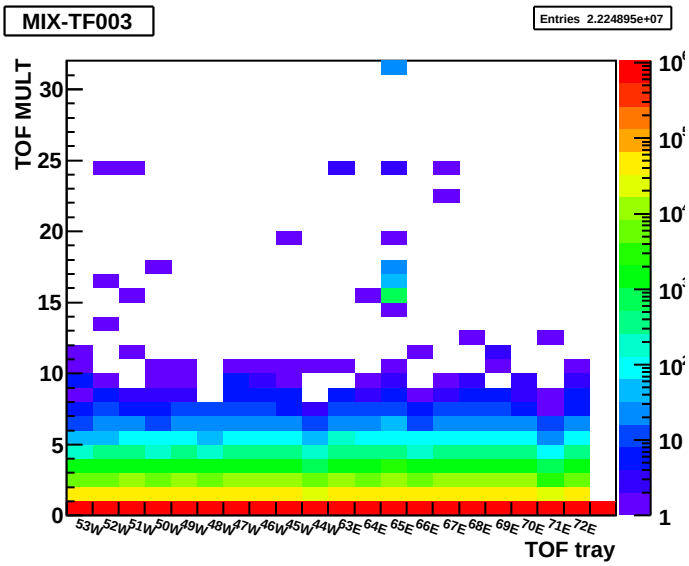

FP201 0-15 (ch5)
Entries 8475792
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

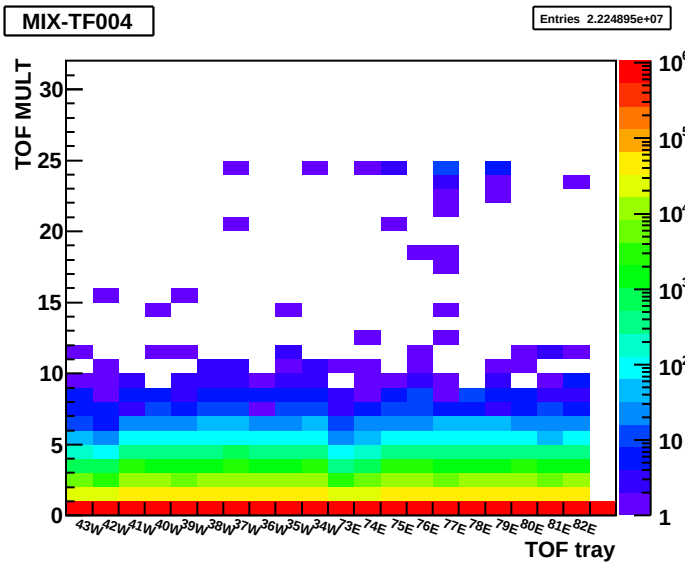

ST201 0-15 (ch6)
Entries 8475792
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetGrpBit0
DetGrpBit1
DetGrpBit2
DetGrpBit3
ZeroBias
Random

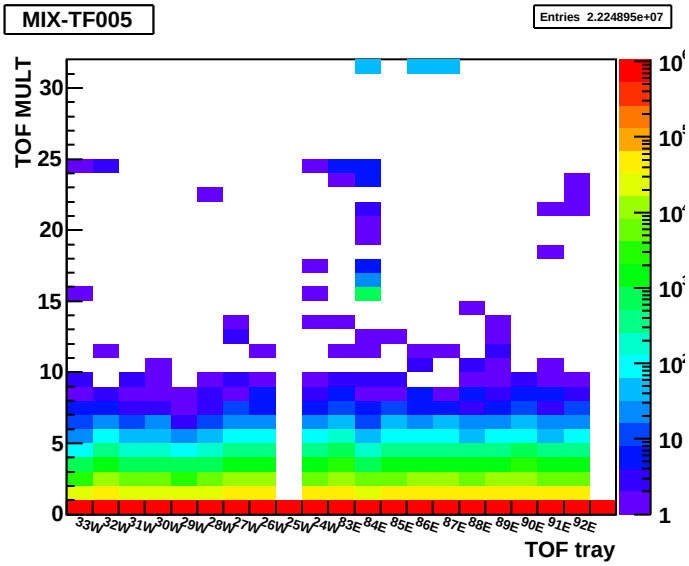

Unused (ch7)
Entries 8475792

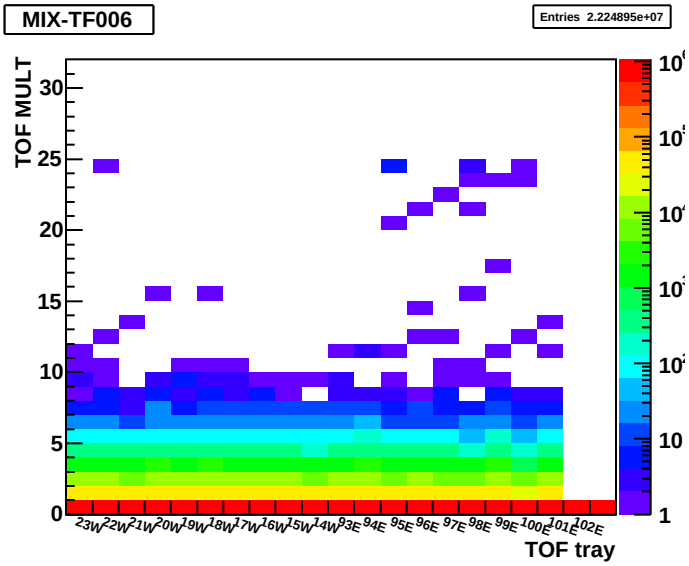

MIX-TF001
Entries 2.224895e+07
TOF MULT
TOF tray

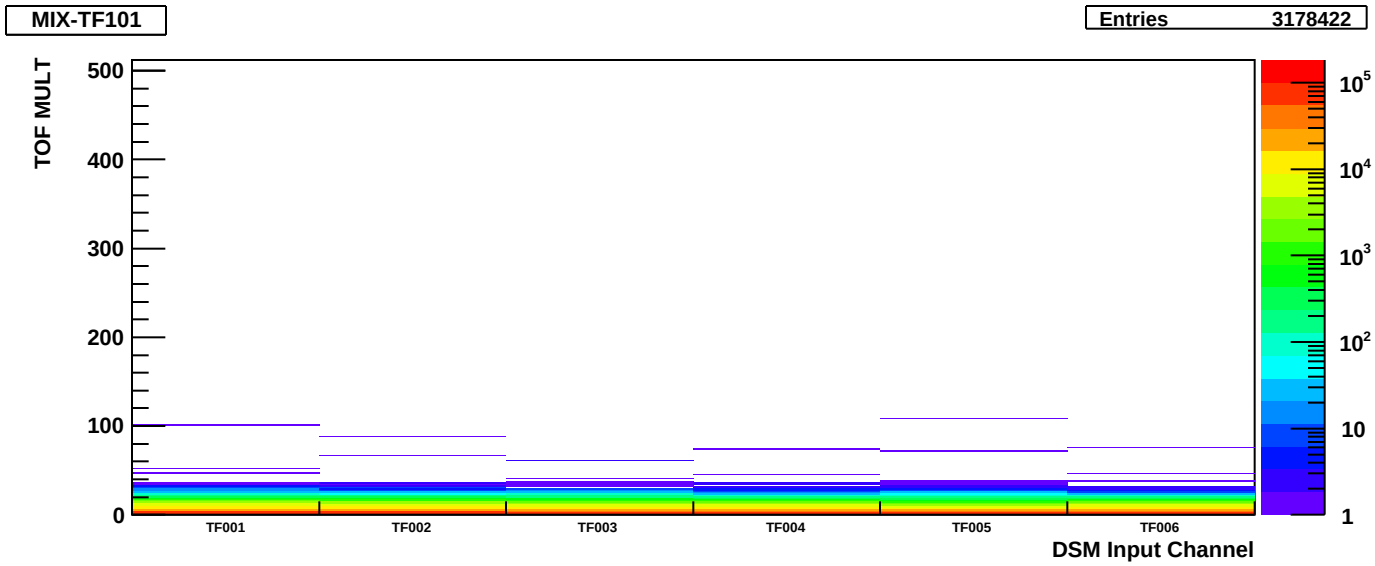

MIX-TF002
Entries 2.224895e+07
TOF MULT
TOF tray

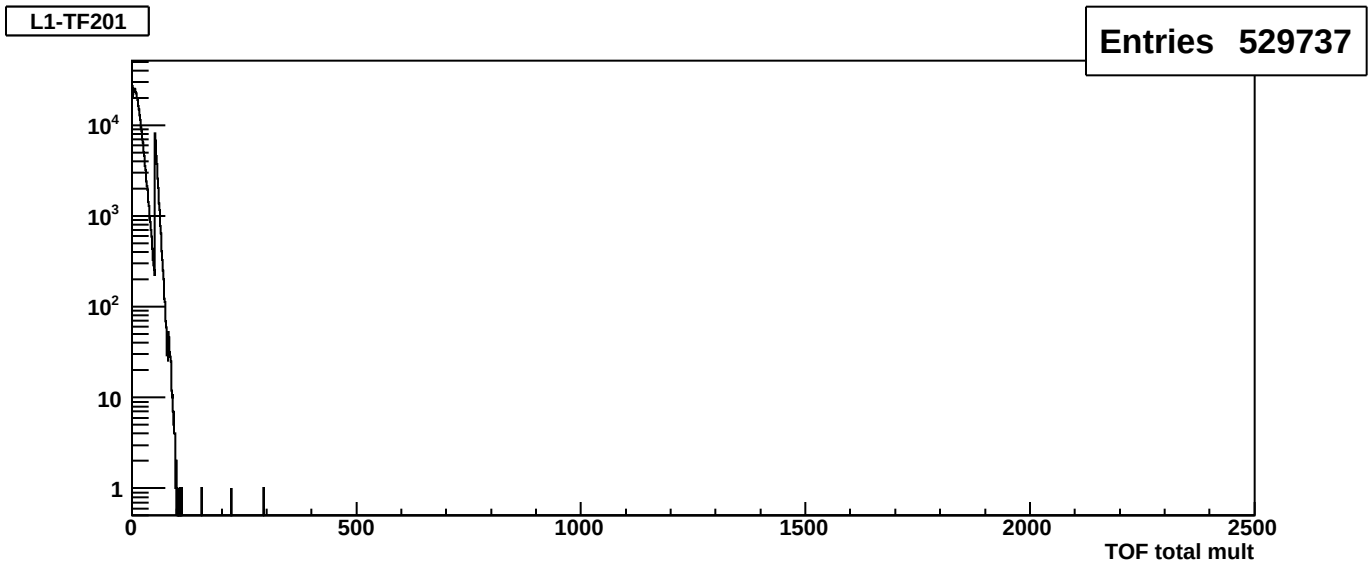

MIX-TF003
Entries 2.224895e+07
TOF MULT
TOF tray

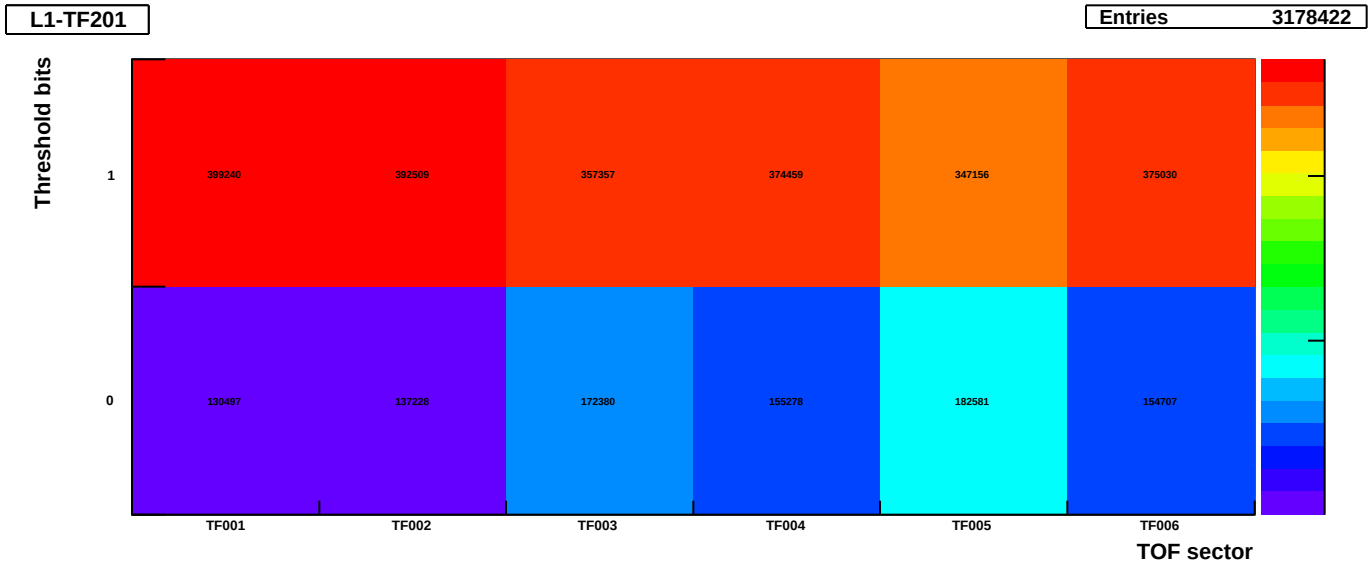

MIX-TF004
Entries 2.224895e+07
TOF MULT
TOF tray

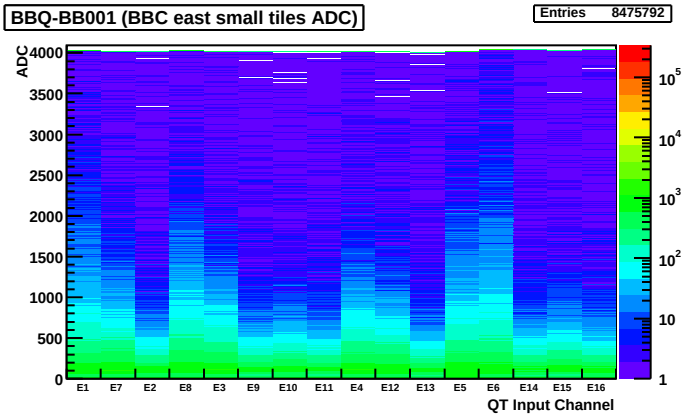

MIX-TF005
Entries 2.224895e+07
TOF MULT
TOF tray

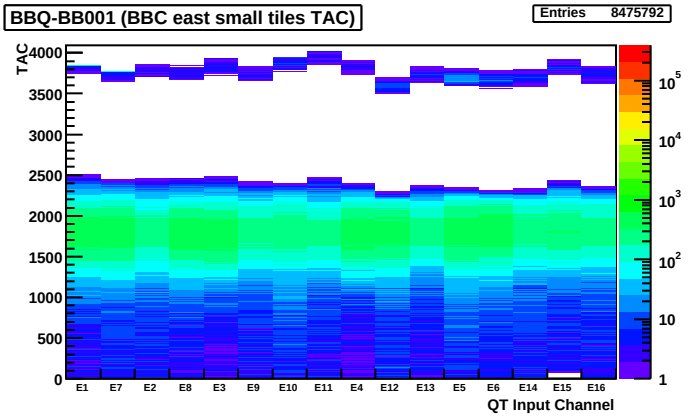

MIX-TF006
Entries 2.224895e+07
TOF MULT
TOF tray

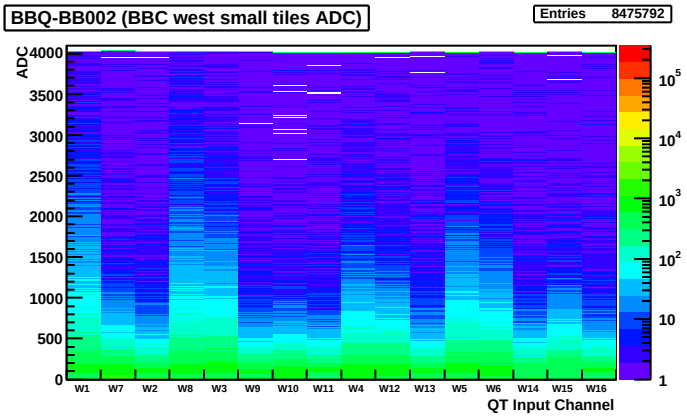

MIX-TF101
Entries 3178422
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

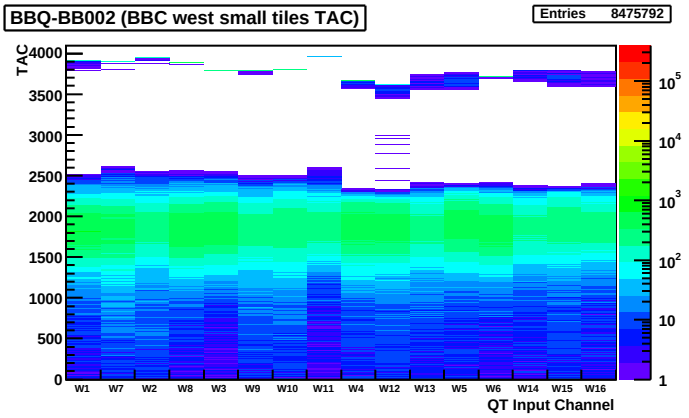

L1-TF201
Entries 529737
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

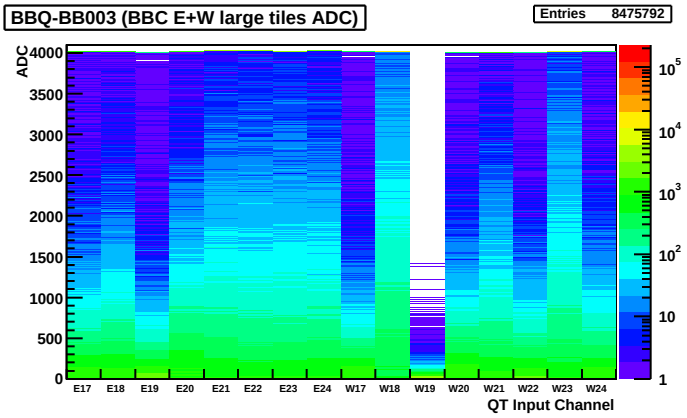

L1-TF201
Entries 3178422
Threshold bits
1
0
399240
392509
357357
374459
347156
375030
130497
137228
172380
155278
182581
154707
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

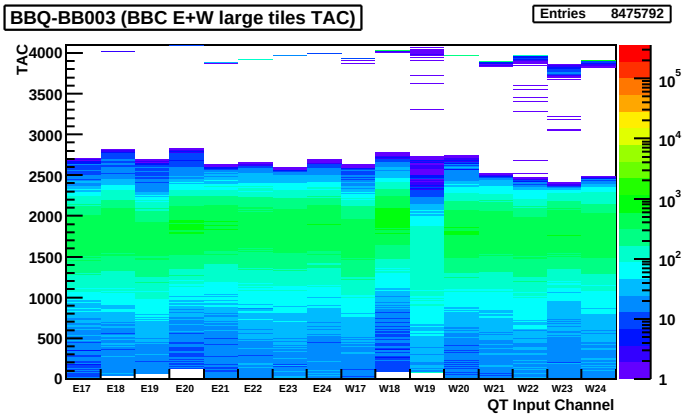

BBQ-BB001 (BBC east small tiles ADC)
Entries 8475792
ADC
QT Input Channel

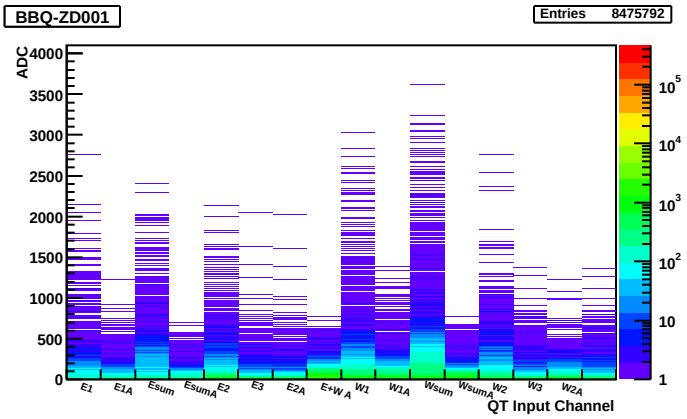

BBQ-BB001 (BBC east small tiles TAC)
Entries 8475792
TAC
QT Input Channel

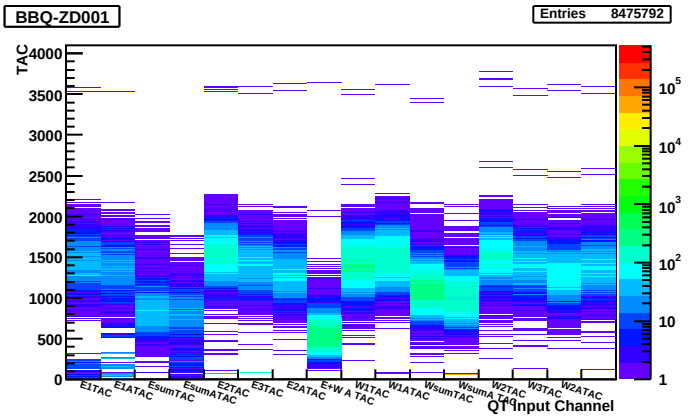

BBQ-BB002 (BBC west small tiles ADC)
Entries 8475792
ADC
QT Input Channel

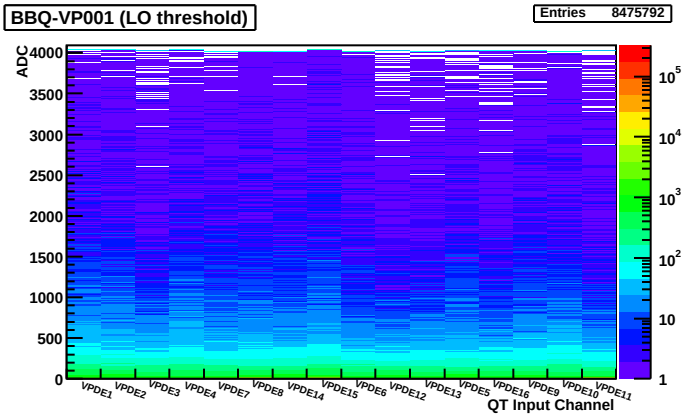

BBQ-BB002 (BBC west small tiles TAC)
Entries 8475792
TAC
QT Input Channel

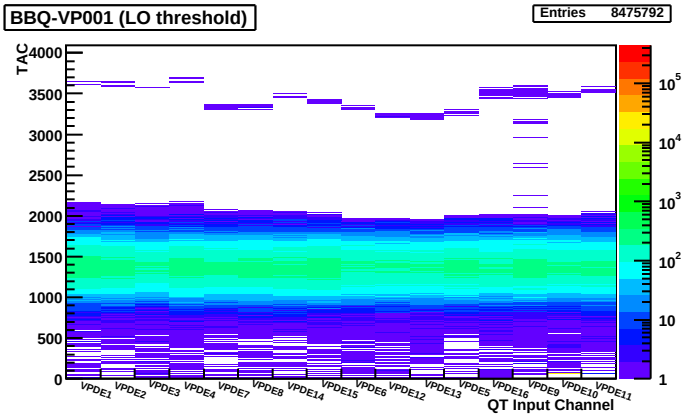

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 8475792
ADC
QT Input Channel

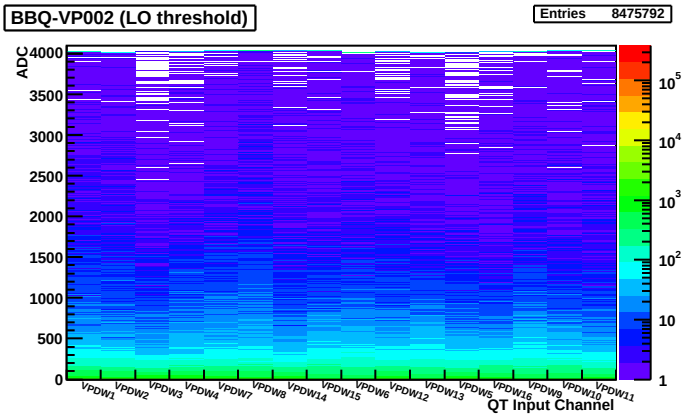

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 8475792
TAC
QT Input Channel

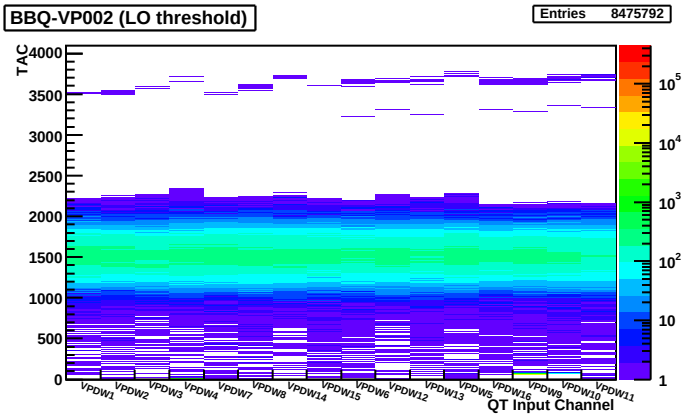

BBQ-ZD001
Entries 8475792
ADC
QT Input Channel

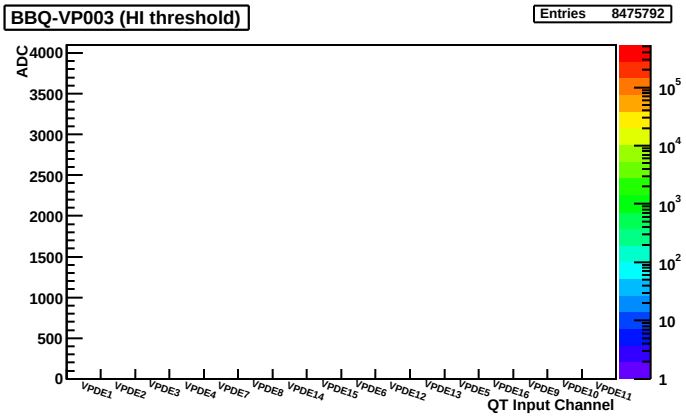

BBQ-ZD001
Entries 8475792
TAC
QT Input Channel

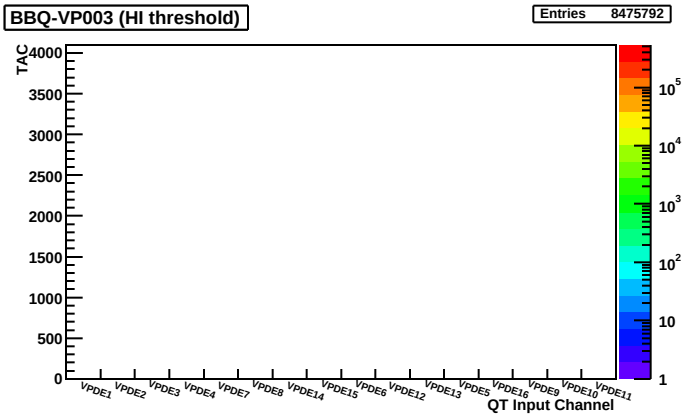

BBQ-VP001 (LO threshold)
Entries 8475792
ADC
QT Input Channel

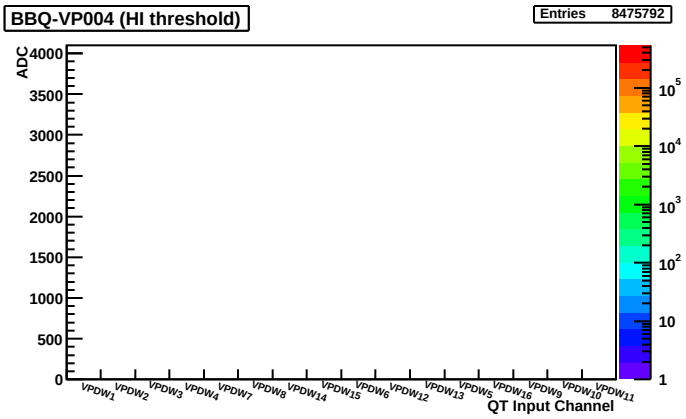

BBQ-VP001 (LO threshold)
Entries 8475792
TAC
QT Input Channel

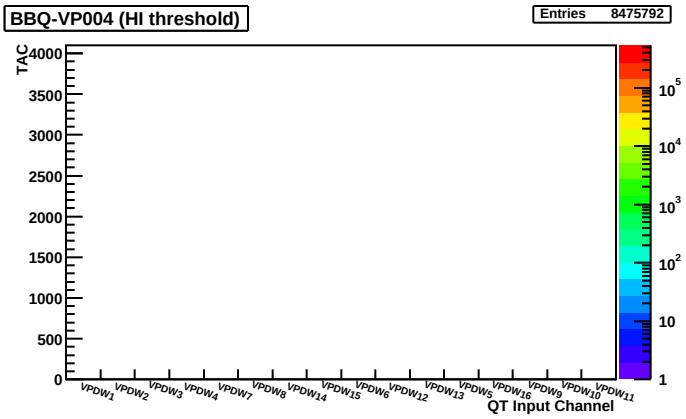

BBQ-VP002 (LO threshold)
Entries 8475792
ADC
QT Input Channel

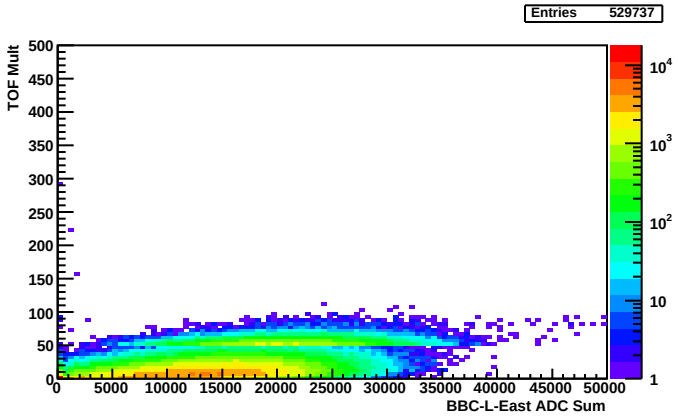

BBQ-VP002 (LO threshold)
Entries 8475792
TAC
QT Input Channel

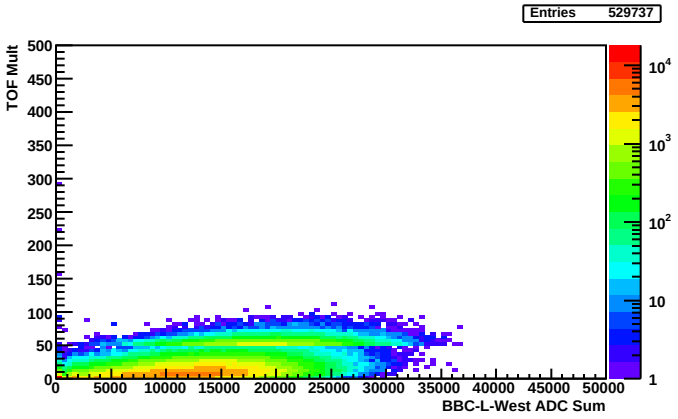

BBQ-VP003 (HI threshold)
Entries 8475792
ADC
QT Input Channel

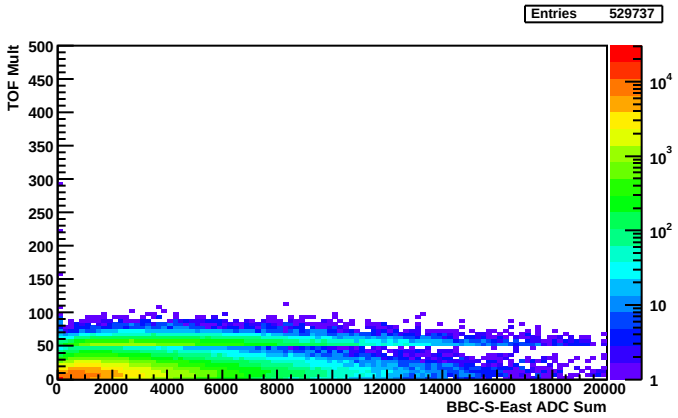

BBQ-VP003 (HI threshold)
Entries 8475792
TAC
QT Input Channel

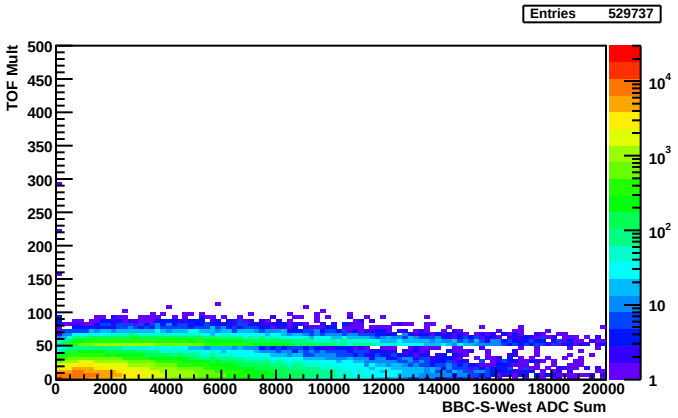

BBQ-VP004 (HI threshold)
Entries 8475792
ADC
QT Input Channel

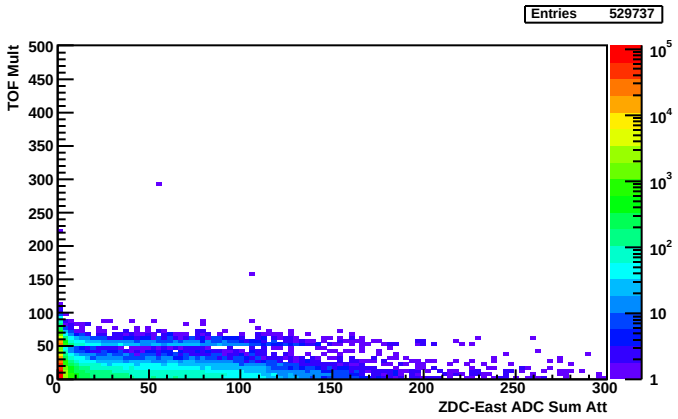

BBQ-VP004 (HI threshold)
Entries 8475792
TAC
QT Input Channel

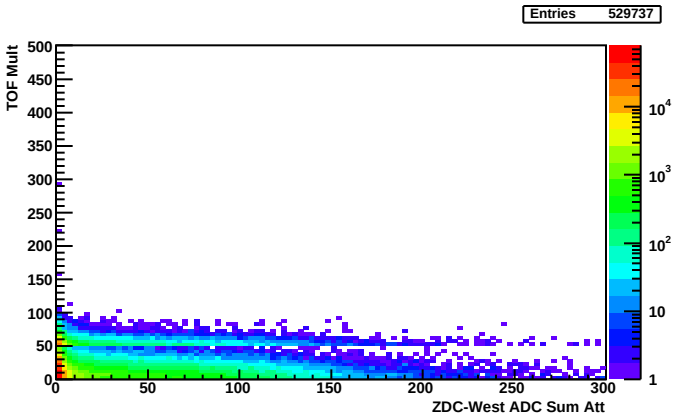

Entries 529737
TOF Mult
BBC-L-East ADC Sum

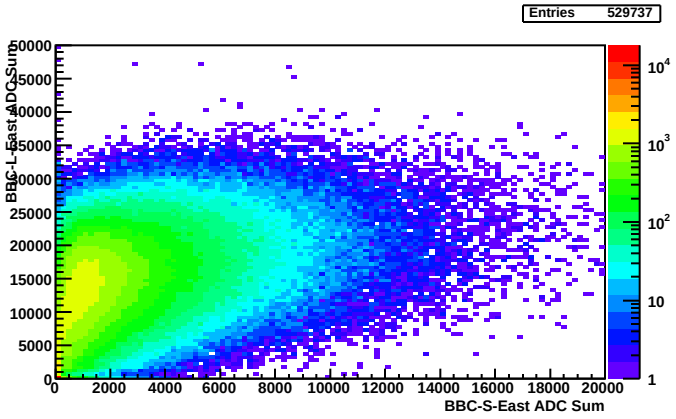

Entries 529737
TOF Mult
BBC-L-West ADC Sum

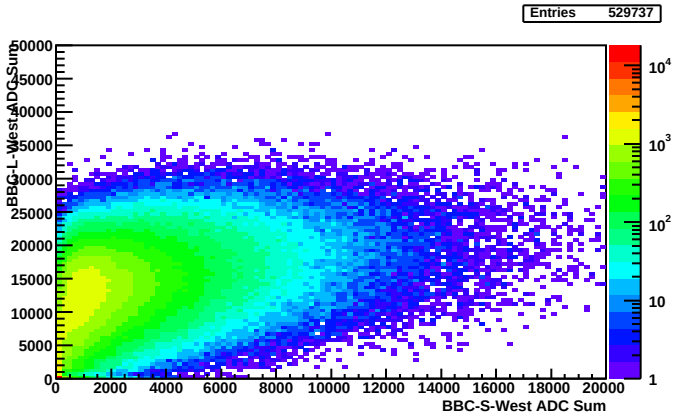

Entries 529737
TOF Mult
BBC-S-East ADC Sum

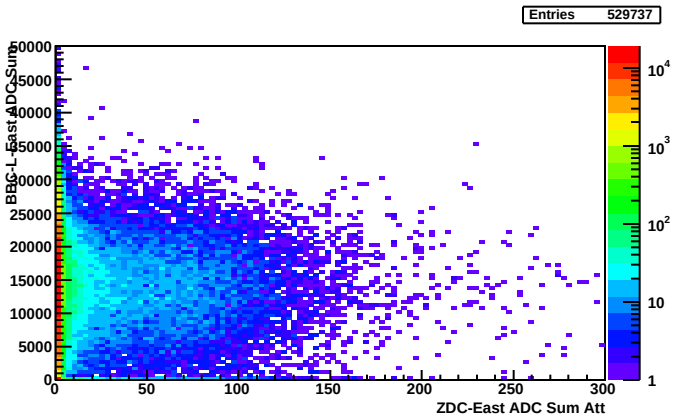

Entries 529737
TOF Mult
BBC-S-West ADC Sum

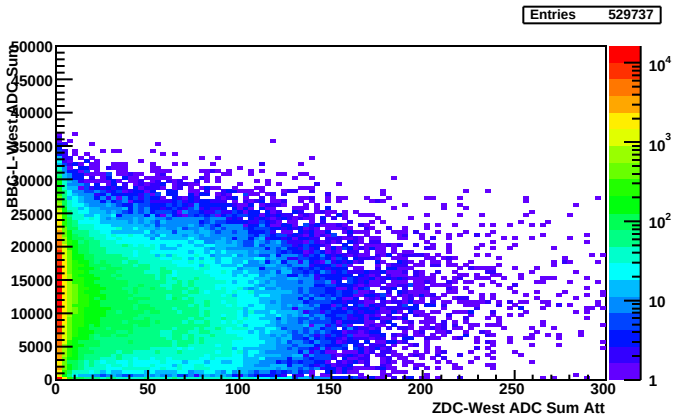

Entries 529737
TOF Mult
ZDC-East ADC Sum Att

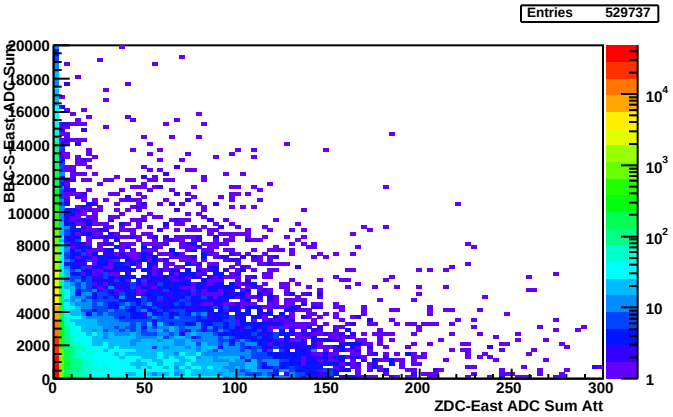

Entries 529737
TOF Mult
ZDC-West ADC Sum Att

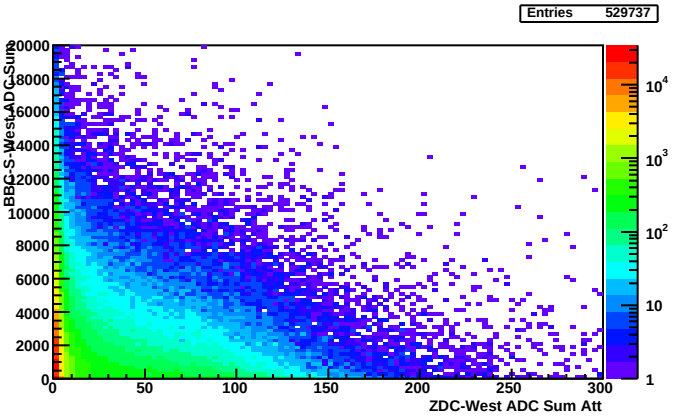

Entries 529737
BBC-L-East ADC Sum
BBC-S-East ADC Sum

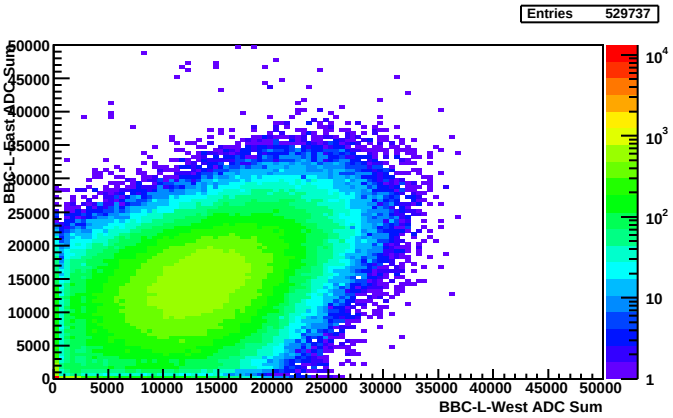

Entries 529737
BBC-L-West ADC Sum
BBC-S-West ADC Sum

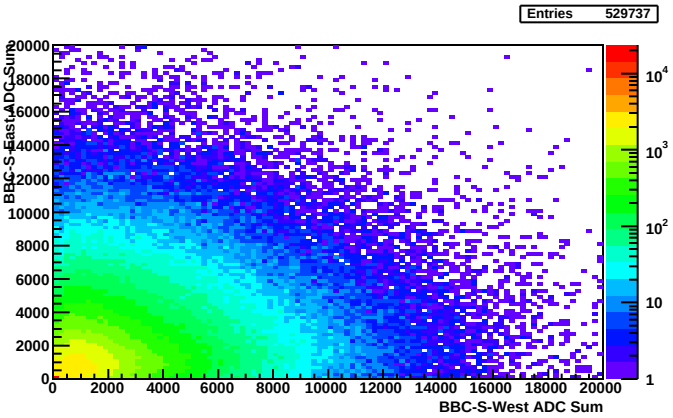

Entries 529737
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

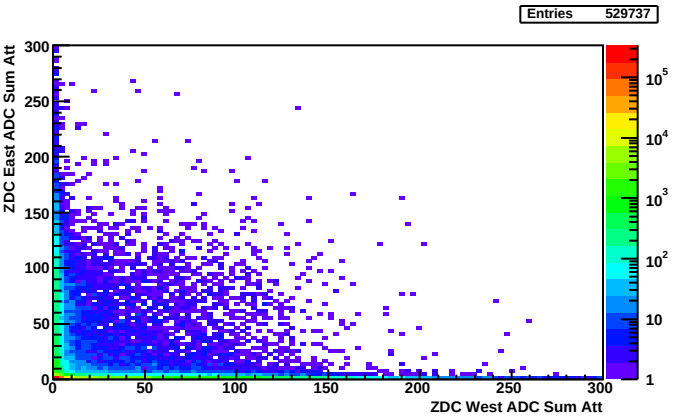

Entries 529737
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

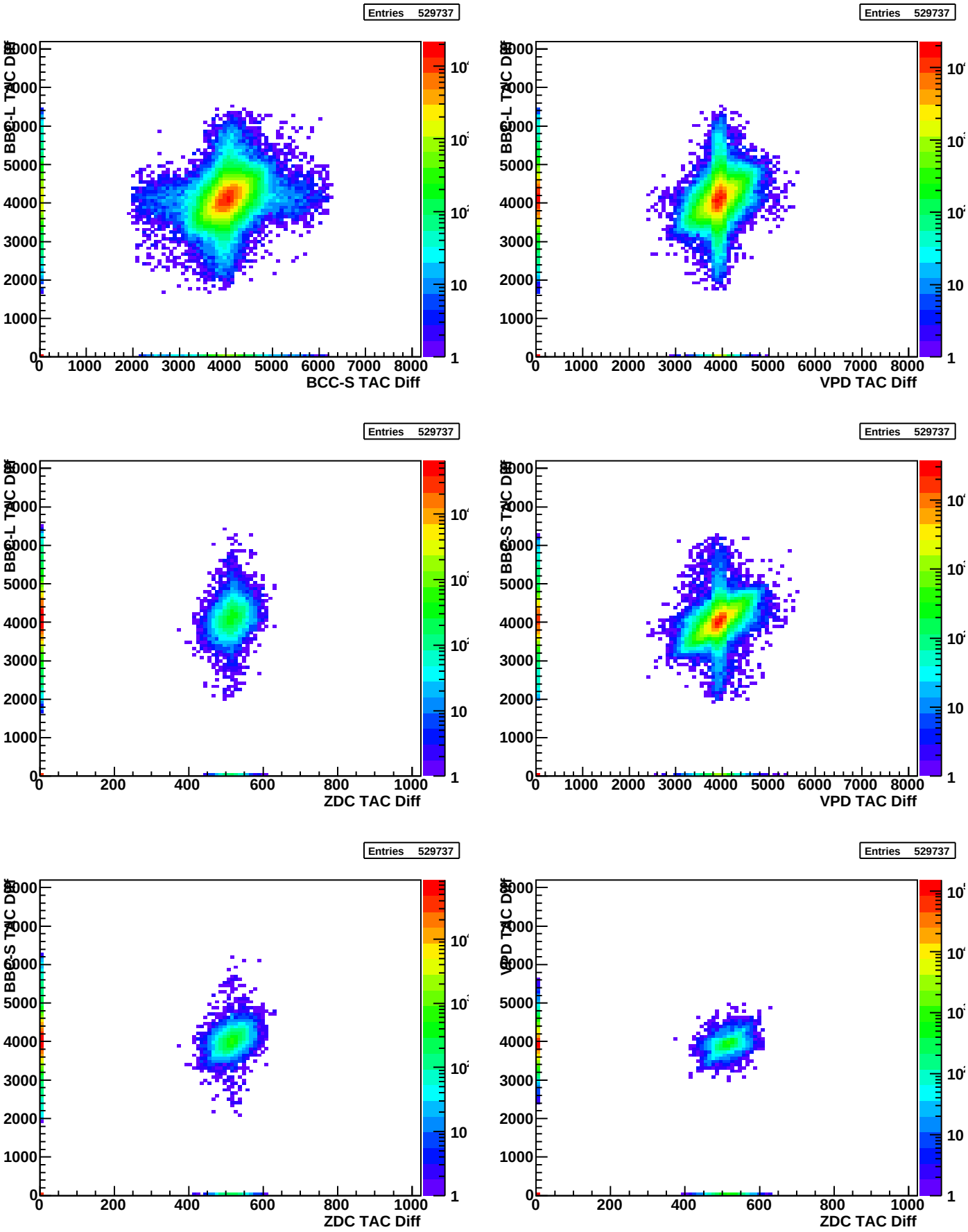

Entries 529737
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

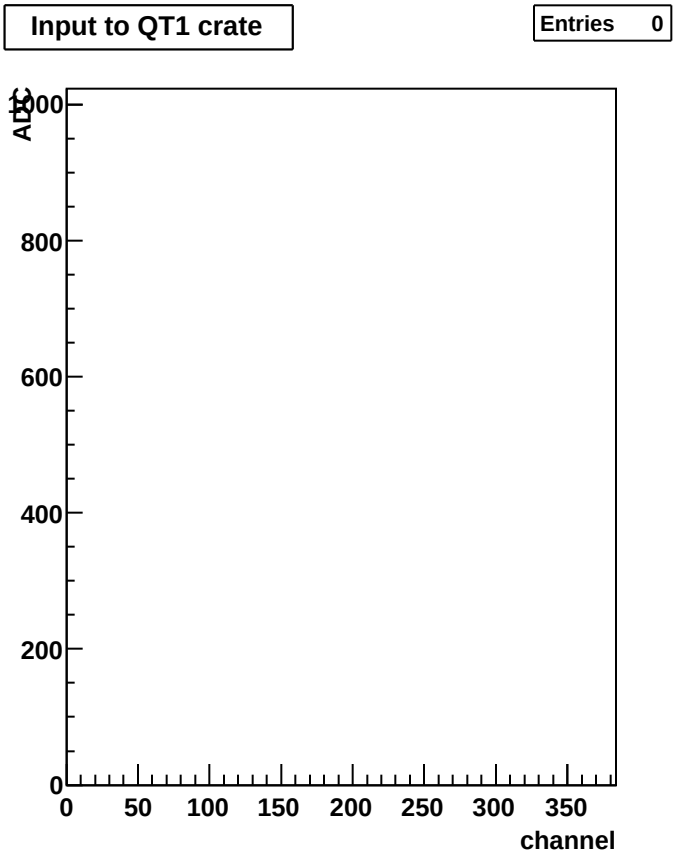

Entries 529737
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

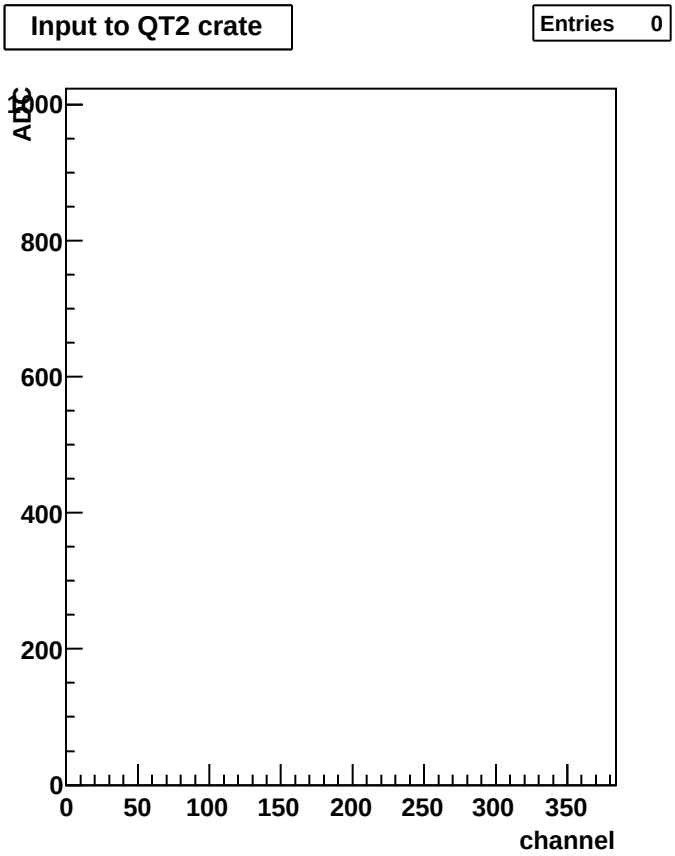

Entries 529737
BBC-L-East ADC Sum
BBC-L-West ADC Sum

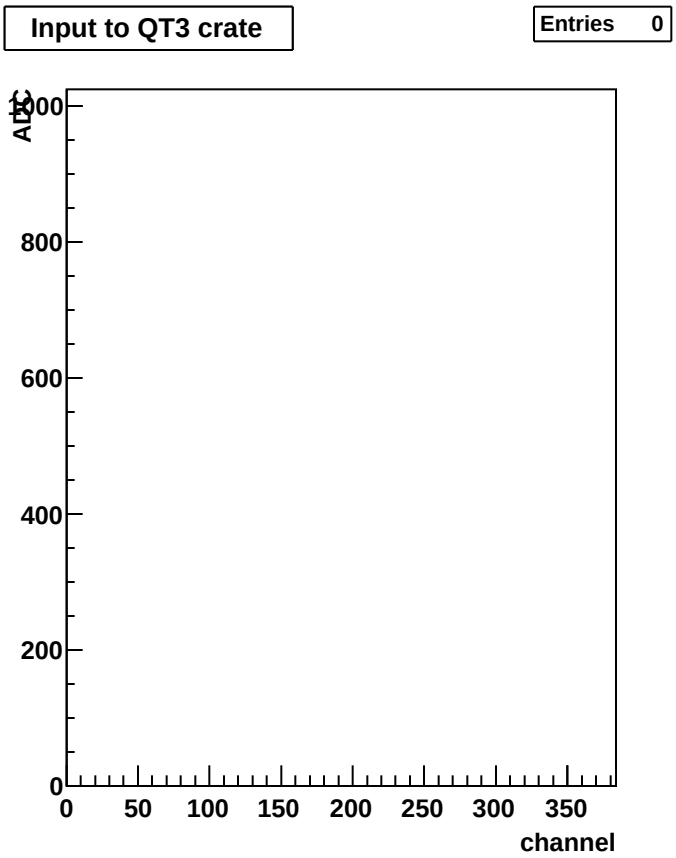

Entries 529737
BBC-S-East ADC Sum
BBC-S-West ADC Sum

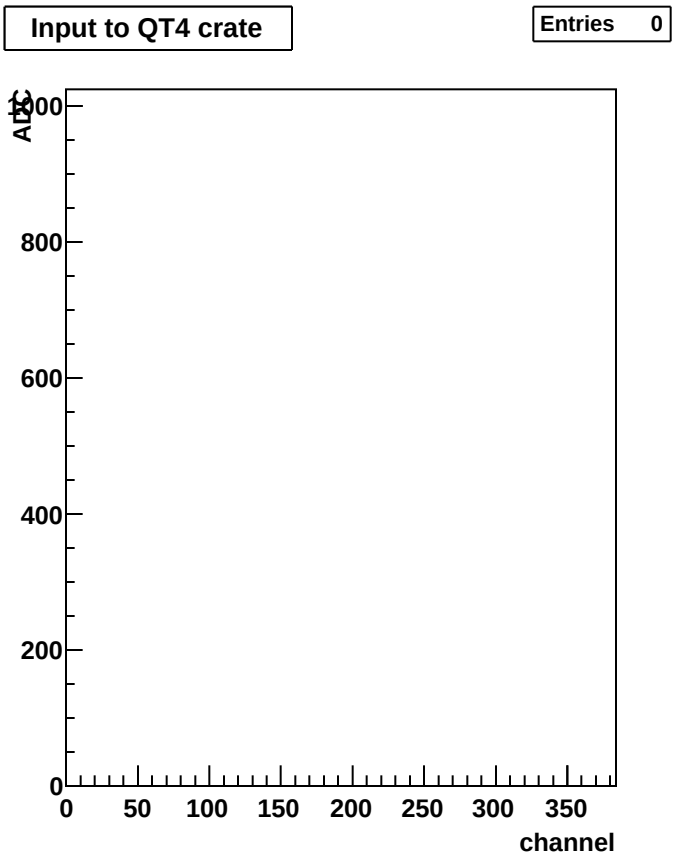

Entries 529737
ZDC East ADC Sum Att
ZDC West ADC Sum Att

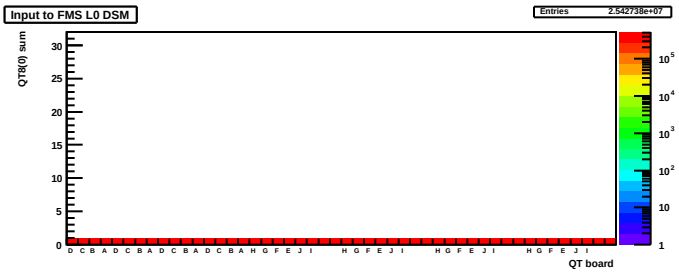

Entries 529737
BBC-L TAC Diff
BCC-S TAC Diff
Entries 529737
BBC-L TAC Diff
VPD TAC Diff
Entries 529737
BBC-L TAC Diff
ZDC TAC Diff
Entries 529737
BBC-S TAC Diff
VPD TAC Diff
Entries 529737
BBC-S TAC Diff
ZDC TAC Diff
Entries 529737
VPD TAC Diff
ZDC TAC Diff

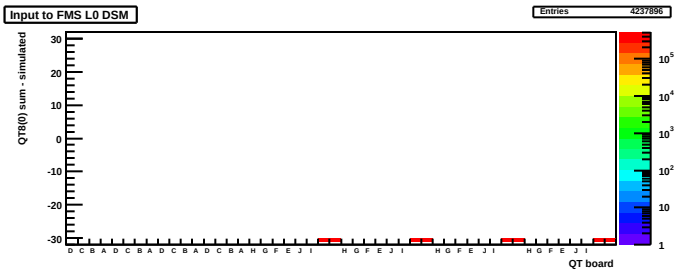

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

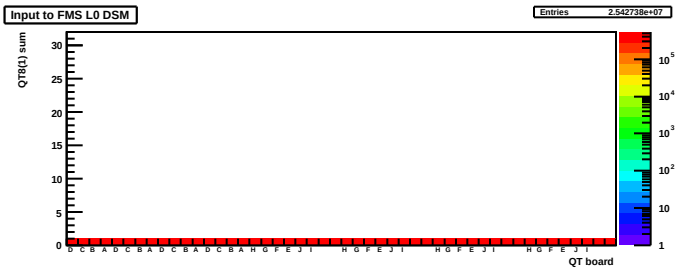

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

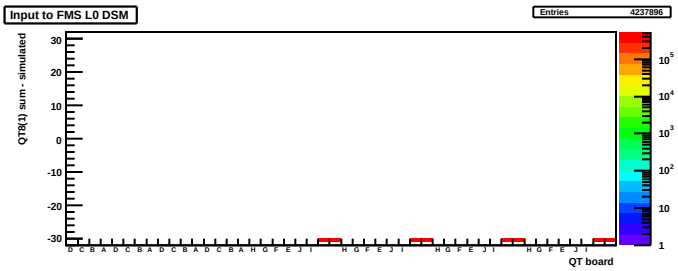

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

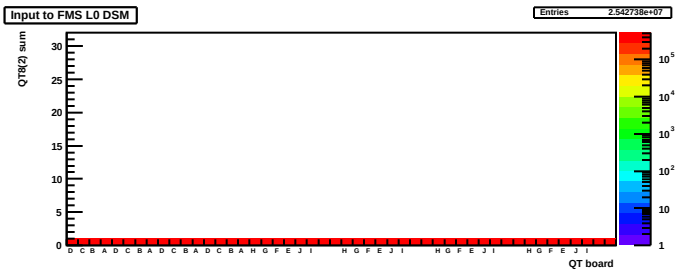

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

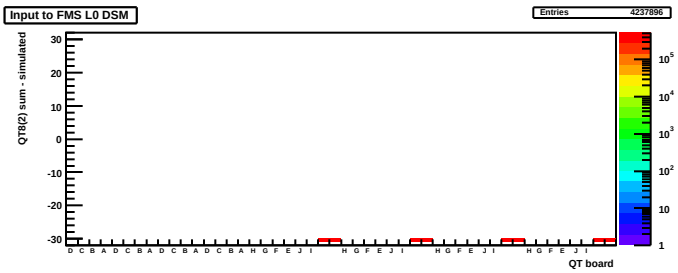

Input to FMS L0 DSM
Entries 2.542738e+07
QT8(0) sum
QT board

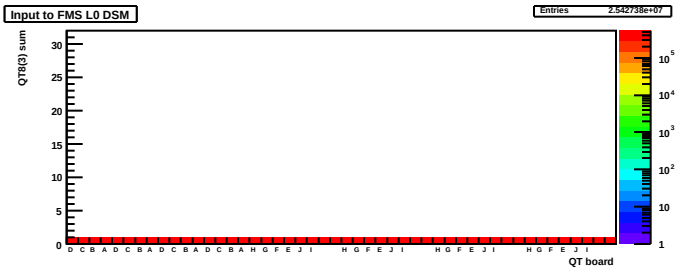

Input to FMS L0 DSM
Entries 4237896
QT8(0) sum - simulated
QT board

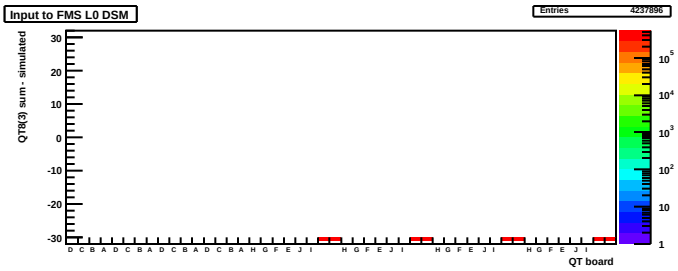

Input to FMS L0 DSM
Entries 2.542738e+07
QT8(1) sum
QT board

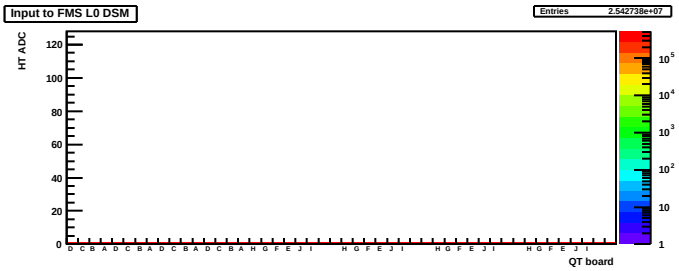

Input to FMS L0 DSM
Entries 4237896
QT8(1) sum - simulated
QT board

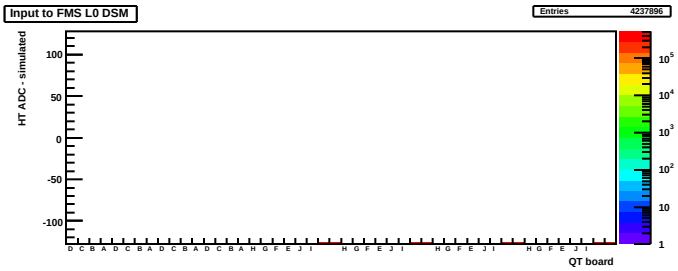

Input to FMS L0 DSM
Entries 2.542738e+07
QT8(2) sum
QT board

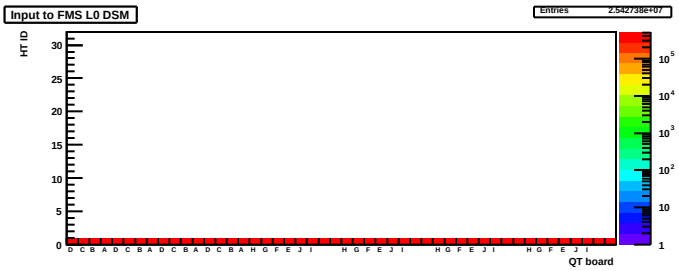

Input to FMS L0 DSM
Entries 4237896
QT8(2) sum - simulated
QT board

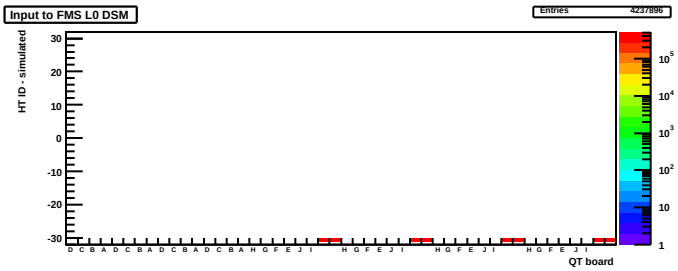

Input to FMS L0 DSM
Entries 2.542738e+07
QT8(3) sum
QT board

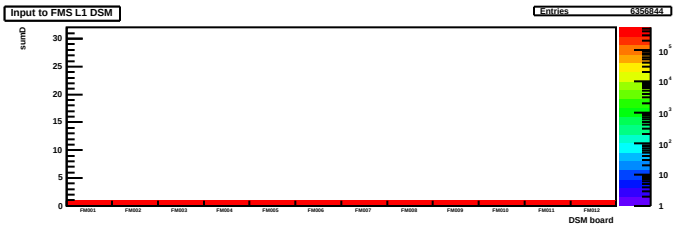

Input to FMS L0 DSM
Entries 4237896
QT8(3) sum - simulated
QT board

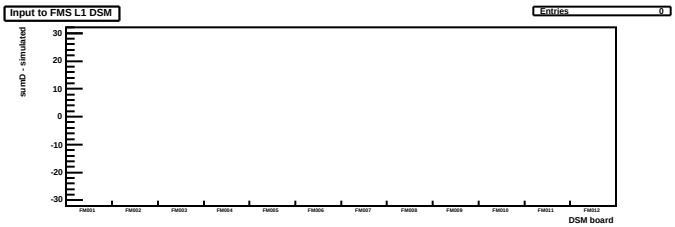

Input to FMS L0 DSM
Entries 2.542738e+07
HT ADC
QT board

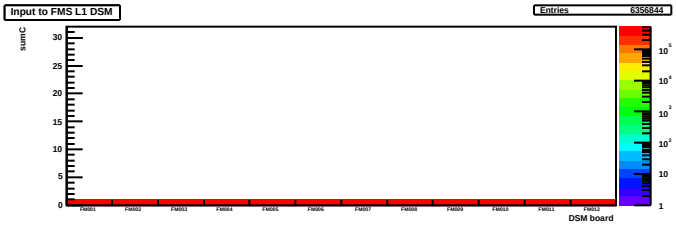

Input to FMS L0 DSM
Entries 4237896
HT ADC - simulated
QT board

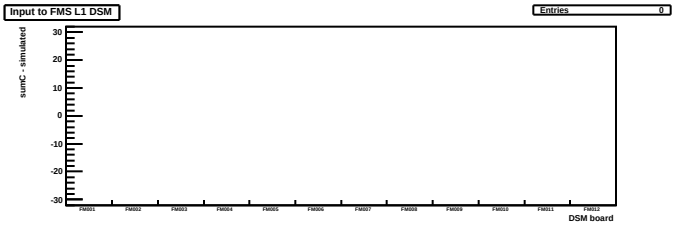

Input to FMS L0 DSM
Entries 2.542738e+07
HT ID
QT board

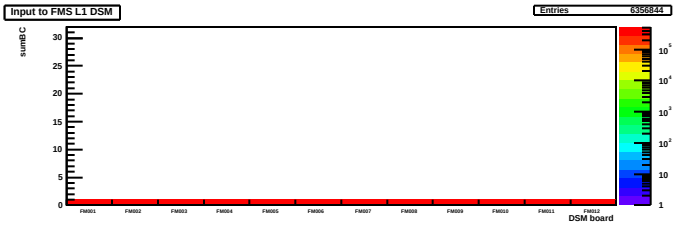

Input to FMS L0 DSM
Entries 4237896
HT ID - simulated
QT board

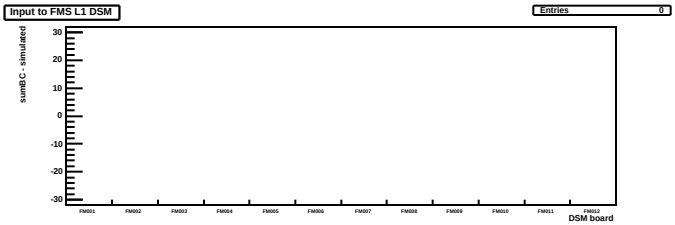

Input to FMS L1 DSM
Entries 6356844
sumD
DSM board

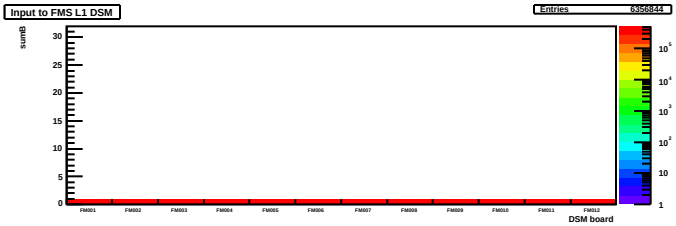

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

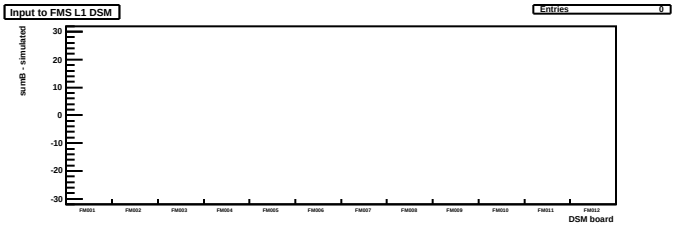

Input to FMS L1 DSM
Entries 6356844
sumC
DSM board

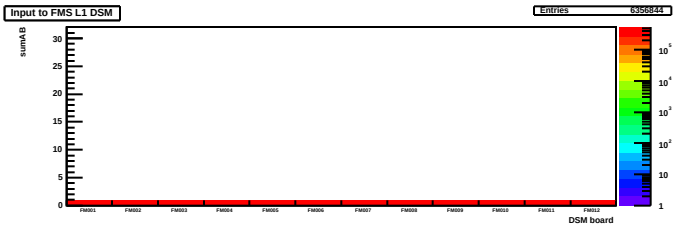

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

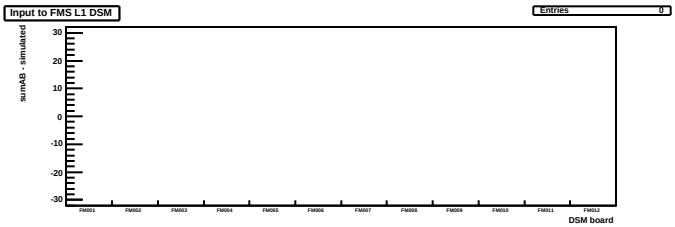

Input to FMS L1 DSM
Entries 6356844
sumBC
DSM board

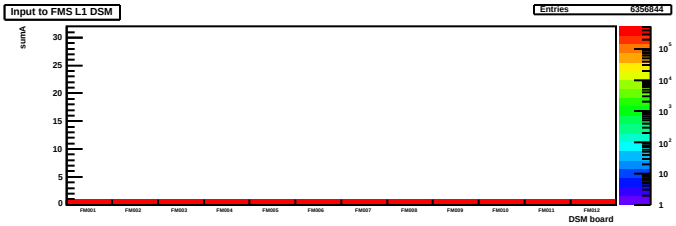

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

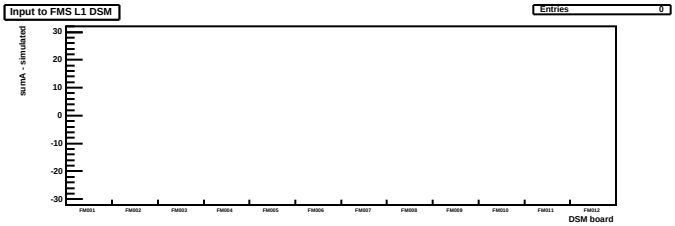

Input to FMS L1 DSM
Entries 6356844
sumB
DSM board

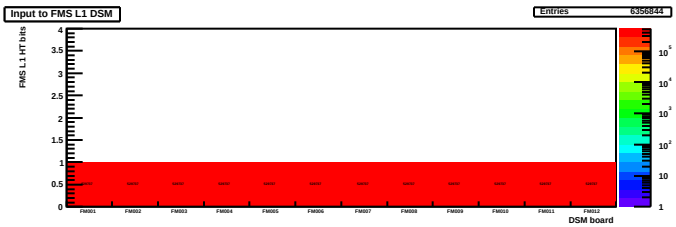

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

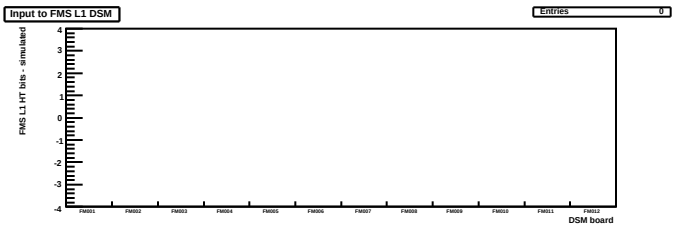

Input to FMS L1 DSM
Entries 6356844
sumAB
DSM board

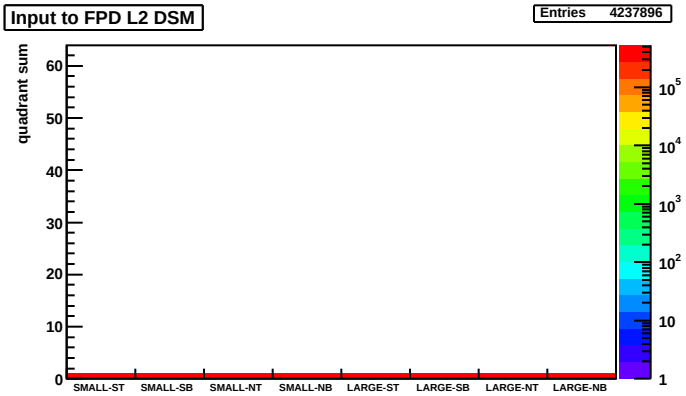

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

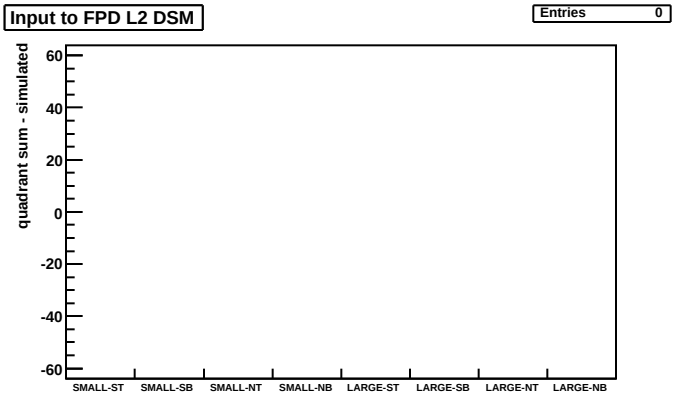

Input to FMS L1 DSM
Entries 6356844
sumA
DSM board

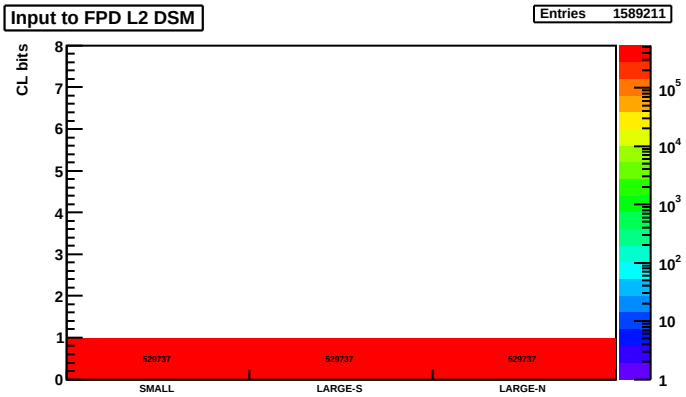

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

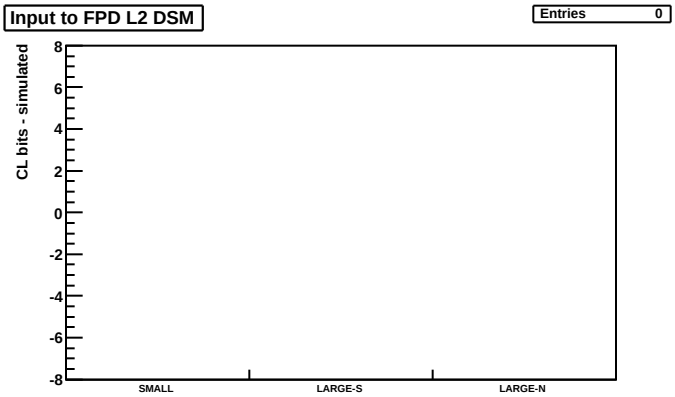

Input to FMS L1 DSM
Entries 6356844
FMS L1 HT bits
DSM board

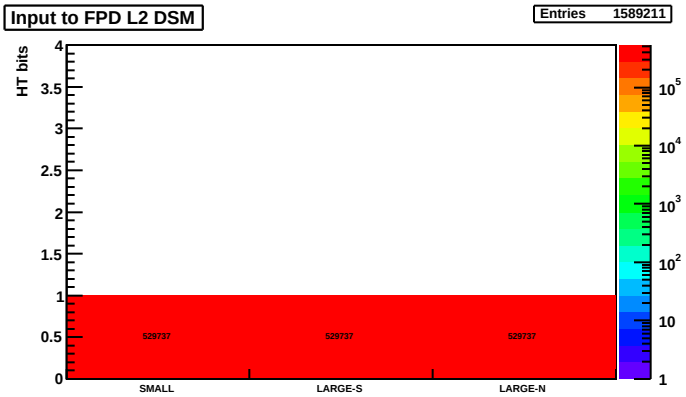

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

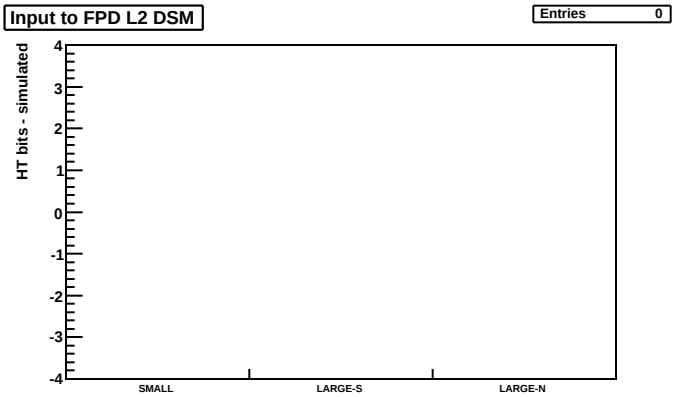

Input to FPD L2 DSM
Entries 4237896
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

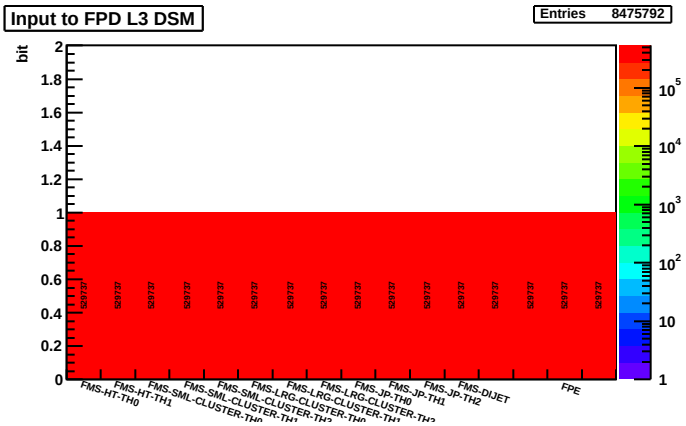

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

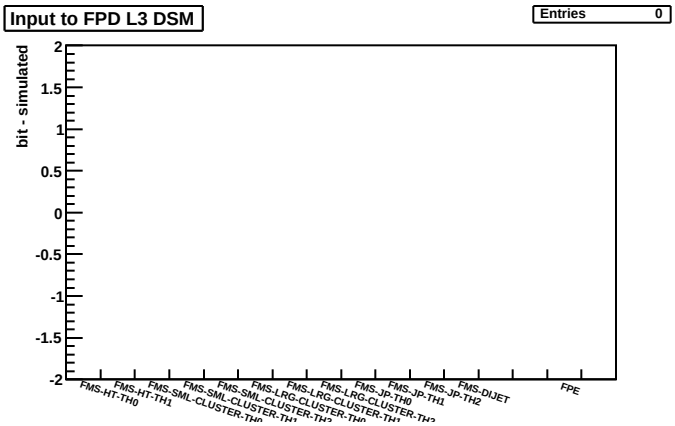

Input to FPD L2 DSM
Entries 1589211
CL bits
529737
529737
529737
SMALL
LARGE-S
LARGE-N

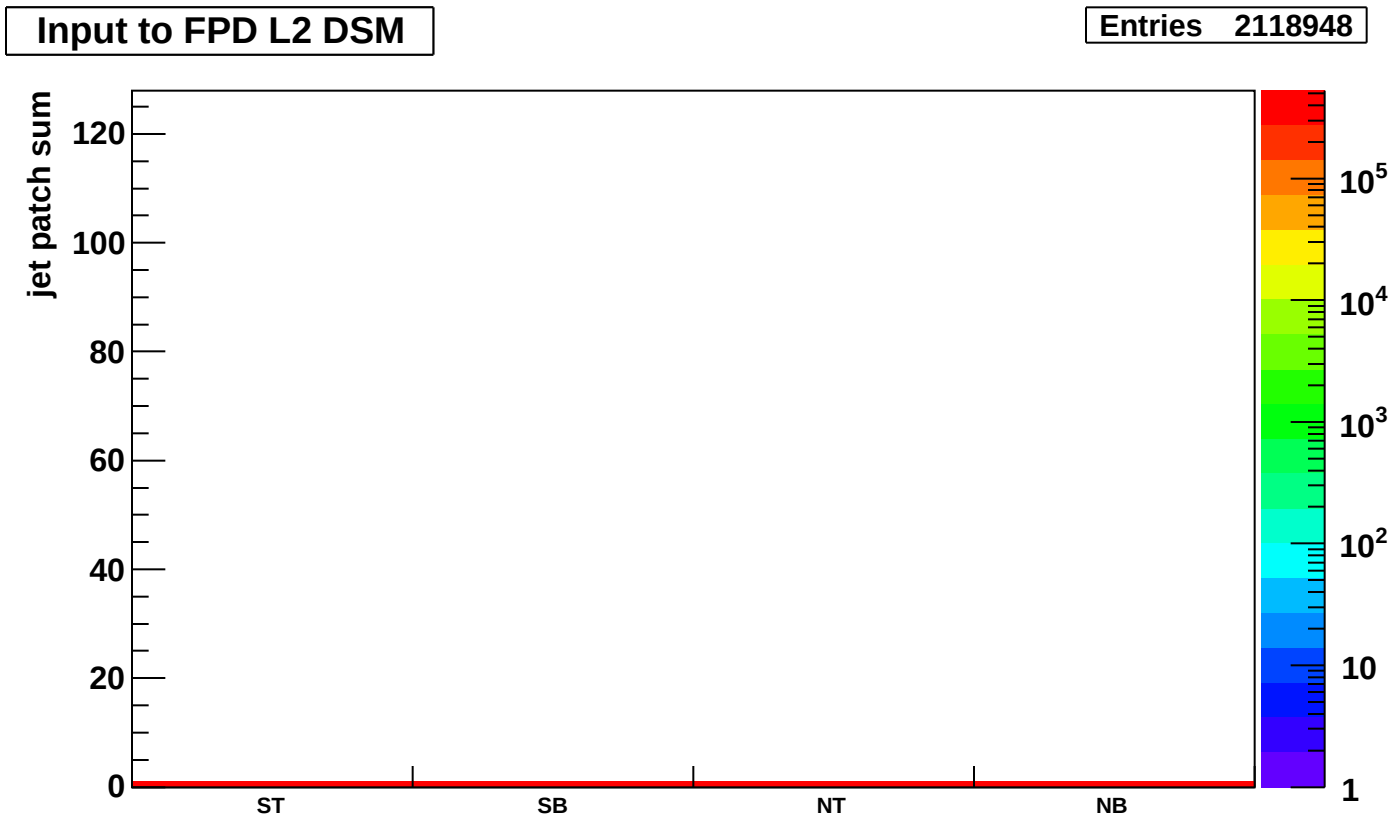

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

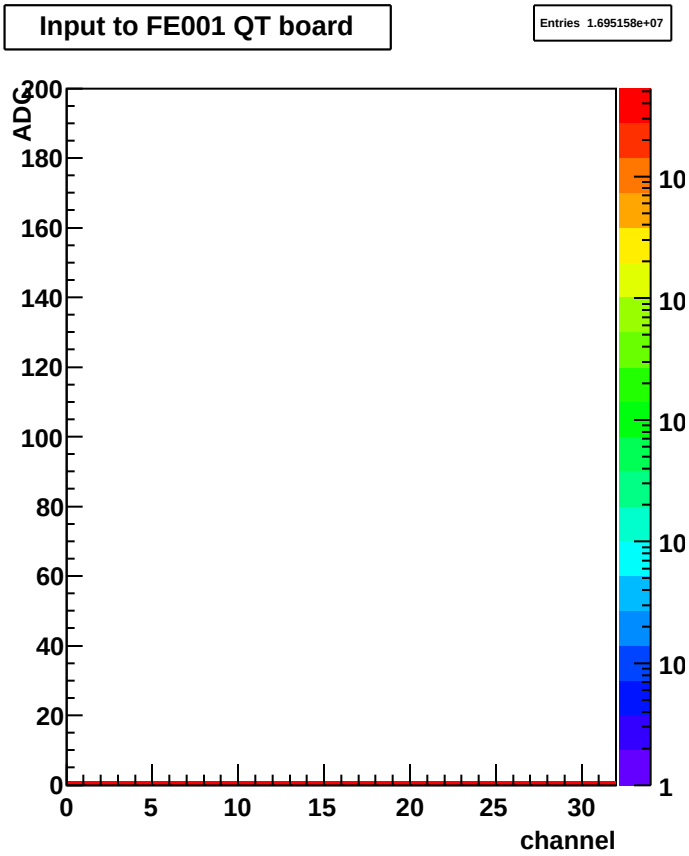

Input to FPD L2 DSM
Entries 1589211
HT bits
529737
529737
529737
SMALL
LARGE-S
LARGE-N

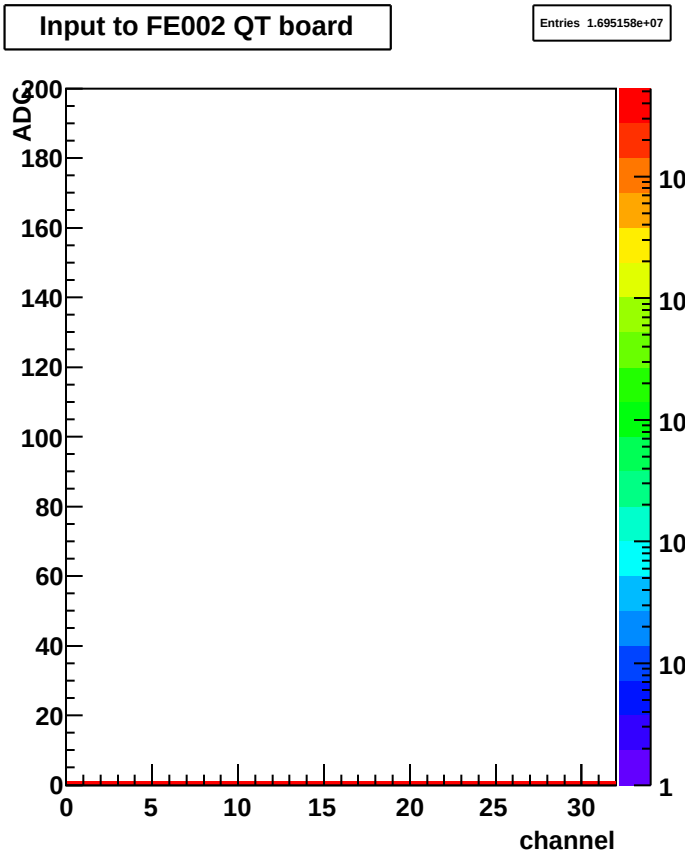

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

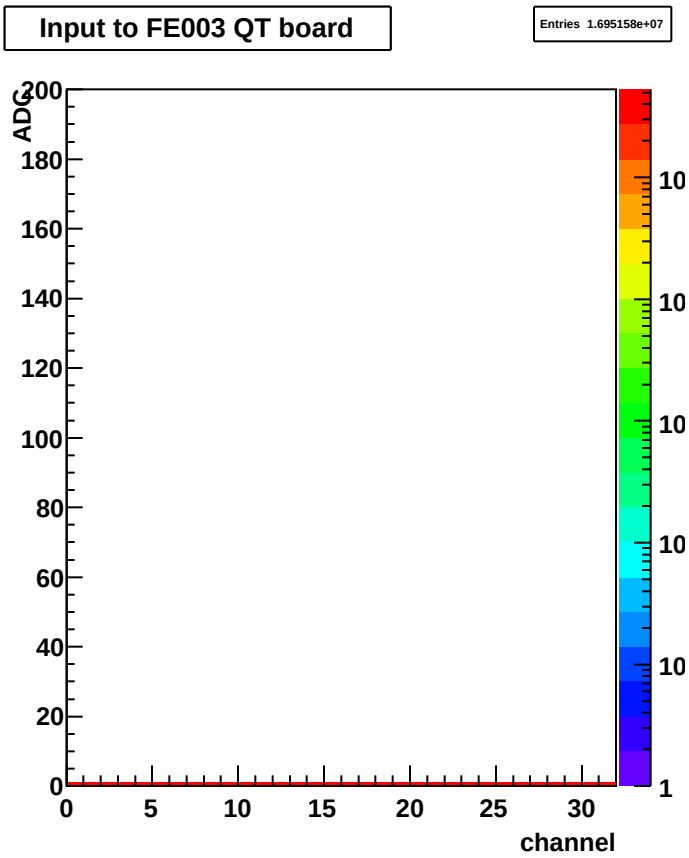

Input to FPD L3 DSM
Entries 8475792
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

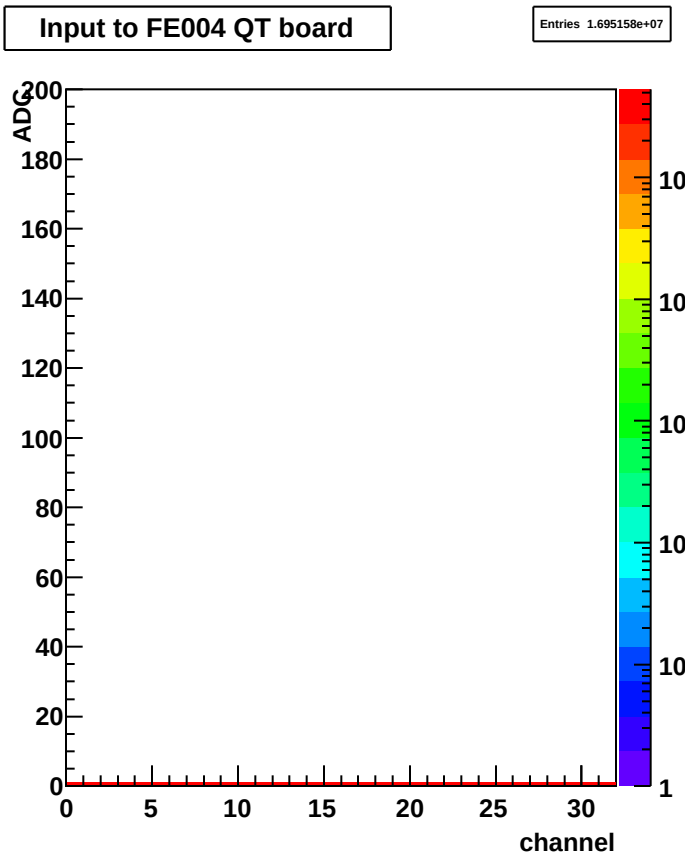

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

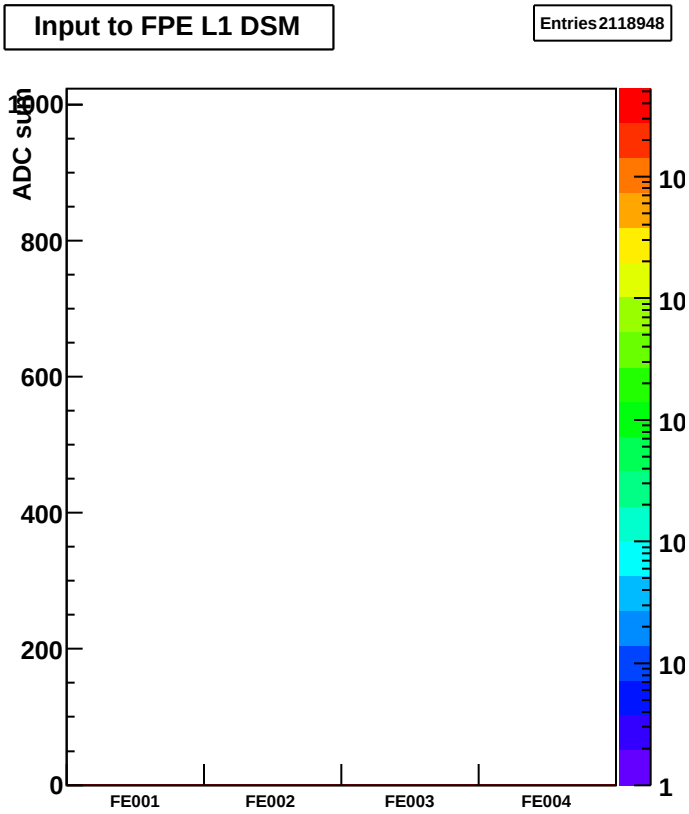

Input to FPD L2 DSM
Entries 2118948
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

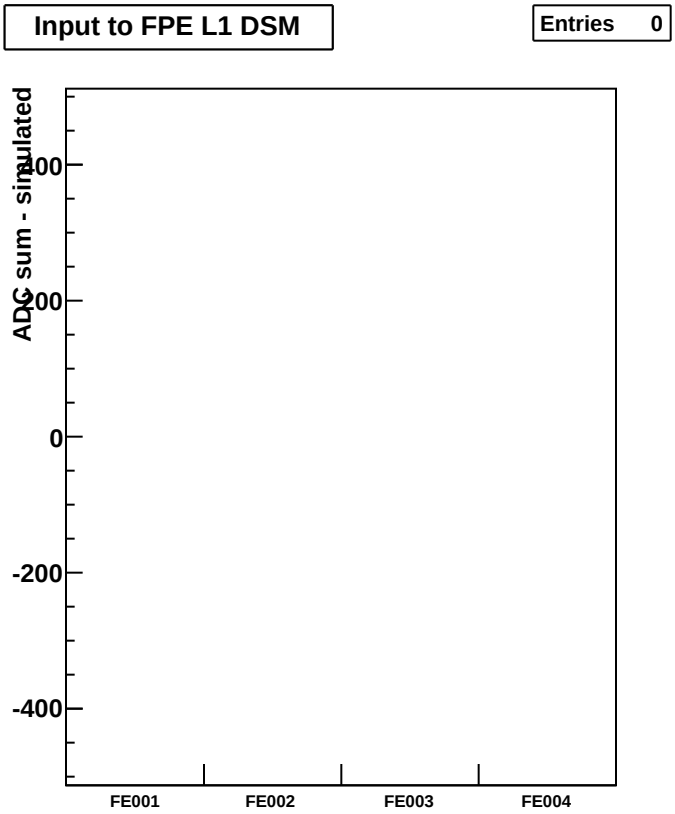

Input to FE001 QT board
Entries 1.695158e+07
ADC
channel

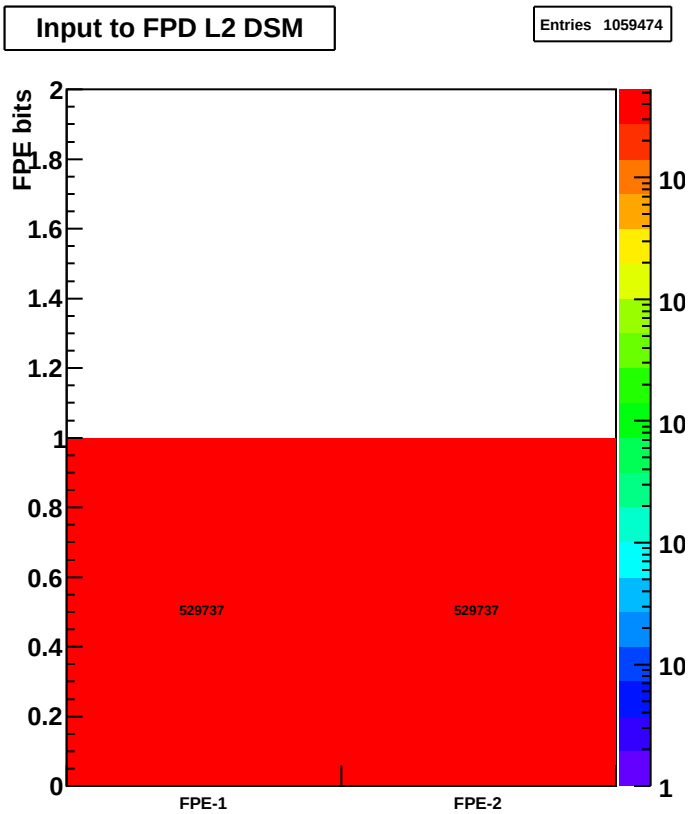

Input to FE002 QT board
Entries 1.695158e+07
ADC
channel

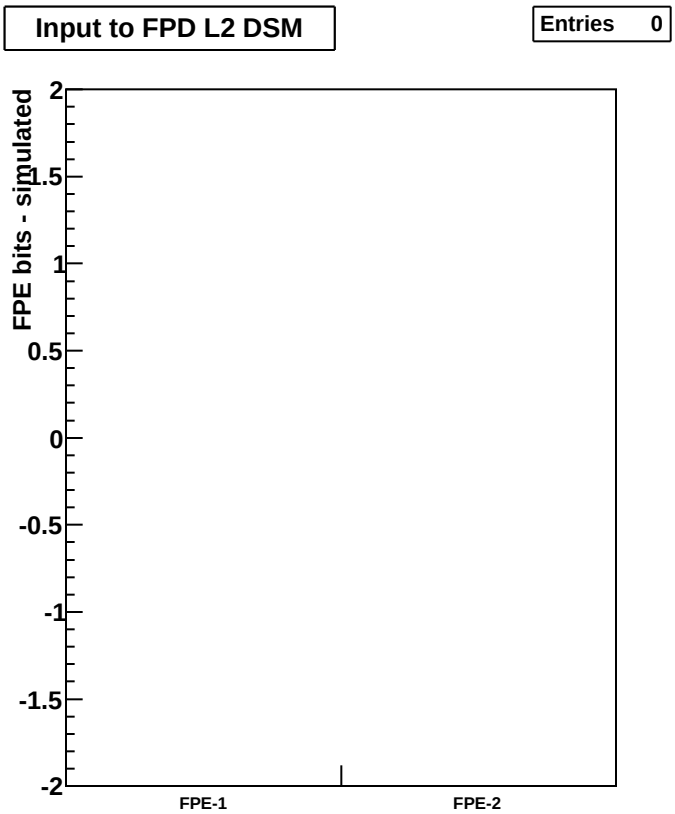

Input to FE003 QT board
Entries 1.695158e+07
ADC
channel


Input to FE004 QT board
Entries 1.695158e+07
ADC
channel


Input to FPE L1 DSM
Entries 2118948
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 1059474
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
529737
529737
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2