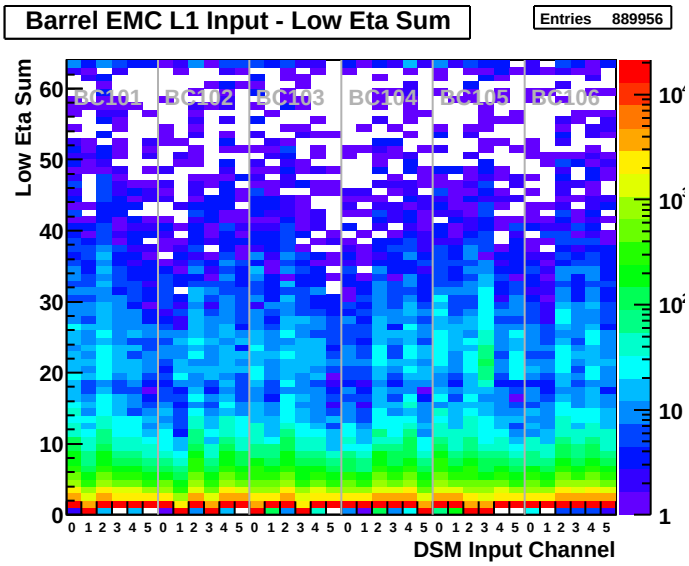
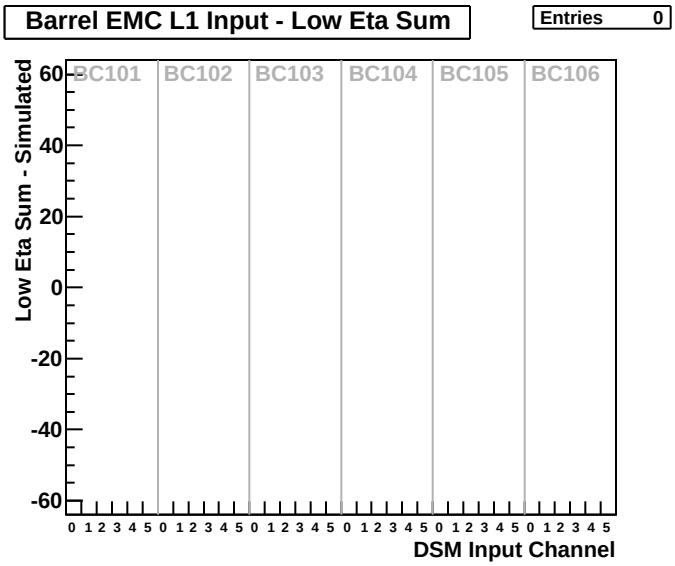
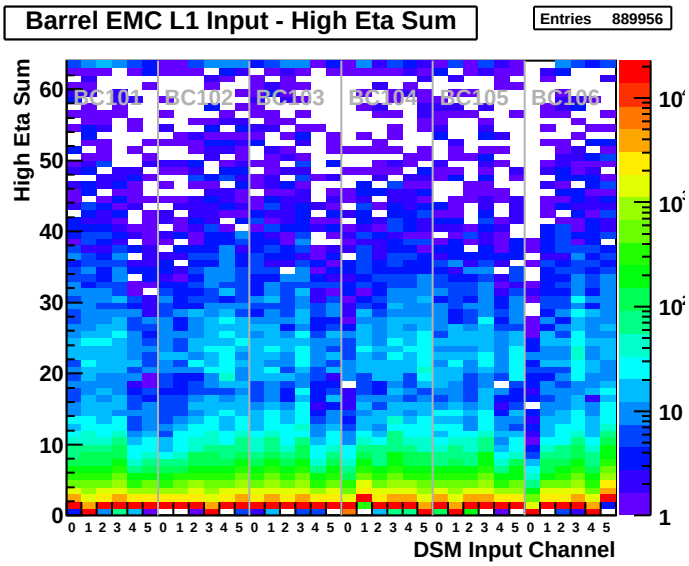
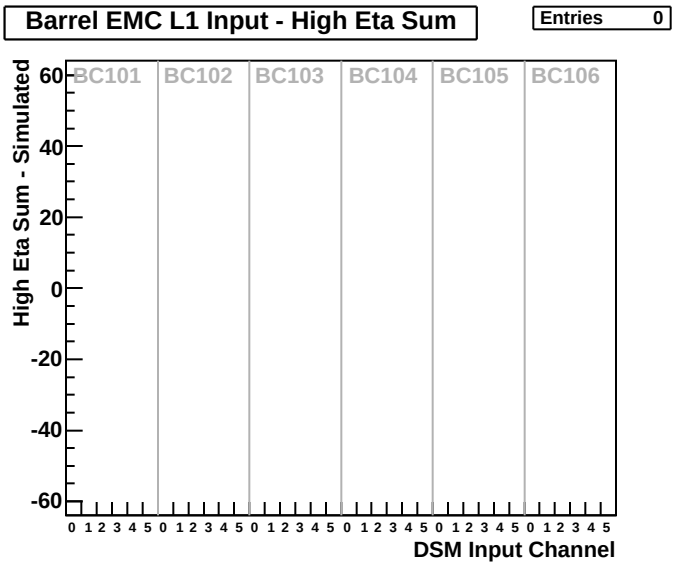
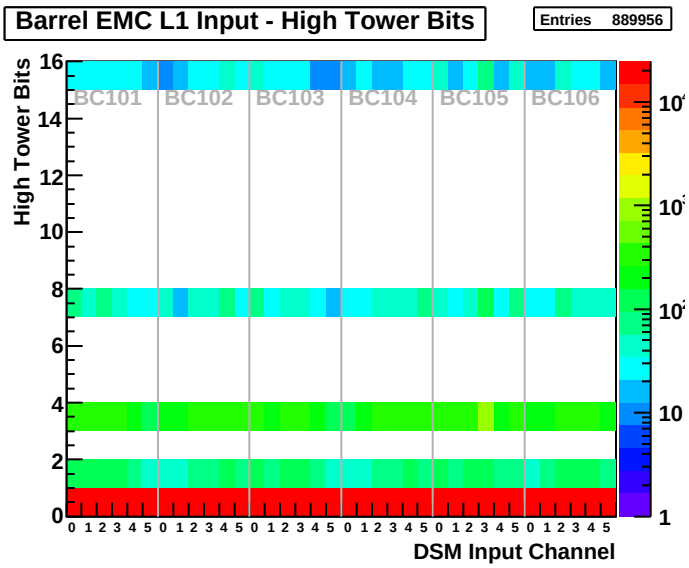
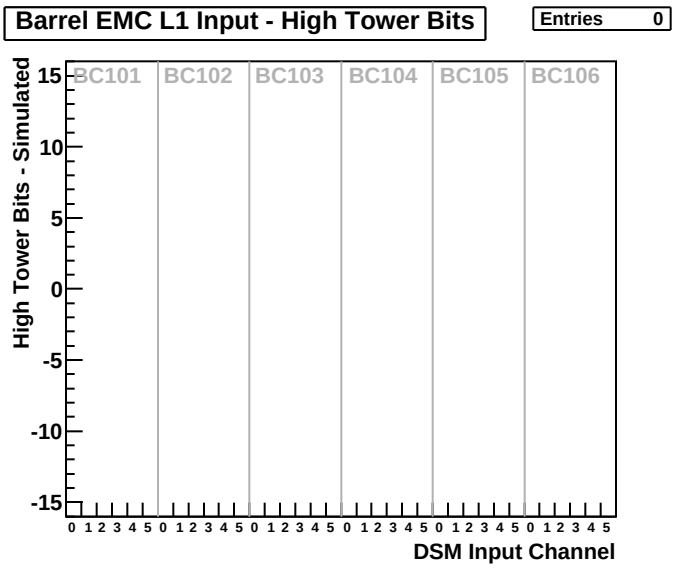

Barrel EMC L1 Input - Low Eta Sum
Entries 889956
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

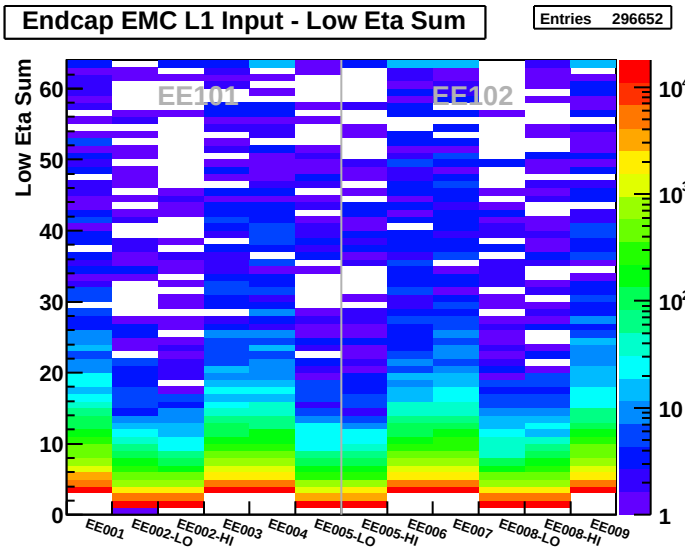

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

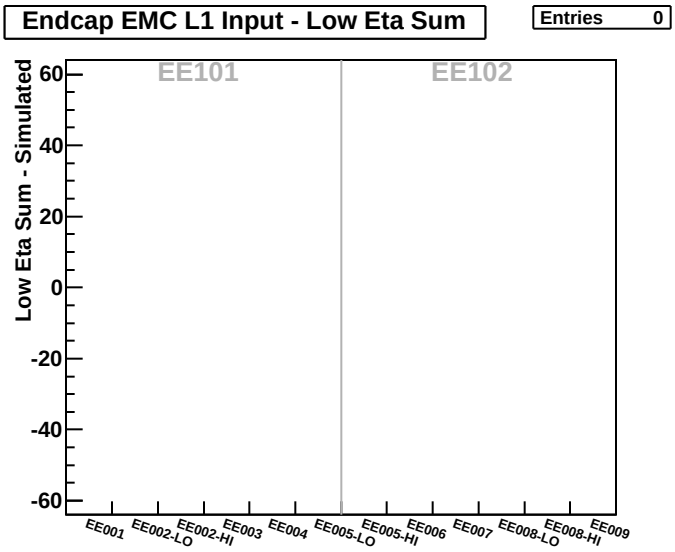

Barrel EMC L1 Input - High Eta Sum
Entries 889956
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

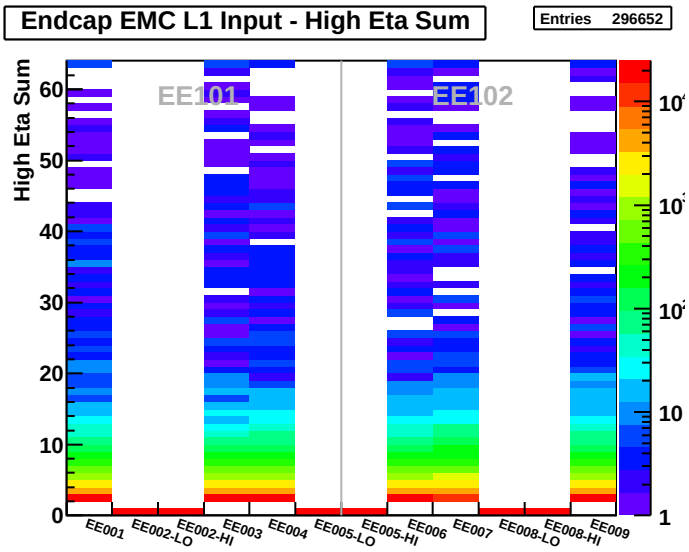

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

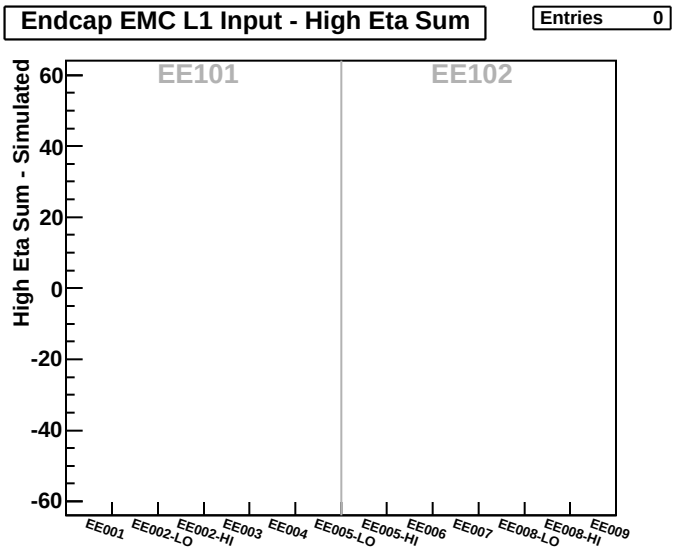

Barrel EMC L1 Input - High Tower Bits
Entries 889956
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

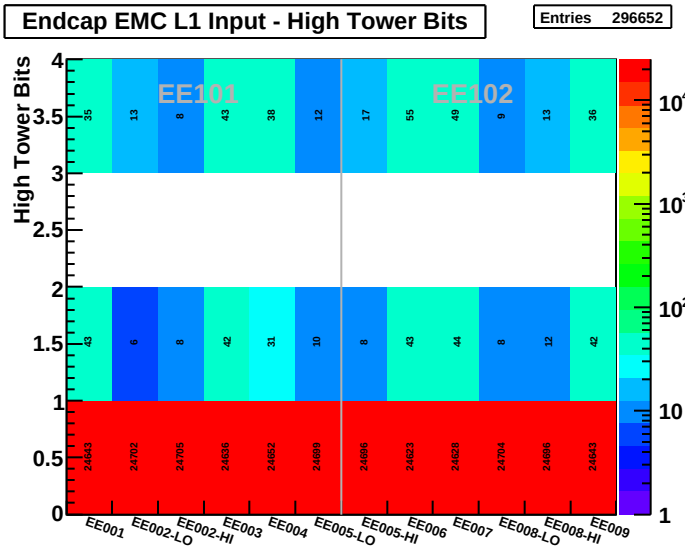

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

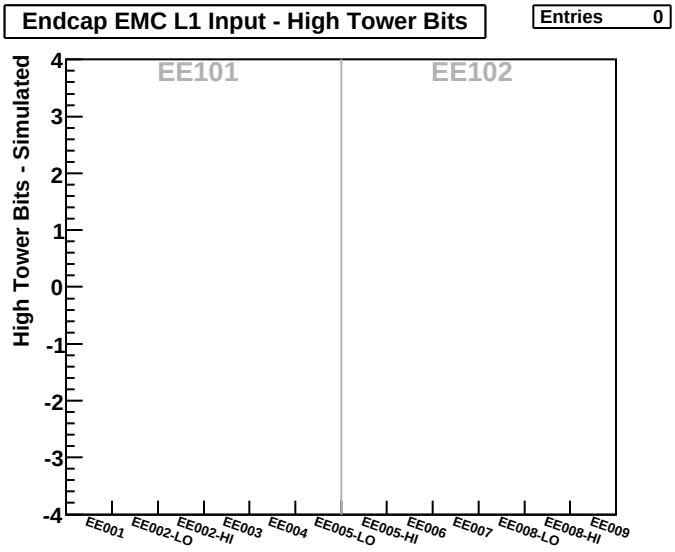

Endcap EMC L1 Input - Low Eta Sum
Entries 296652
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

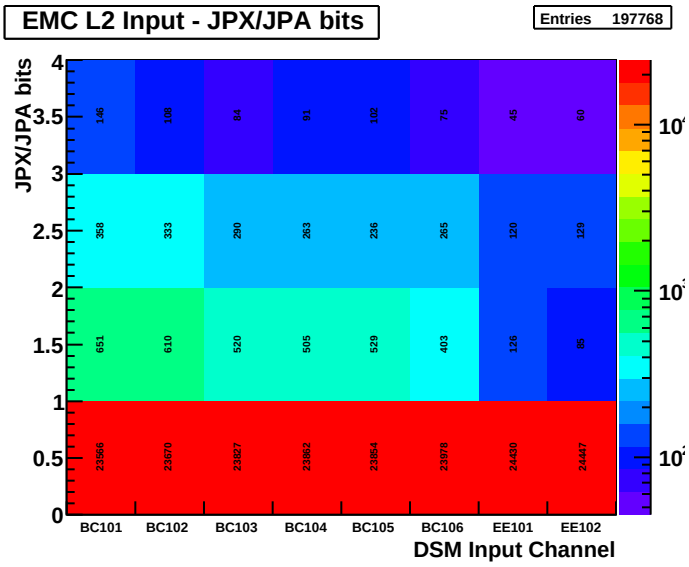

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

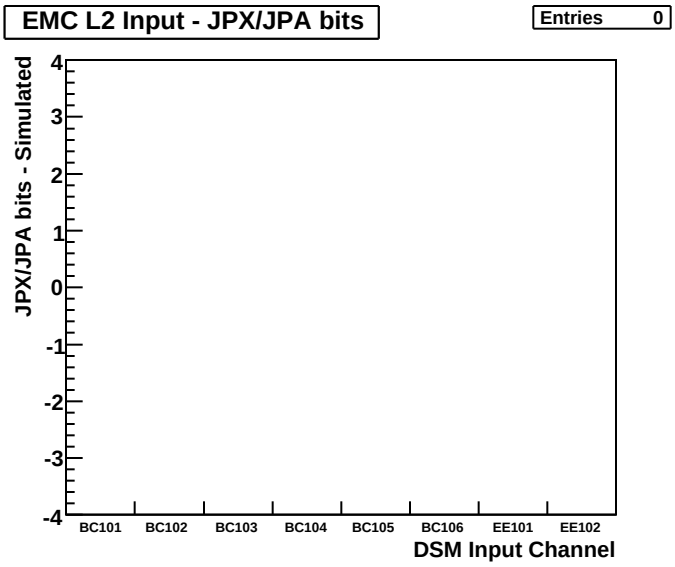

Endcap EMC L1 Input - High Eta Sum
Entries 296652
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

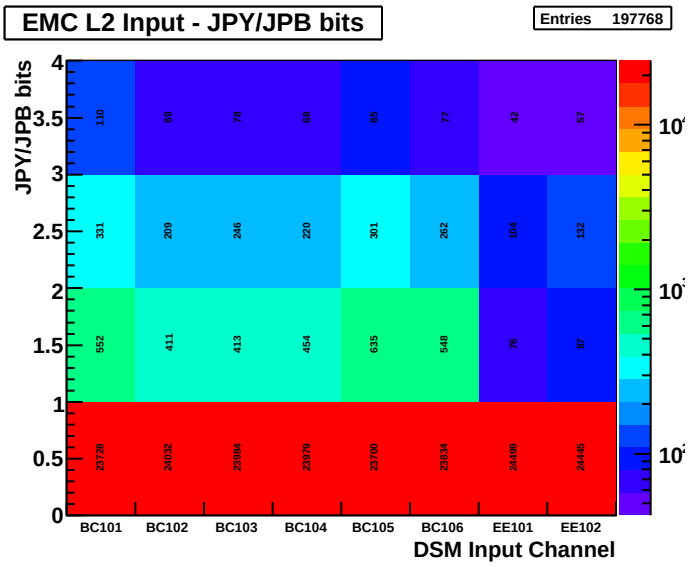

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

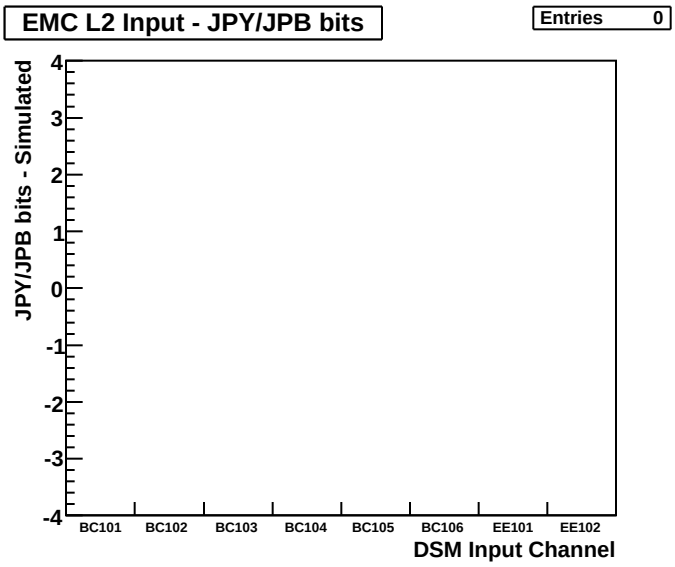

Endcap EMC L1 Input - High Tower Bits
Entries 296652
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

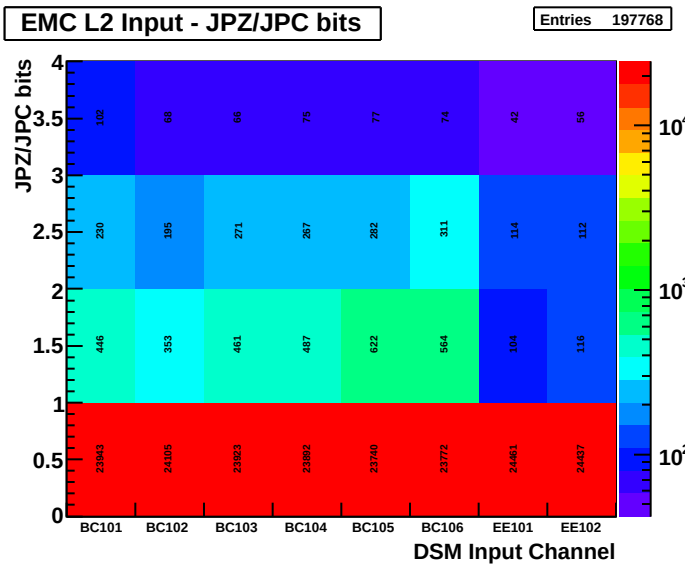

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

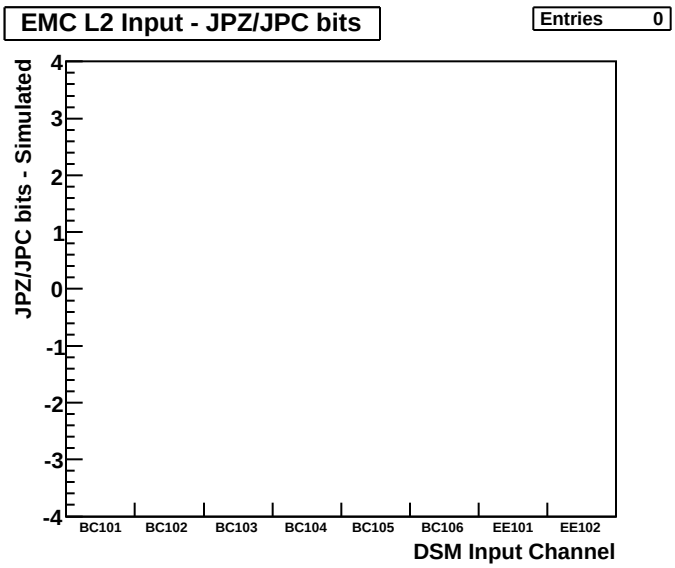

EMC L2 Input - JPX/JPA bits
Entries 197768
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
146 108 84 91 102 75 45 60
358 333 290 263 236 265 120 129
651 610 520 505 529 403 126 85
23566 23670 23827 23862 23854 23978 24430 24447

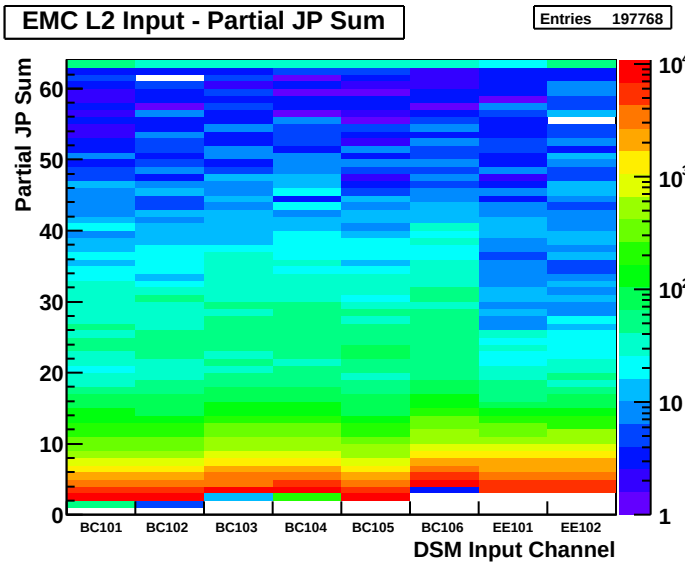

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

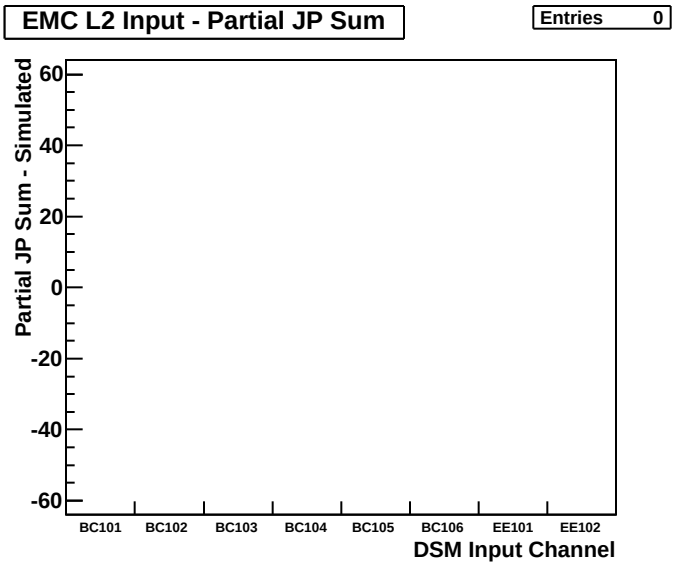

EMC L2 Input - JPY/JPB bits
Entries 197768
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
110 69 78 68 85 77 42 57
331 209 246 220 301 262 104 132
552 411 413 454 635 548 76 87
23728 24032 23894 23979 23700 23834 24499 24445

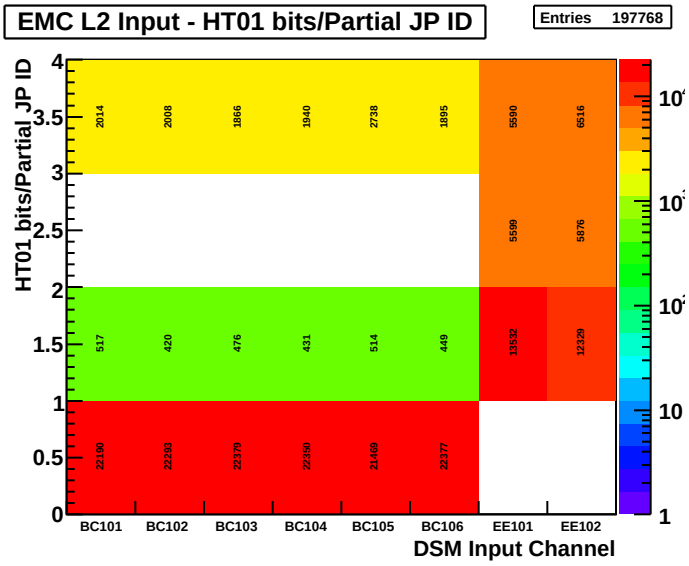

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

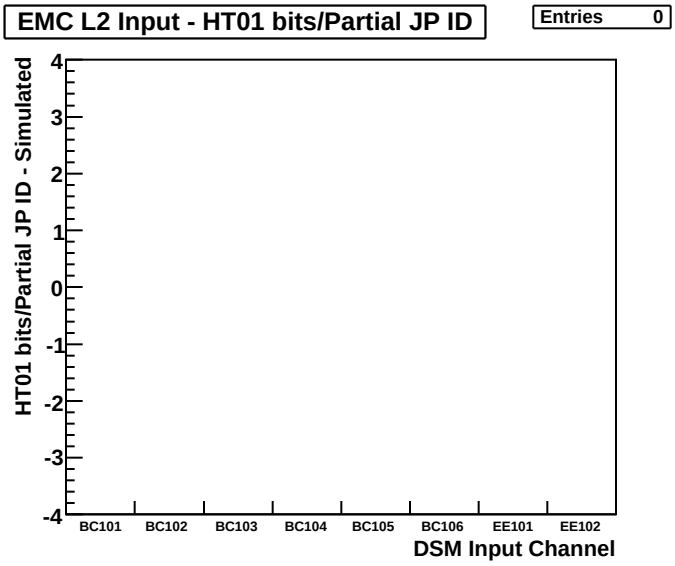

EMC L2 Input - JPZ/JPC bits
Entries 197768
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
102 68 66 75 77 74 42 56
230 195 271 267 282 311 114 112
446 353 461 487 622 564 104 116
23943 24105 23923 23892 23740 23772 24461 24437

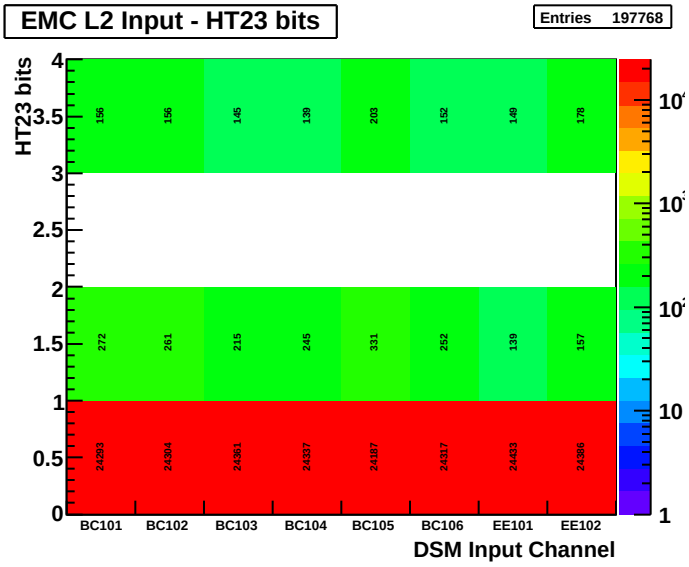

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

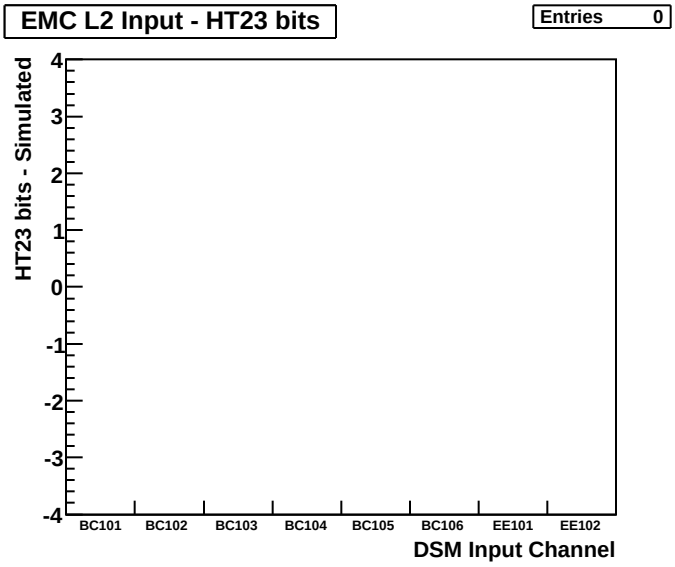

EMC L2 Input - Partial JP Sum
Entries 197768
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

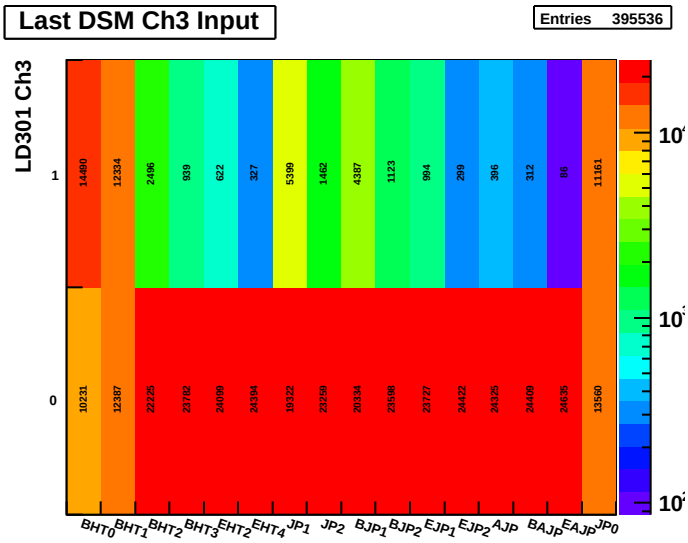

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

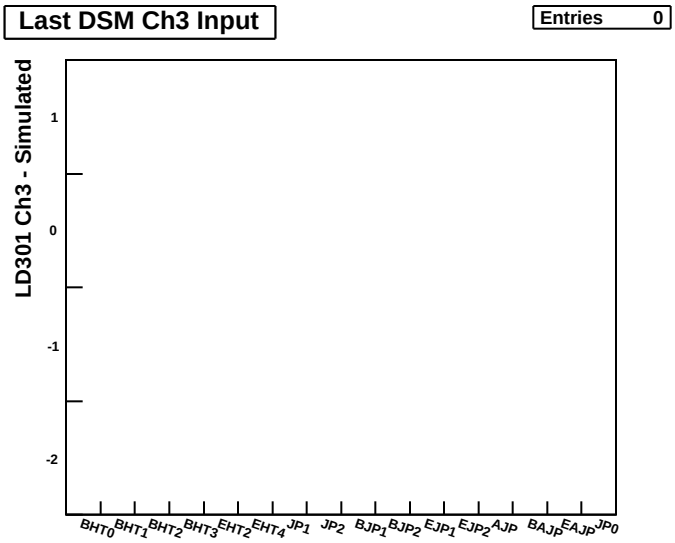

EMC L2 Input - HT01 bits/Partial JP ID
Entries 197768
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

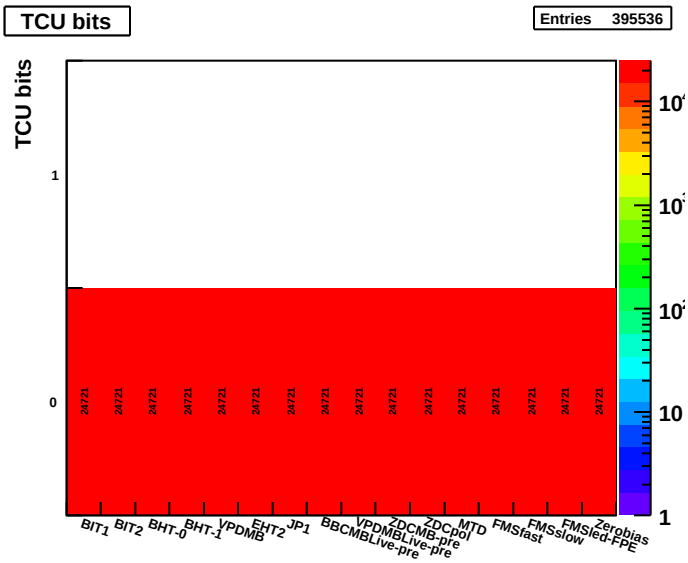

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

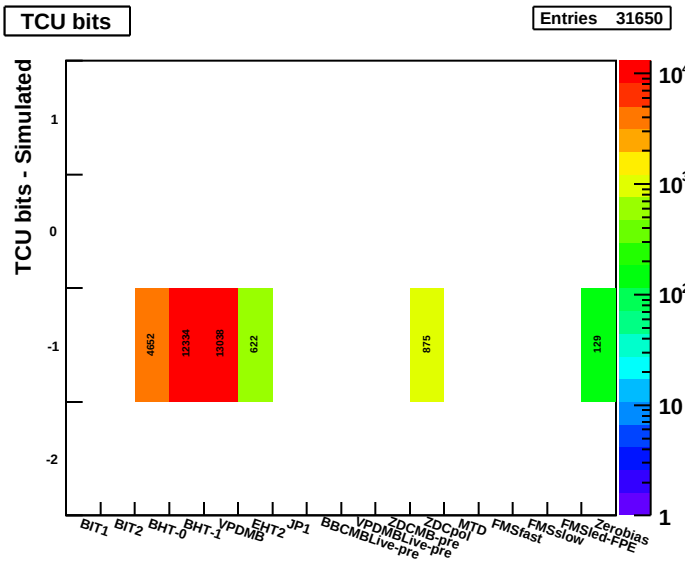

EMC L2 Input - HT23 bits
Entries 197768
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

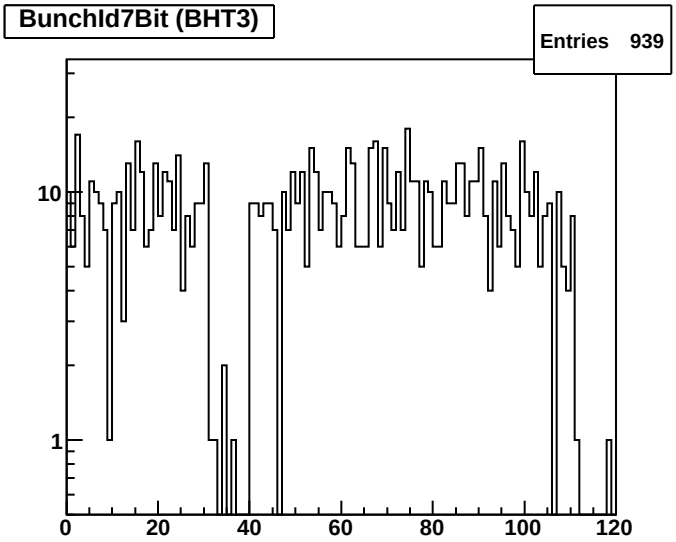

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

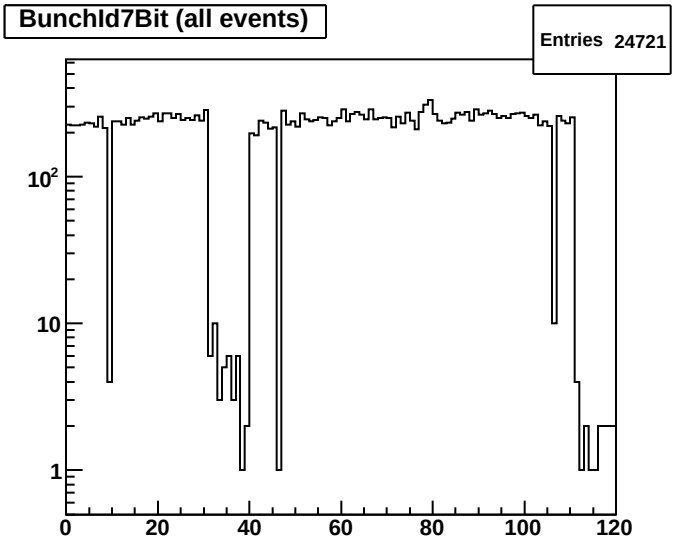

Last DSM Ch3 Input
Entries 395536
LD301 Ch3
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

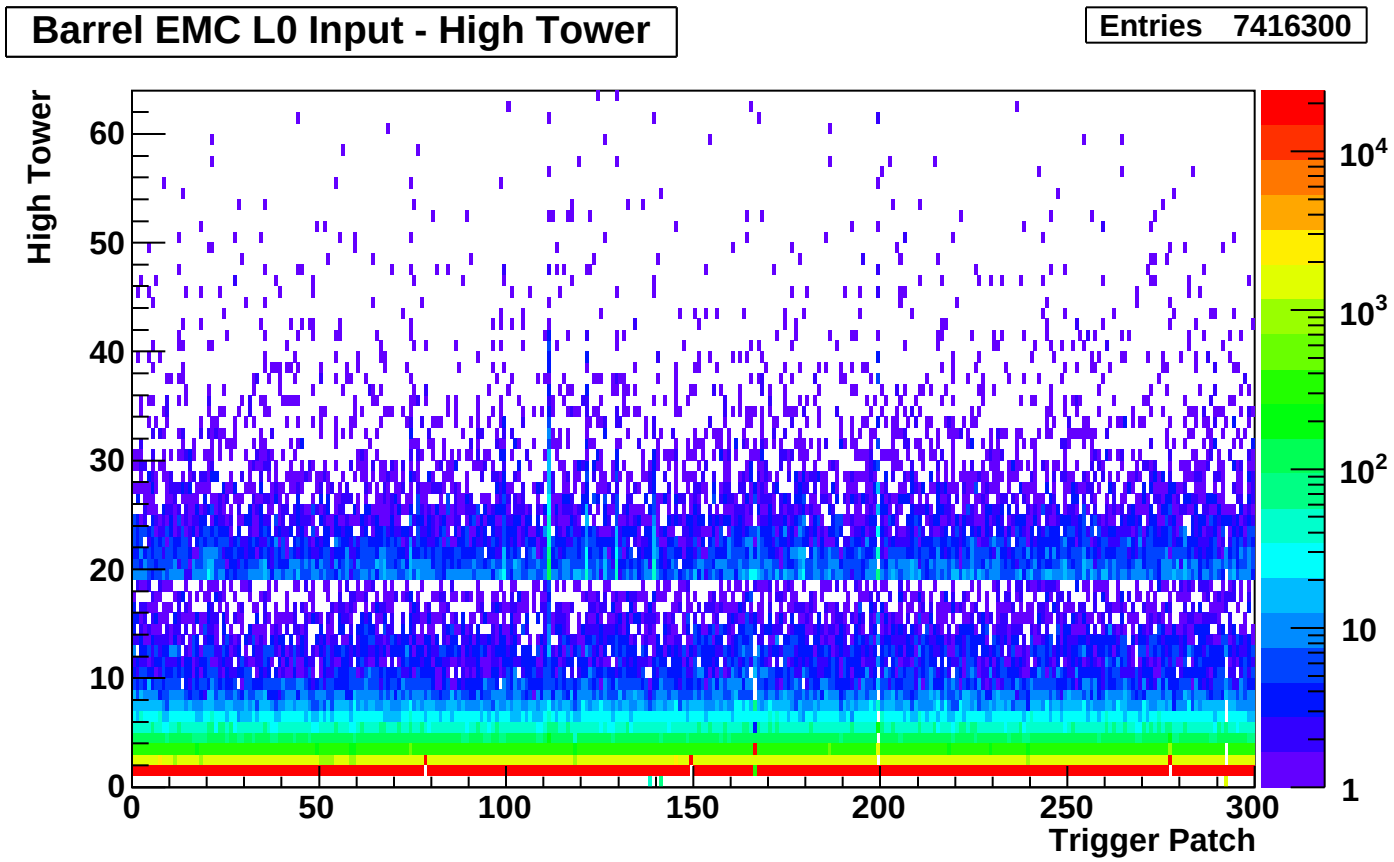

Last DSM Ch3 Input
Entries 0
LD301 Ch3 - Simulated
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

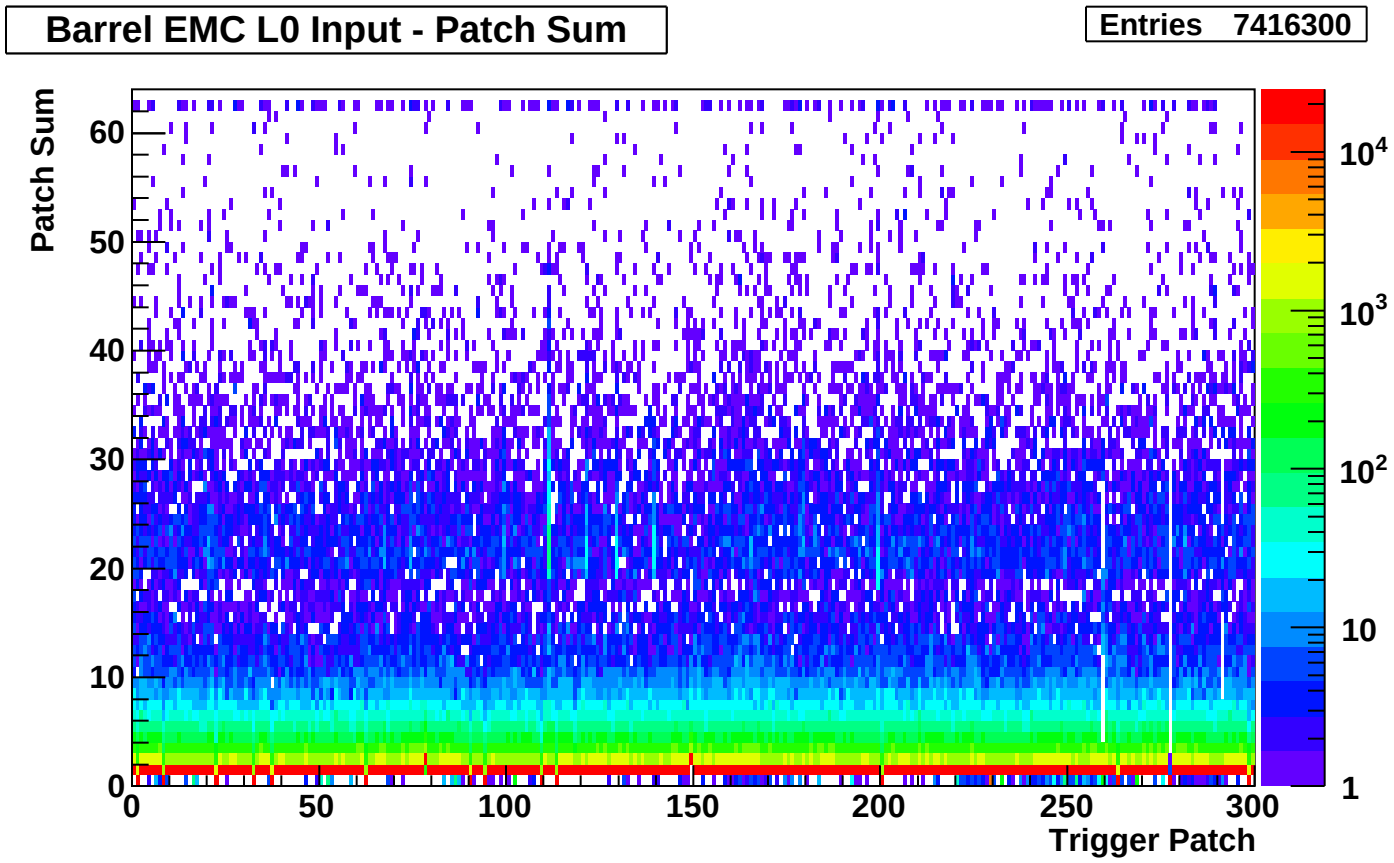

TCU bits
Entries 395536
TCU bits
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

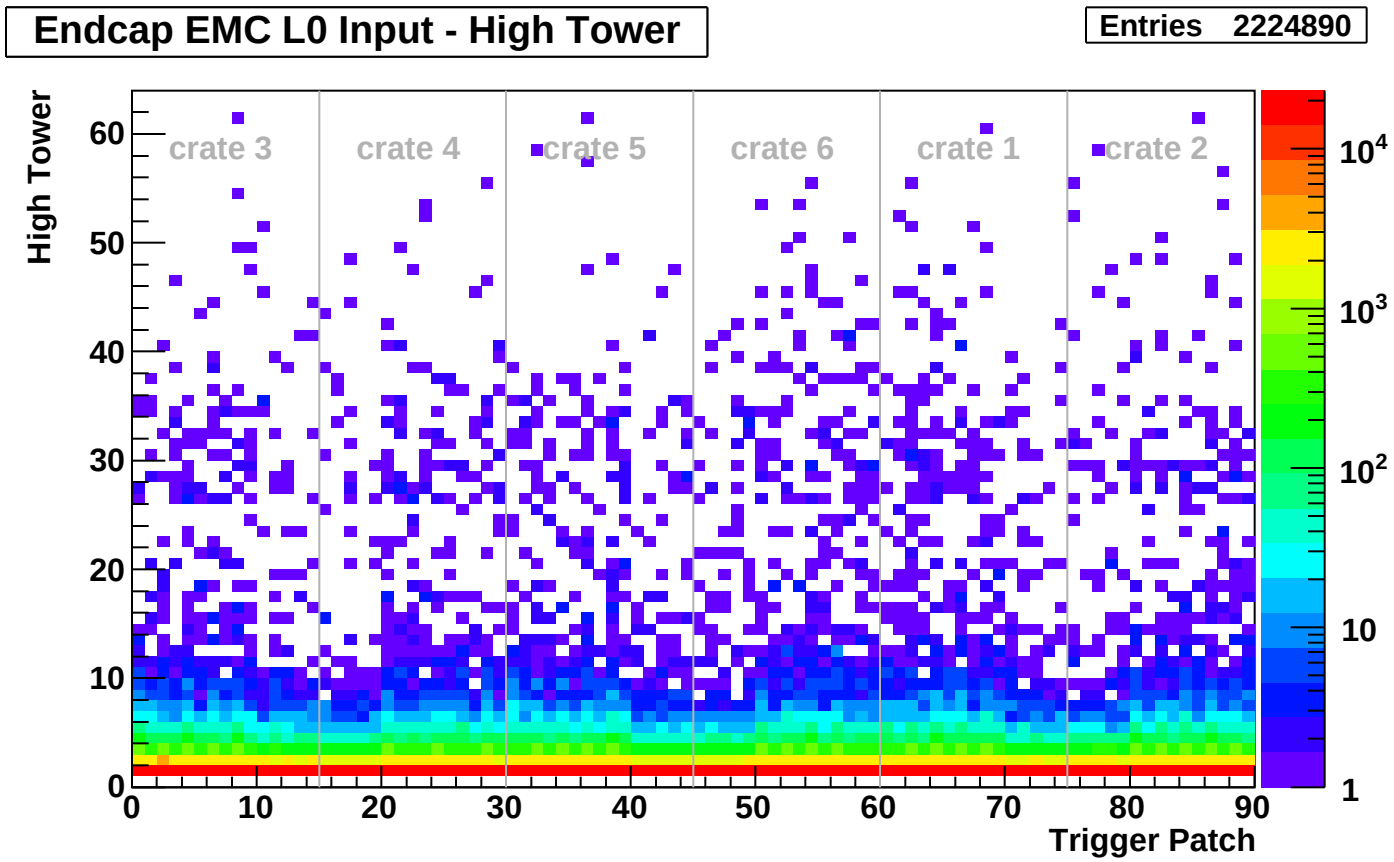

TCU bits
Entries 31650
TCU bits - Simulated
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

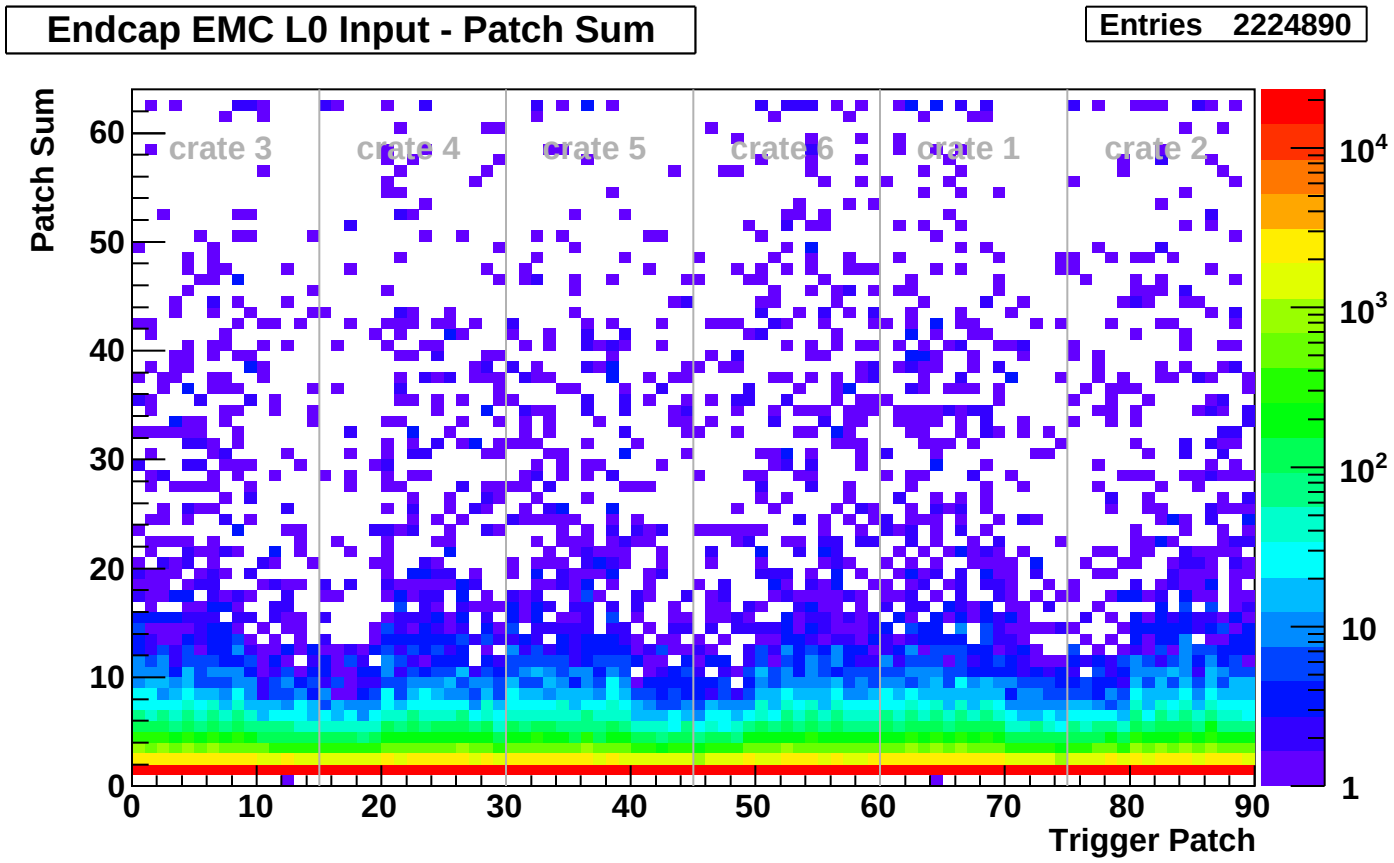

BunchId7Bit (BHT3)
Entries 939

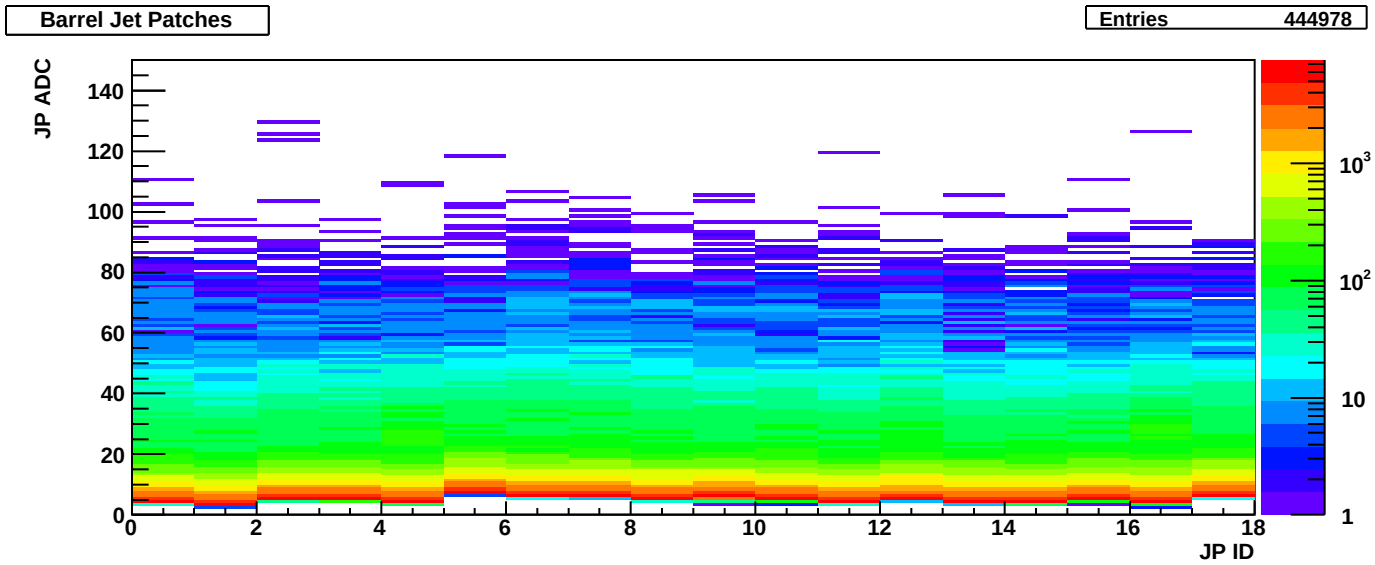

BunchId7Bit (all events)
Entries 24721

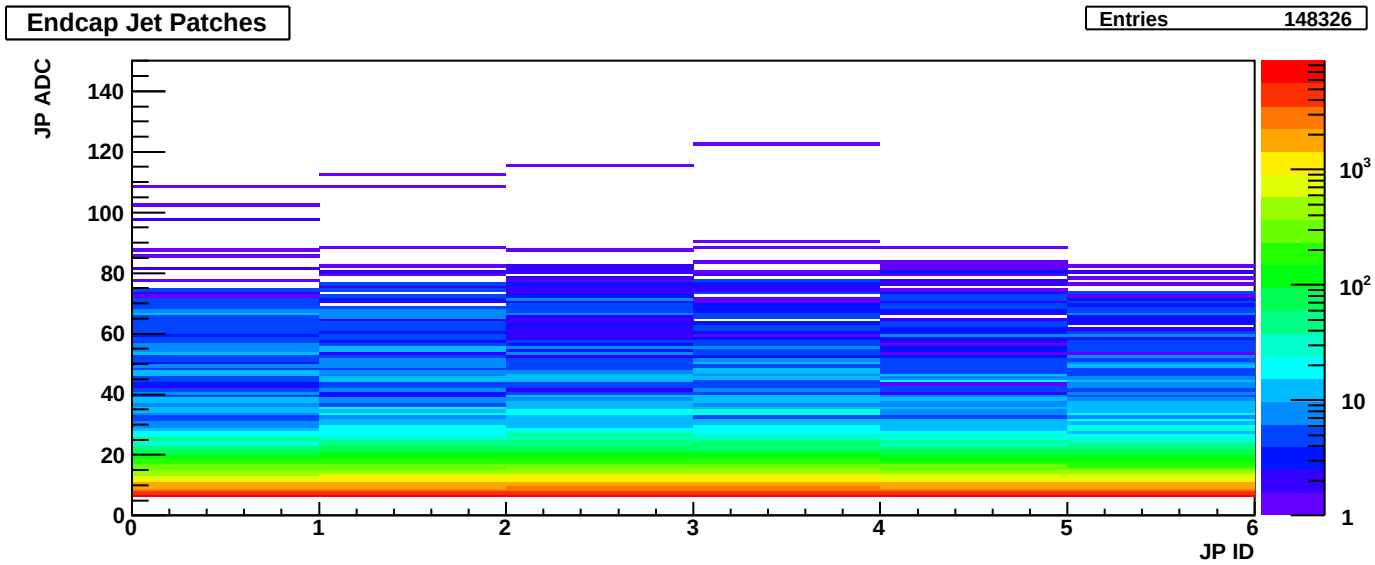

Barrel EMC L0 Input - High Tower
Entries 7416300
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

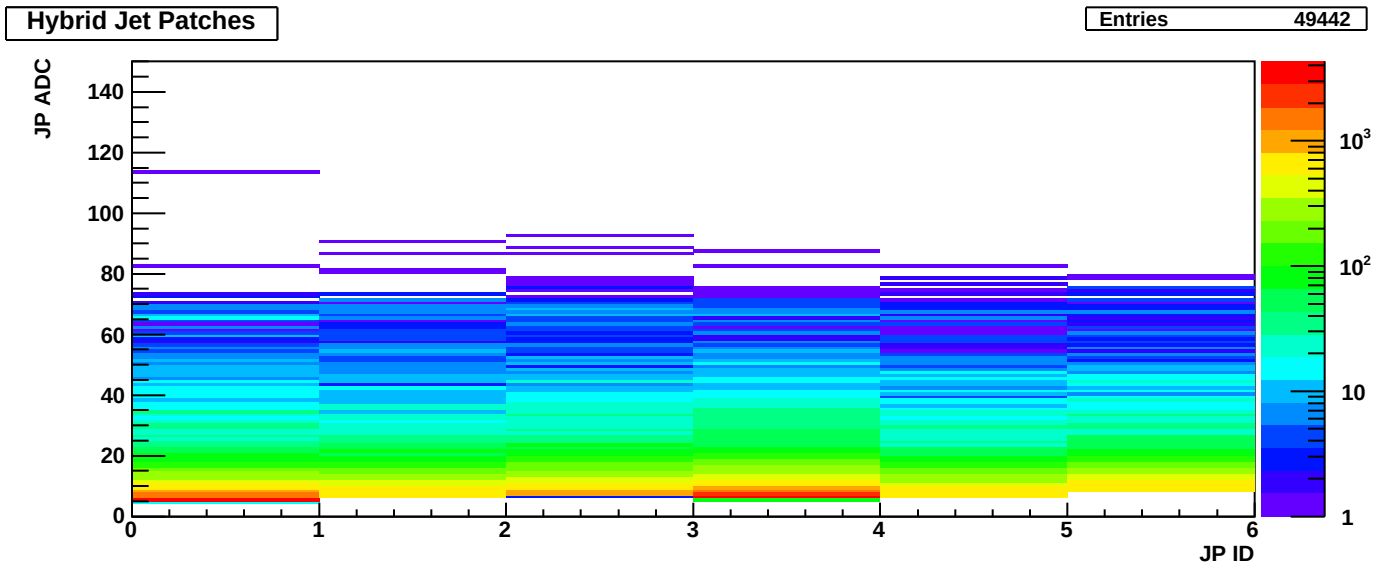

Barrel EMC L0 Input - Patch Sum
Entries 7416300
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4

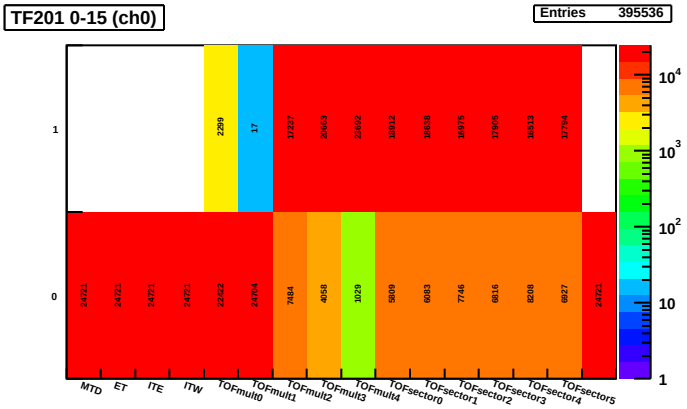

Endcap EMC L0 Input - High Tower
Entries 2224890
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

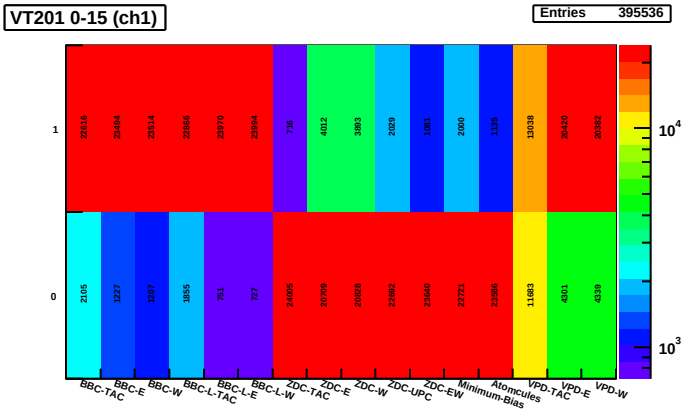

Endcap EMC L0 Input - Patch Sum
Entries 2224890
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

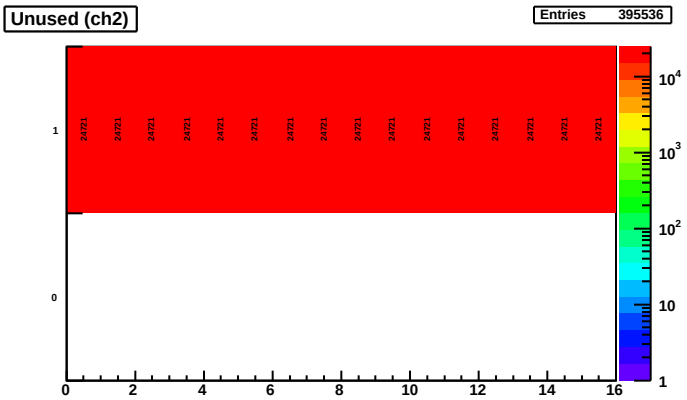

Barrel Jet Patches
Entries 444978
JP ADC
JP ID

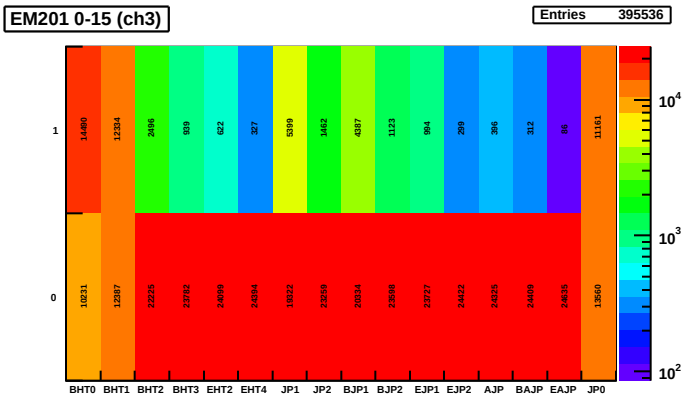

Endcap Jet Patches
Entries 148326
JP ADC
JP ID

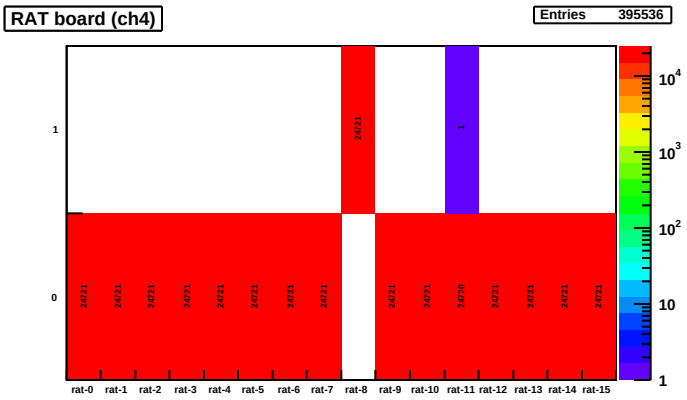

Hybrid Jet Patches
Entries 49442
JP ADC
JP ID

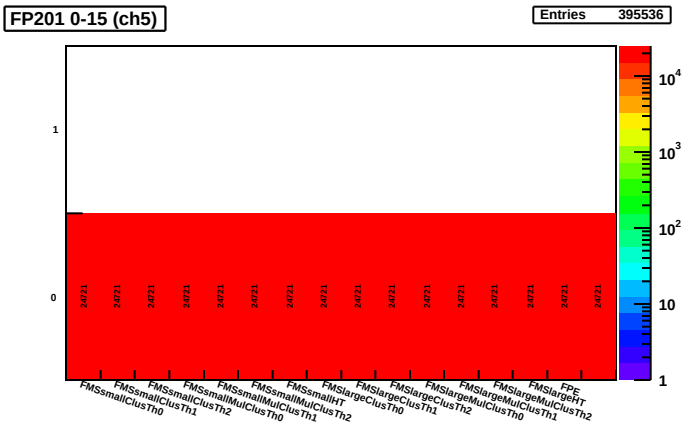

TF201 0-15 (ch0)
Entries 395536
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

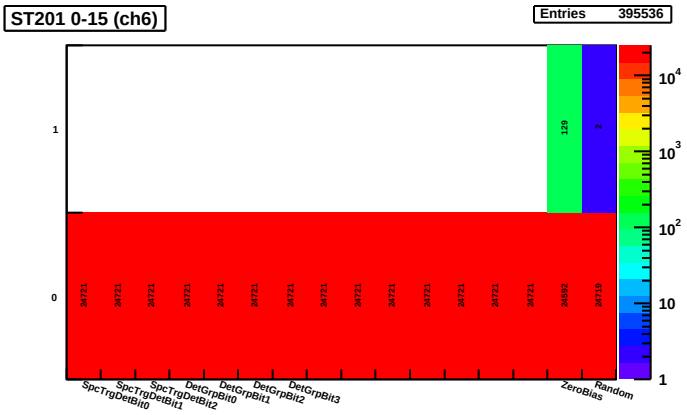

VT201 0-15 (ch1)
Entries 395536
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

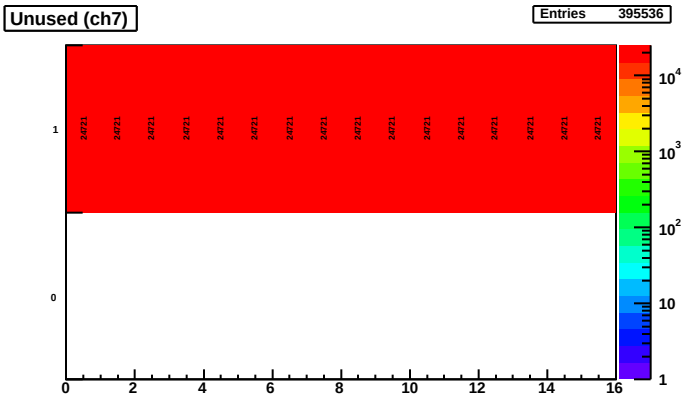

Unused (ch2)
Entries 395536

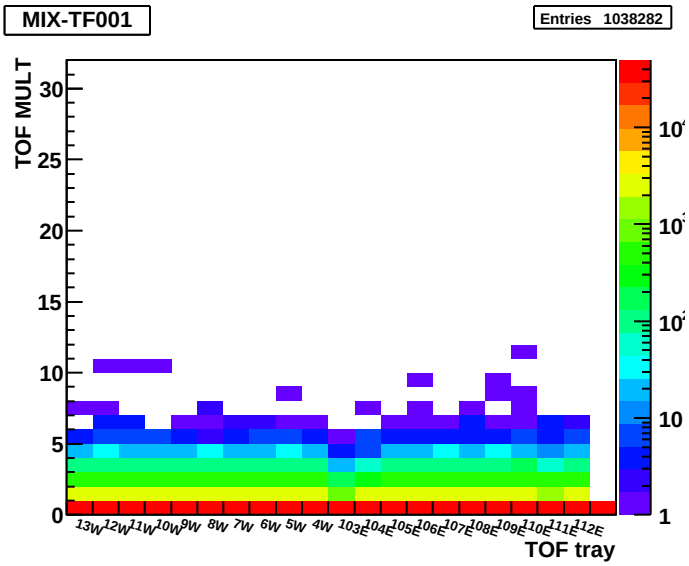

EM201 0-15 (ch3)
Entries 395536
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

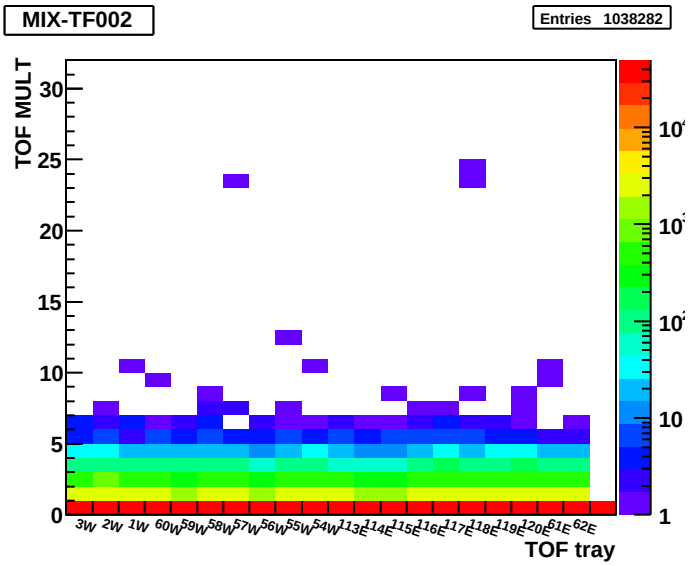

RAT board (ch4)
Entries 395536
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

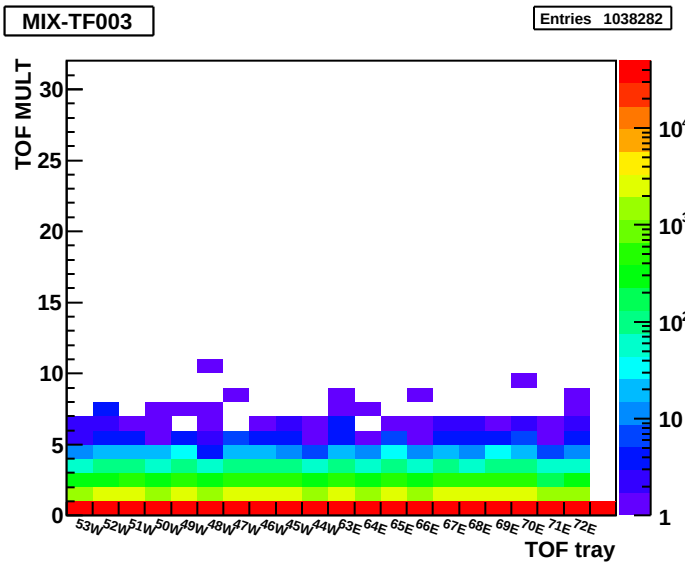

FP201 0-15 (ch5)
Entries 395536
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

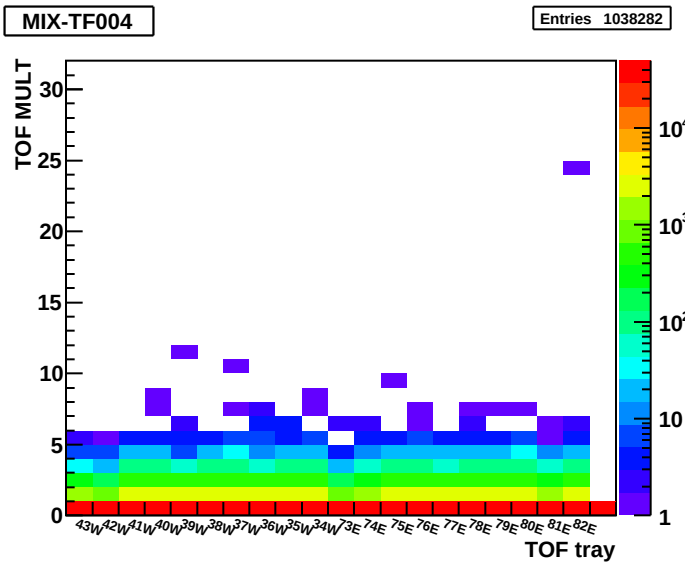

ST201 0-15 (ch6)
Entries 395536
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetGrpBit0 DetGrpBit1 DetGrpBit2 DetGrpBit3 ZeroBias Random

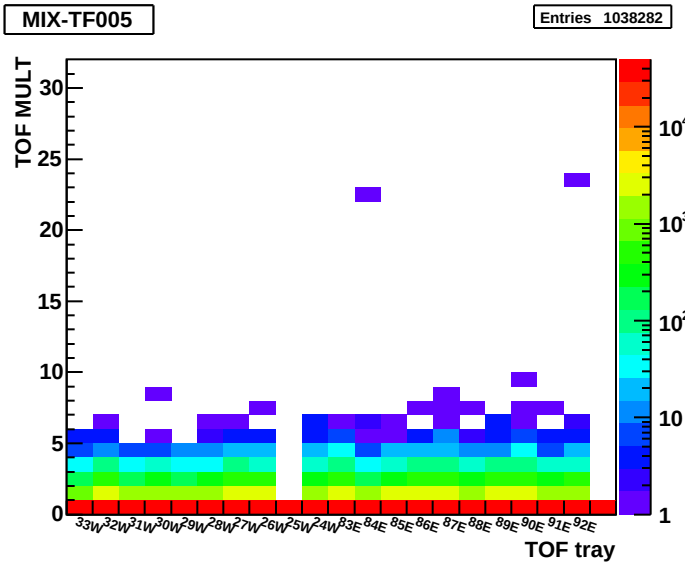

Unused (ch7)
Entries 395536

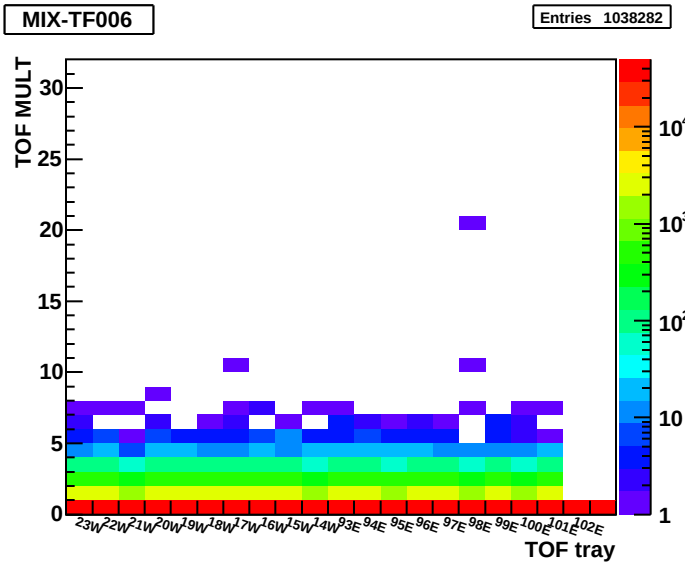

MIX-TF001
Entries 1038282
TOF MULT
TOF tray

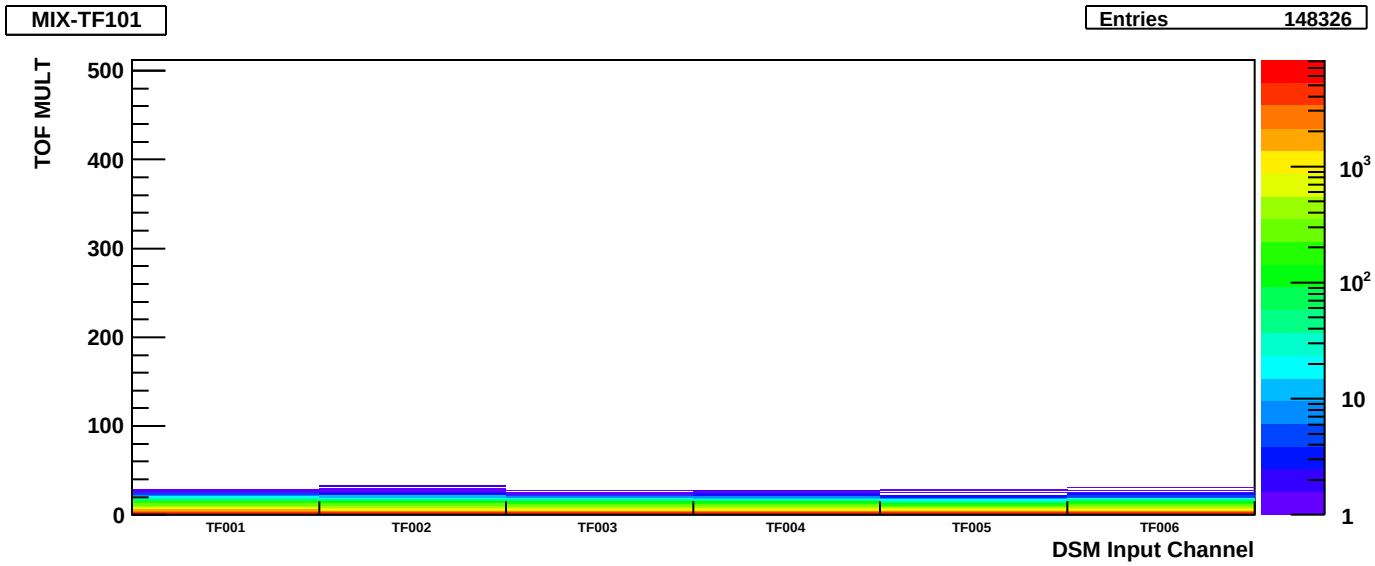

MIX-TF002
Entries 1038282
TOF MULT
TOF tray

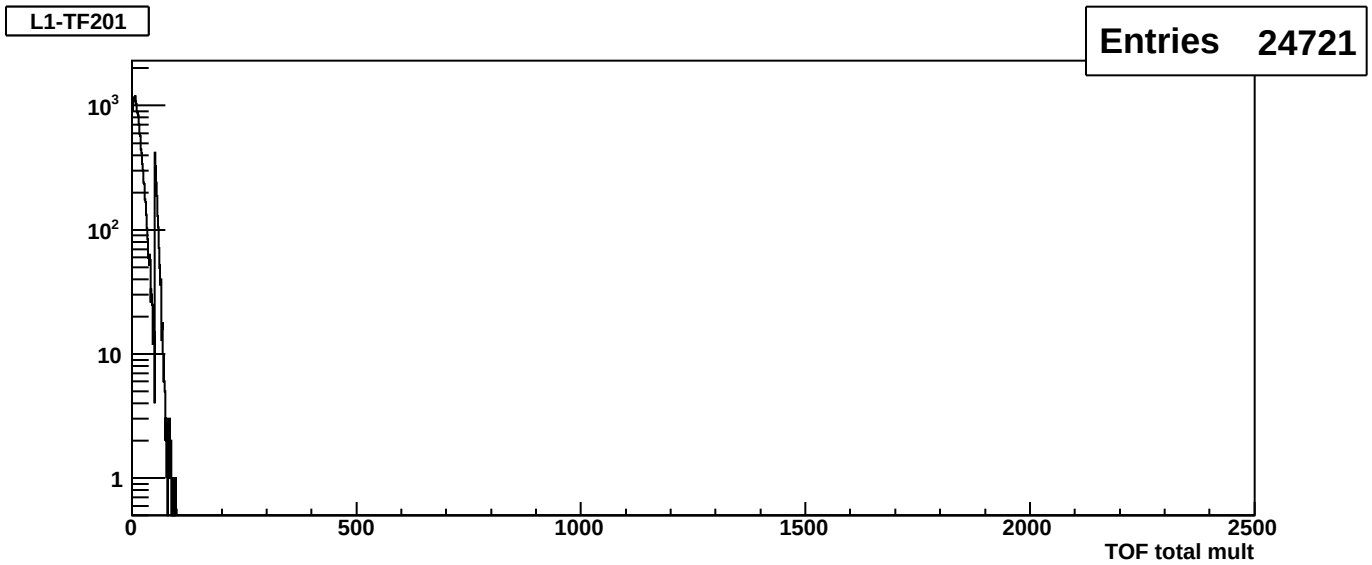

MIX-TF003
Entries 1038282
TOF MULT
TOF tray

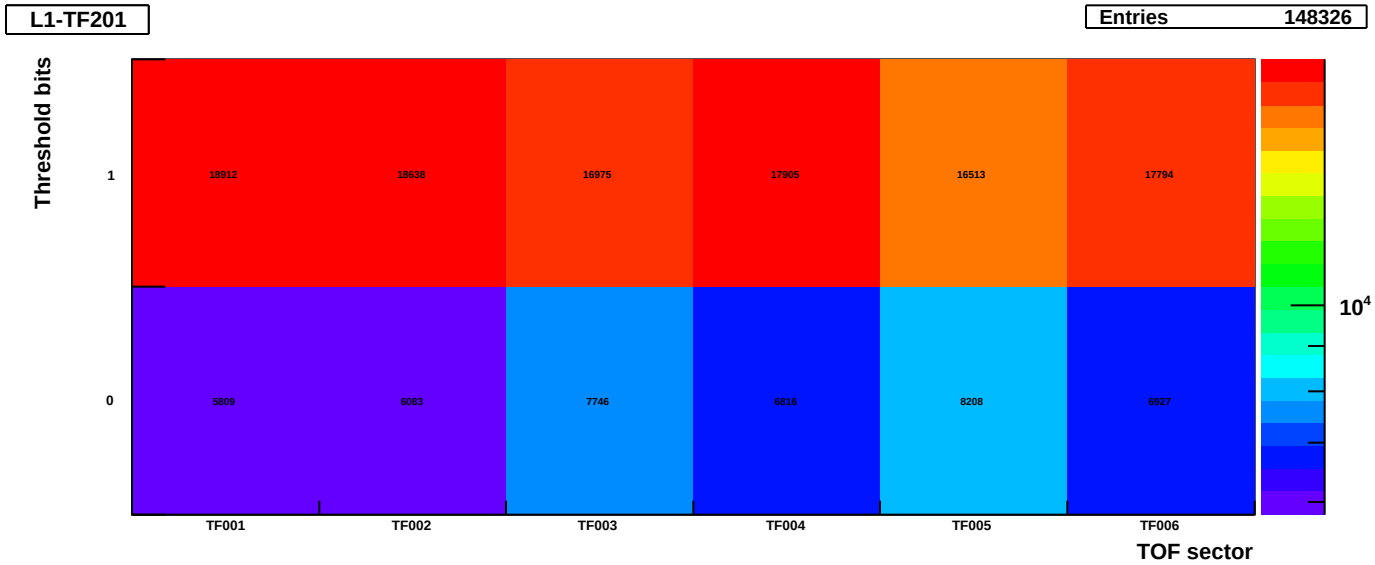

MIX-TF004
Entries 1038282
TOF MULT
TOF tray

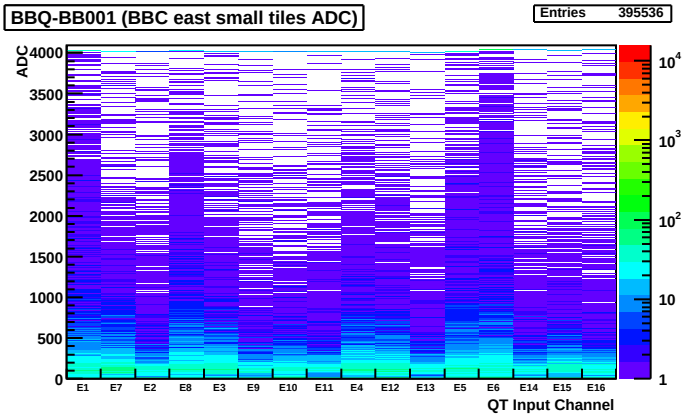

MIX-TF005
Entries 1038282
TOF MULT
TOF tray

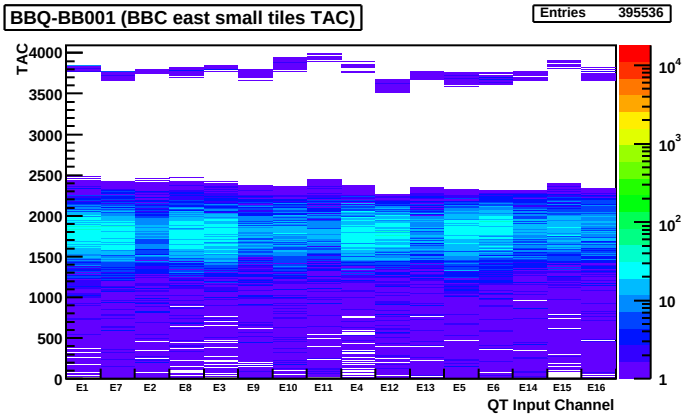

MIX-TF006
Entries 1038282
TOF MULT
TOF tray

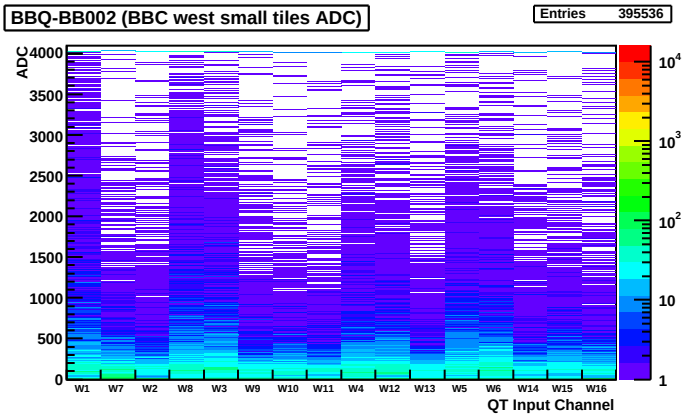

MIX-TF101
Entries 148326
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^3
10^2
10
1

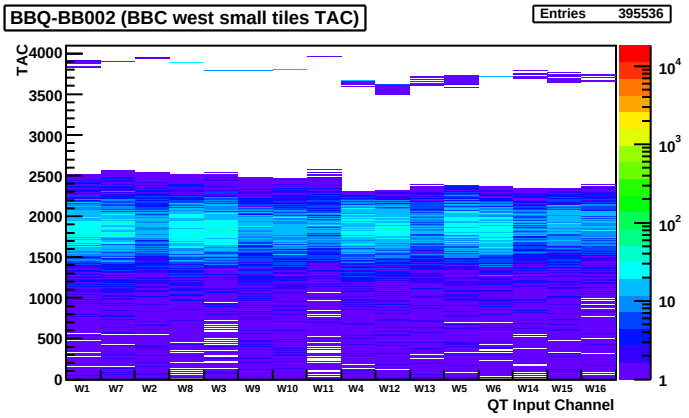

L1-TF201
Entries 24721
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

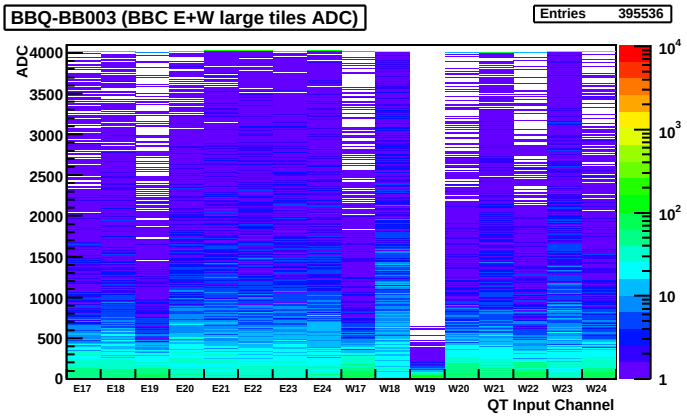

L1-TF201
Entries 148326
Threshold bits
1
0
18912
18638
16975
17905
16513
17794
5809
6083
7746
6816
8208
6927
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^4

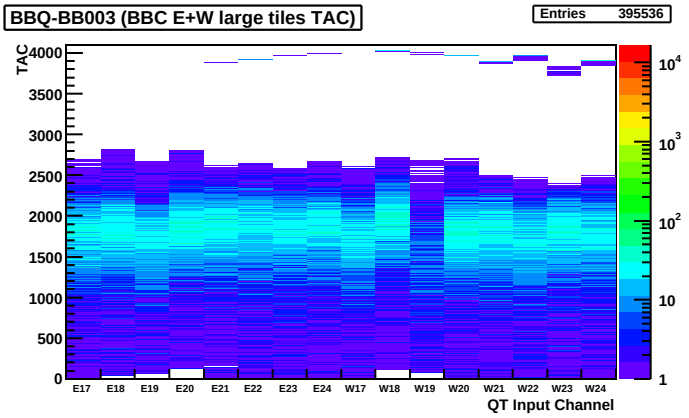

BBQ-BB001 (BBC east small tiles ADC)
Entries 395536
ADC
QT Input Channel

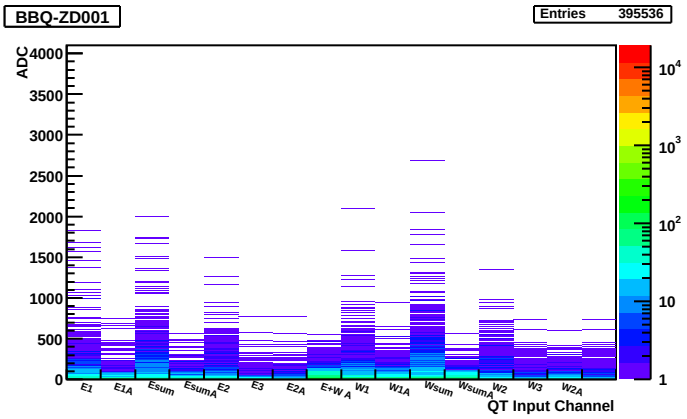

BBQ-BB001 (BBC east small tiles TAC)
Entries 395536
TAC
QT Input Channel

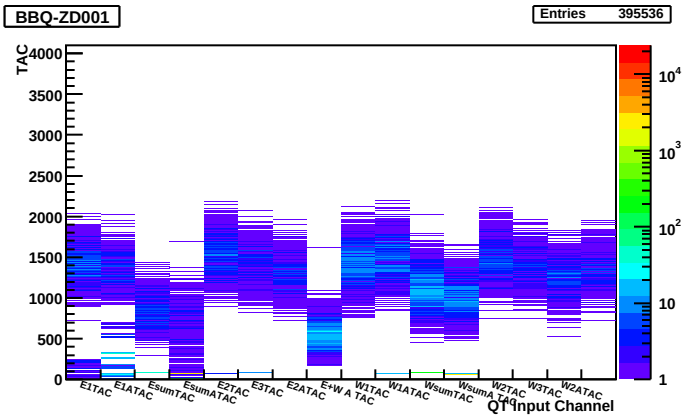

BBQ-BB002 (BBC west small tiles ADC)
Entries 395536
ADC
QT Input Channel

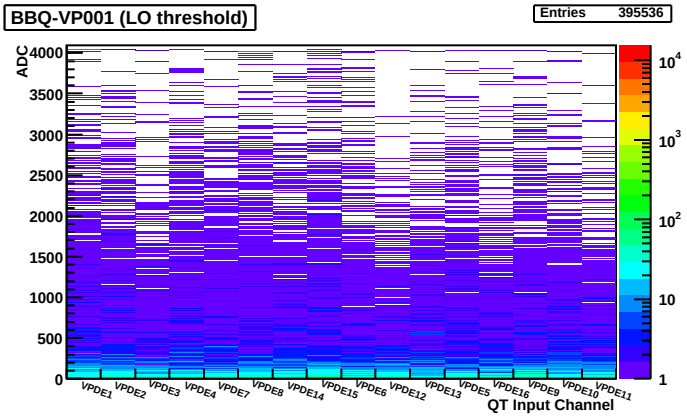

BBQ-BB002 (BBC west small tiles TAC)
Entries 395536
TAC
QT Input Channel

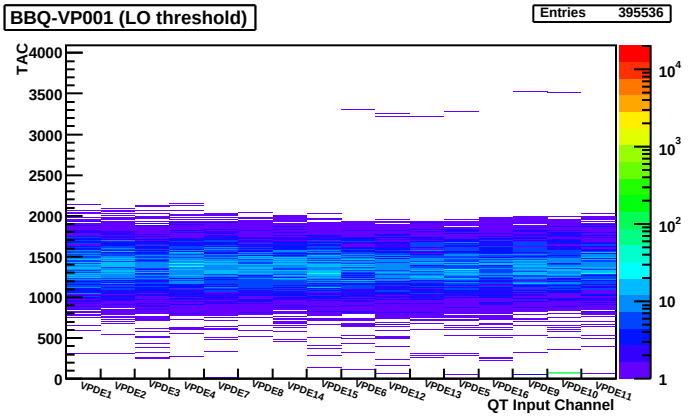

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 395536
ADC
QT Input Channel

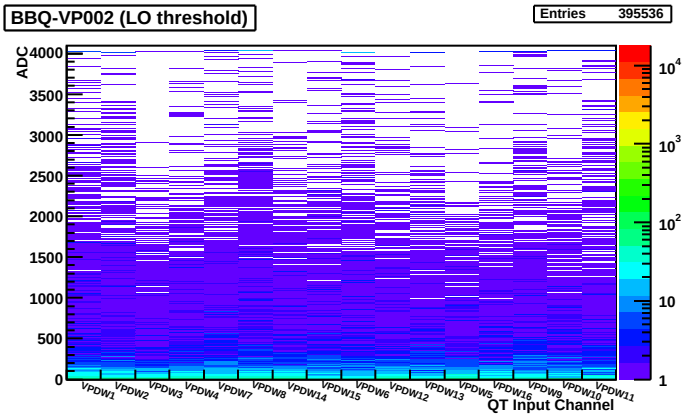

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 395536
TAC
QT Input Channel

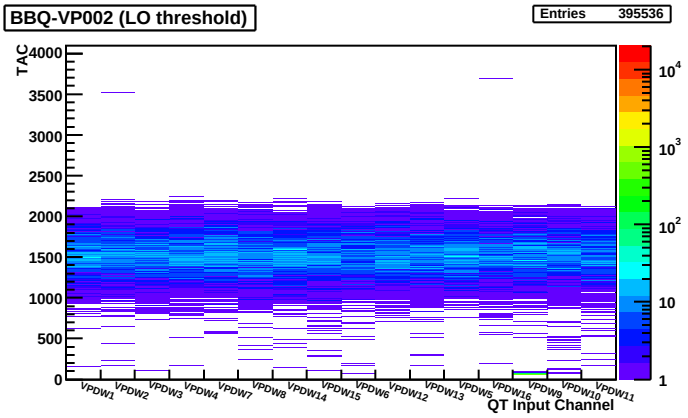

BBQ-ZD001
Entries 395536
ADC
QT Input Channel

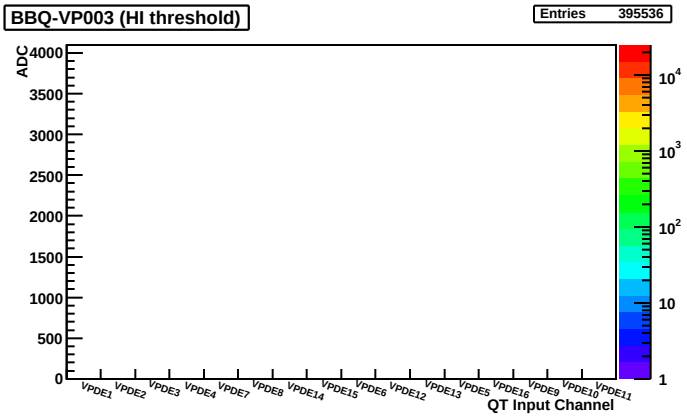

BBQ-ZD001
Entries 395536
TAC
QT Input Channel

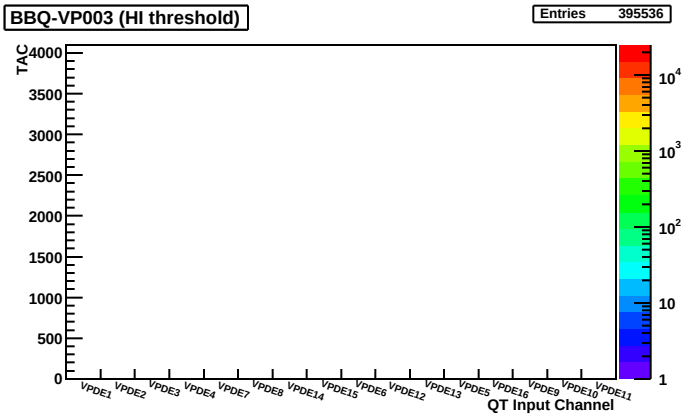

BBQ-VP001 (LO threshold)
Entries 395536
ADC
QT Input Channel

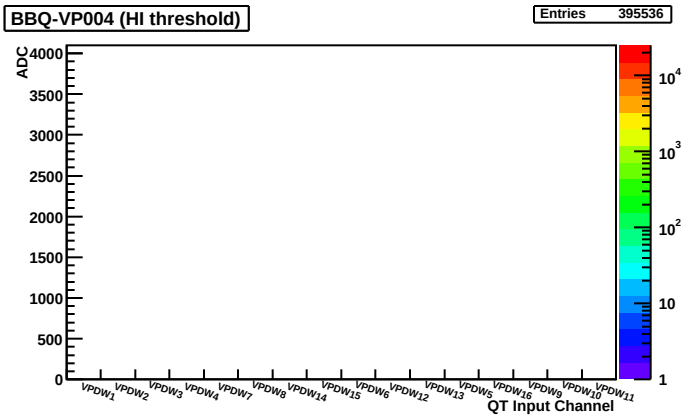

BBQ-VP001 (LO threshold)
Entries 395536
TAC
QT Input Channel

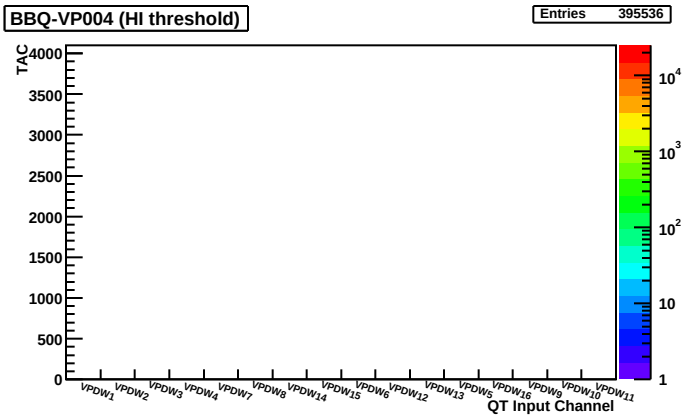

BBQ-VP002 (LO threshold)
Entries 395536
ADC
QT Input Channel

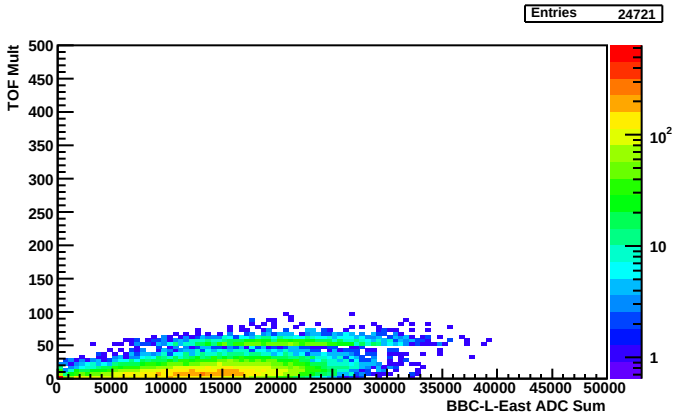

BBQ-VP002 (LO threshold)
Entries 395536
TAC
QT Input Channel

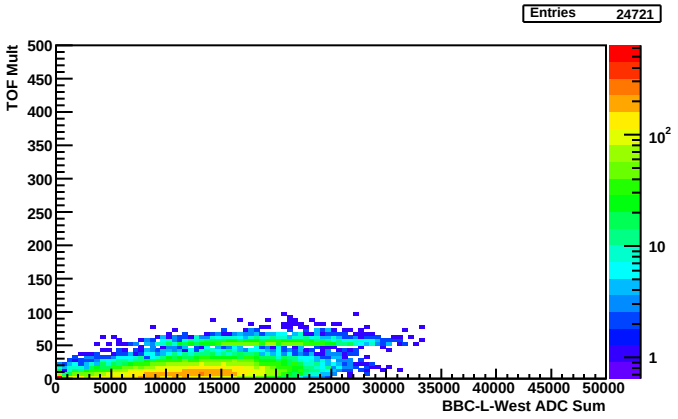

BBQ-VP003 (HI threshold)
Entries 395536
ADC
QT Input Channel

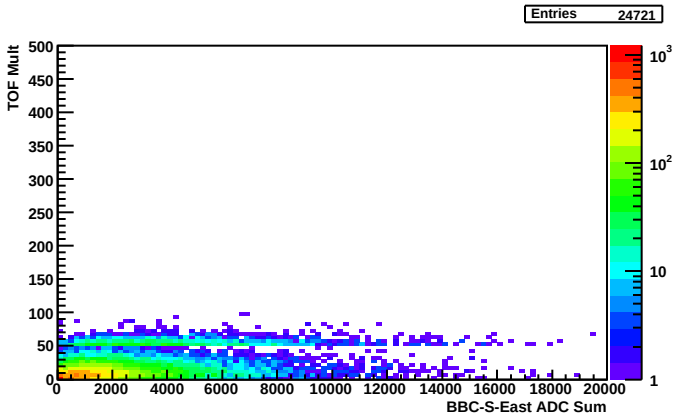

BBQ-VP003 (HI threshold)
Entries 395536
TAC
QT Input Channel

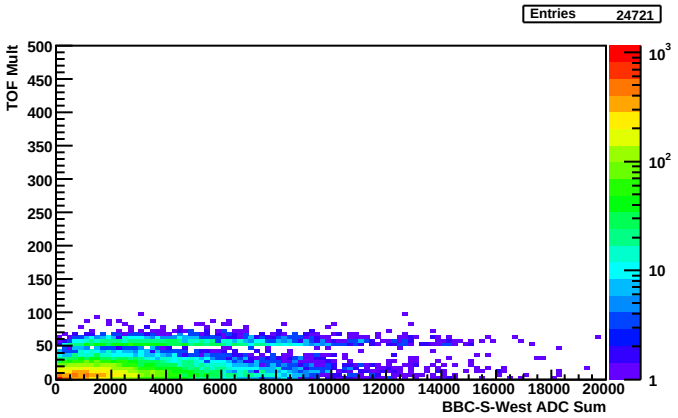

BBQ-VP004 (HI threshold)
Entries 395536
ADC
QT Input Channel

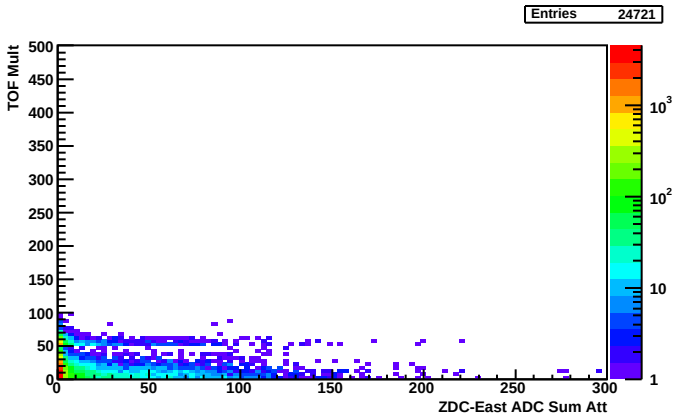

BBQ-VP004 (HI threshold)
Entries 395536
TAC
QT Input Channel

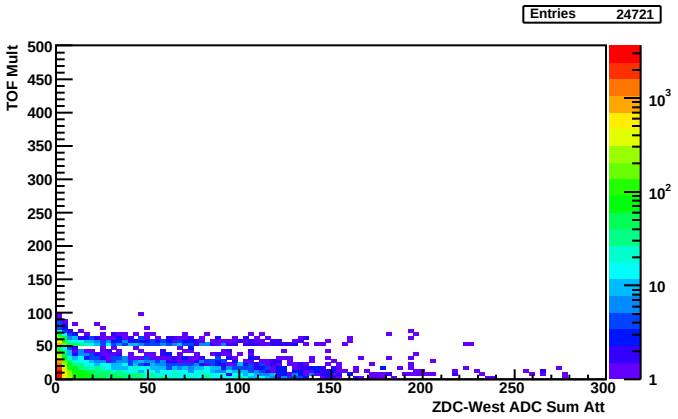

Entries 24721
TOF Mult
BBC-L-East ADC Sum

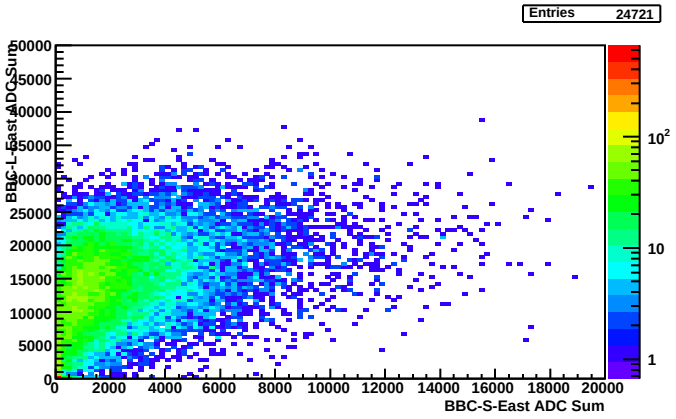

Entries 24721
TOF Mult
BBC-L-West ADC Sum

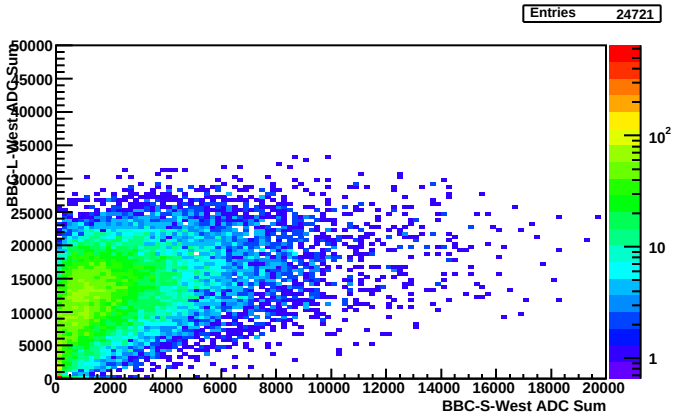

Entries 24721
TOF Mult
BBC-S-East ADC Sum

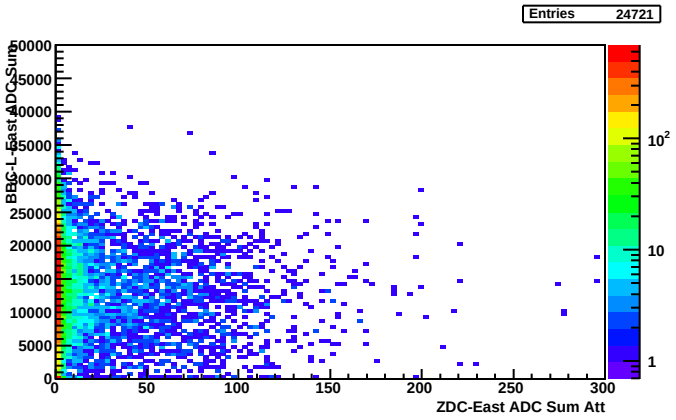

Entries 24721
TOF Mult
BBC-S-West ADC Sum

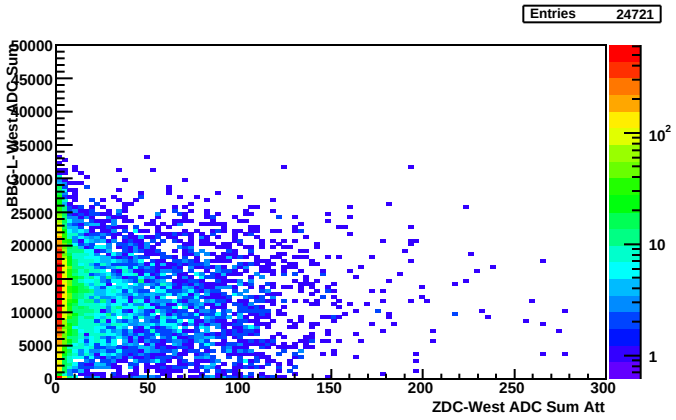

Entries 24721
TOF Mult
ZDC-East ADC Sum Att

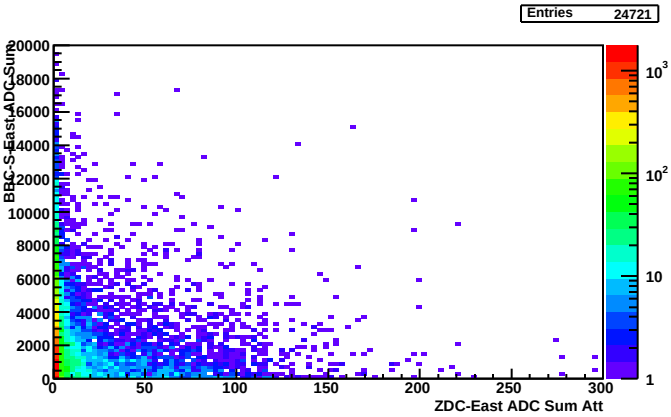

Entries 24721
TOF Mult
ZDC-West ADC Sum Att

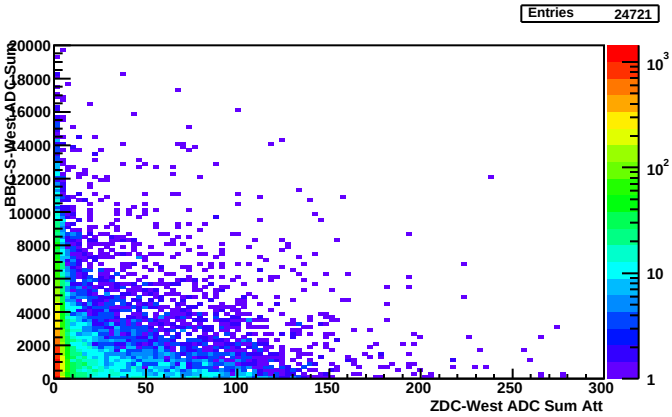

Entries 24721
BBC-L-East ADC Sum
BBC-S-East ADC Sum

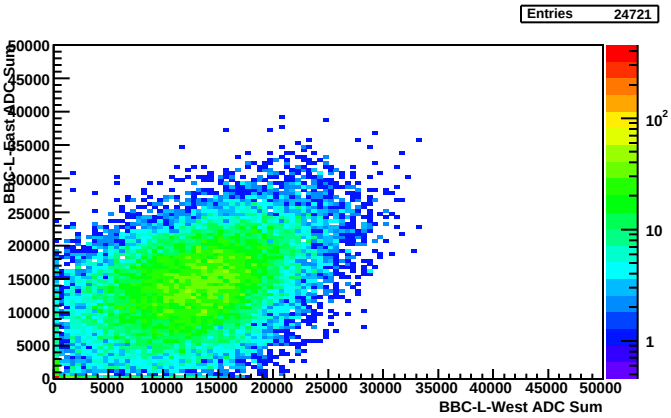

Entries 24721
BBC-L-West ADC Sum
BBC-S-West ADC Sum

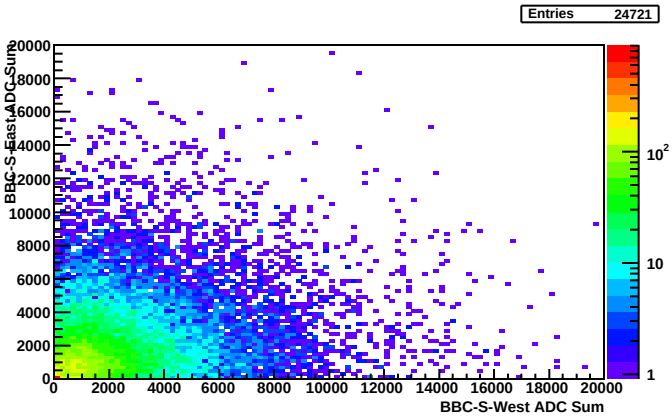

Entries 24721
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

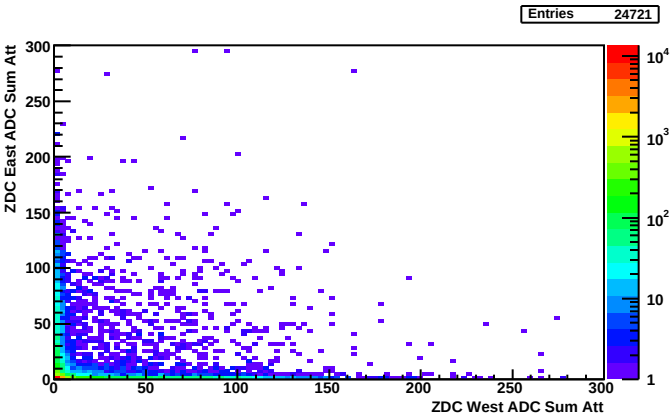

Entries 24721
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

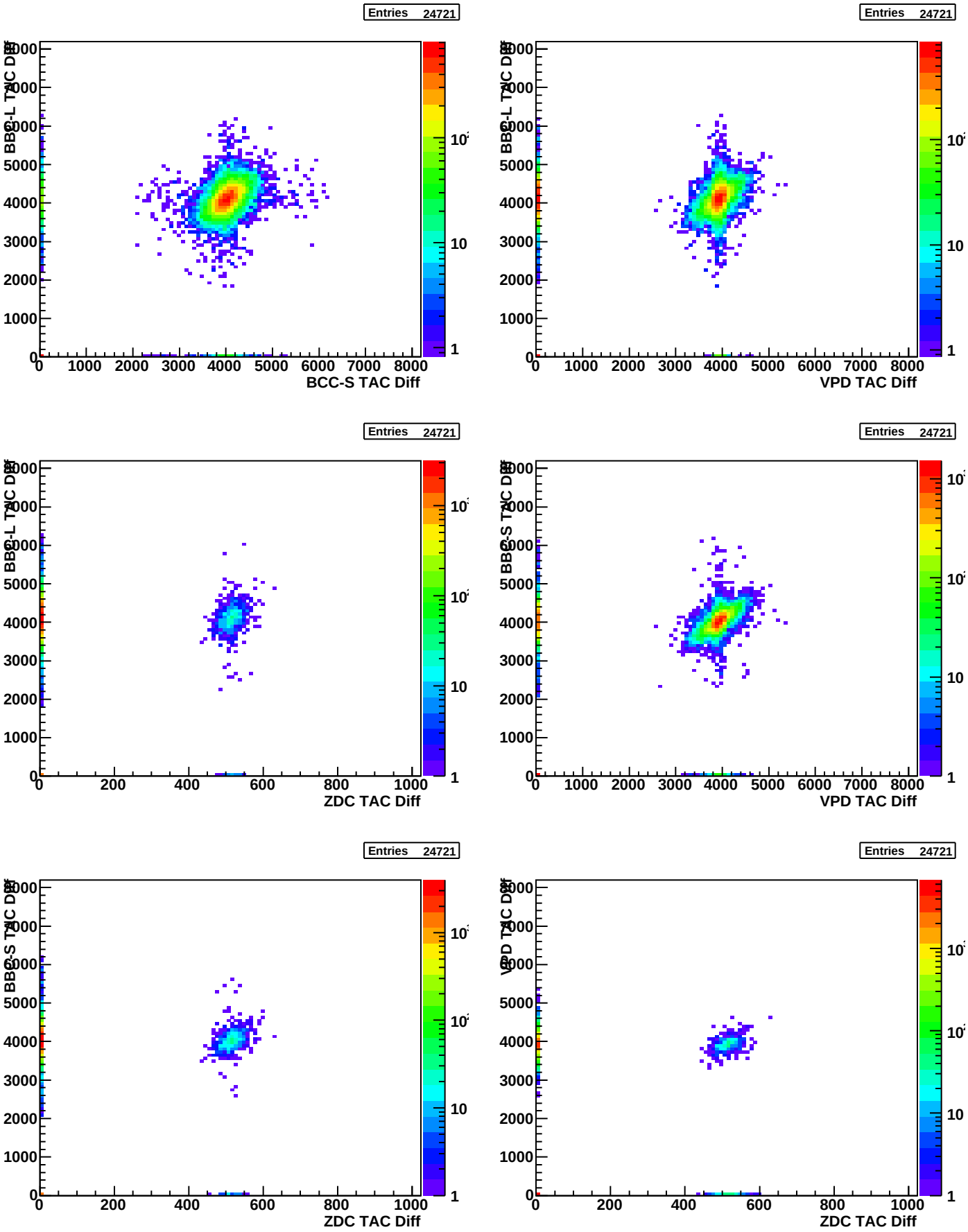

Entries 24721
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

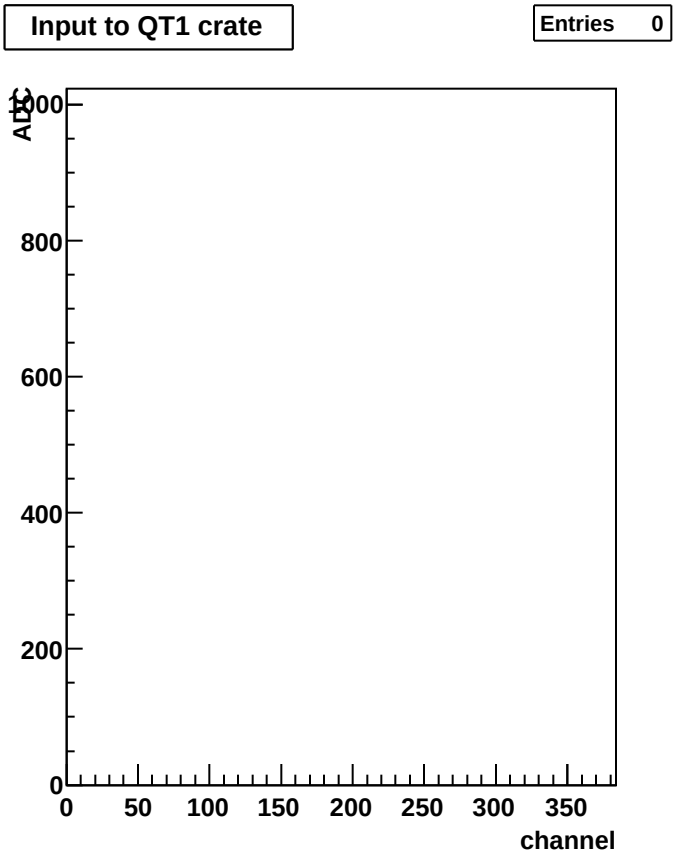

Entries 24721
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

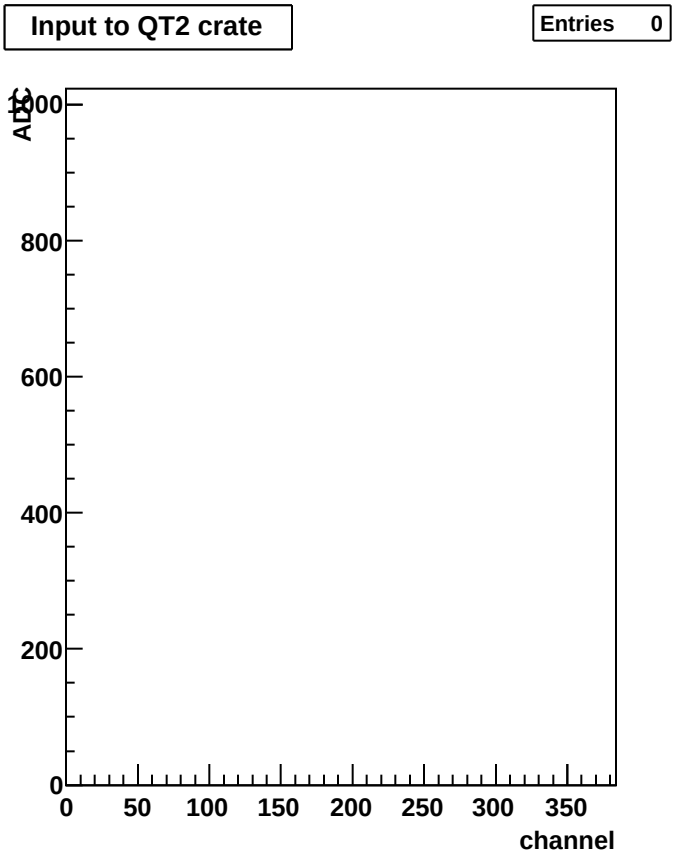

Entries 24721
BBC-L-East ADC Sum
BBC-L-West ADC Sum

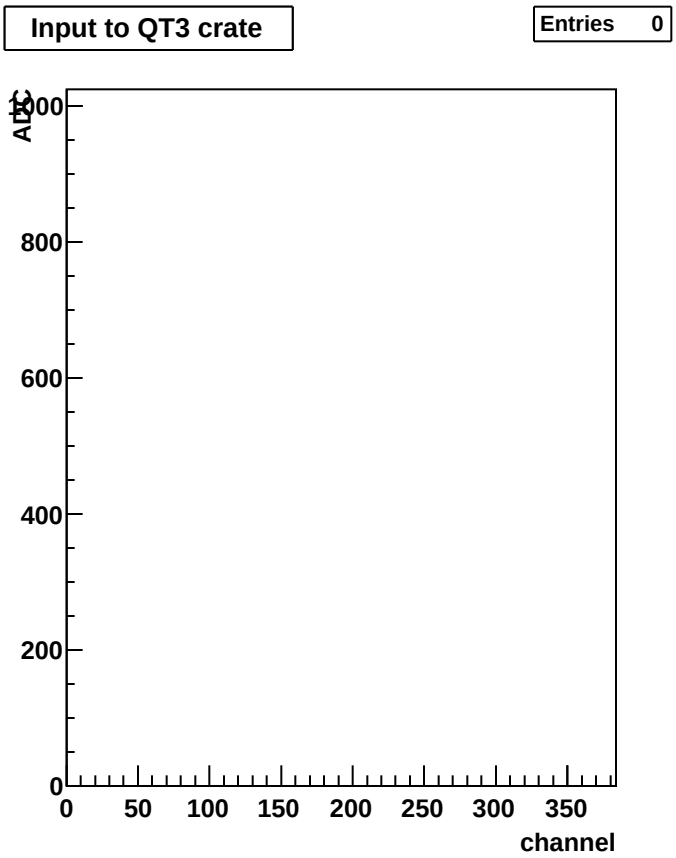

Entries 24721
BBC-S-East ADC Sum
BBC-S-West ADC Sum

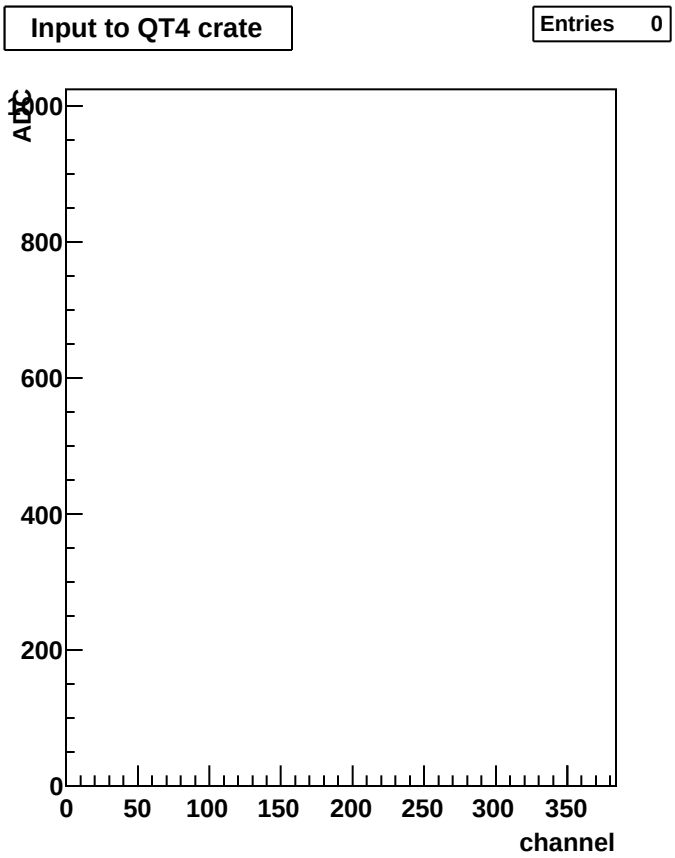

Entries 24721
ZDC East ADC Sum Att
ZDC West ADC Sum Att

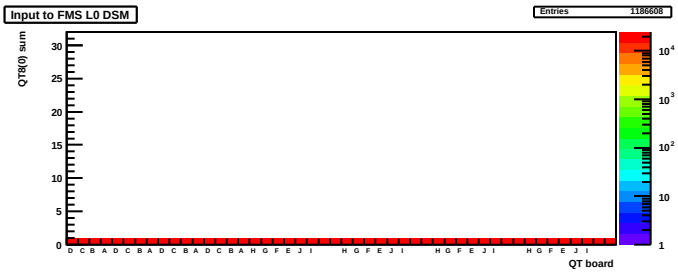

Entries 24721
BBC-L TAC Diff
BCC-S TAC Diff
Entries 24721
BBC-L TAC Diff
VPD TAC Diff
Entries 24721
BBC-L TAC Diff
ZDC TAC Diff
Entries 24721
BBC-S TAC Diff
VPD TAC Diff
Entries 24721
BBC-S TAC Diff
ZDC TAC Diff
Entries 24721
VPD TAC Diff
ZDC TAC Diff

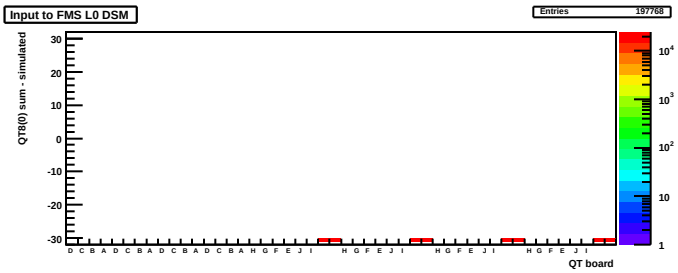

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

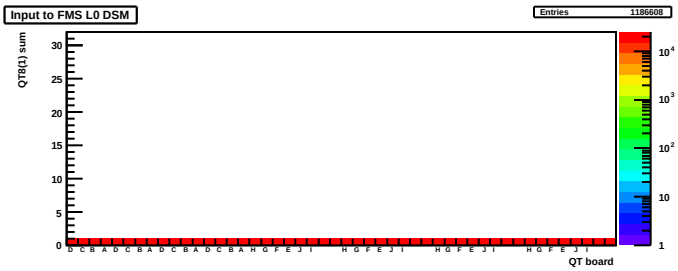

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

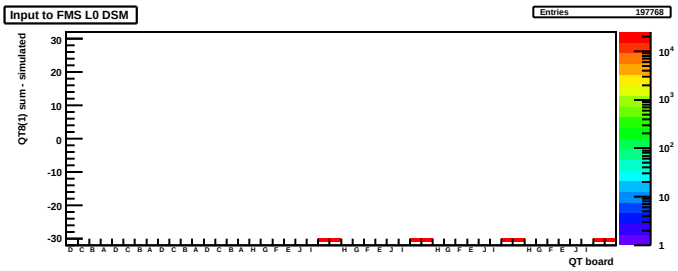

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

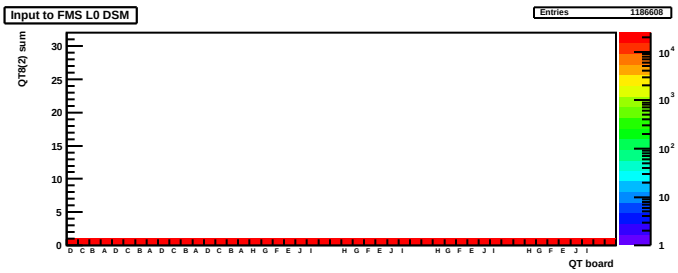

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

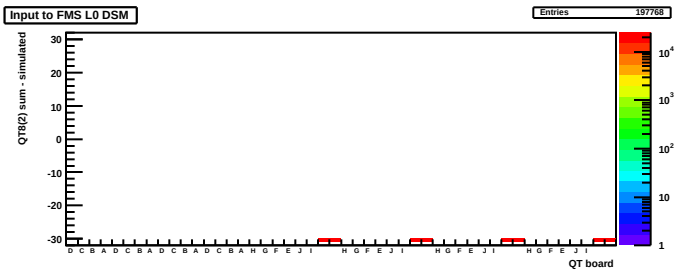

Input to FMS L0 DSM
Entries 1186608
QT8(0) sum
QT board

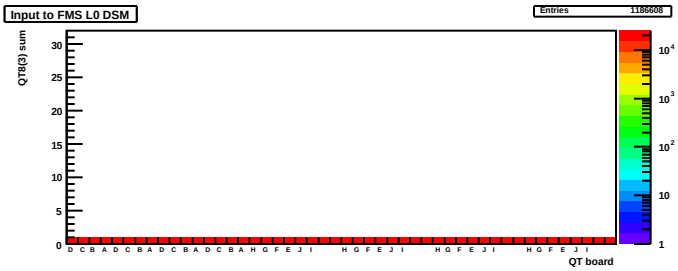

Input to FMS L0 DSM
Entries 197768
QT8(0) sum - simulated
QT board

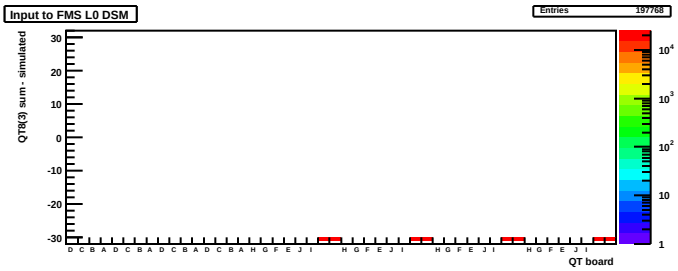

Input to FMS L0 DSM
Entries 1186608
QT8(1) sum
QT board

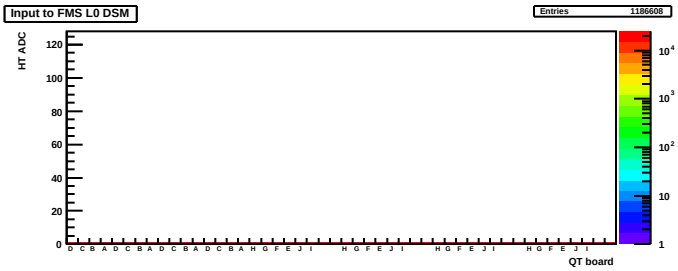

Input to FMS L0 DSM
Entries 197768
QT8(1) sum - simulated
QT board

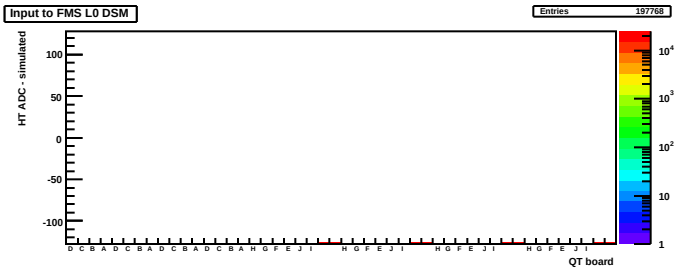

Input to FMS L0 DSM
Entries 1186608
QT8(2) sum
QT board

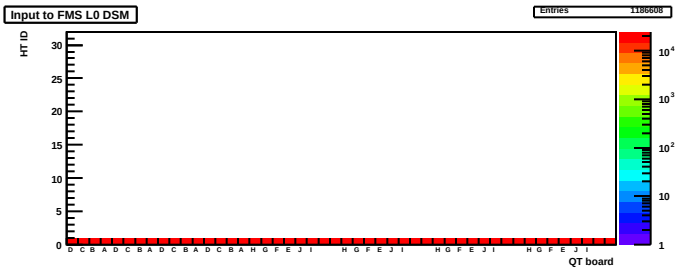

Input to FMS L0 DSM
Entries 197768
QT8(2) sum - simulated
QT board

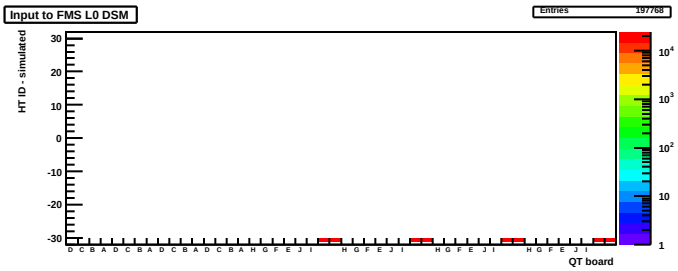

Input to FMS L0 DSM
Entries 1186608
QT8(3) sum
QT board

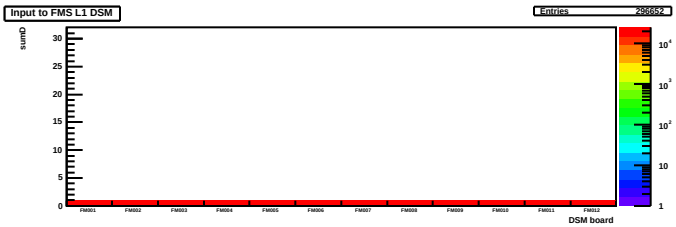

Input to FMS L0 DSM
Entries 197768
QT8(3) sum - simulated
QT board

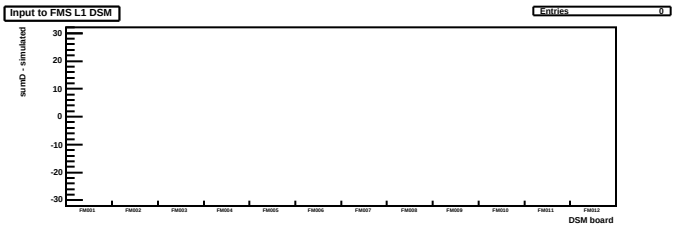

Input to FMS L0 DSM
Entries 1186608
HT ADC
QT board

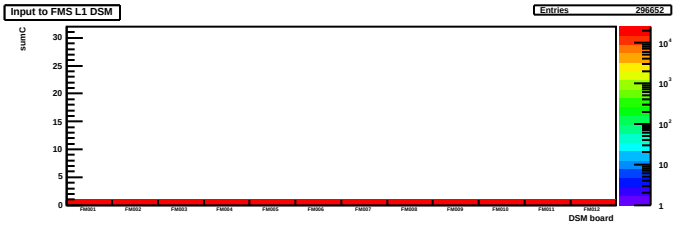

Input to FMS L0 DSM
Entries 197768
HT ADC - simulated
QT board

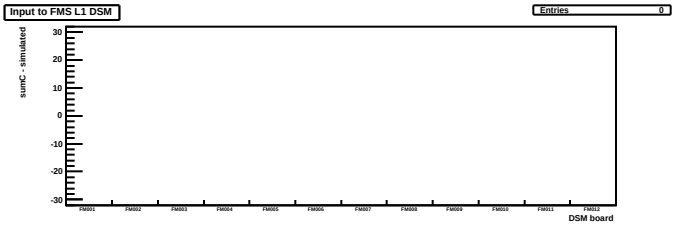

Input to FMS L0 DSM
Entries 1186608
HT ID
QT board

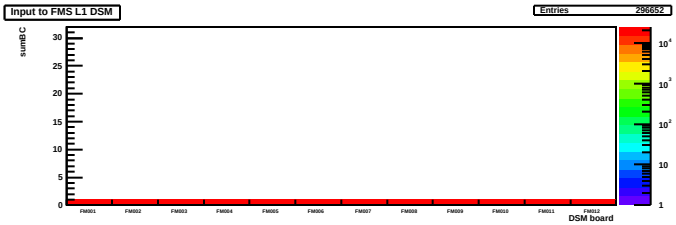

Input to FMS L0 DSM
Entries 197768
HT ID - simulated
QT board

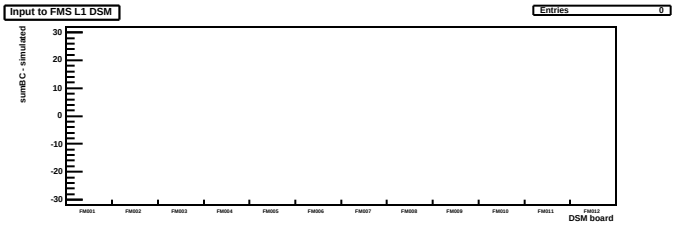

Input to FMS L1 DSM
Entries 296652
sumD
DSM board

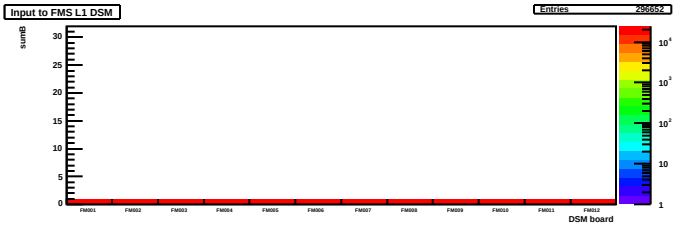

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

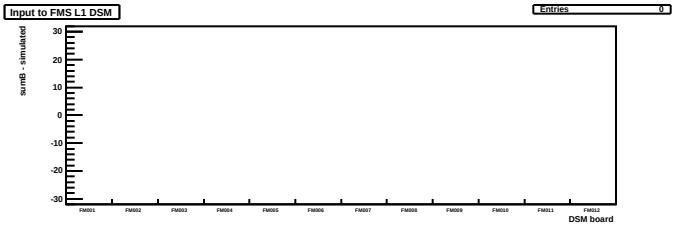

Input to FMS L1 DSM
Entries 296652
sumC
DSM board

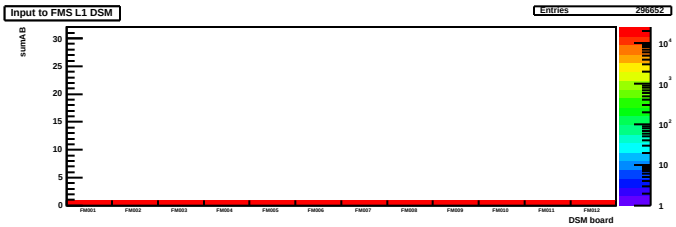

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

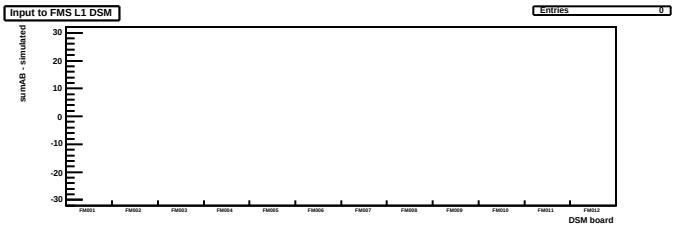

Input to FMS L1 DSM
Entries 296652
sumBC
DSM board

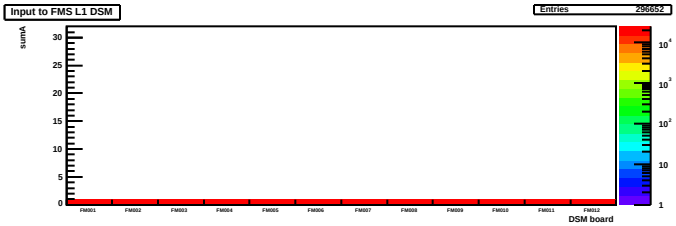

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

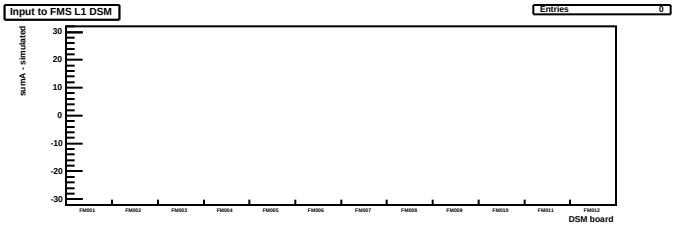

Input to FMS L1 DSM
Entries 296652
sumB
DSM board

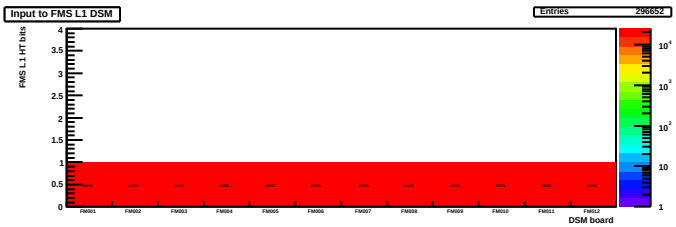

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

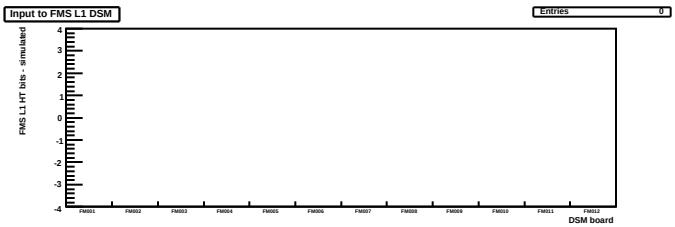

Input to FMS L1 DSM
Entries 296652
sumAB
DSM board

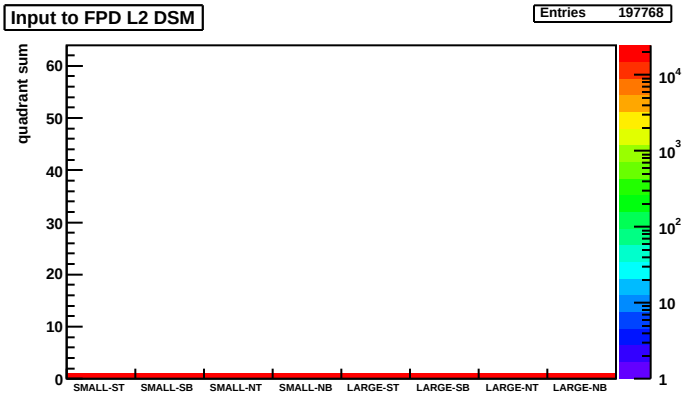

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

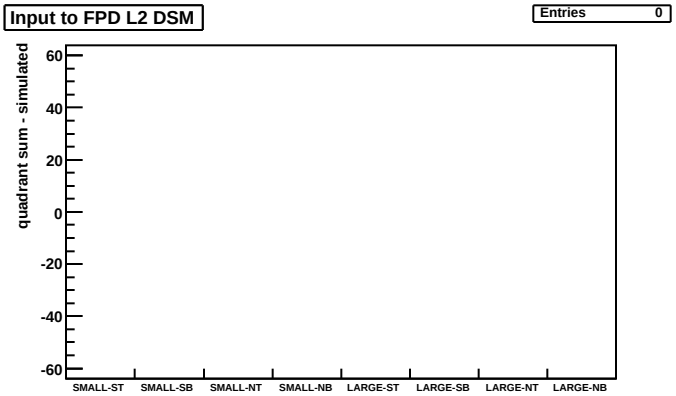

Input to FMS L1 DSM
Entries 296652
sumA
DSM board

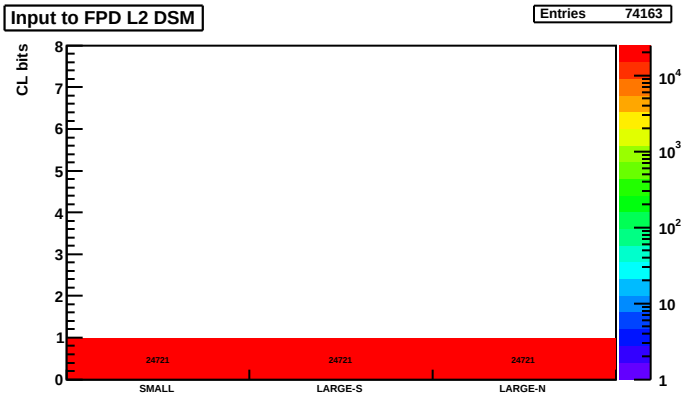

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

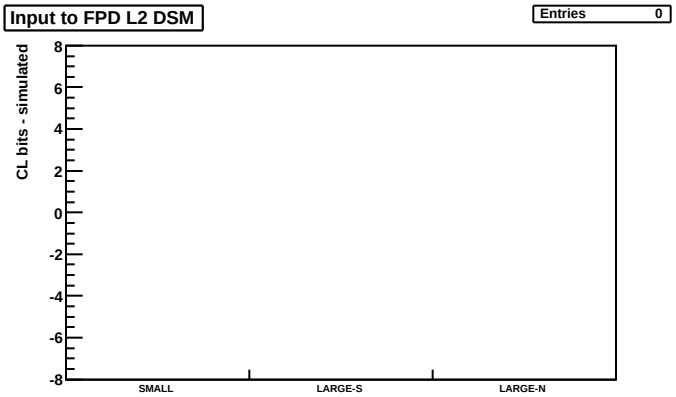

Input to FMS L1 DSM
Entries 296652
FMS L1 HT bits
DSM board

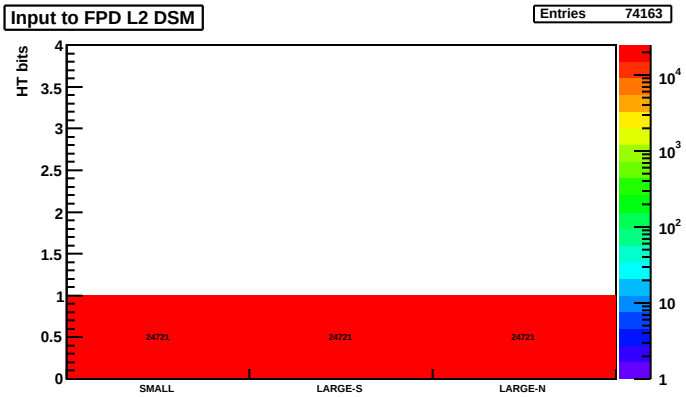

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

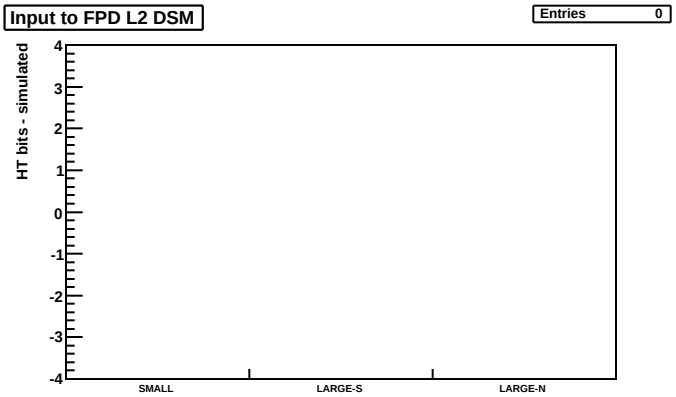

Input to FPD L2 DSM
Entries 197768
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

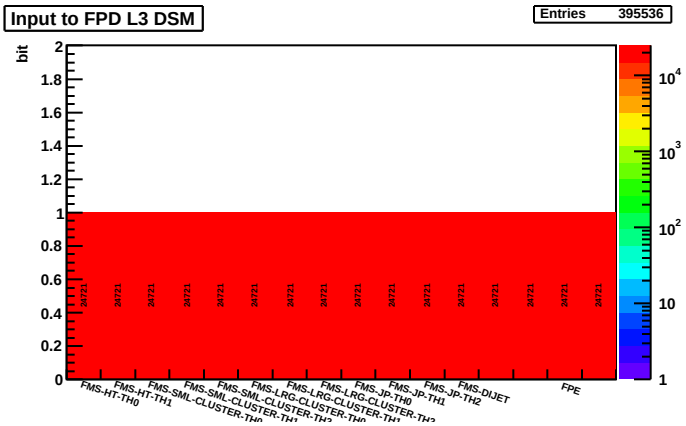

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

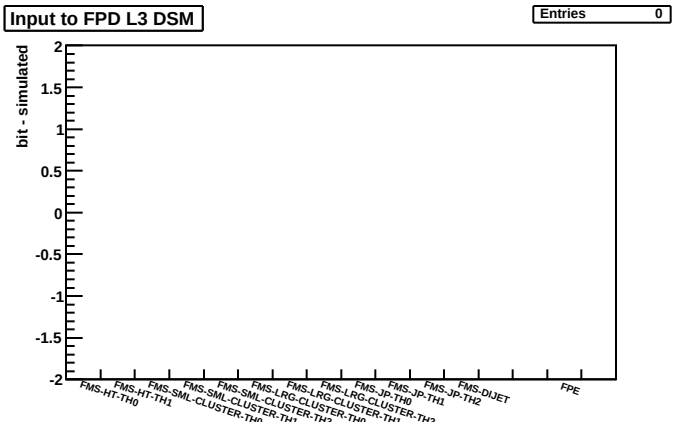

Input to FPD L2 DSM
Entries 74163
CL bits
24721
24721
24721
SMALL
LARGE-S
LARGE-N

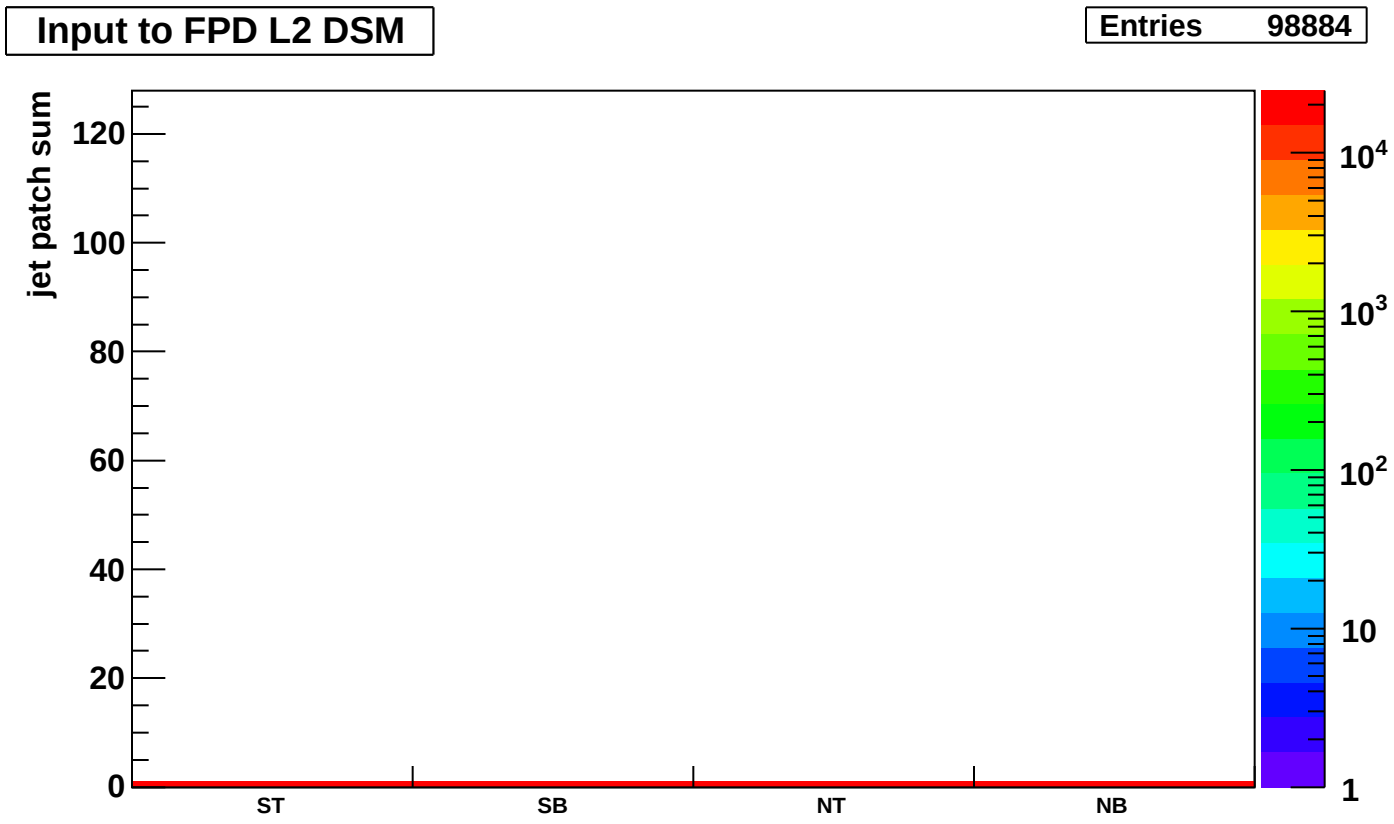

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

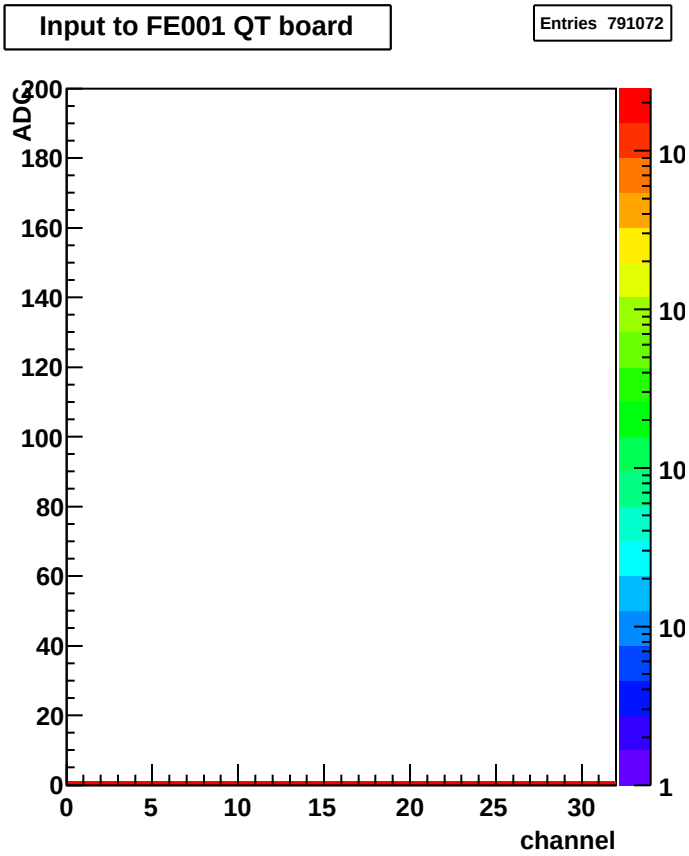

Input to FPD L2 DSM
Entries 74163
HT bits
24721
24721
24721
SMALL
LARGE-S
LARGE-N

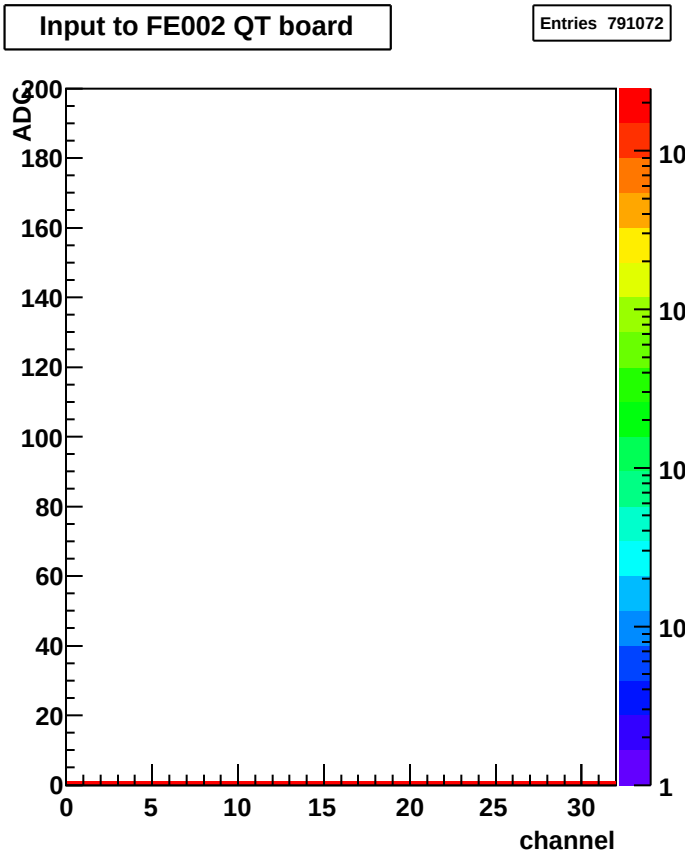

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

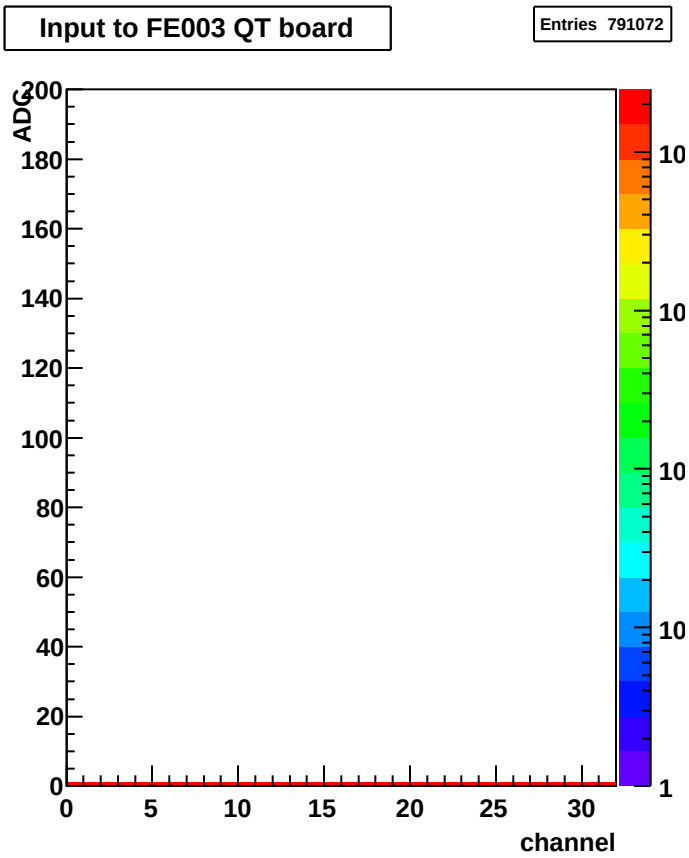

Input to FPD L3 DSM
Entries 395536
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

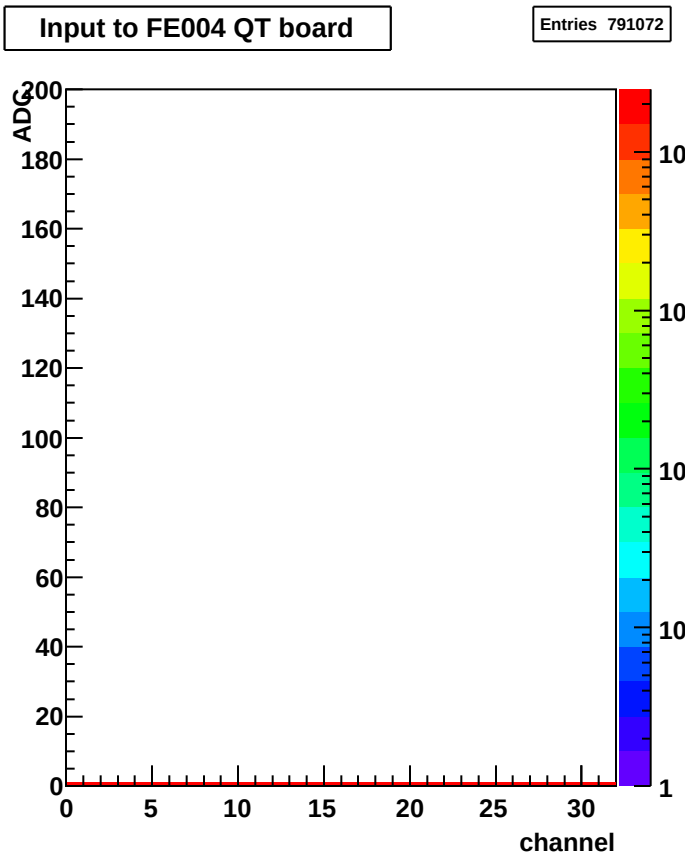

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

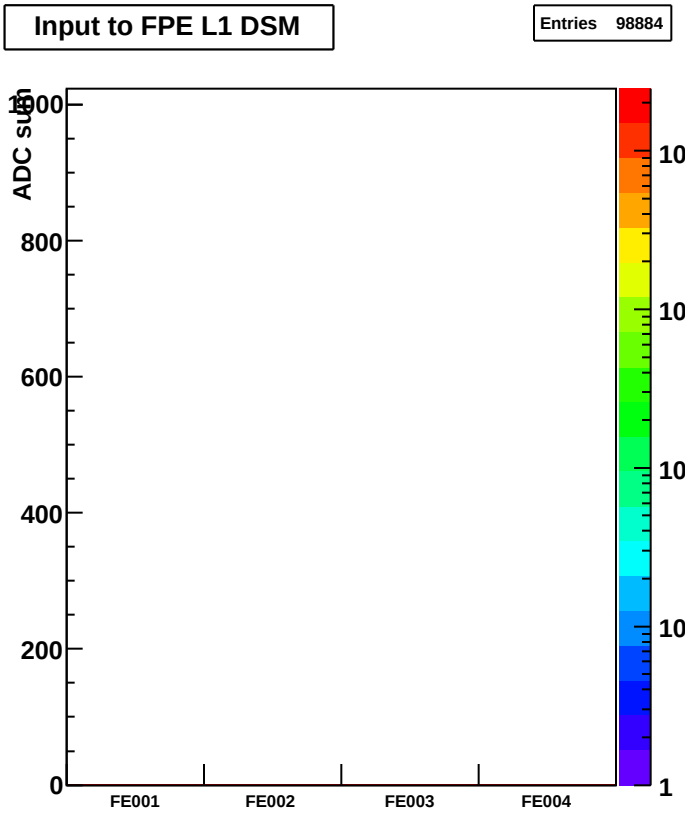

Input to FPD L2 DSM
Entries 98884
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^4
10^3
10^2
10
1

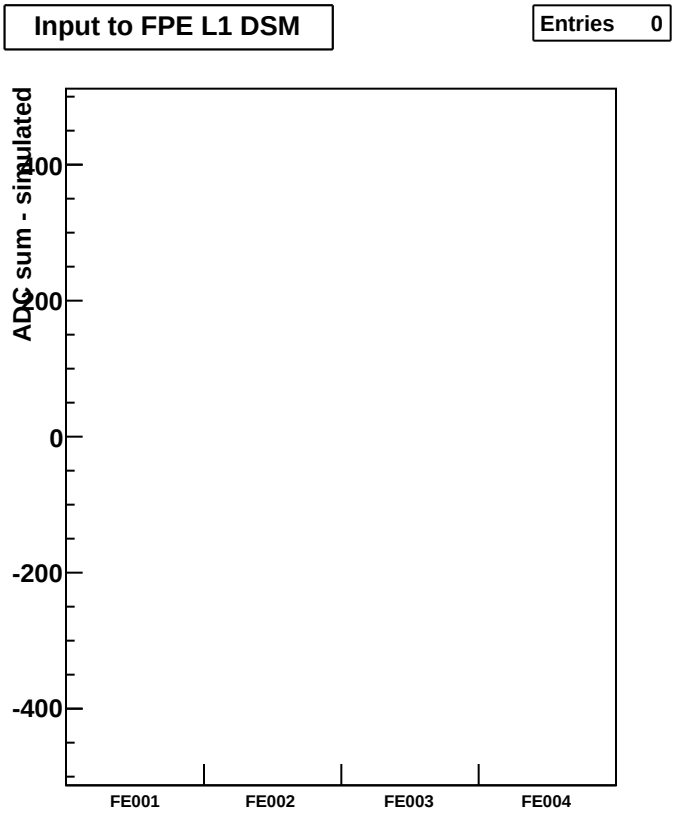

Input to FE001 QT board
Entries 791072
ADC
channel

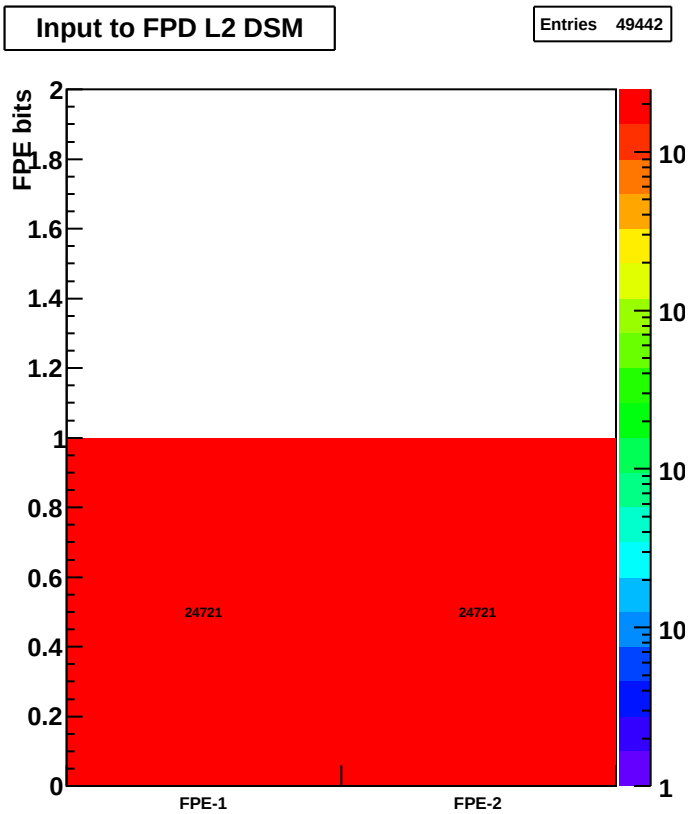

Input to FE002 QT board
Entries 791072
ADC
channel

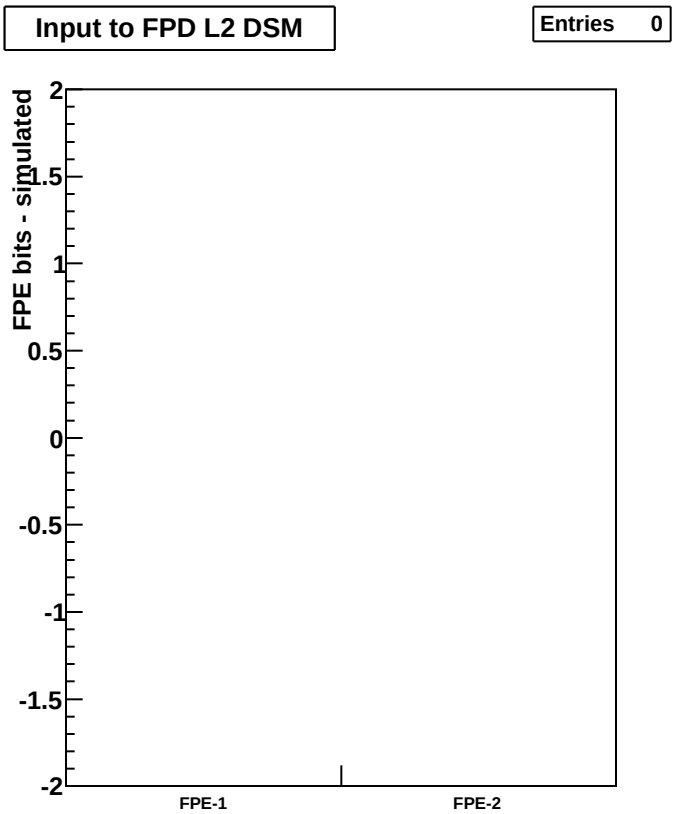

Input to FE003 QT board
Entries 791072
ADC
channel


Input to FE004 QT board
Entries 791072
ADC
channel


Input to FPE L1 DSM
Entries 98884
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries 49442
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
24721
24721
FPE-1
FPE-2
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2