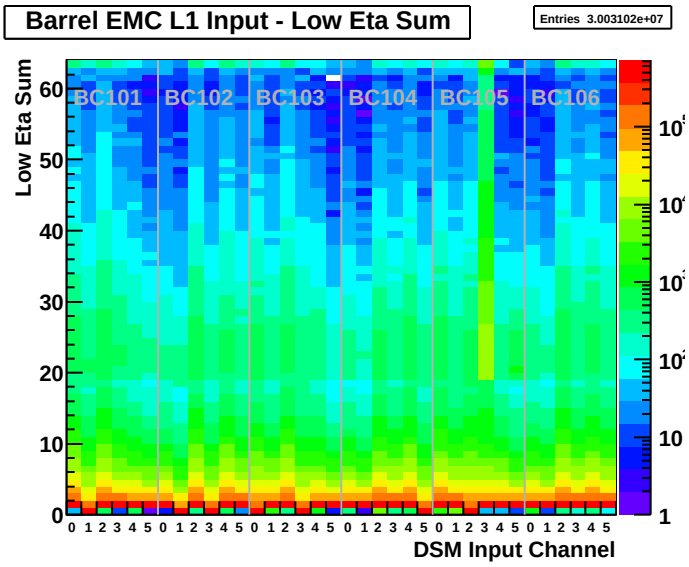
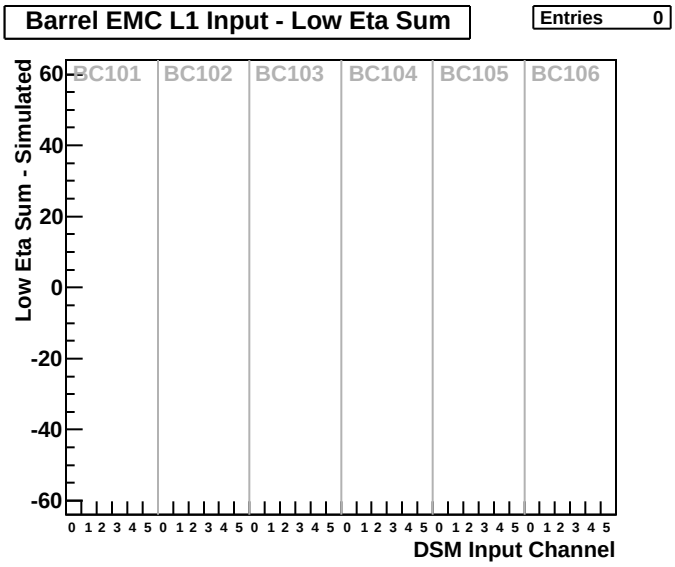
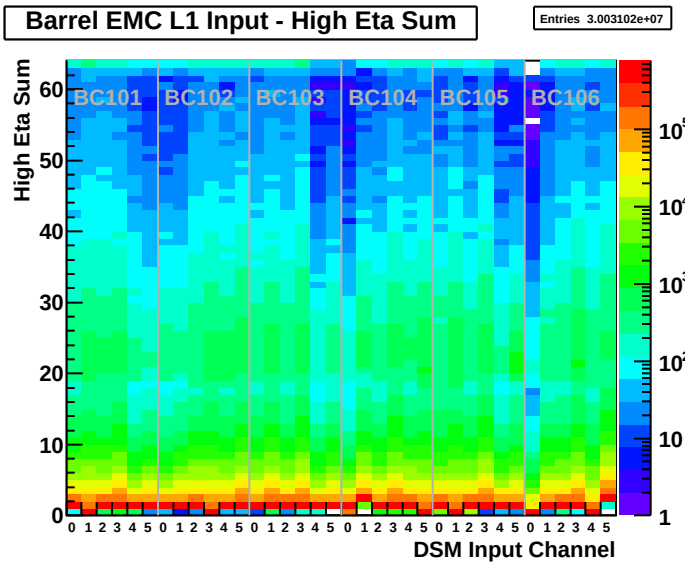
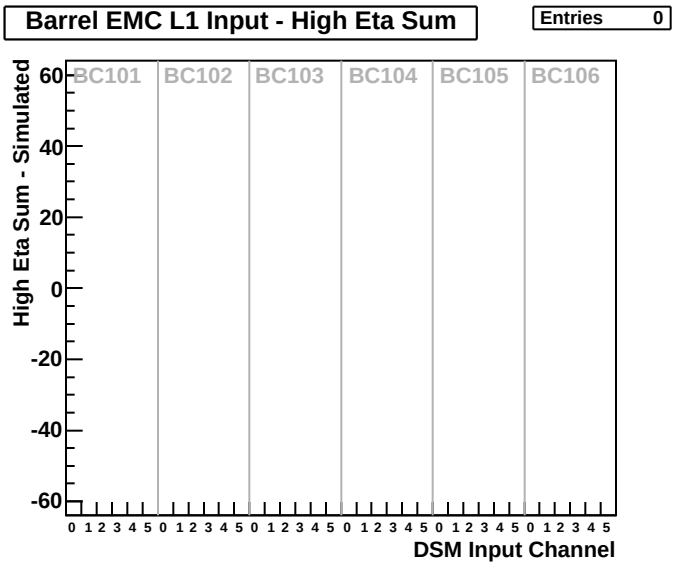
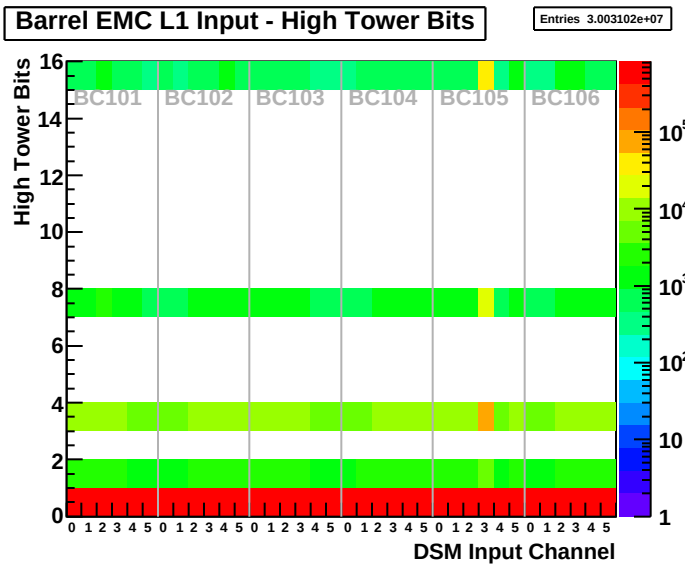
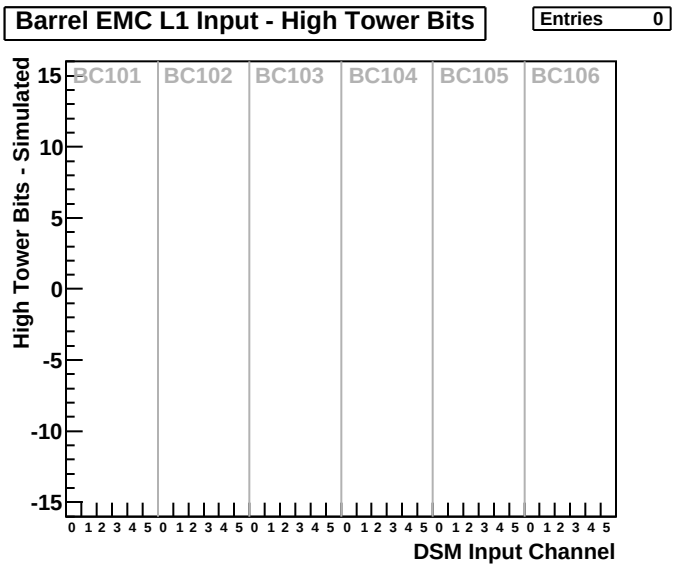

Barrel EMC L1 Input - Low Eta Sum
Entries 3.003102e+07
Low Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

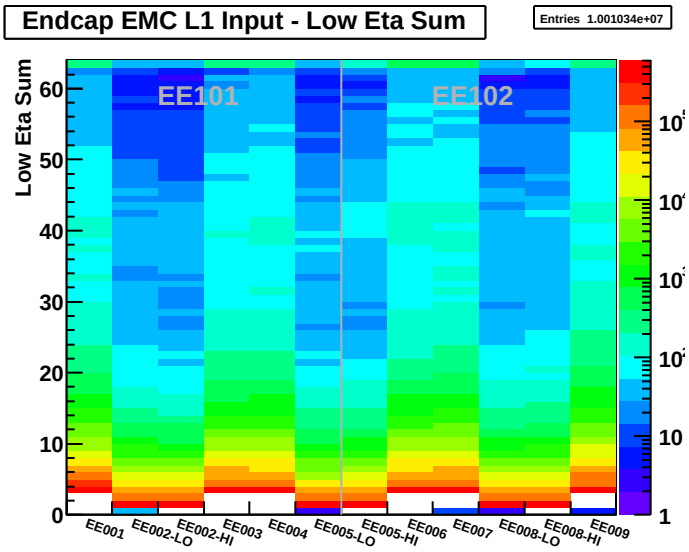

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

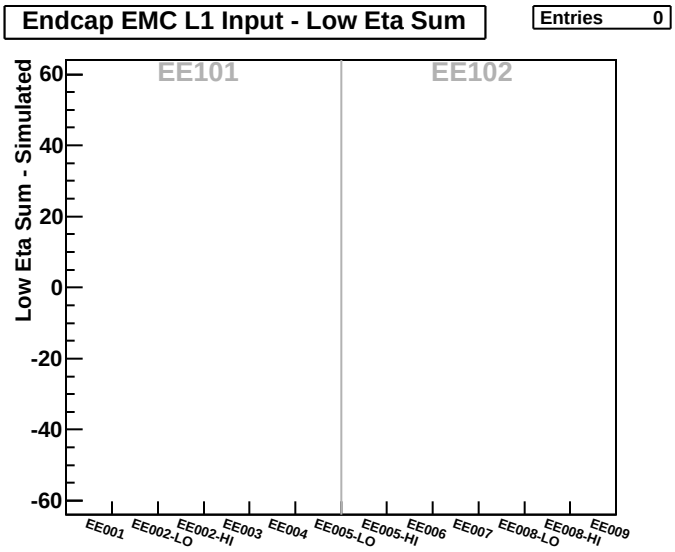

Barrel EMC L1 Input - High Eta Sum
Entries 3.003102e+07
High Eta Sum
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

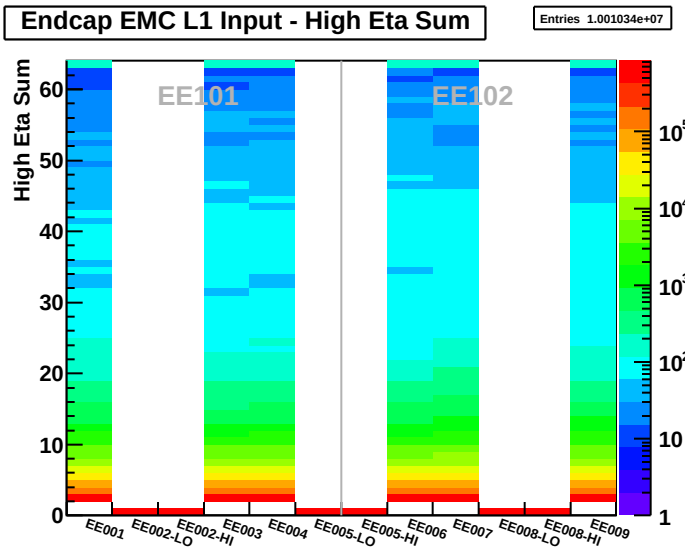

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

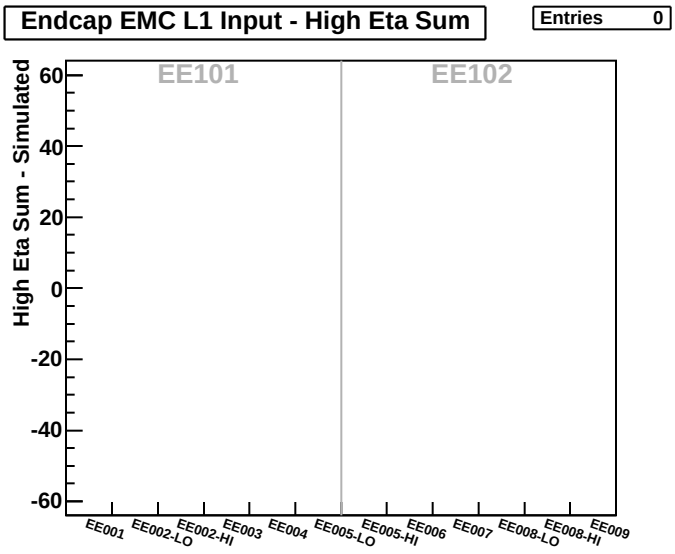

Barrel EMC L1 Input - High Tower Bits
Entries 3.003102e+07
High Tower Bits
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

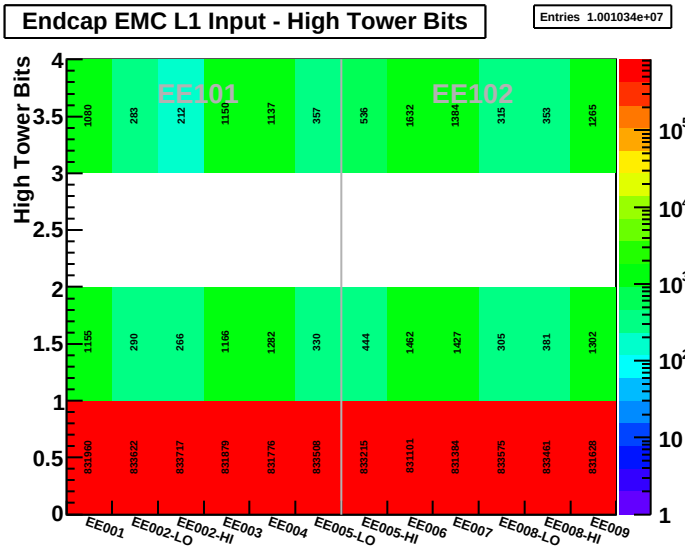

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101 BC102 BC103 BC104 BC105 BC106
DSM Input Channel

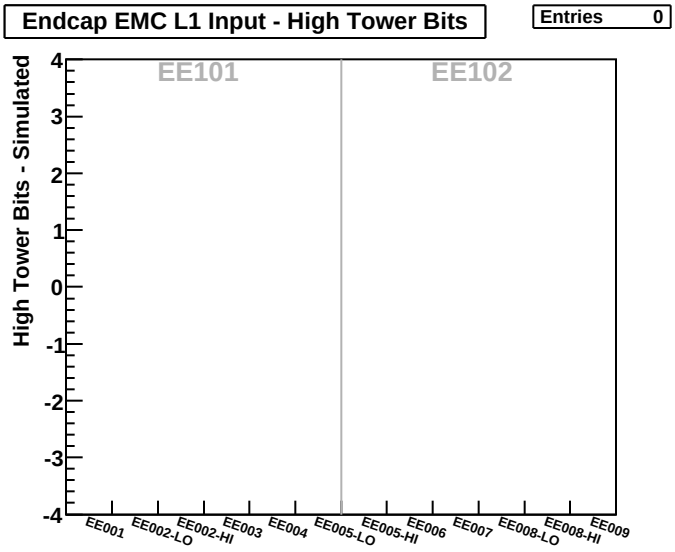

Endcap EMC L1 Input - Low Eta Sum
Entries 1.001034e+07
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

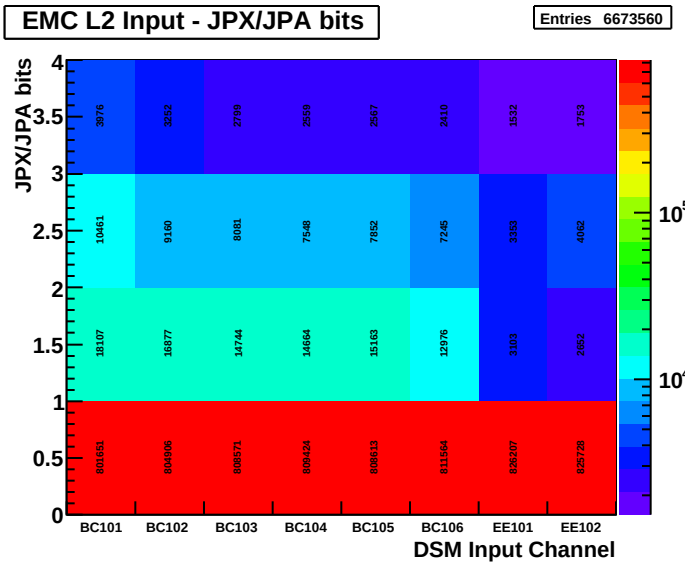

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

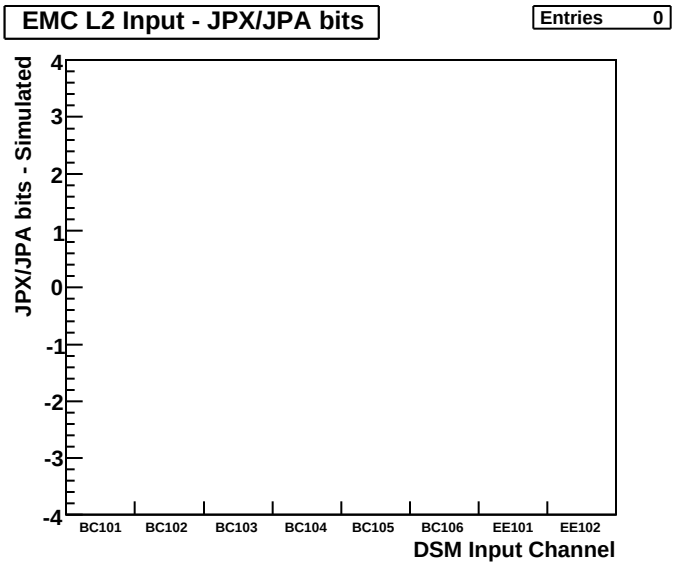

Endcap EMC L1 Input - High Eta Sum
Entries 1.001034e+07
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

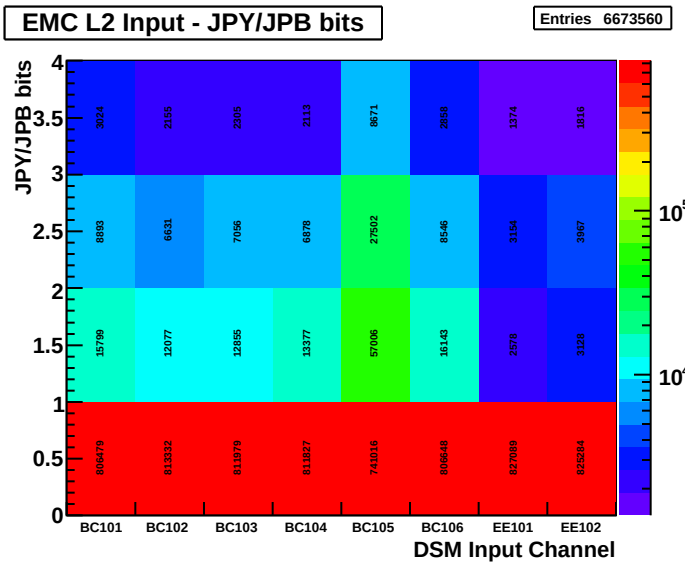

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

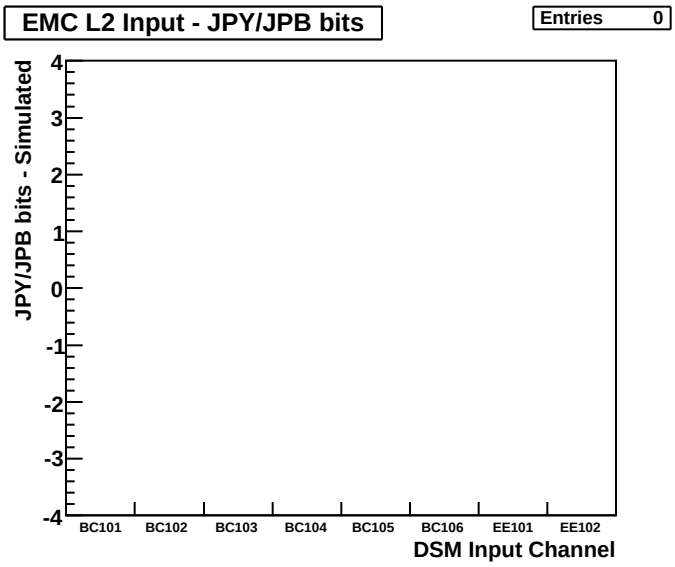

Endcap EMC L1 Input - High Tower Bits
Entries 1.001034e+07
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

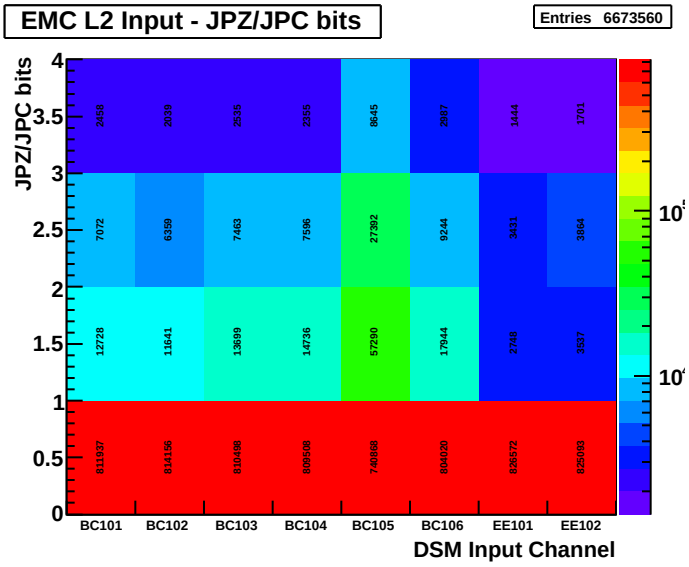

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

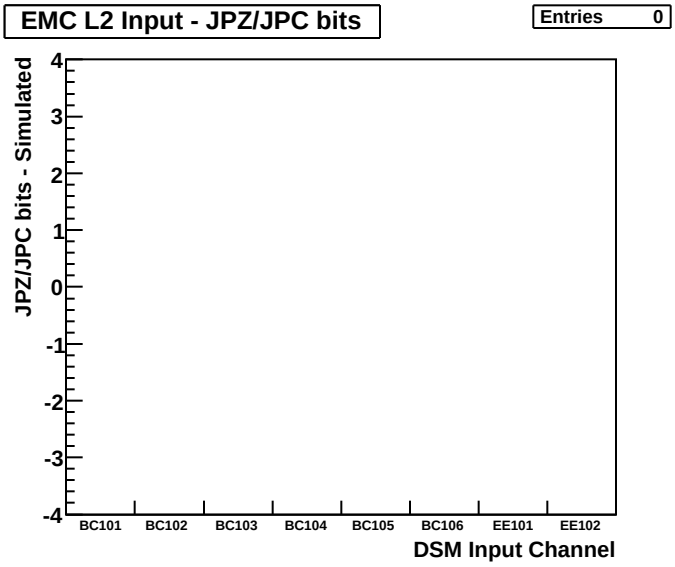

EMC L2 Input - JPX/JPA bits
Entries 6673560
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3976 3262 2799 2559 2567 2410 1532 1753
10461 9160 8081 7548 7852 7245 3353 4062
18107 16877 14744 14664 15163 12976 3103 2652
801651 804906 808571 809424 808613 811564 825207 825728

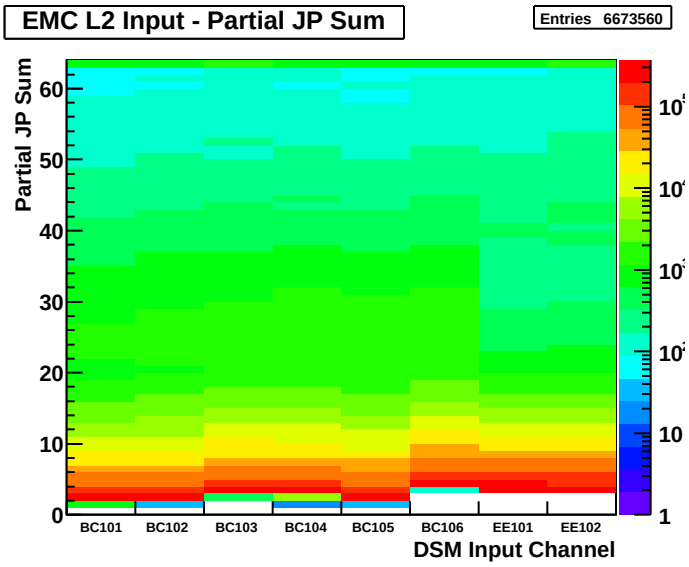

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

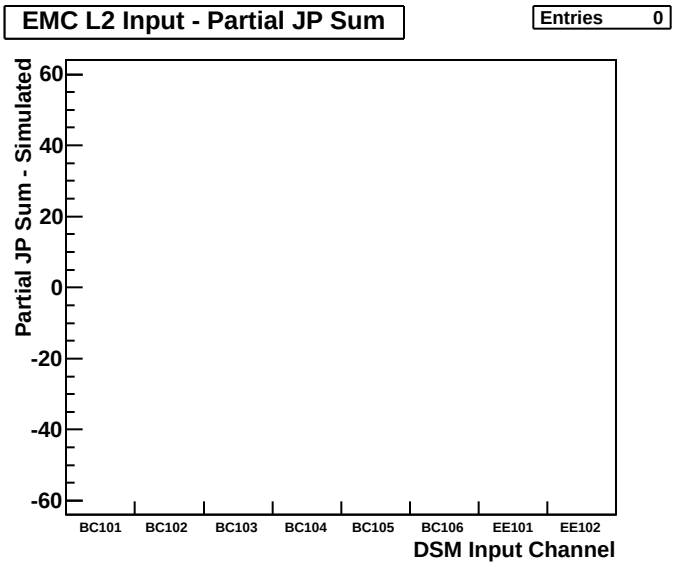

EMC L2 Input - JPY/JPB bits
Entries 6673560
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
3024 2155 2205 2113 8671 2858 1374 1316
8893 6631 7056 6878 27502 8546 3154 3967
15799 12077 12855 13377 57006 16143 2576 3128
806479 813332 811979 811827 743016 808648 827089 825284

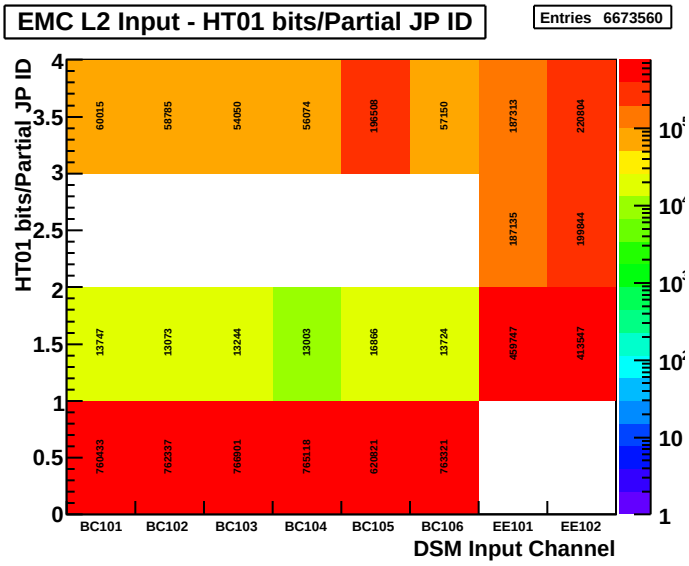

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

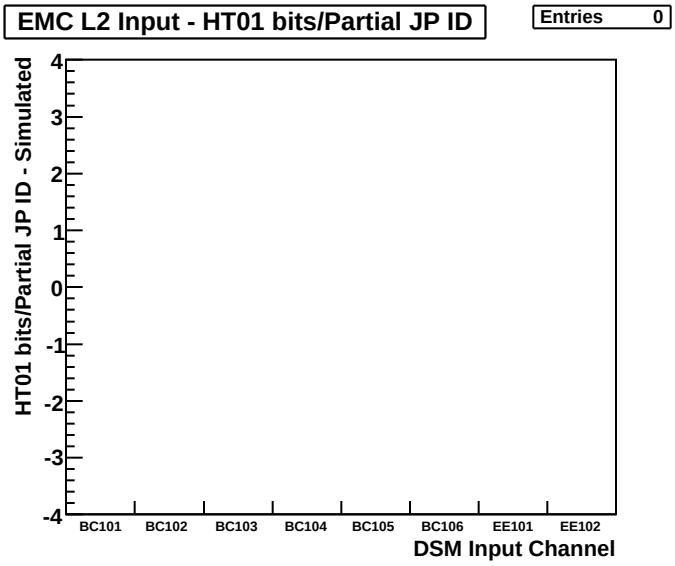

EMC L2 Input - JPZ/JPC bits
Entries 6673560
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
2458 2039 2535 2355 8645 2987 1444 1701
7072 6359 7463 7596 27392 9244 3431 3864
12728 11641 13699 14736 57290 17944 2748 3537
811937 814156 810498 809508 740868 804020 826572 825093

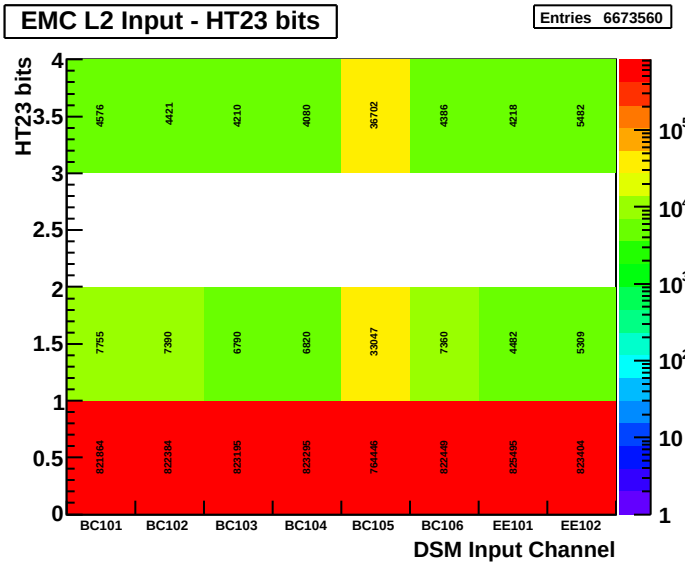

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

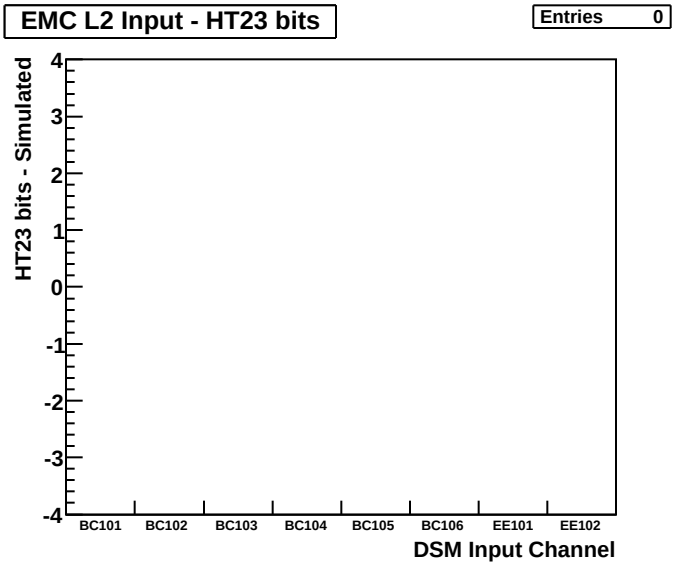

EMC L2 Input - Partial JP Sum
Entries 6673560
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

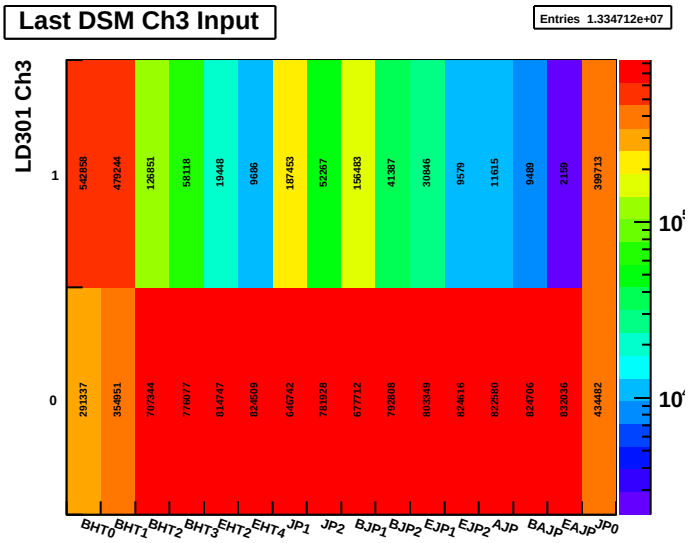

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

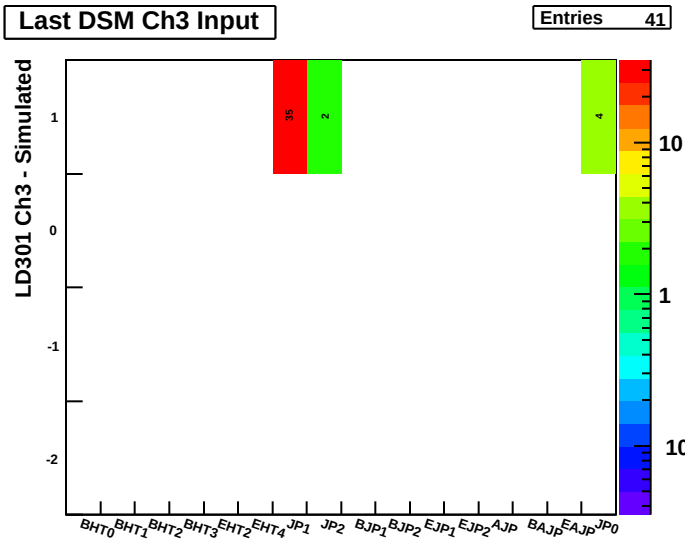

EMC L2 Input - HT01 bits/Partial JP ID
Entries 6673560
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

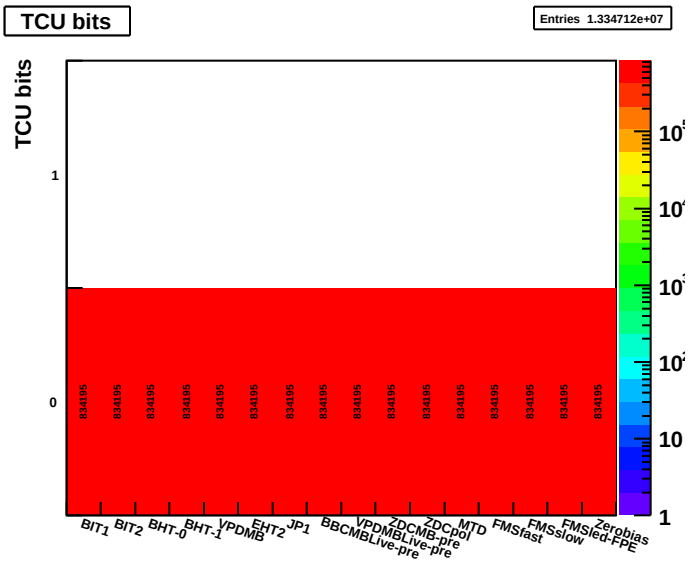

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

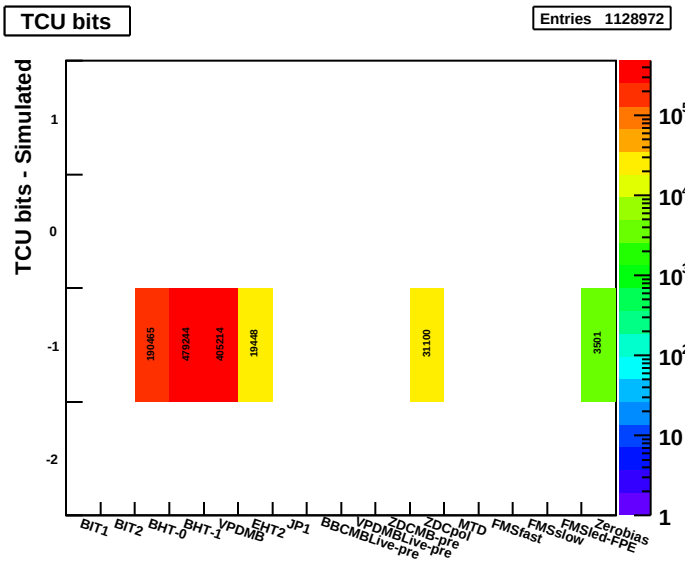

EMC L2 Input - HT23 bits
Entries 6673560
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

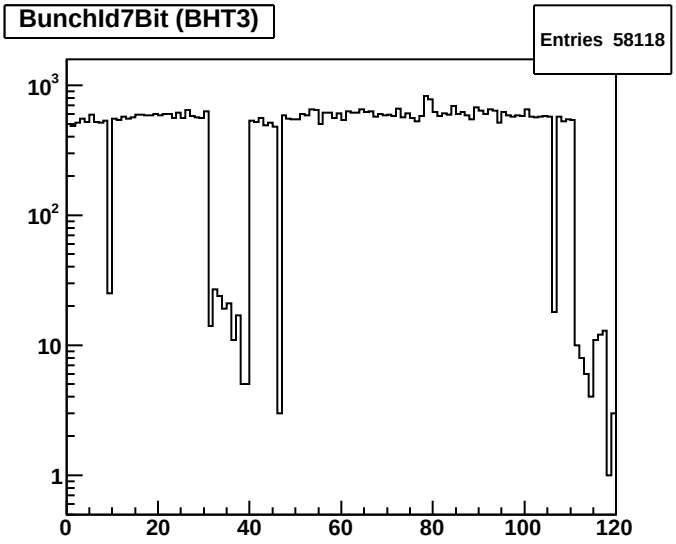

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

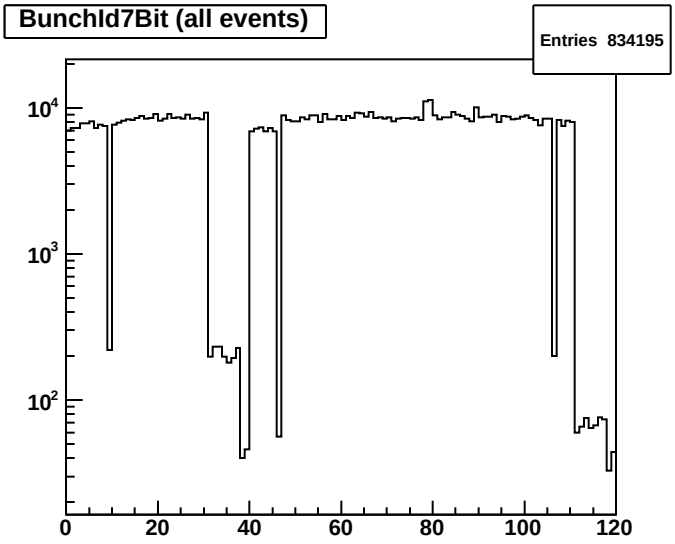

Last DSM Ch3 Input
Entries 1.334712e+07
LD301 Ch3
542858 479244 126851 58118 19448 9686 107453 52267 156483 41387 30846 9579 11615 9489 2159 399713
291337 354951 707344 776077 814747 824509 646742 781928 677712 792808 803349 824616 822580 824706 832036 434482
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

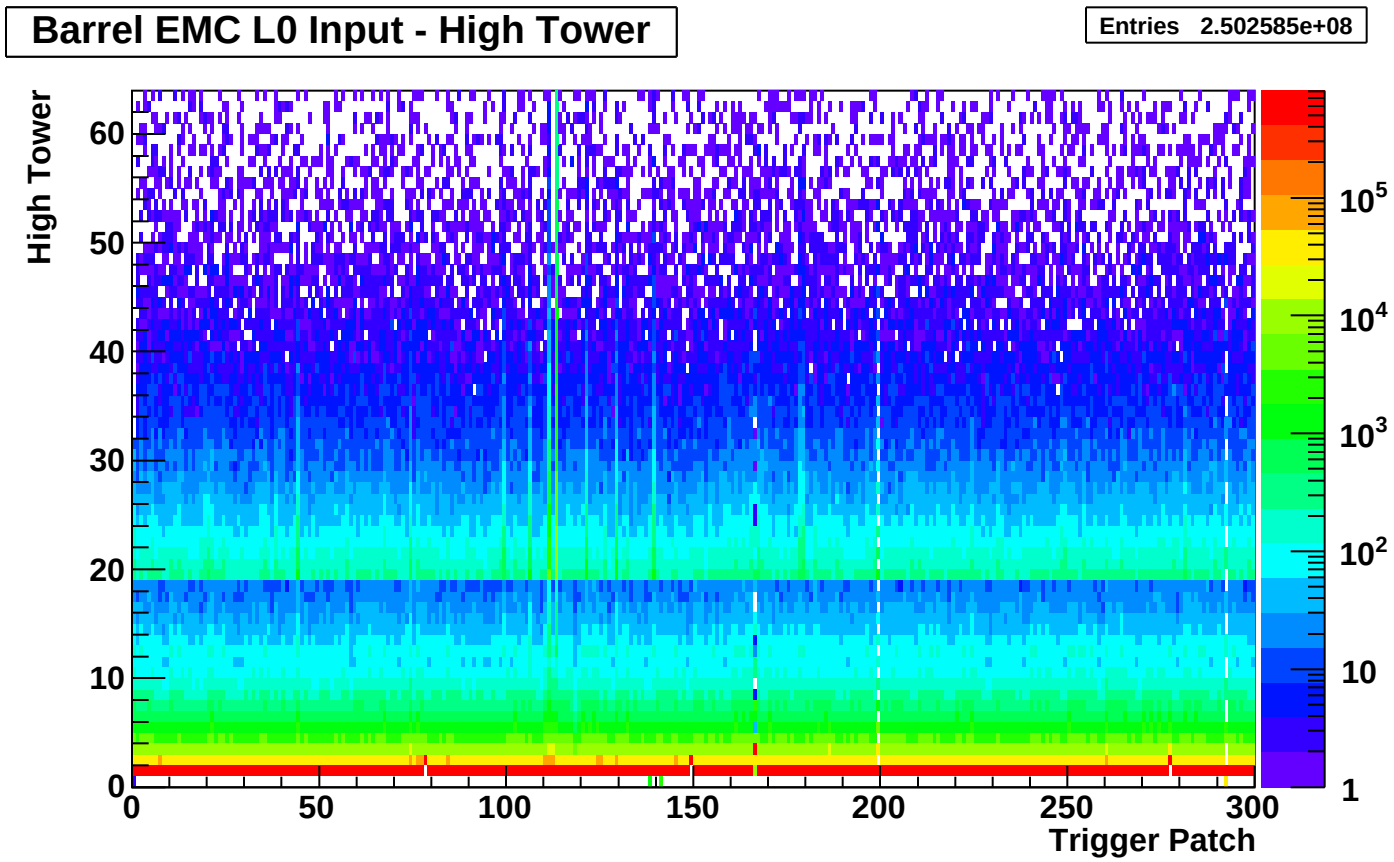

Last DSM Ch3 Input
Entries 41
LD301 Ch3 - Simulated
35 2 4
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

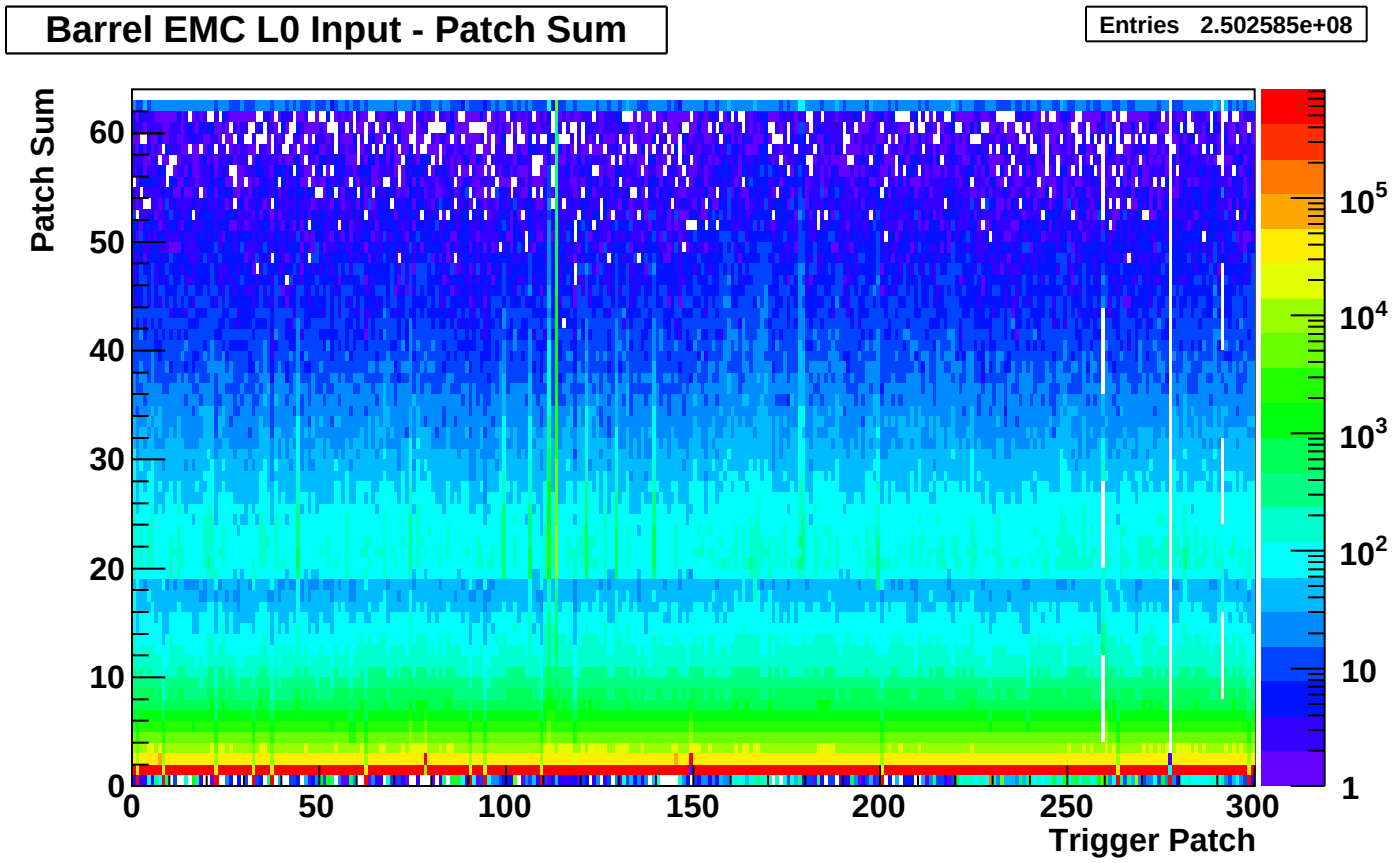

TCU bits
Entries 1.334712e+07
TCU bits
834195 834195 834195 834195 834195 834195 834195 834195 834195 834195 834195 834195 834195 834195 834195 834195
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

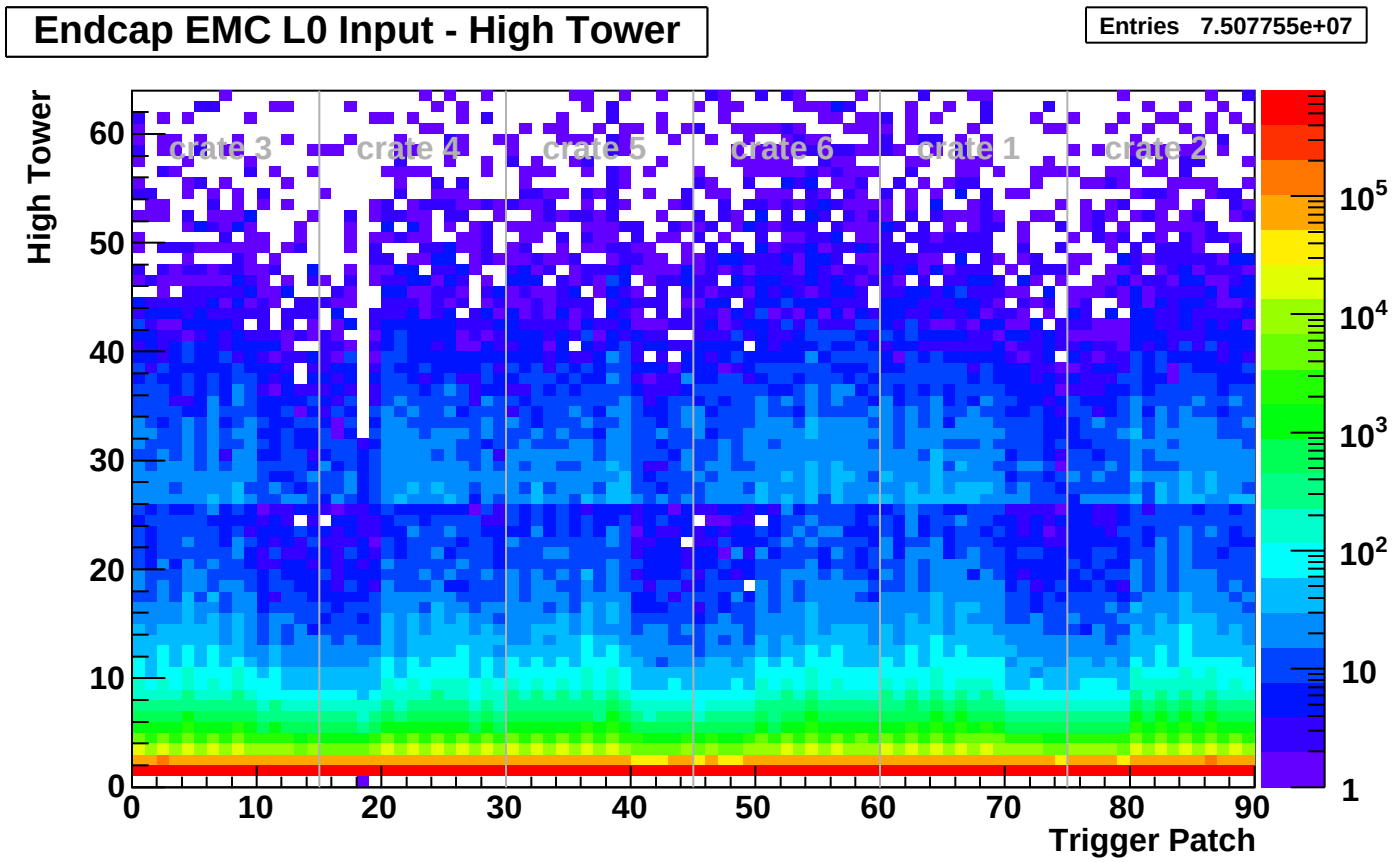

TCU bits
Entries 1128972
TCU bits - Simulated
190465 479244 405214 19448 31100 3501
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

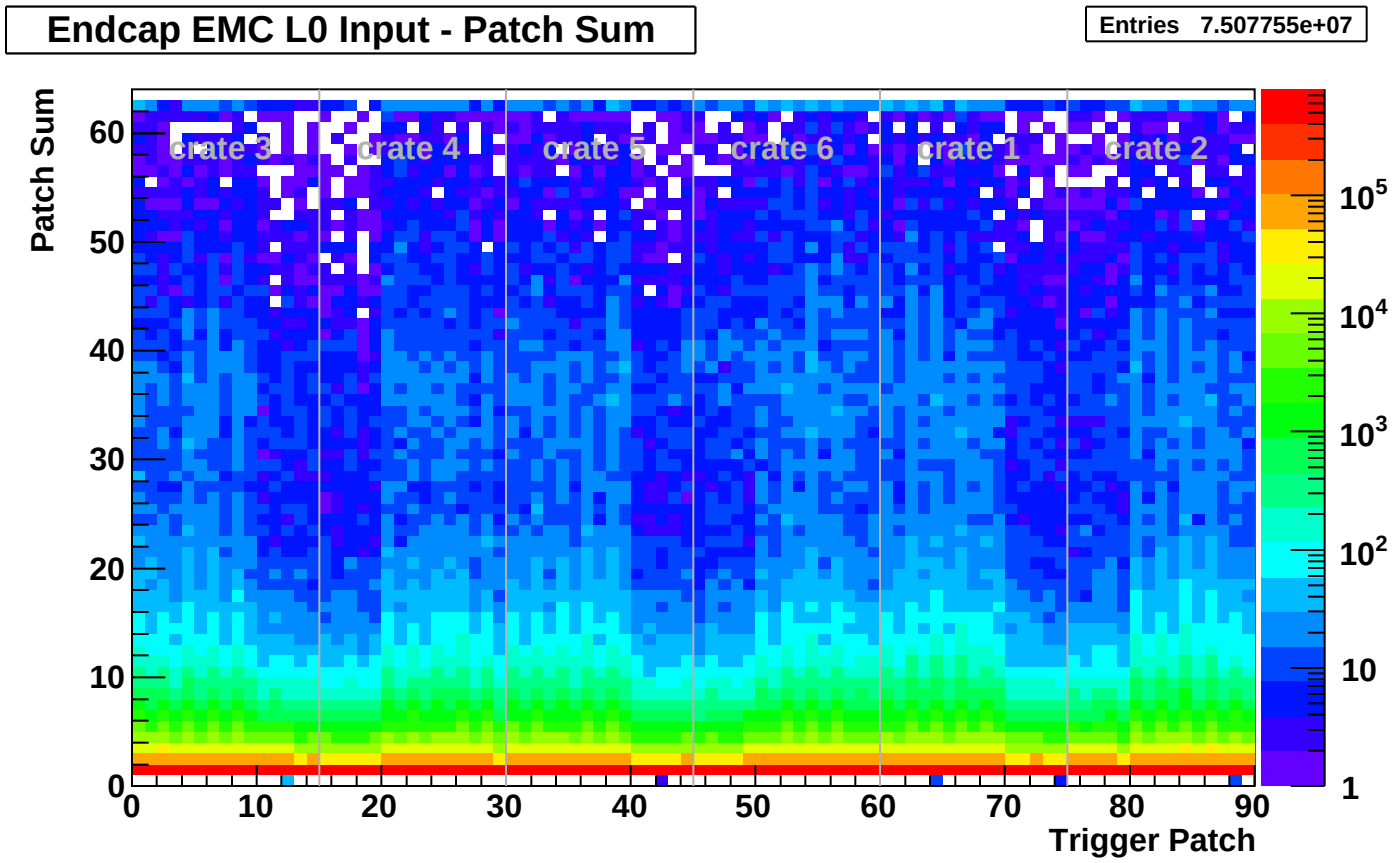

BunchId7Bit (BHT3)
Entries 58118

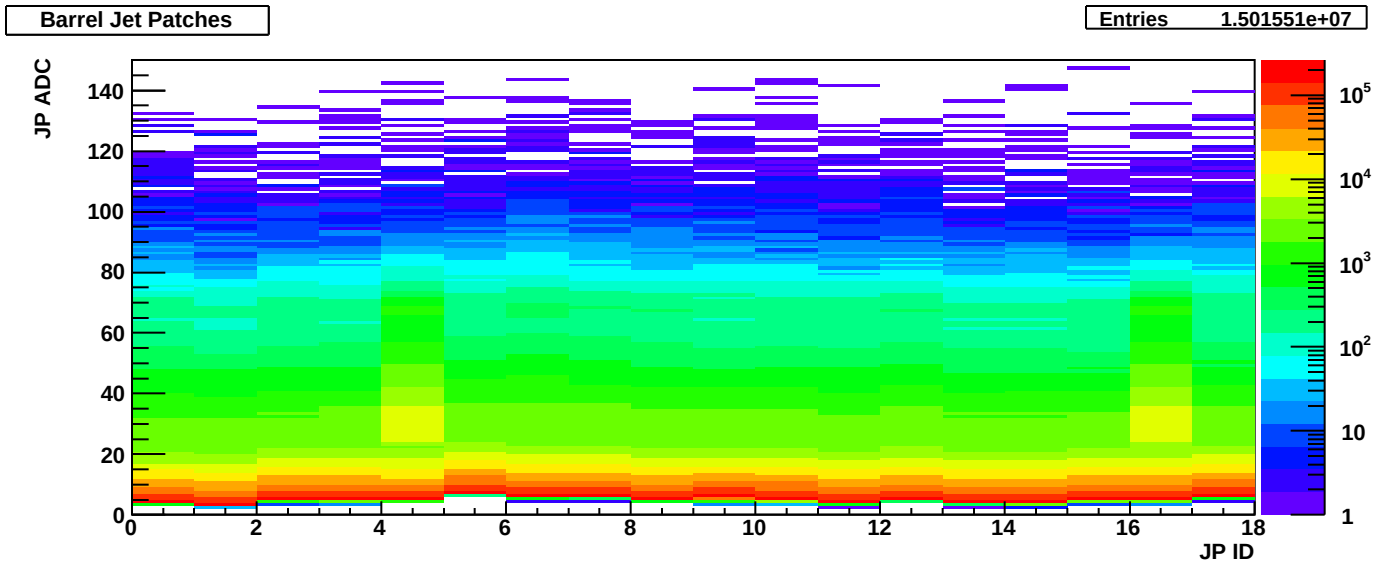

BunchId7Bit (all events)
Entries 834195

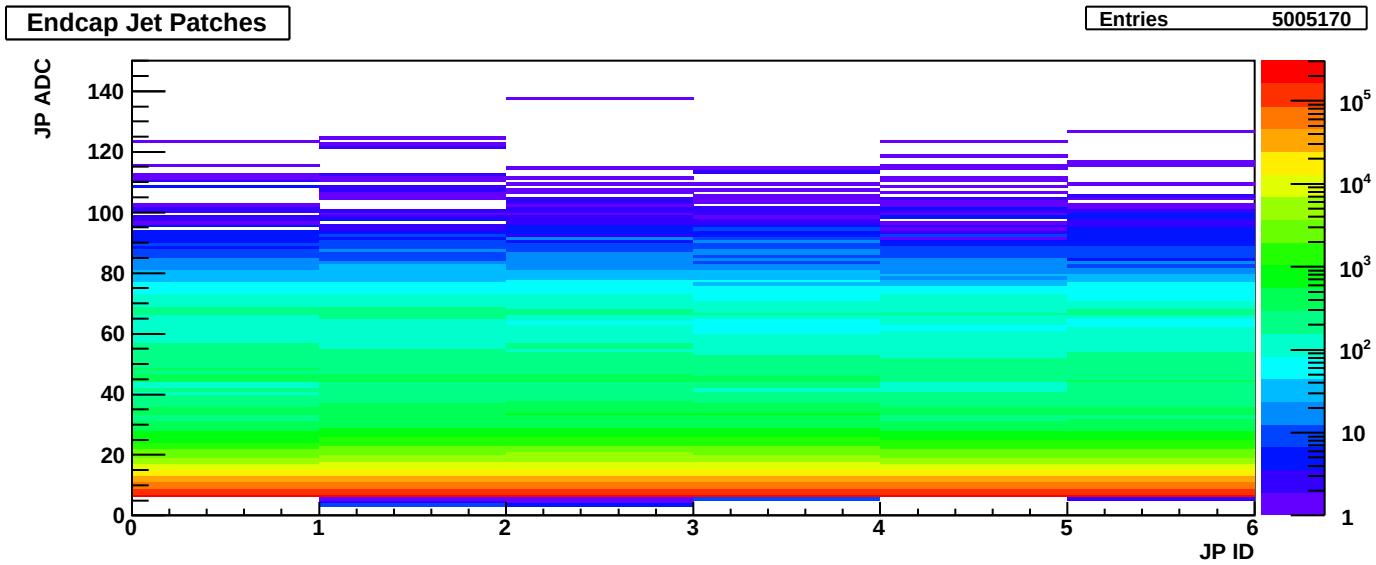

Barrel EMC L0 Input - High Tower
Entries 2.502585e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

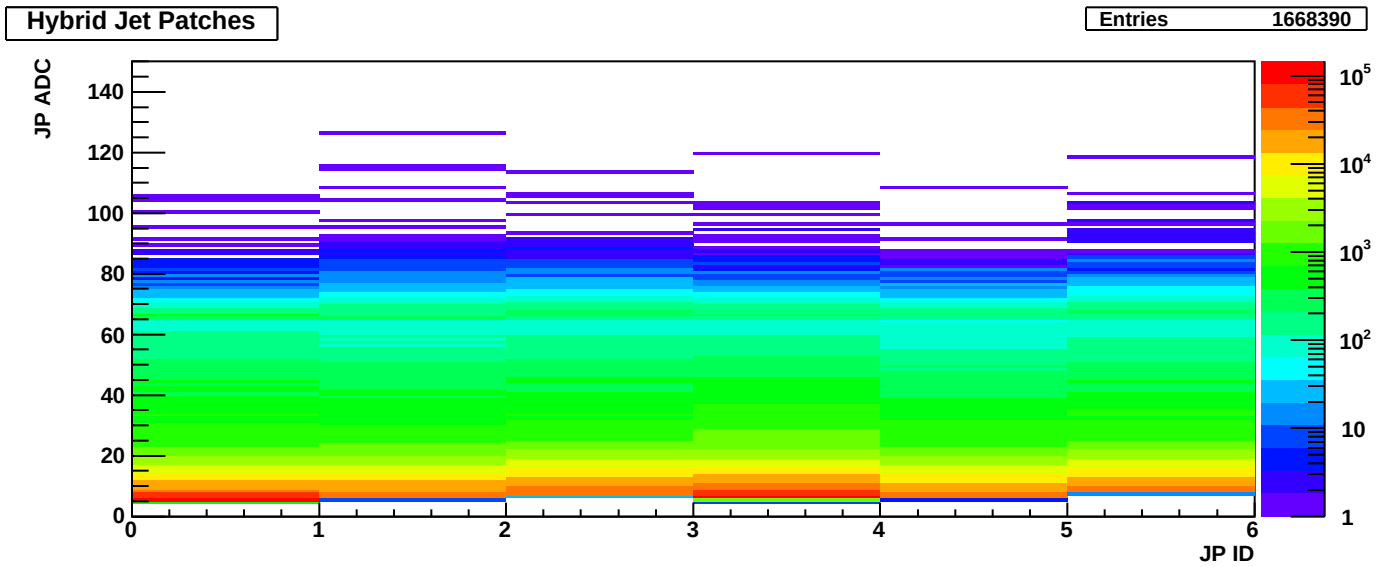

Barrel EMC L0 Input - Patch Sum
Entries 2.502585e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5


Endcap EMC L0 Input - High Tower
Entries 7.507755e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2


Endcap EMC L0 Input - Patch Sum
Entries 7.507755e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

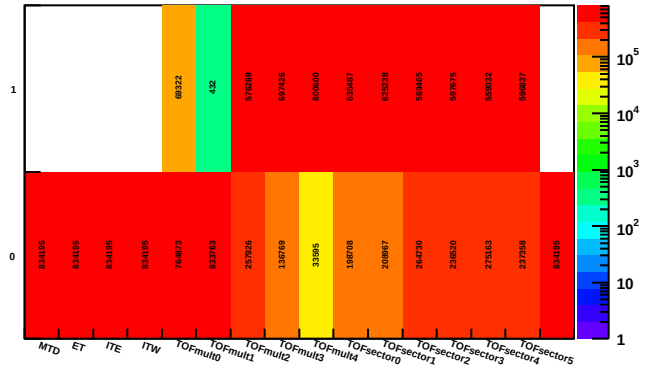

Barrel Jet Patches
Entries 1.501551e+07
JP ADC
JP ID


Endcap Jet Patches
Entries 5005170
JP ADC
JP ID


Hybrid Jet Patches
Entries 1668390
JP ADC
JP ID

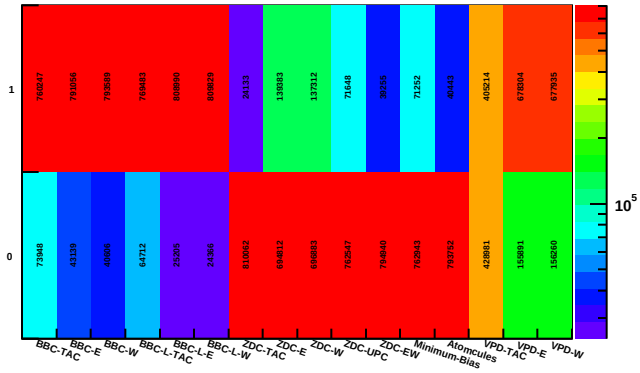
TF201 0-15 (ch0)

Entries 1.334712e+07



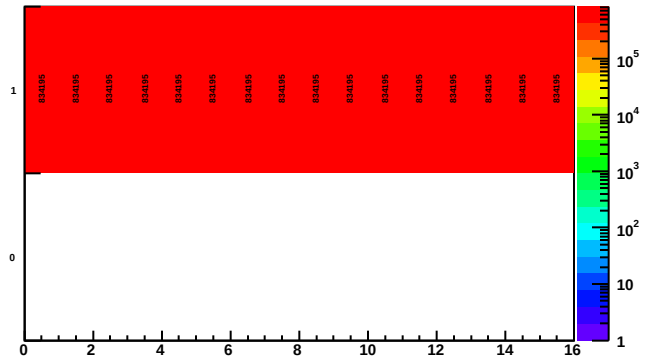
VT201 0-15 (ch1)

Entries 1.334712e+07



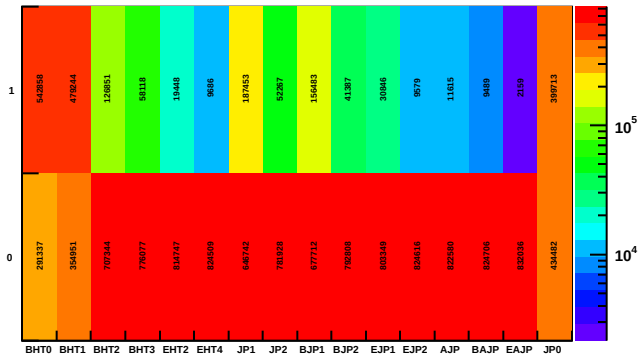
Unused (ch2)

Entries 1.334712e+07



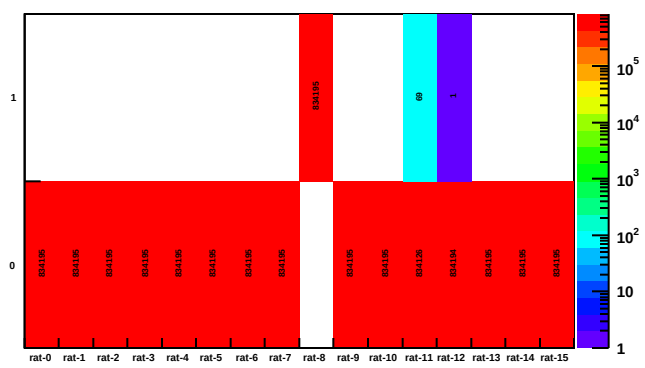
EM201 0-15 (ch3)

Entries 1.334712e+07



RAT board (ch4)

Entries 1.334712e+07


1
0
834195
69
1
834195
834195
834195
834195
834195
834195
834195
834195
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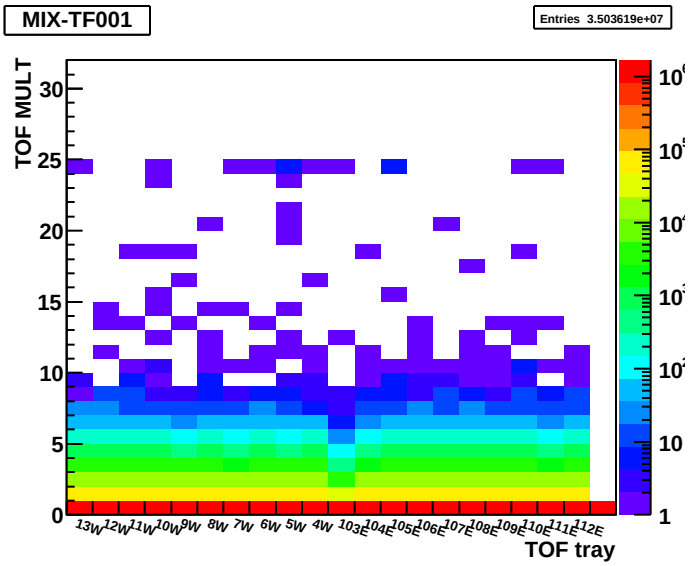
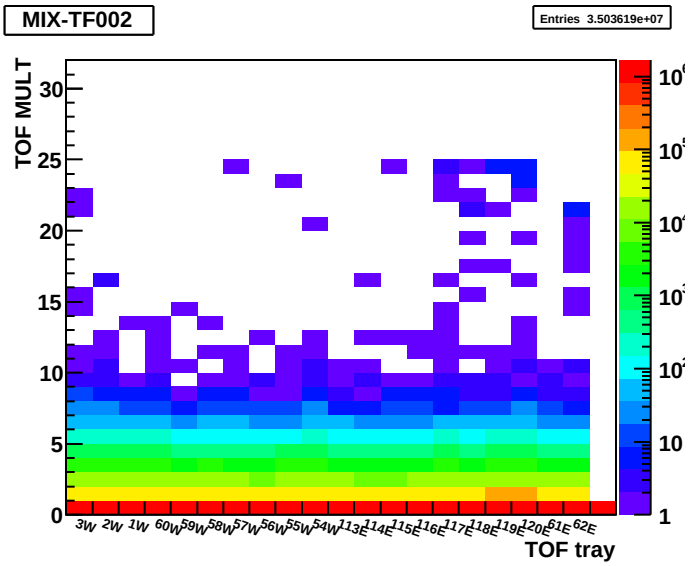
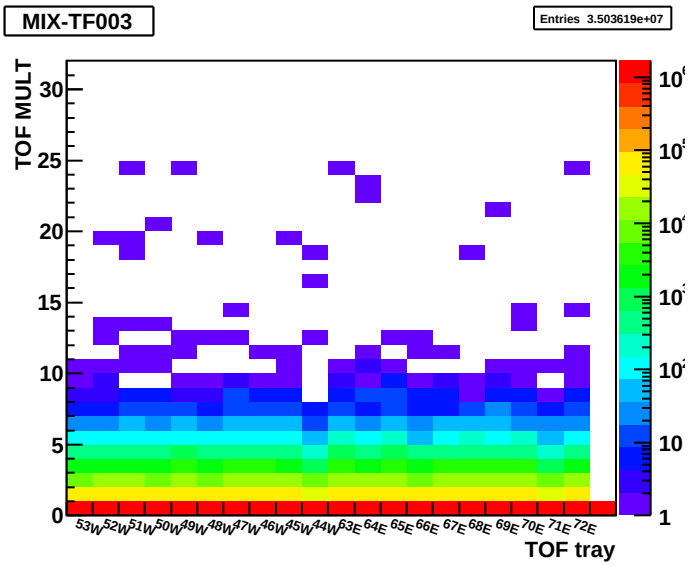
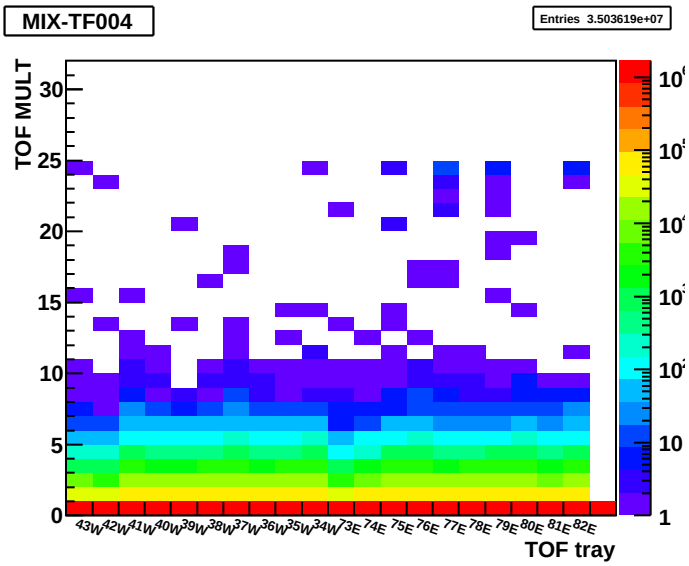
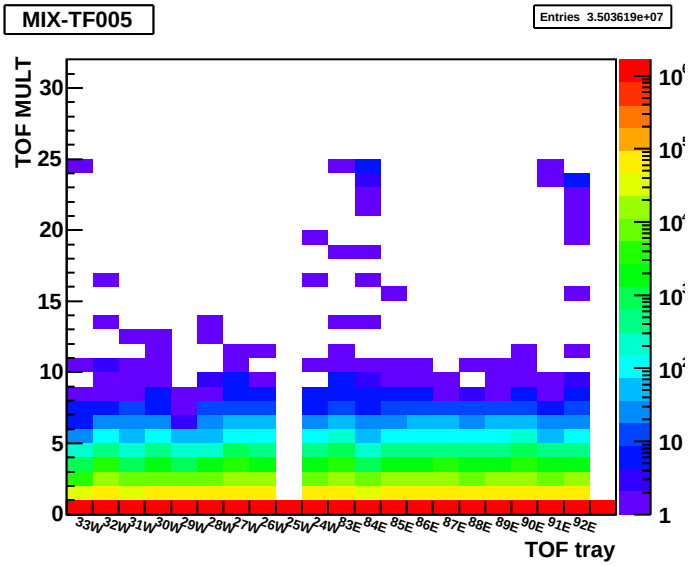
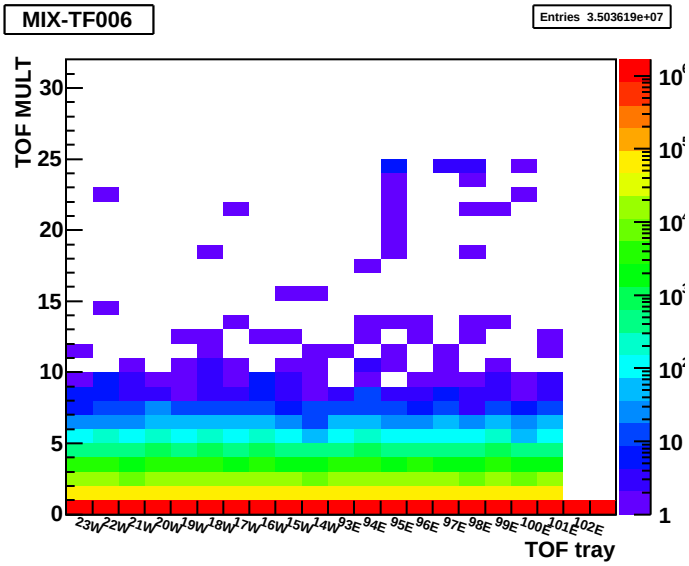

MIX-TF001
Entries 3.503619e+07
TOF MULT
TOF tray

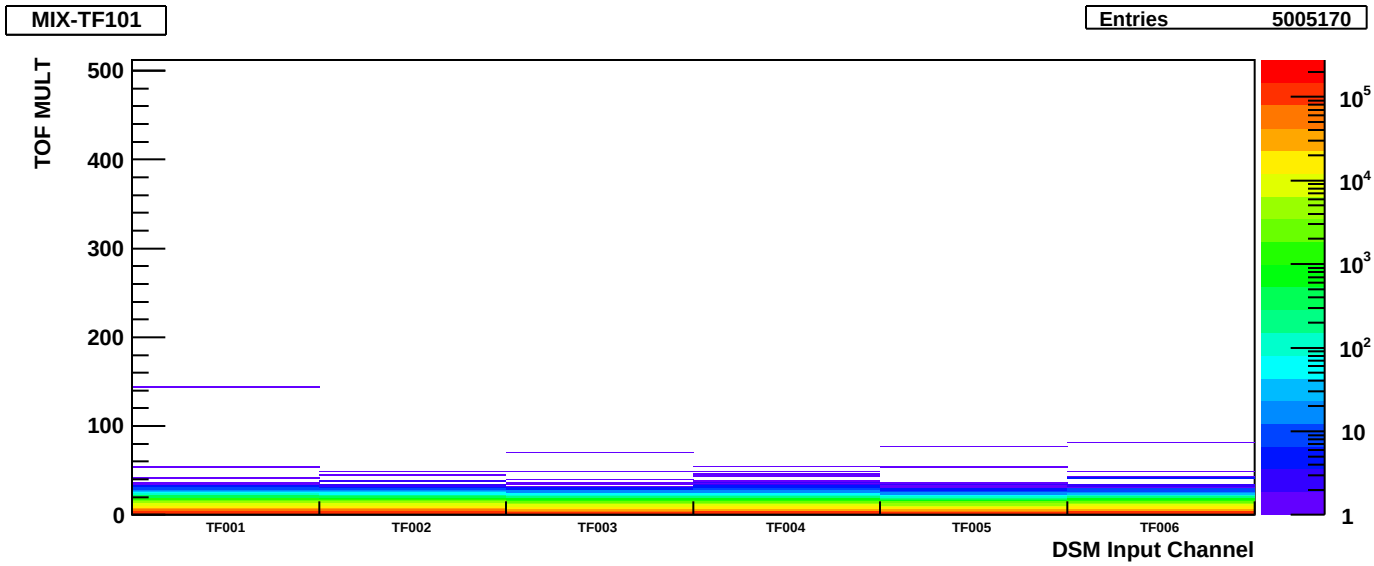

MIX-TF002
Entries 3.503619e+07
TOF MULT
TOF tray

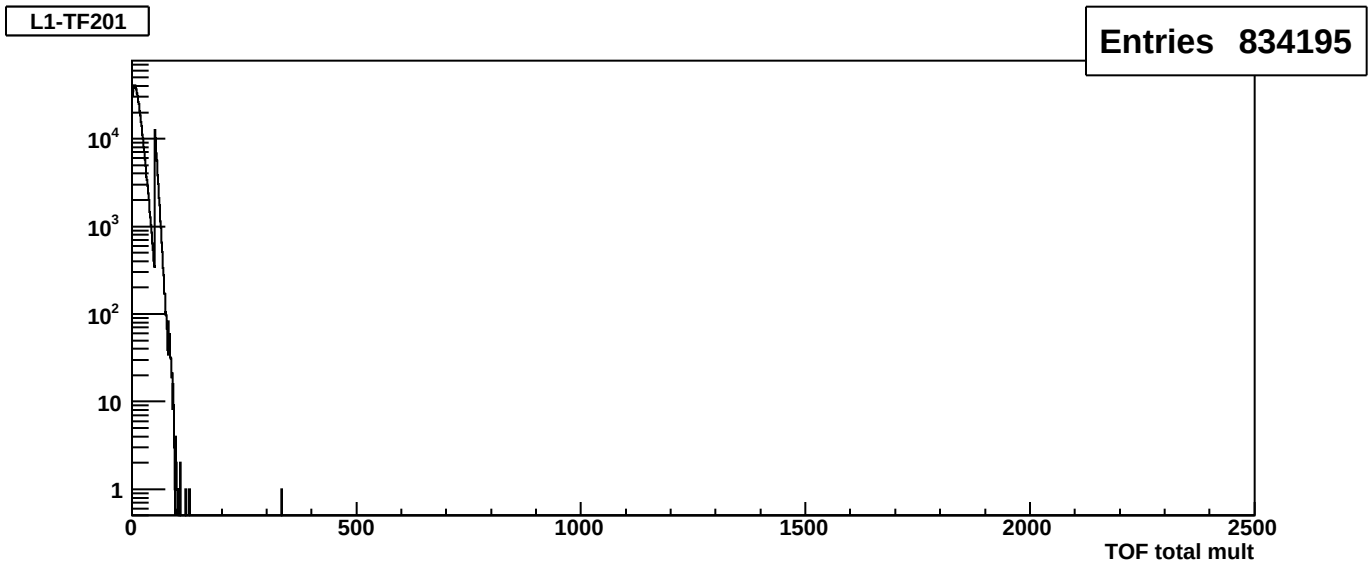

MIX-TF003
Entries 3.503619e+07
TOF MULT
TOF tray

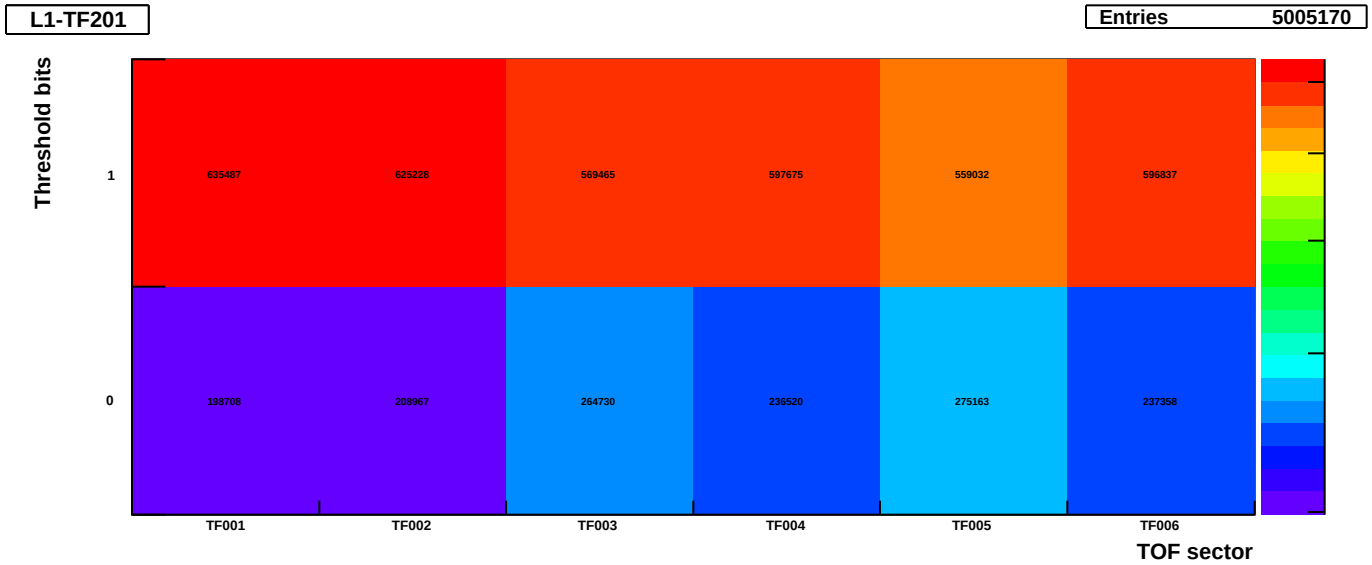

MIX-TF004
Entries 3.503619e+07
TOF MULT
TOF tray

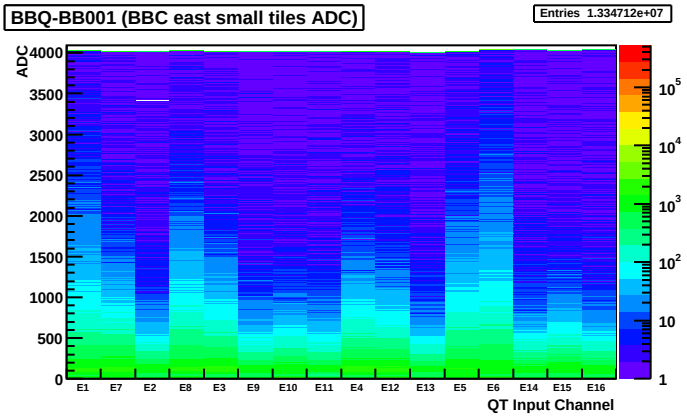

MIX-TF005
Entries 3.503619e+07
TOF MULT
TOF tray

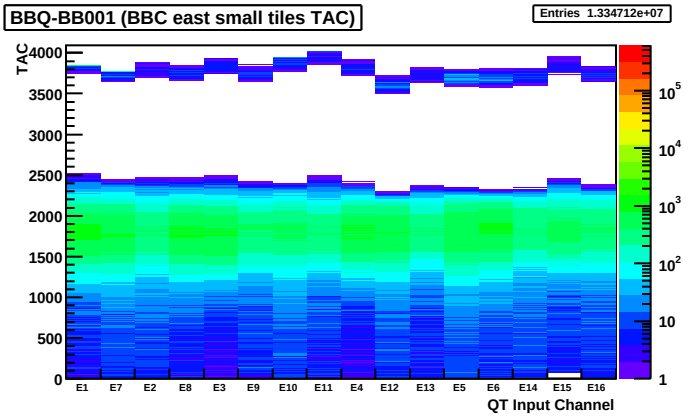

MIX-TF006
Entries 3.503619e+07
TOF MULT
TOF tray

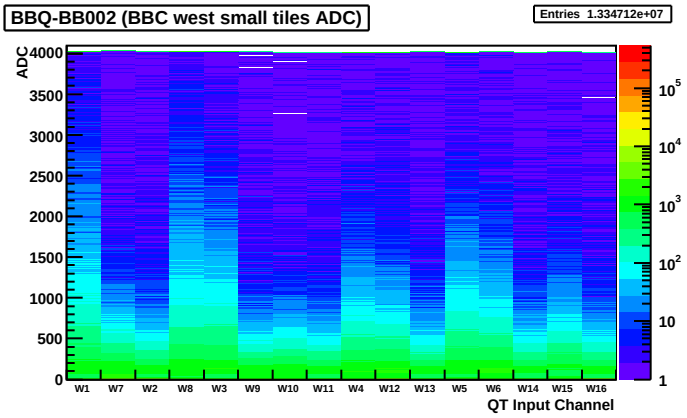

MIX-TF101
Entries 5005170
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

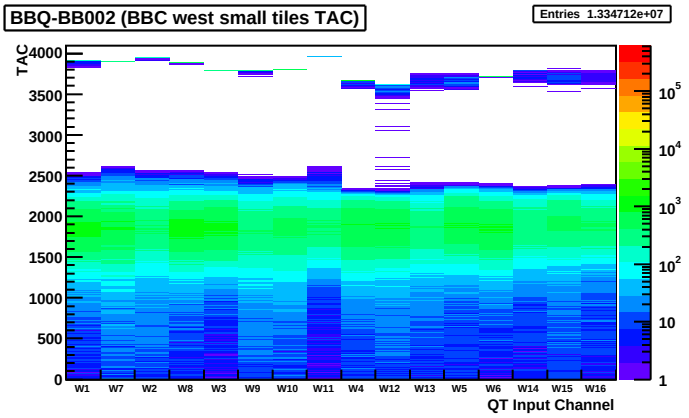

L1-TF201
Entries 834195
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

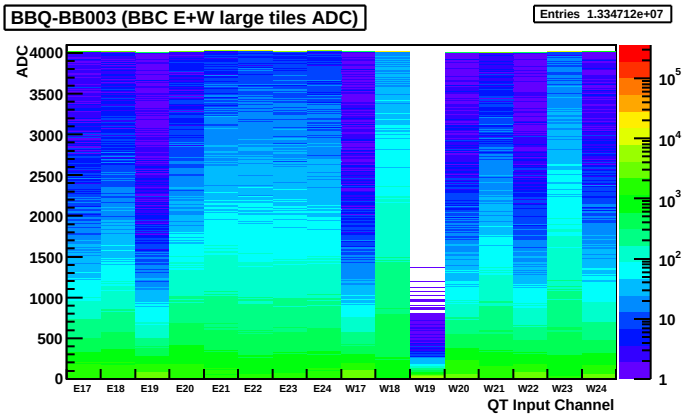

L1-TF201
Entries 5005170
Threshold bits
1
0
635487
625228
569465
597675
559032
596837
198708
208967
264730
236520
275163
237358
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

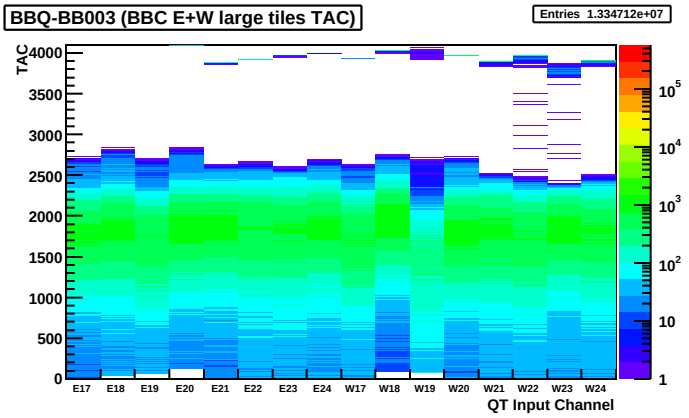

BBQ-BB001 (BBC east small tiles ADC)
Entries 1.334712e+07
ADC
QT Input Channel

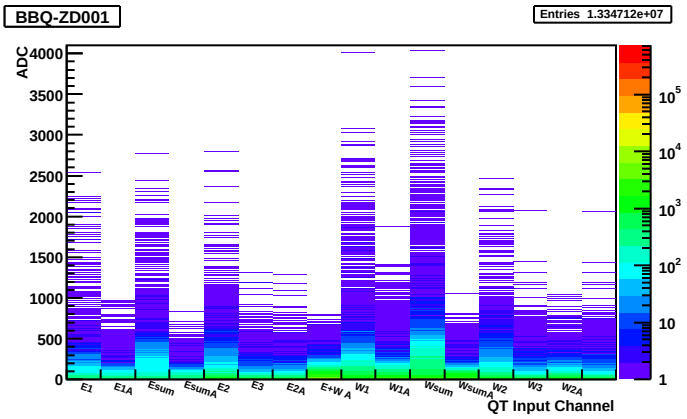

BBQ-BB001 (BBC east small tiles TAC)
Entries 1.334712e+07
TAC
QT Input Channel

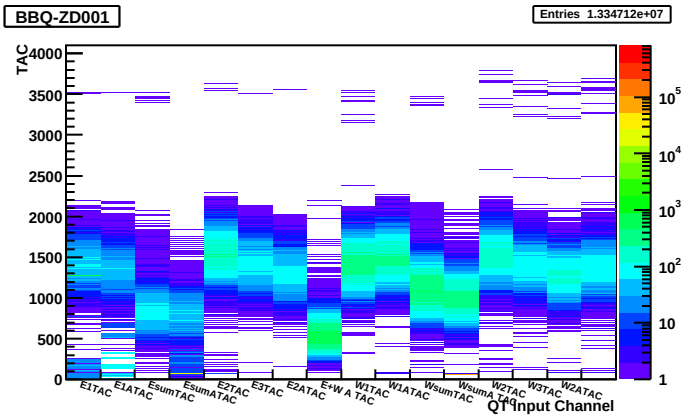

BBQ-BB002 (BBC west small tiles ADC)
Entries 1.334712e+07
ADC
QT Input Channel

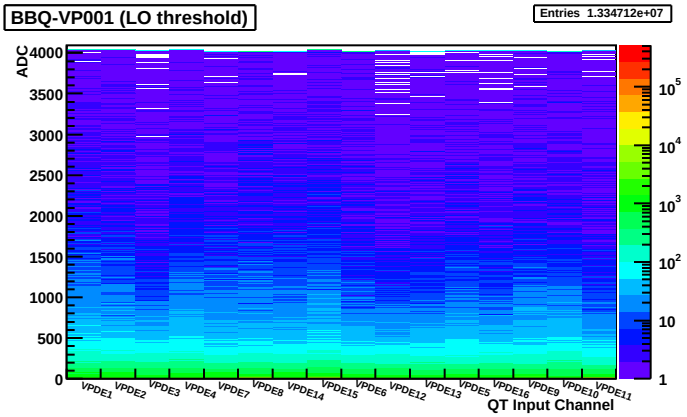

BBQ-BB002 (BBC west small tiles TAC)
Entries 1.334712e+07
TAC
QT Input Channel

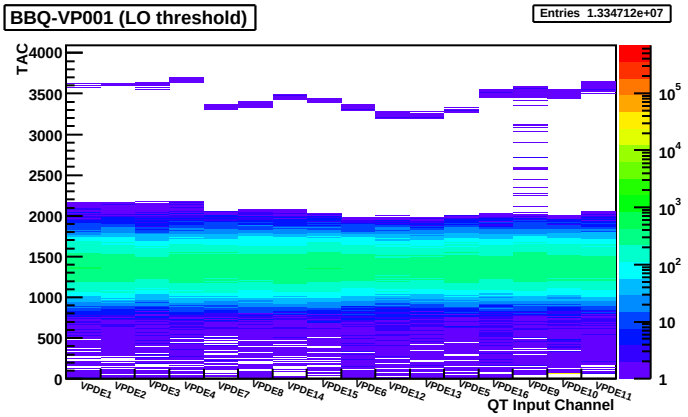

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 1.334712e+07
ADC
QT Input Channel

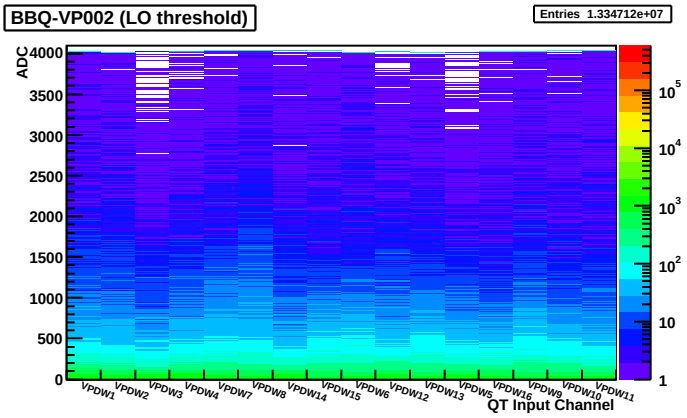

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 1.334712e+07
TAC
QT Input Channel

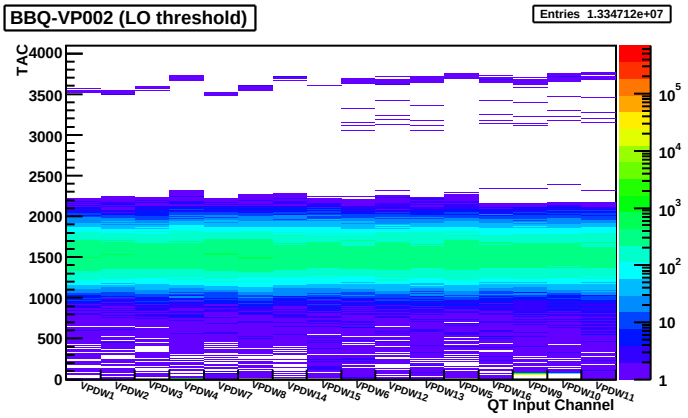

BBQ-ZD001
Entries 1.334712e+07
ADC
QT Input Channel

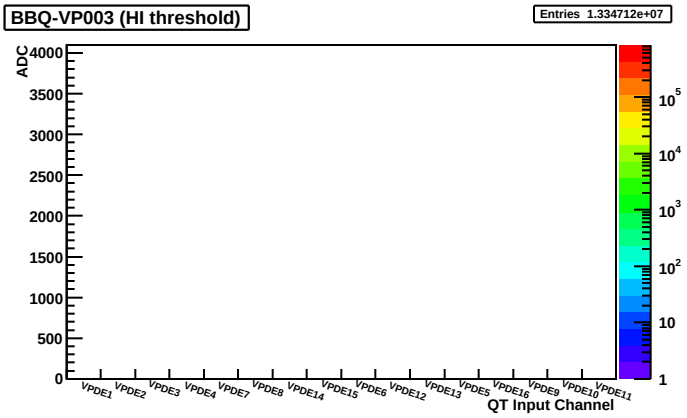

BBQ-ZD001
Entries 1.334712e+07
TAC
QT Input Channel

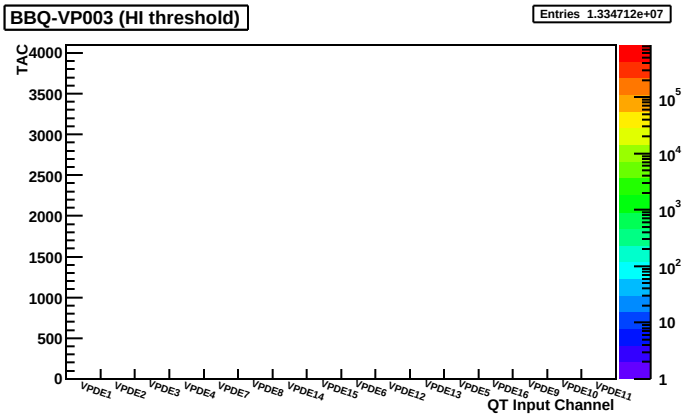

BBQ-VP001 (LO threshold)
Entries 1.334712e+07
ADC
QT Input Channel

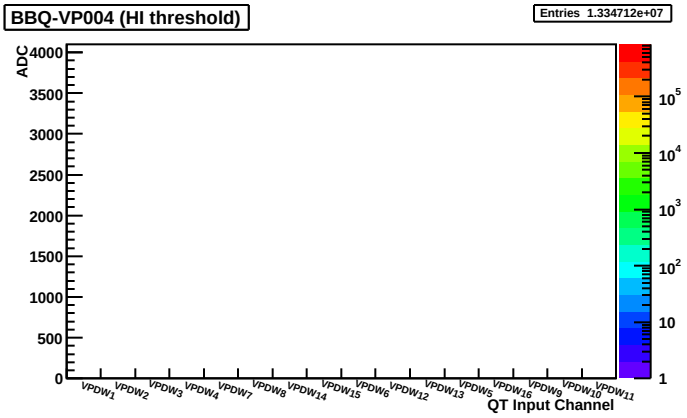

BBQ-VP001 (LO threshold)
Entries 1.334712e+07
TAC
QT Input Channel

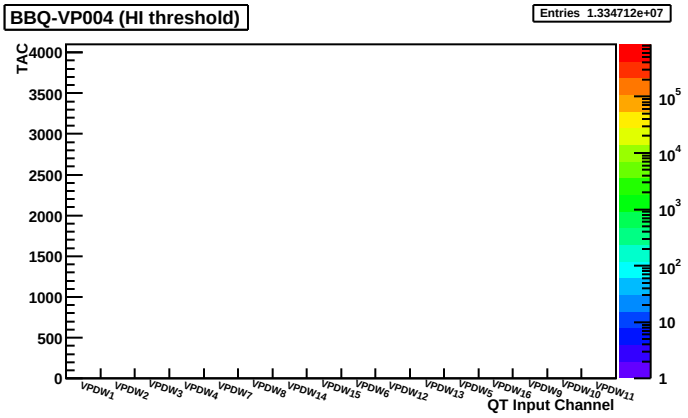

BBQ-VP002 (LO threshold)
Entries 1.334712e+07
ADC
QT Input Channel

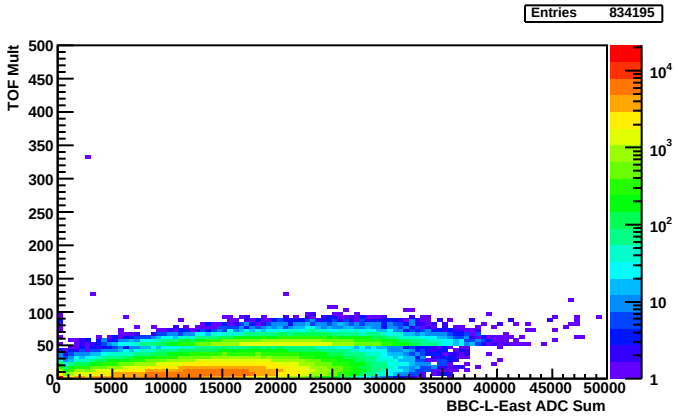

BBQ-VP002 (LO threshold)
Entries 1.334712e+07
TAC
QT Input Channel

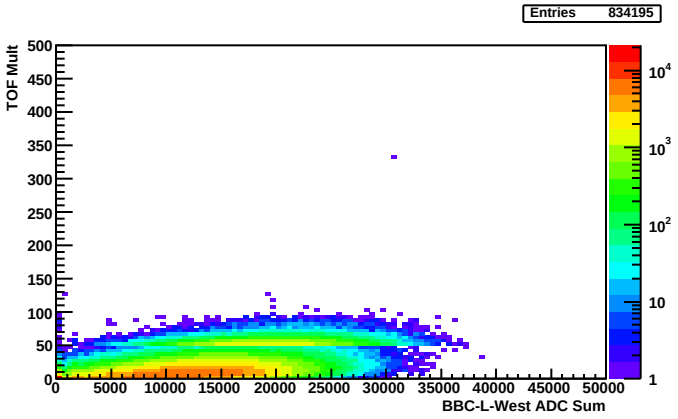

BBQ-VP003 (HI threshold)
Entries 1.334712e+07
ADC
QT Input Channel

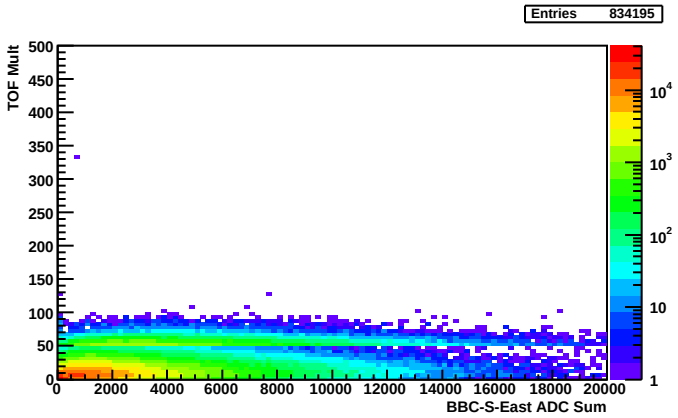

BBQ-VP003 (HI threshold)
Entries 1.334712e+07
TAC
QT Input Channel

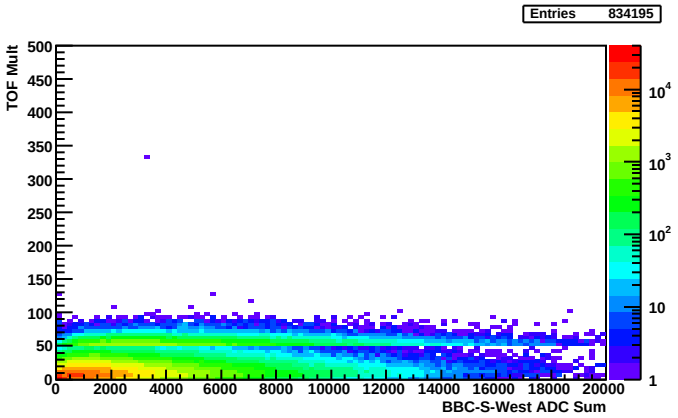

BBQ-VP004 (HI threshold)
Entries 1.334712e+07
ADC
QT Input Channel

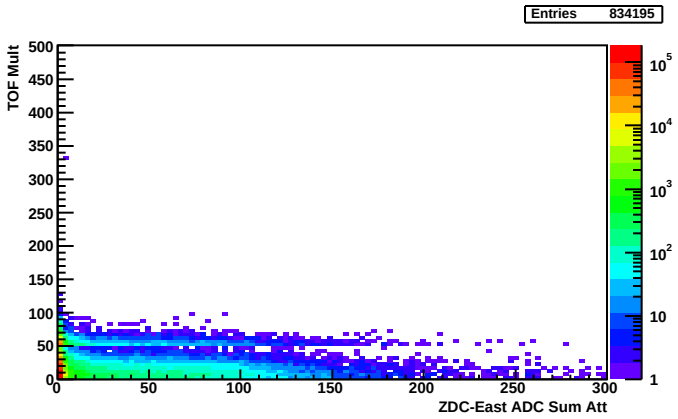

BBQ-VP004 (HI threshold)
Entries 1.334712e+07
TAC
QT Input Channel

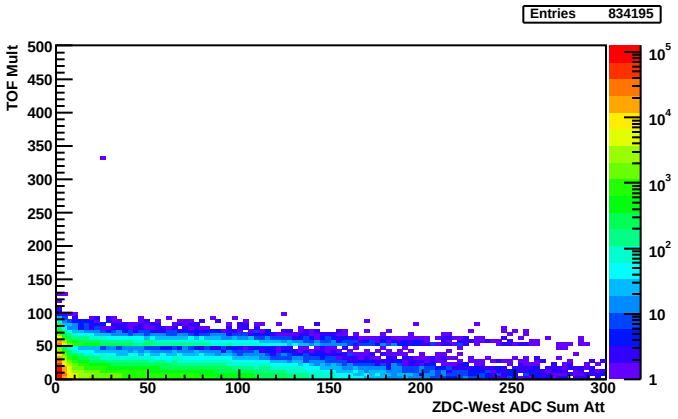

Entries 834195
TOF Mult
BBC-L-East ADC Sum

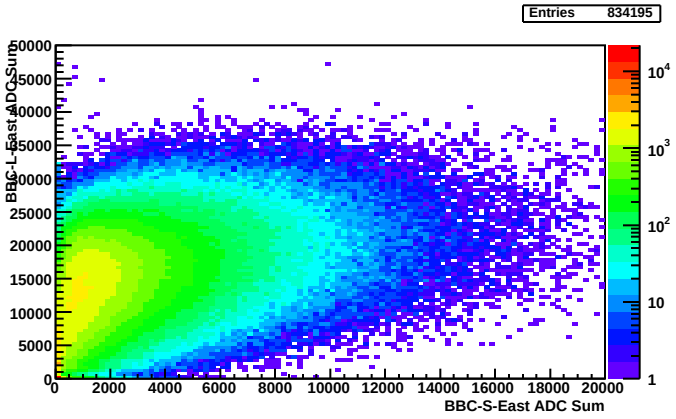

Entries 834195
TOF Mult
BBC-L-West ADC Sum

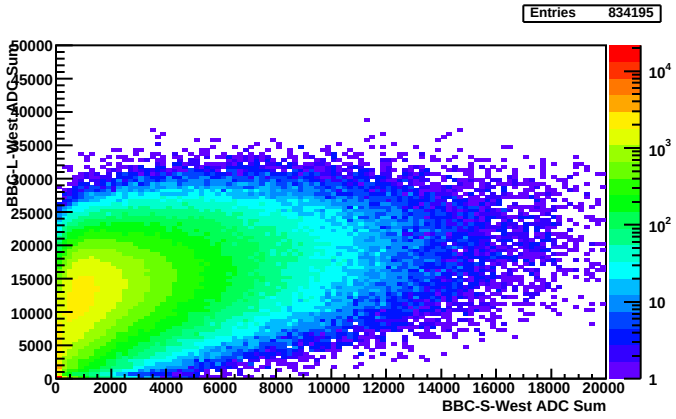

Entries 834195
TOF Mult
BBC-S-East ADC Sum

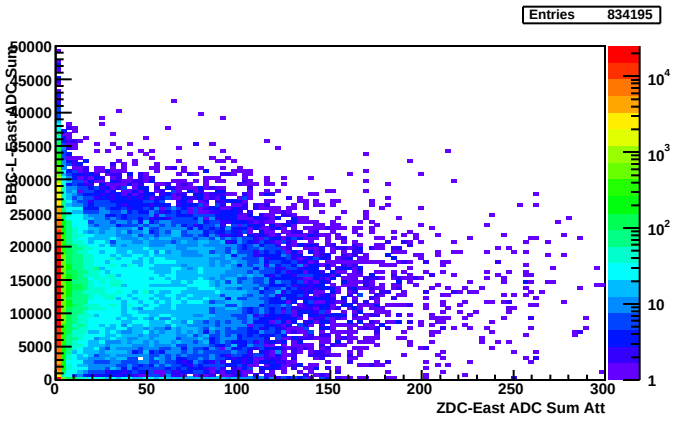

Entries 834195
TOF Mult
BBC-S-West ADC Sum

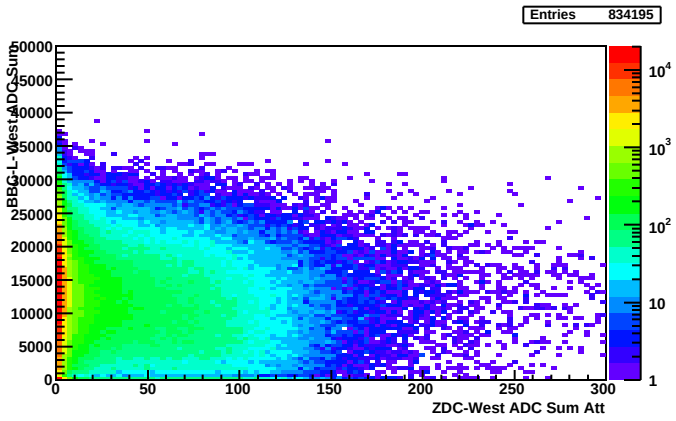

Entries 834195
TOF Mult
ZDC-East ADC Sum Att

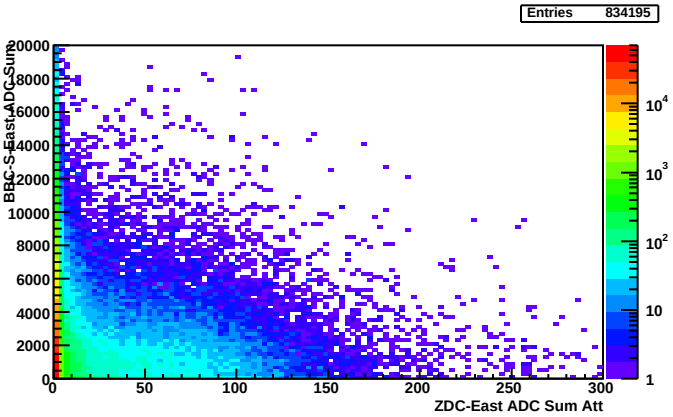

Entries 834195
TOF Mult
ZDC-West ADC Sum Att

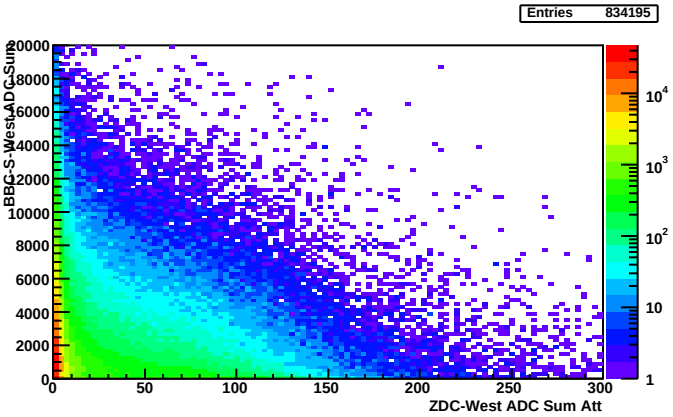

Entries 834195
BBC-L-East ADC Sum
BBC-S-East ADC Sum

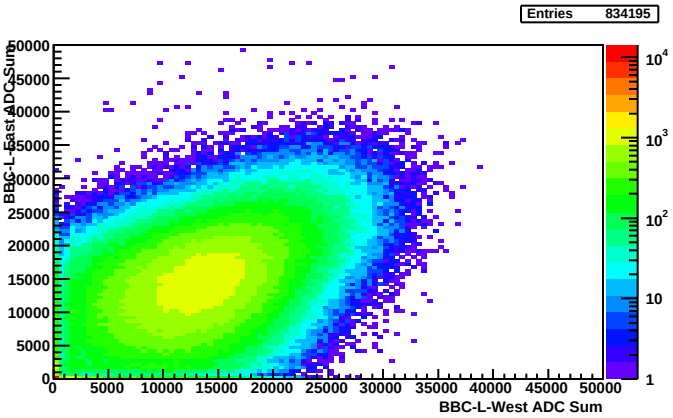

Entries 834195
BBC-L-West ADC Sum
BBC-S-West ADC Sum

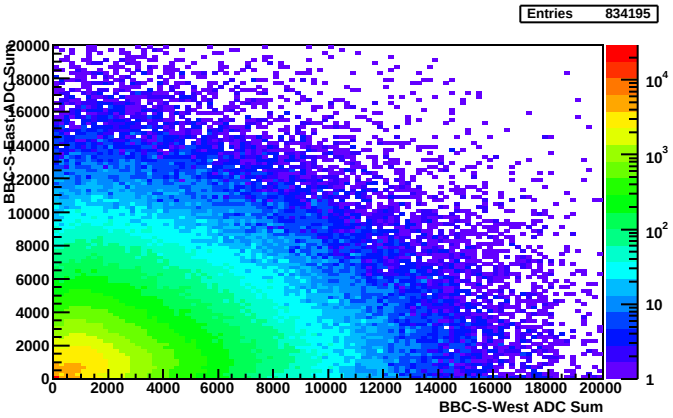

Entries 834195
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

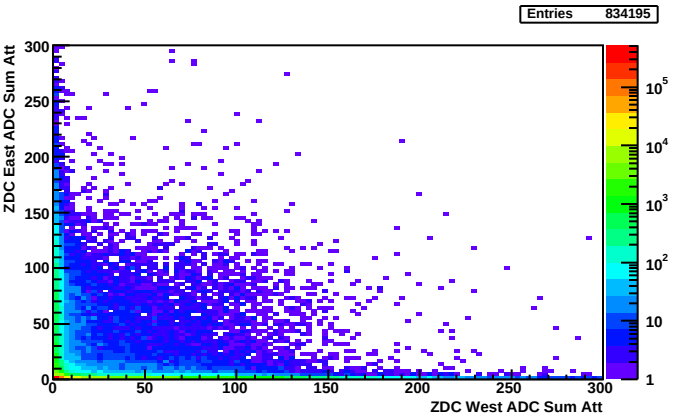

Entries 834195
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

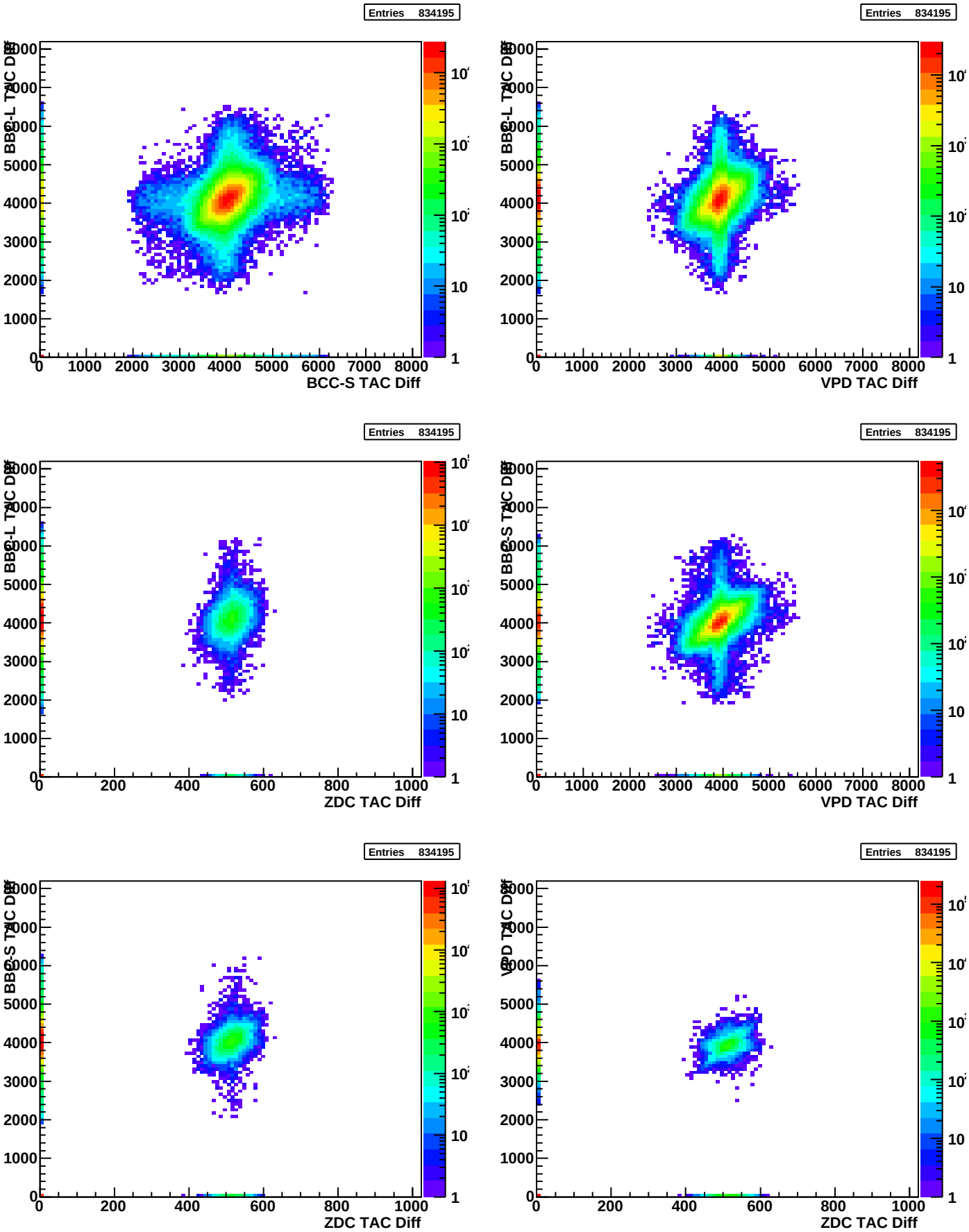

Entries 834195
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

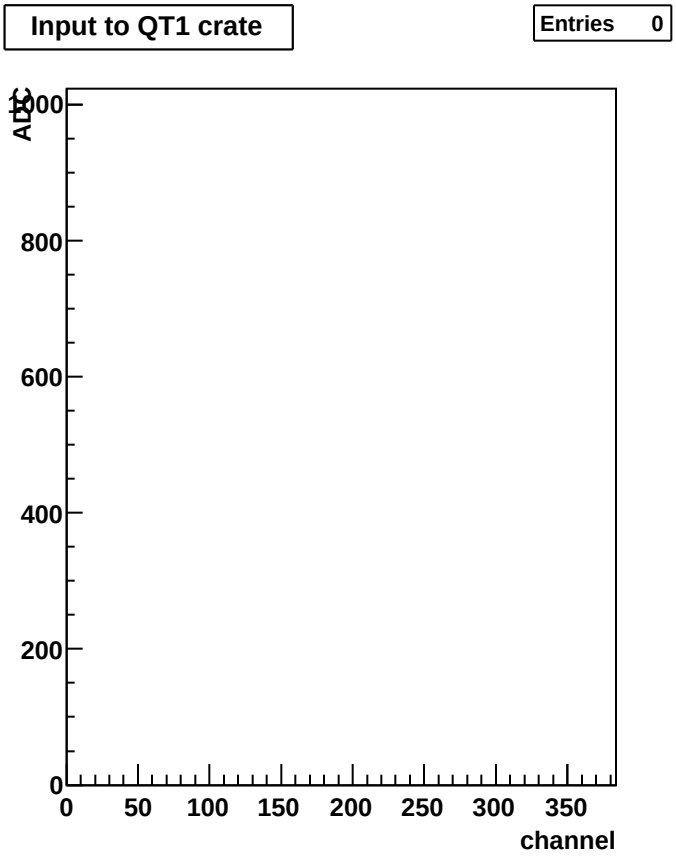

Entries 834195
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

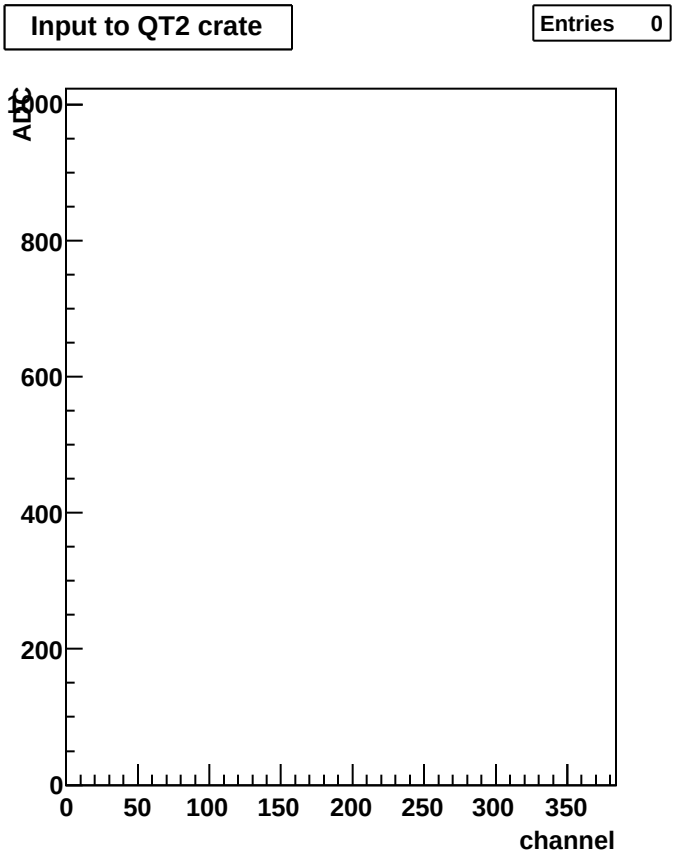

Entries 834195
BBC-L-East ADC Sum
BBC-L-West ADC Sum

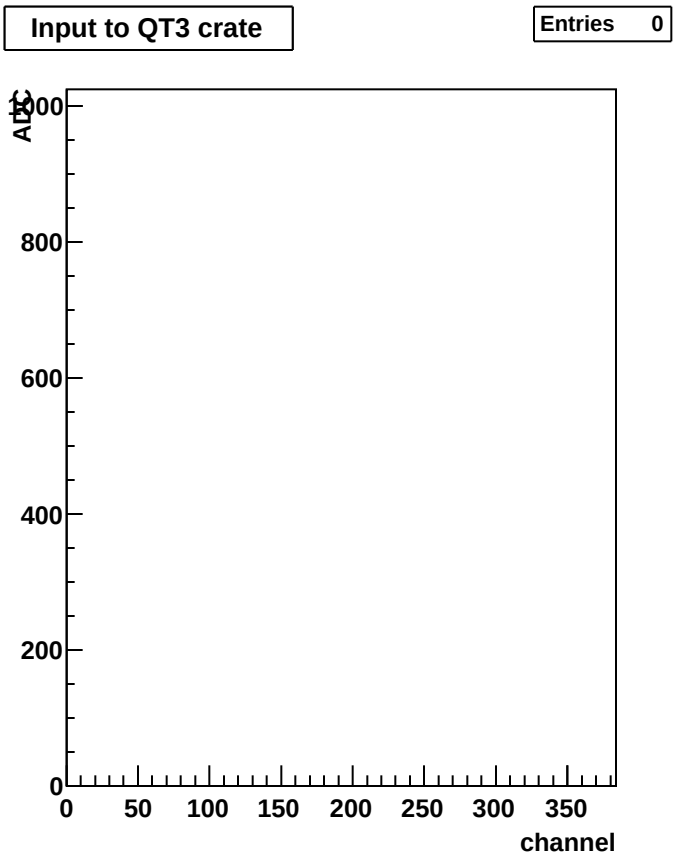

Entries 834195
BBC-S-East ADC Sum
BBC-S-West ADC Sum

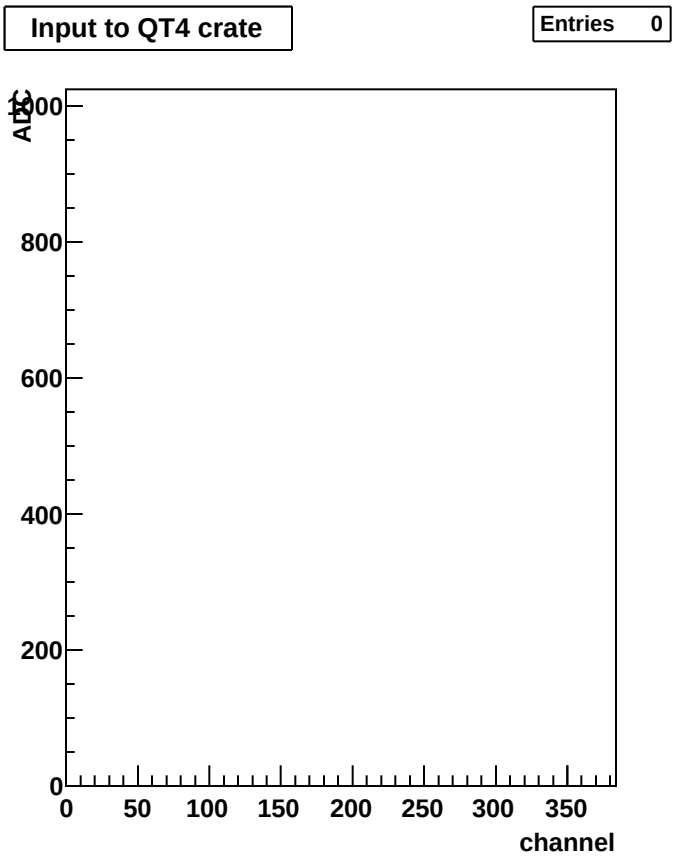

Entries 834195
ZDC East ADC Sum Att
ZDC West ADC Sum Att

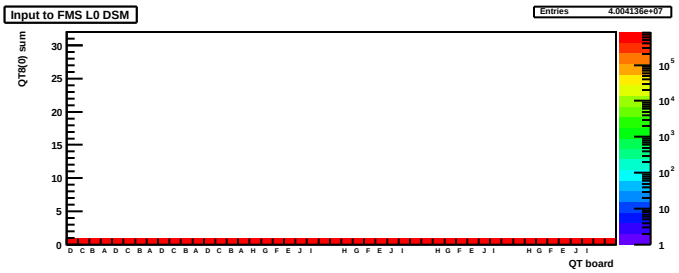

Entries 834195
BBC-L TAC Diff
BCC-S TAC Diff
Entries 834195
BBC-L TAC Diff
VPD TAC Diff
Entries 834195
BBC-L TAC Diff
ZDC TAC Diff
Entries 834195
BBC-S TAC Diff
VPD TAC Diff
Entries 834195
BBC-S TAC Diff
ZDC TAC Diff
Entries 834195
VPD TAC Diff
ZDC TAC Diff

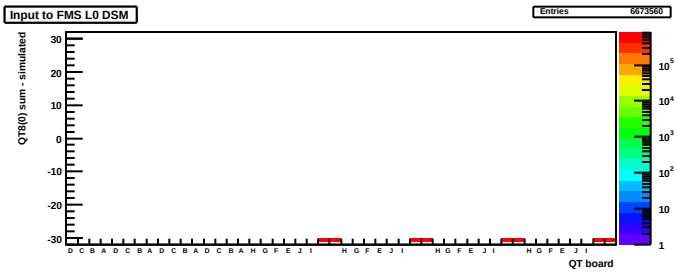

Input to QT1 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

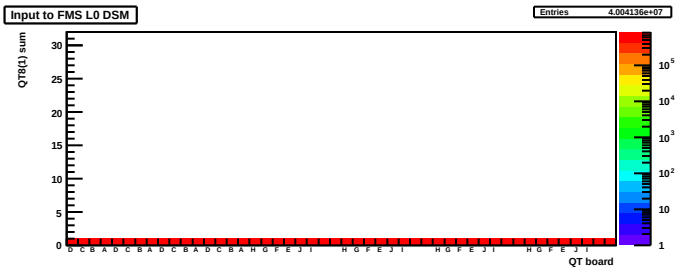

Input to QT2 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

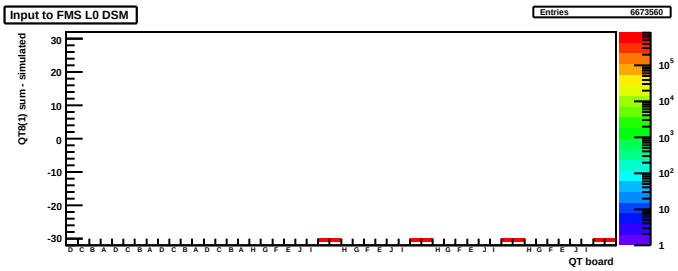

Input to QT3 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

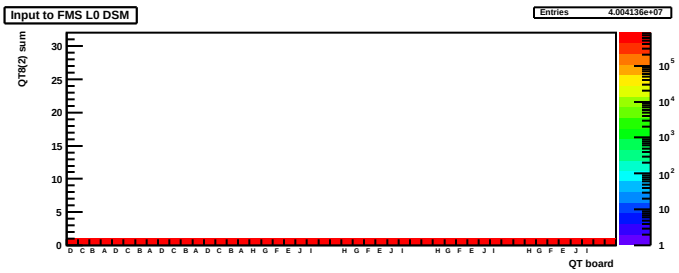

Input to QT4 crate
Entries 0
ADC
1000
800
600
400
200
0
0 50 100 150 200 250 300 350
channel

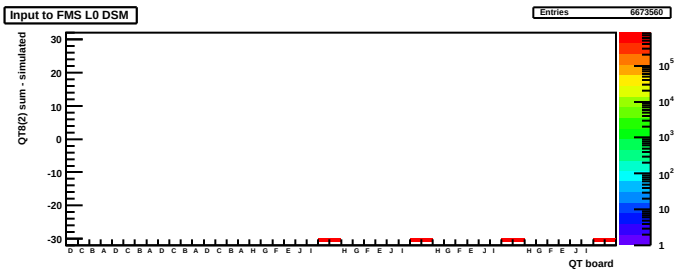

Input to FMS L0 DSM
Entries 4.004136e+07
QT8(0) sum
QT board

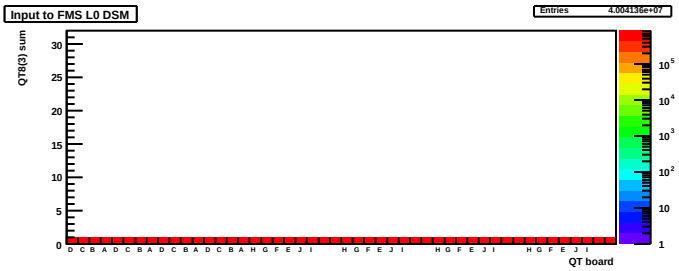

Input to FMS L0 DSM
Entries 6673560
QT8(0) sum - simulated
QT board

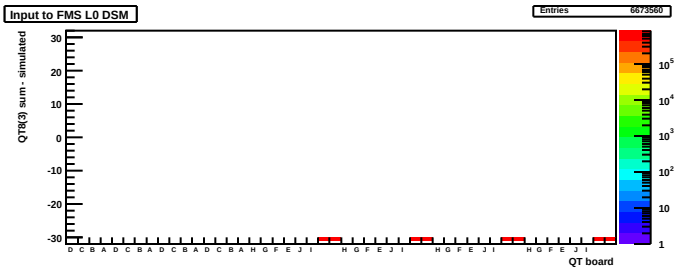

Input to FMS L0 DSM
Entries 4.004136e+07
QT8(1) sum
QT board

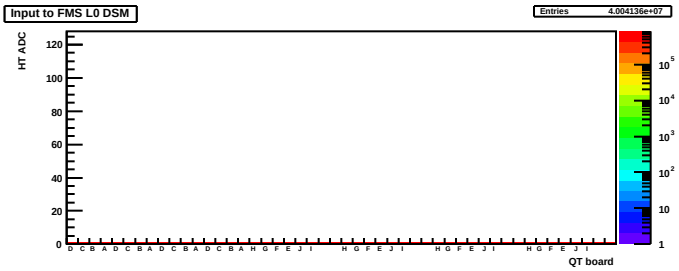

Input to FMS L0 DSM
Entries 6673560
QT8(1) sum - simulated
QT board

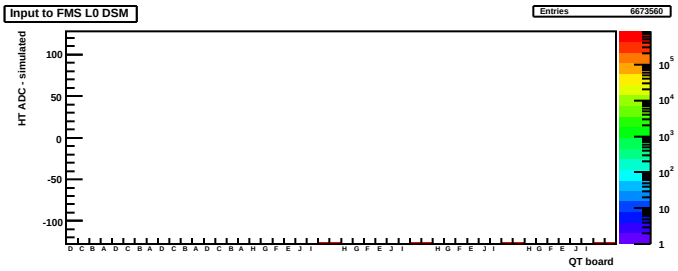

Input to FMS L0 DSM
Entries 4.004136e+07
QT8(2) sum
QT board

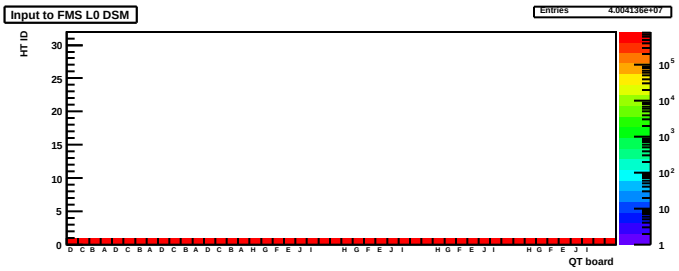

Input to FMS L0 DSM
Entries 6673560
QT8(2) sum - simulated
QT board

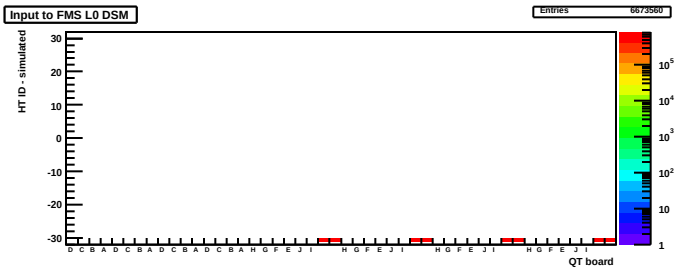

Input to FMS L0 DSM
Entries 4.004136e+07
QT8(3) sum
QT board

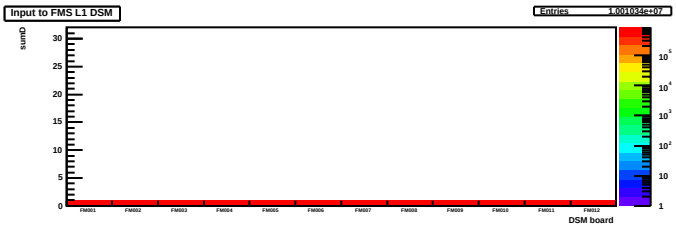

Input to FMS L0 DSM
Entries 6673560
QT8(3) sum - simulated
QT board

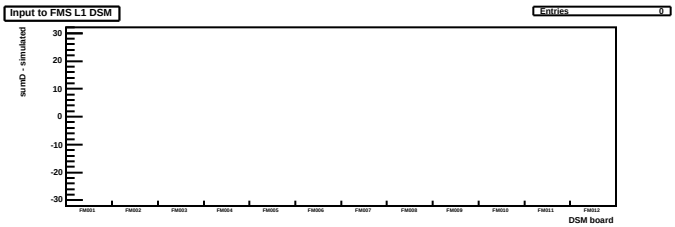

Input to FMS L0 DSM
Entries 4.004136e+07
HT ADC
QT board

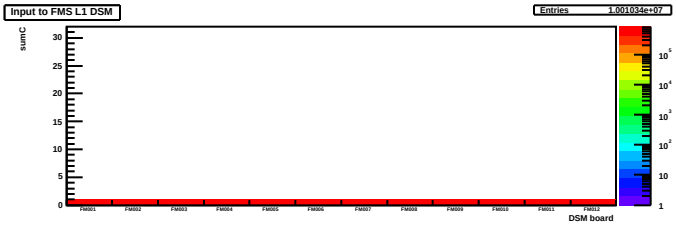

Input to FMS L0 DSM
Entries 6673560
HT ADC - simulated
QT board

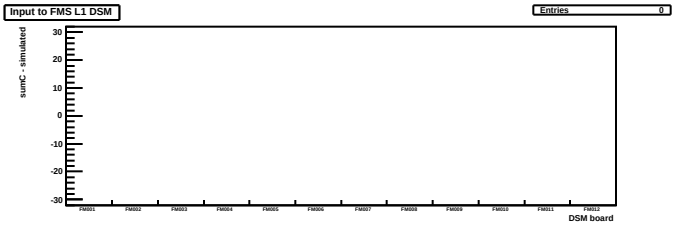

Input to FMS L0 DSM
Entries 4.004136e+07
HT ID
QT board

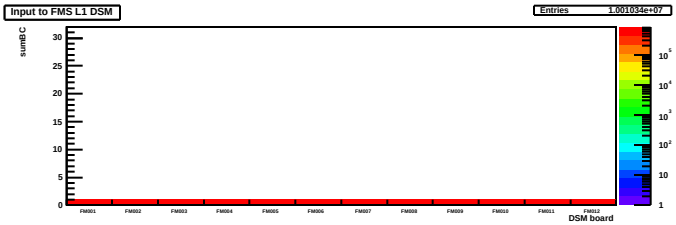

Input to FMS L0 DSM
Entries 6673560
HT ID - simulated
QT board

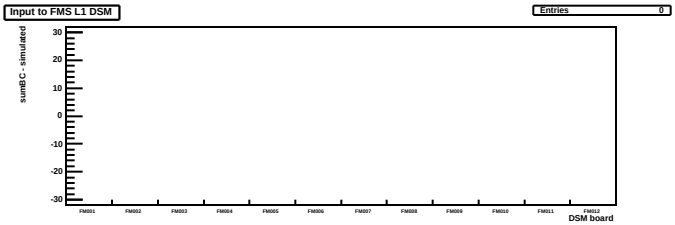

Input to FMS L1 DSM
Entries 1.001034e+07
sumD
DSM board

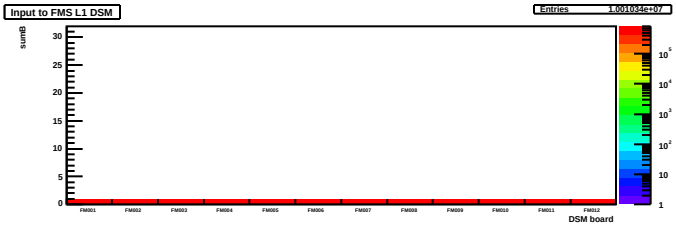

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

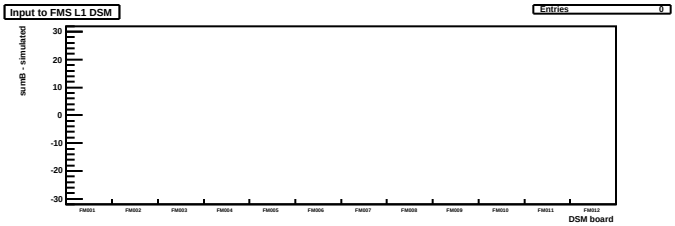

Input to FMS L1 DSM
Entries 1.001034e+07
sumC
DSM board

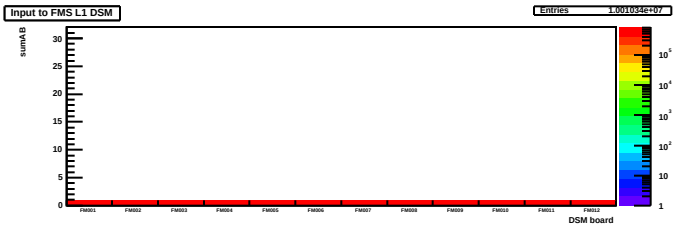

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

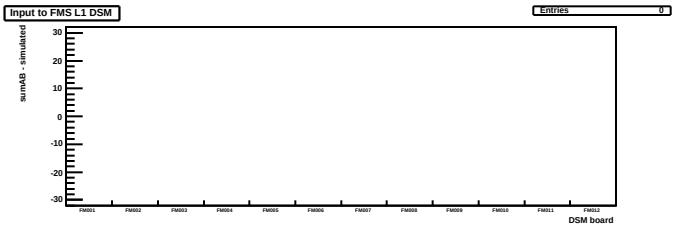

Input to FMS L1 DSM
Entries 1.001034e+07
sumBC
DSM board

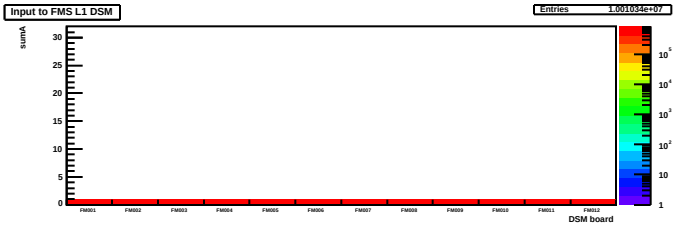

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

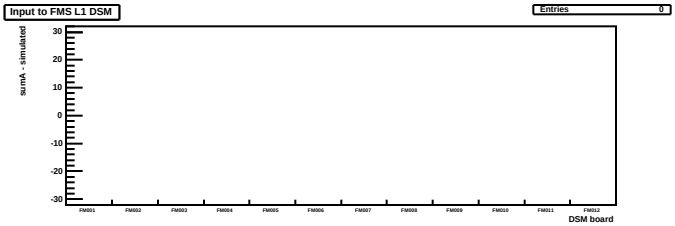

Input to FMS L1 DSM
Entries 1.001034e+07
sumB
DSM board

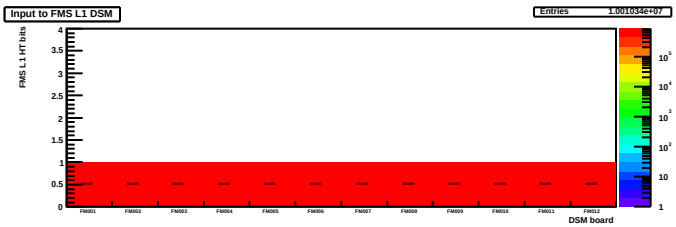

Input to FMS L1 DSM
Entries 0
sumB - simulated
DSM board

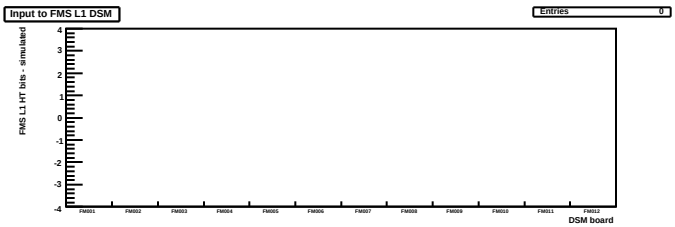

Input to FMS L1 DSM
Entries 1.001034e+07
sumAB
DSM board

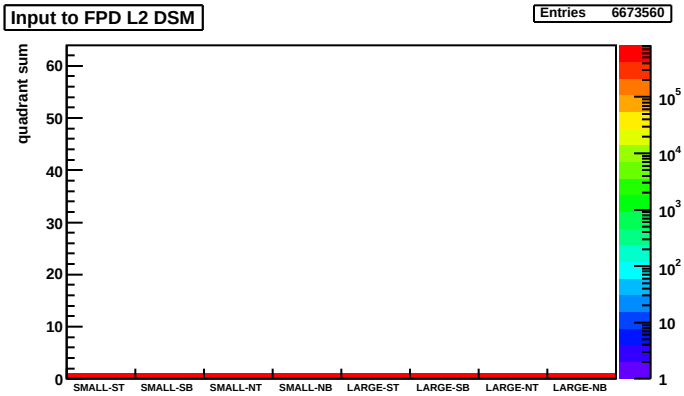

Input to FMS L1 DSM
Entries 0
sumAB - simulated
DSM board

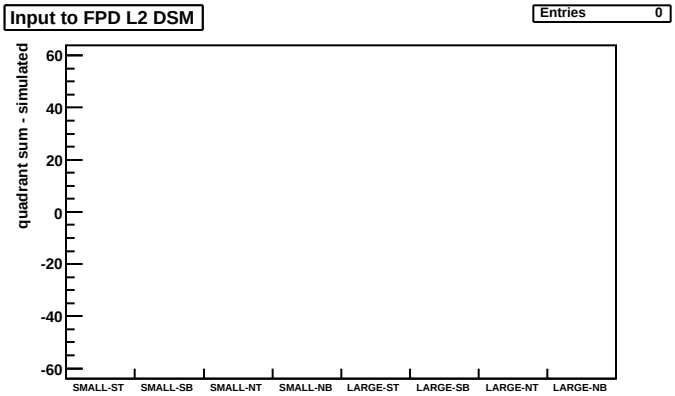

Input to FMS L1 DSM
Entries 1.001034e+07
sumA
DSM board

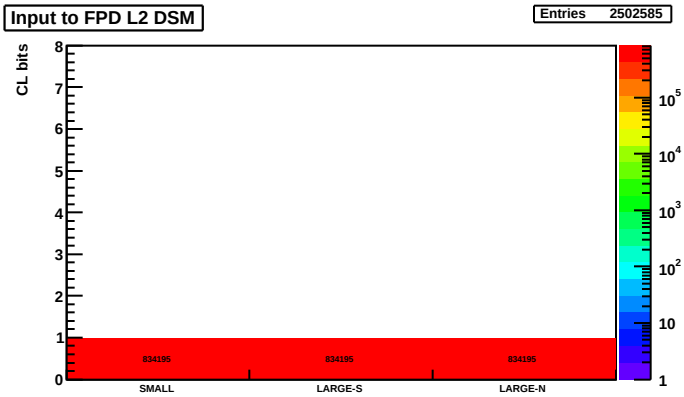

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

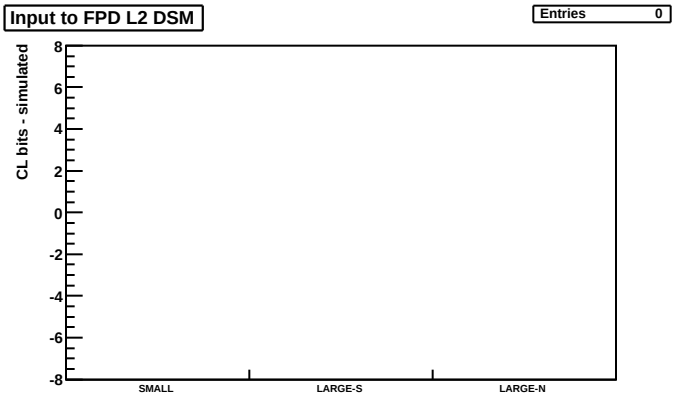

Input to FMS L1 DSM
Entries 1.001034e+07
FMS L1 HT bits
DSM board

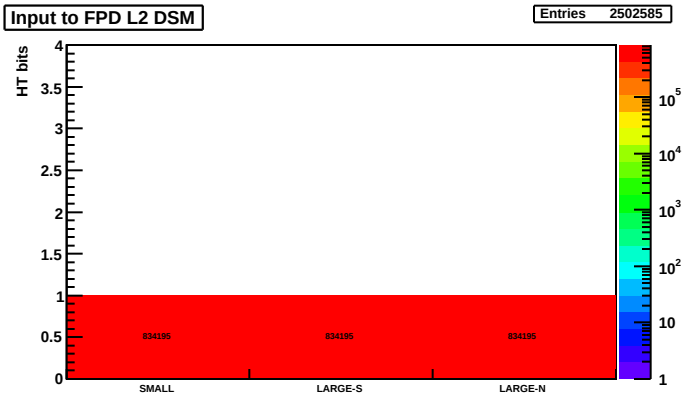

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

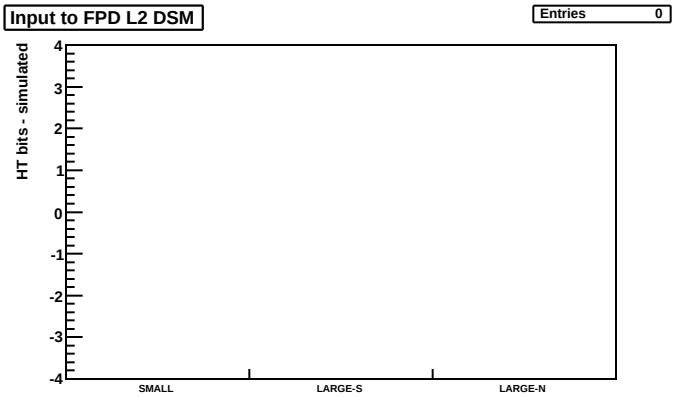

Input to FPD L2 DSM
Entries 6673560
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

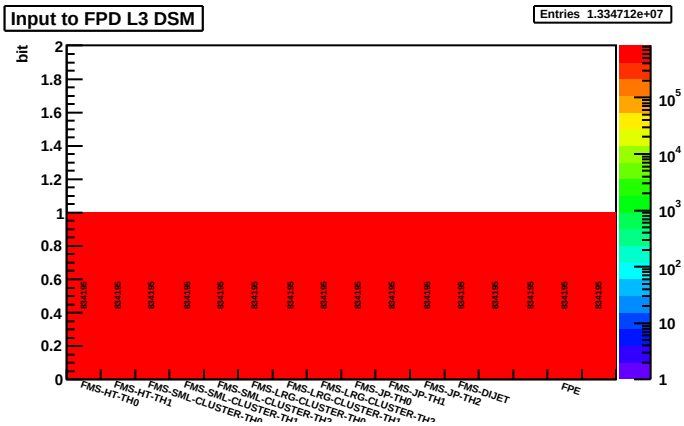

Input to FPD L2 DSM
Entries 0
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

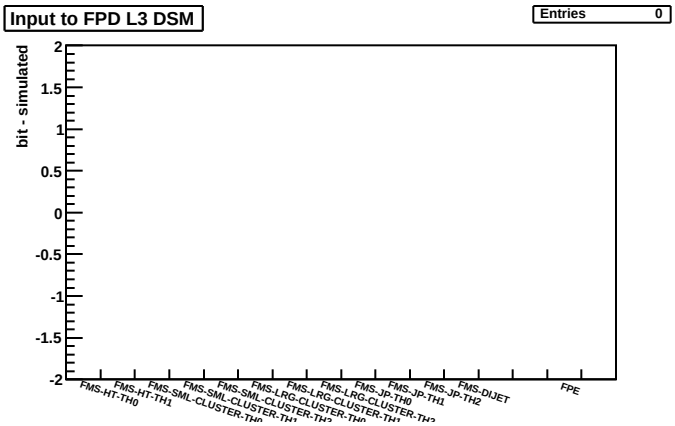

Input to FPD L2 DSM
Entries 2502585
CL bits
834195
834195
834195
SMALL
LARGE-S
LARGE-N

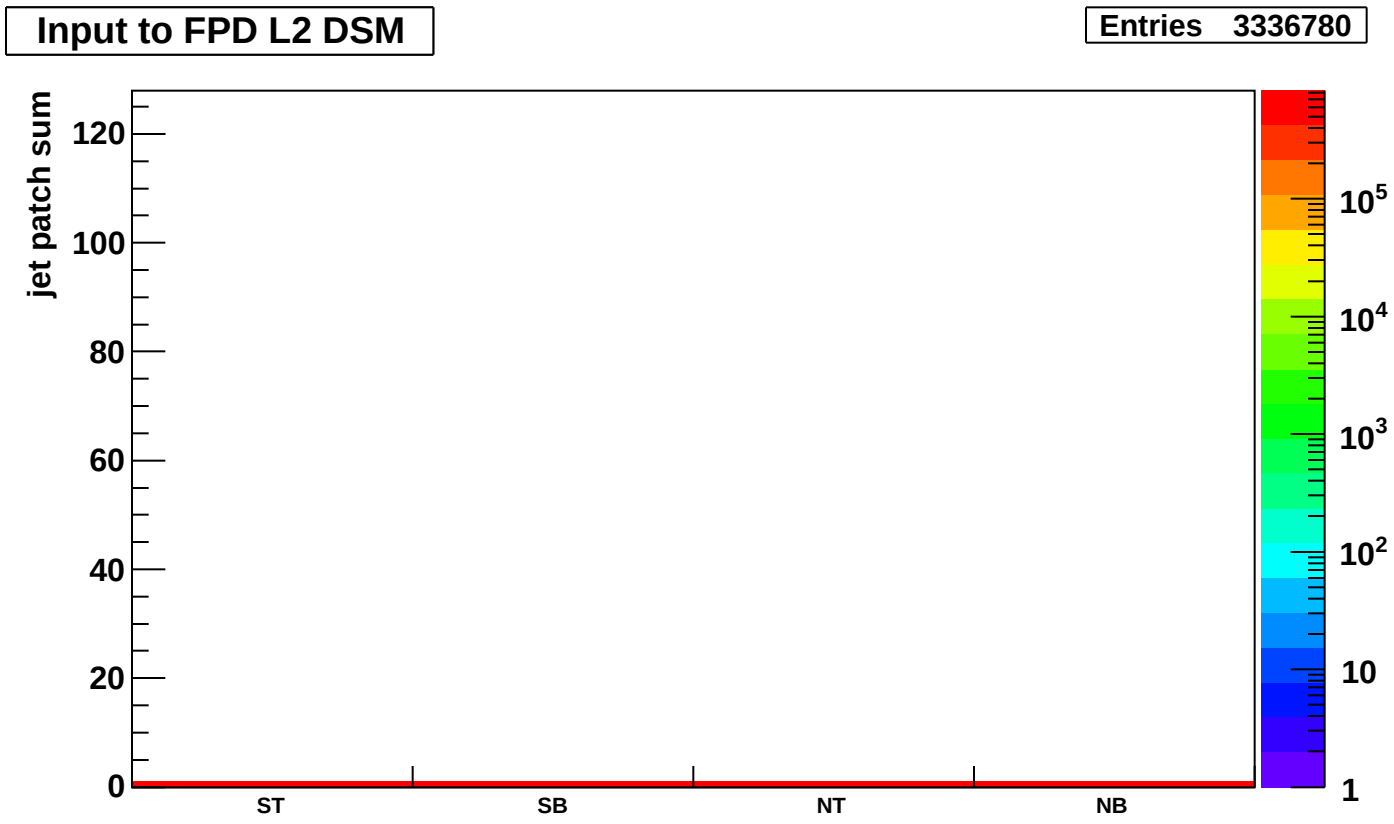

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

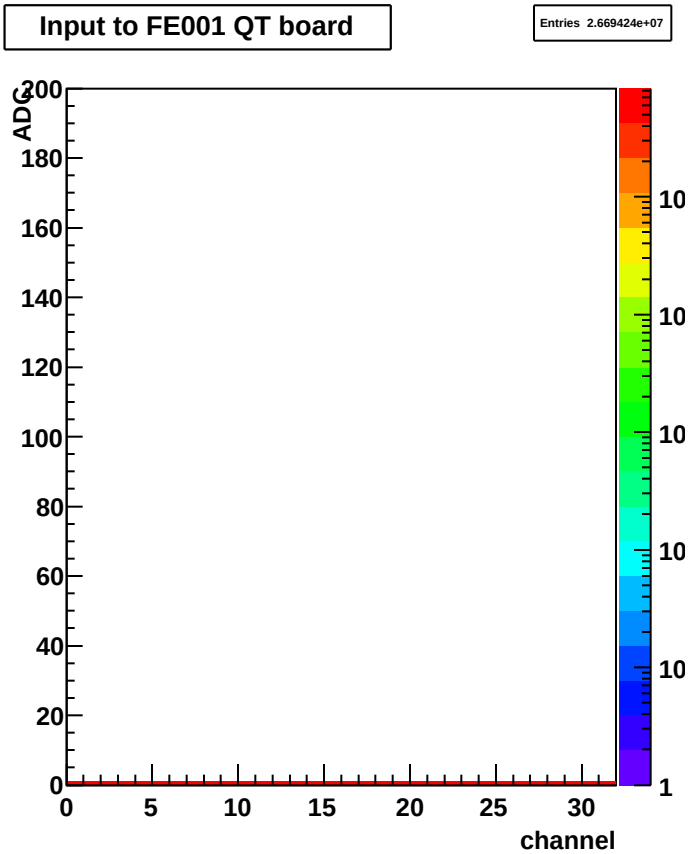

Input to FPD L2 DSM
Entries 2502585
HT bits
834195
834195
834195
SMALL
LARGE-S
LARGE-N

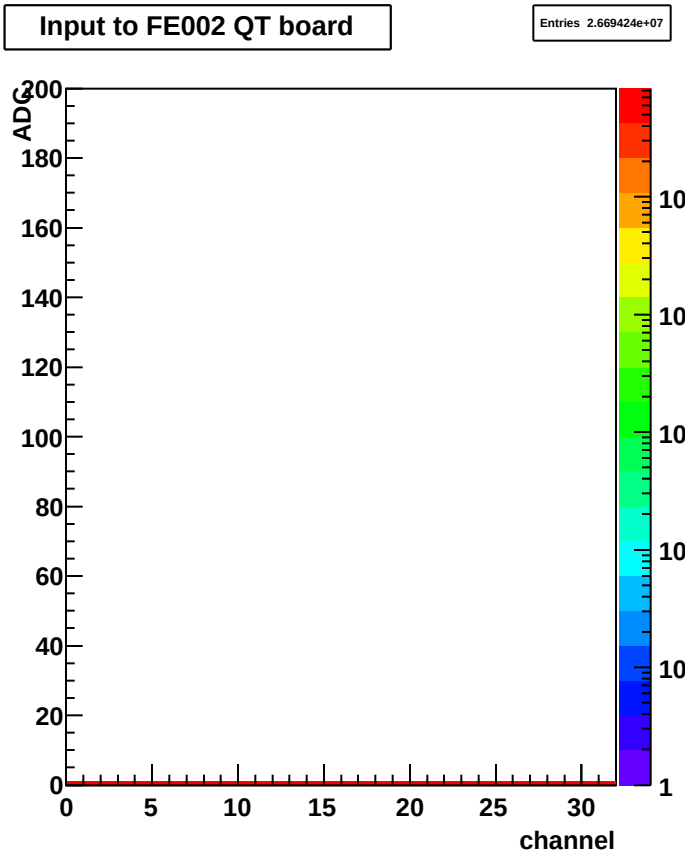

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

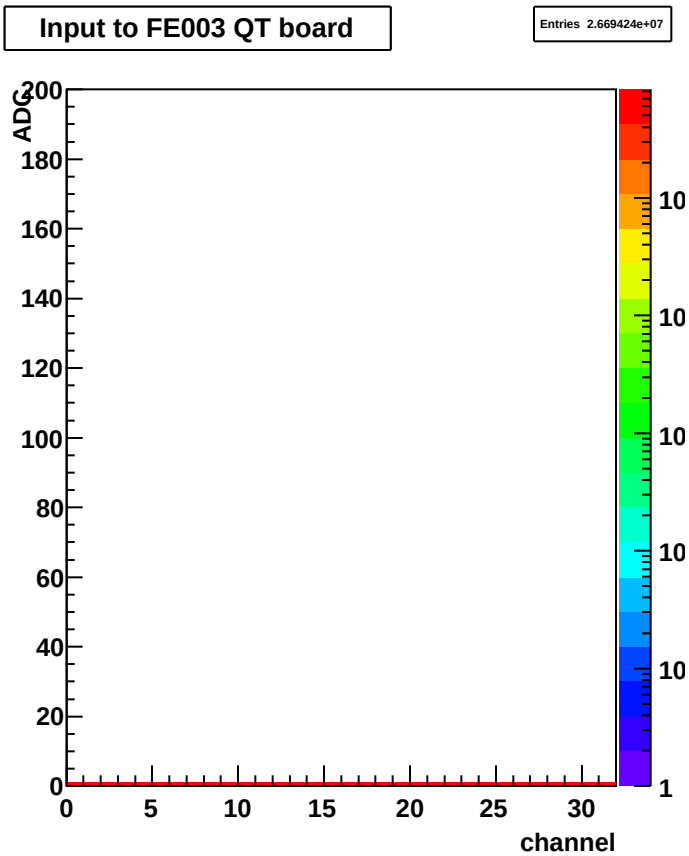

Input to FPD L3 DSM
Entries 1.334712e+07
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

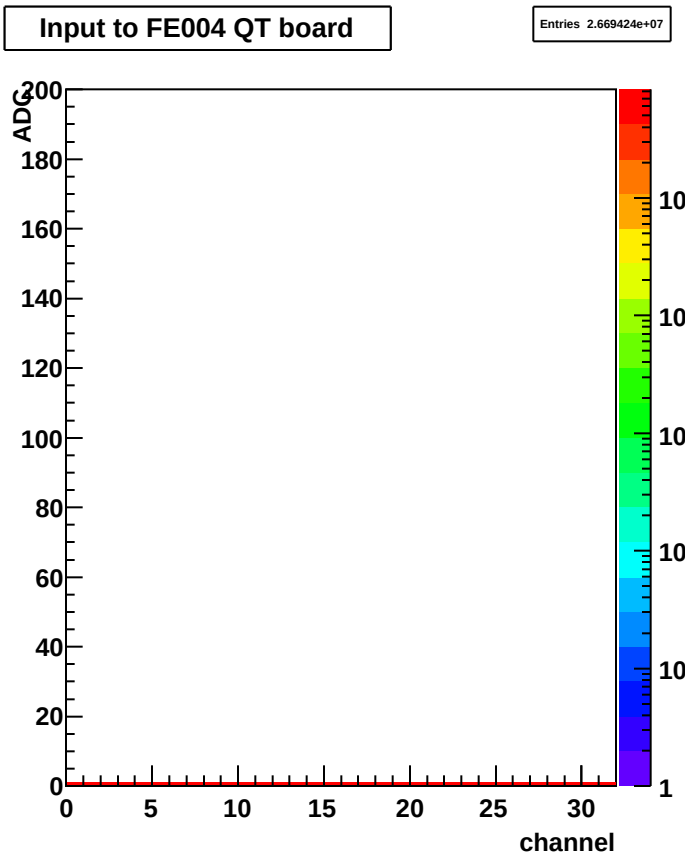

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

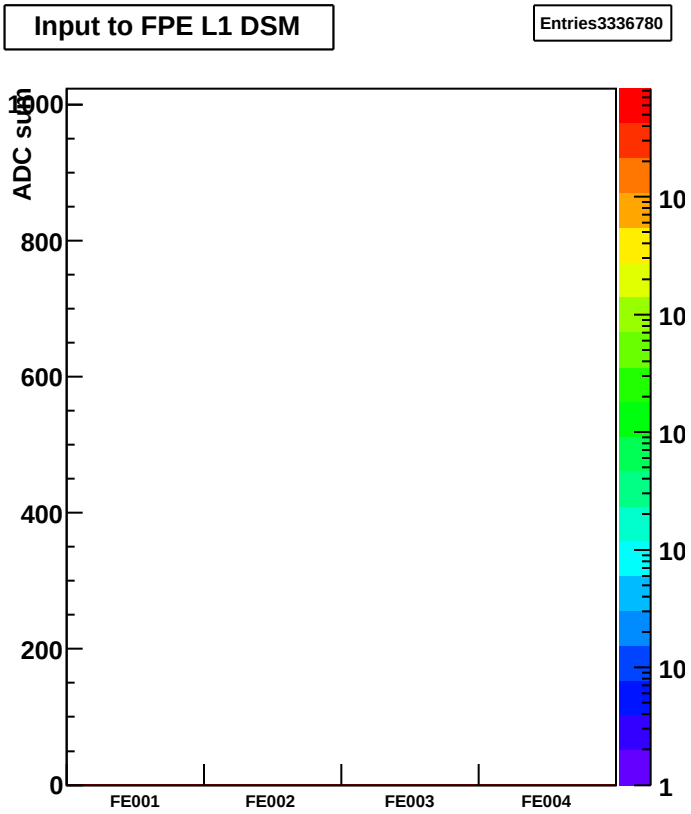

Input to FPD L2 DSM
Entries 3336780
jet patch sum
120
100
80
60
40
20
0
ST
SB
NT
NB
10^5
10^4
10^3
10^2
10
1

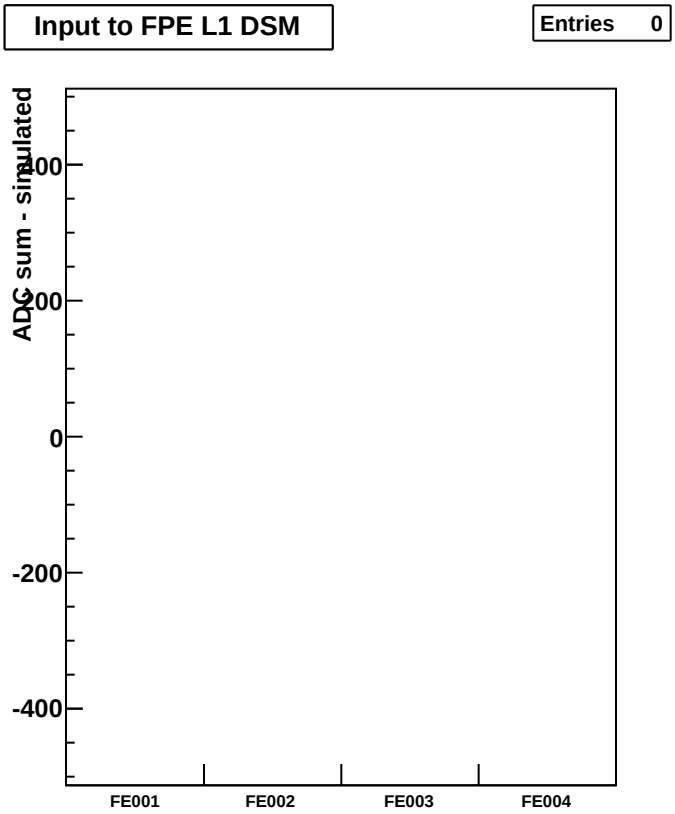

Input to FE001 QT board
Entries 2.669424e+07
ADC
channel

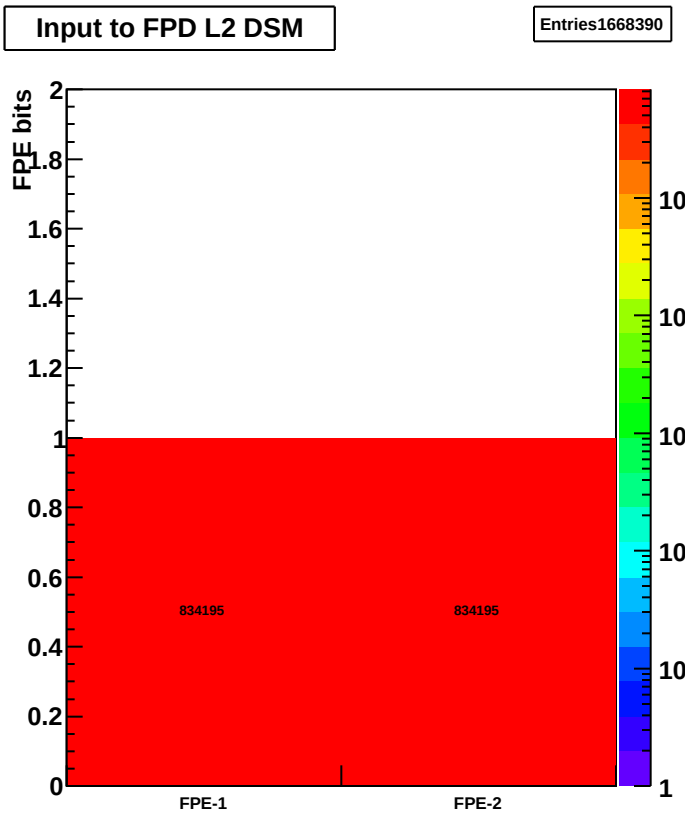

Input to FE002 QT board
Entries 2.669424e+07
ADC
channel

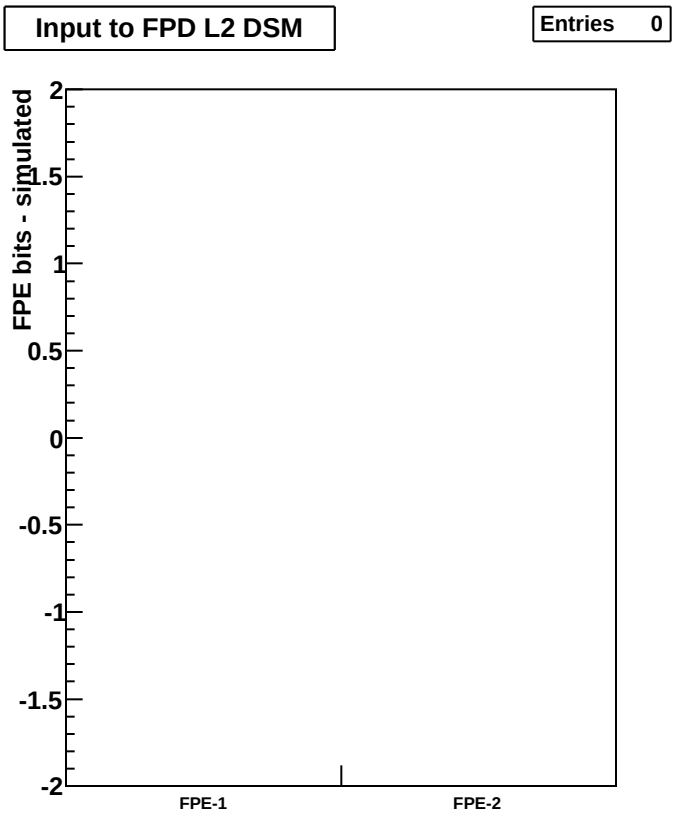

Input to FE003 QT board
Entries 2.669424e+07
ADC
channel


Input to FE004 QT board
Entries 2.669424e+07
ADC
channel


Input to FPE L1 DSM
Entries3336780
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 0
ADC sum - simulated
0
-200
-400
FE001
FE002
FE003
FE004


Input to FPD L2 DSM
Entries1668390
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
834195
834195
FPE-1
FPE-2
10
10
10
10
10
1


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2