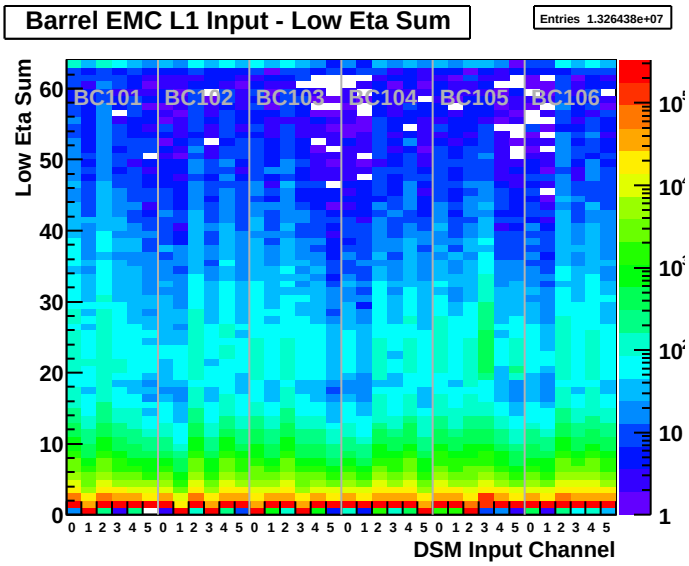
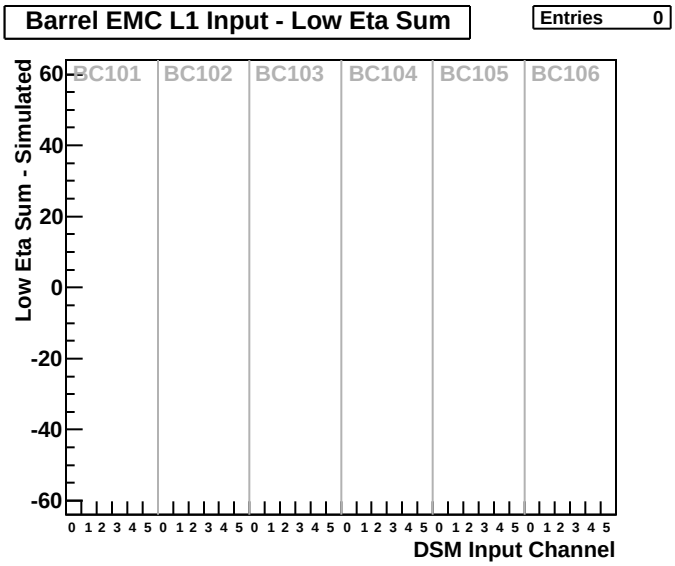
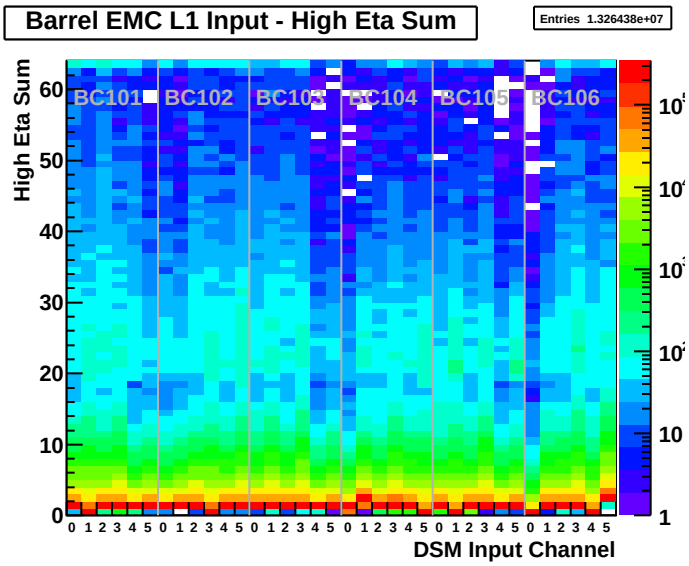
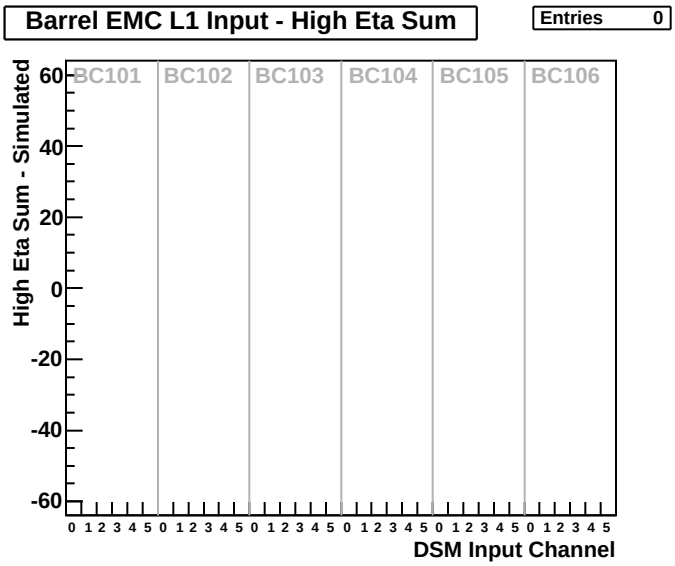
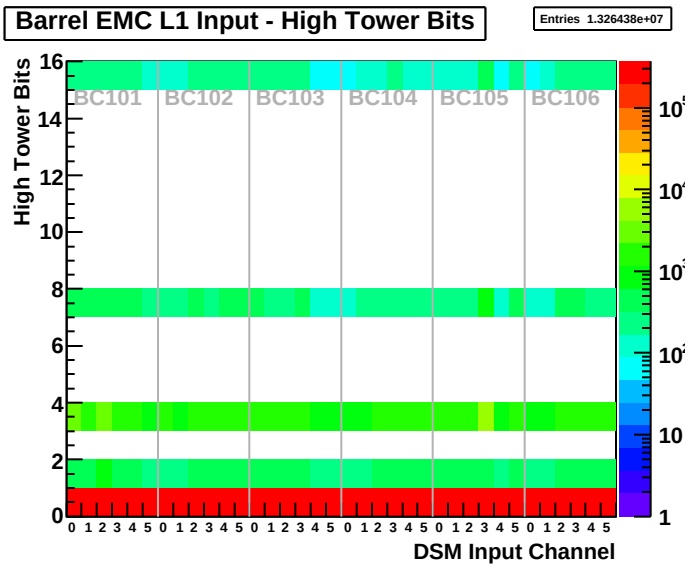
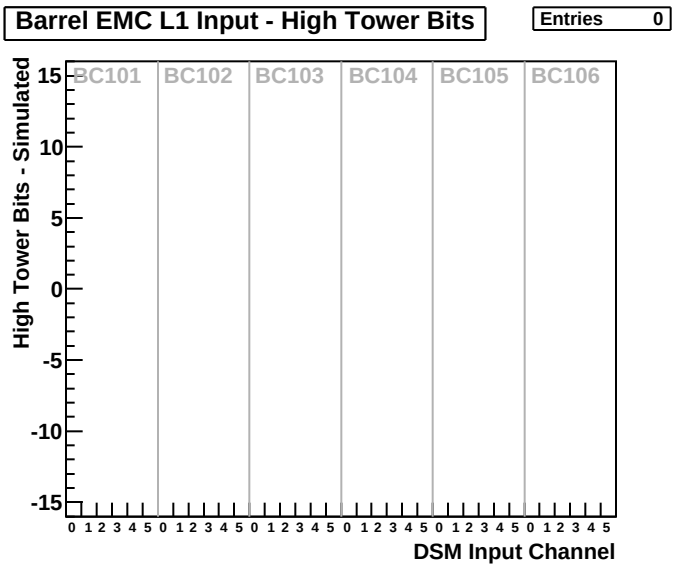

Barrel EMC L1 Input - Low Eta Sum
Entries 1.326438e+07
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

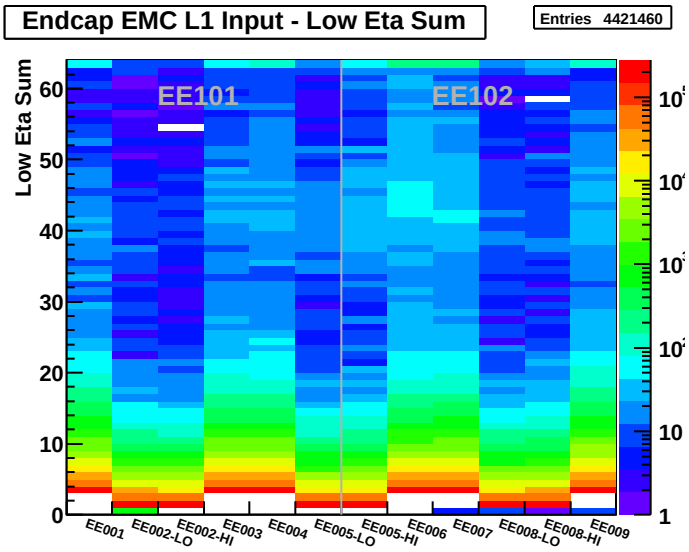

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

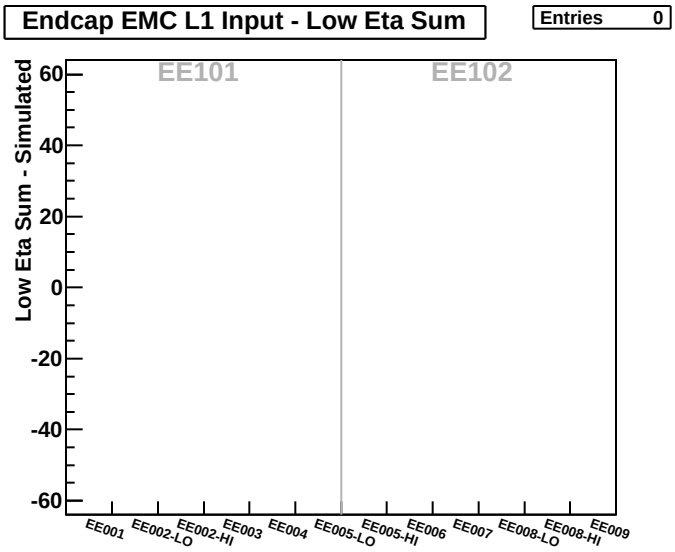

Barrel EMC L1 Input - High Eta Sum
Entries 1.326438e+07
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

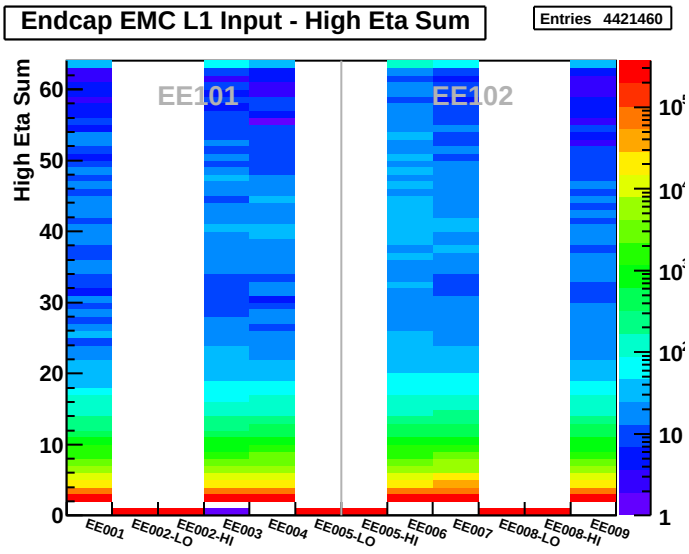

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

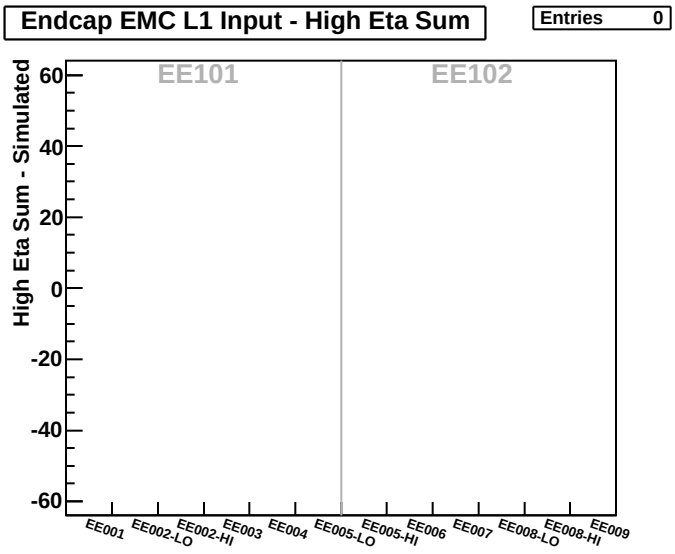

Barrel EMC L1 Input - High Tower Bits
Entries 1.326438e+07
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

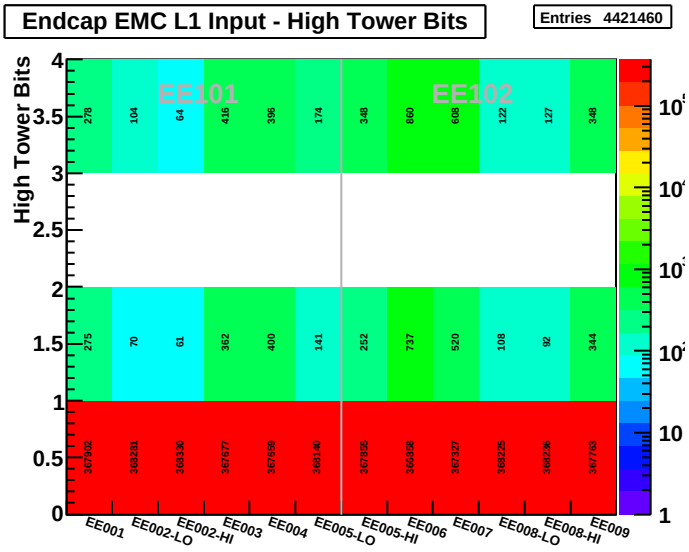

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

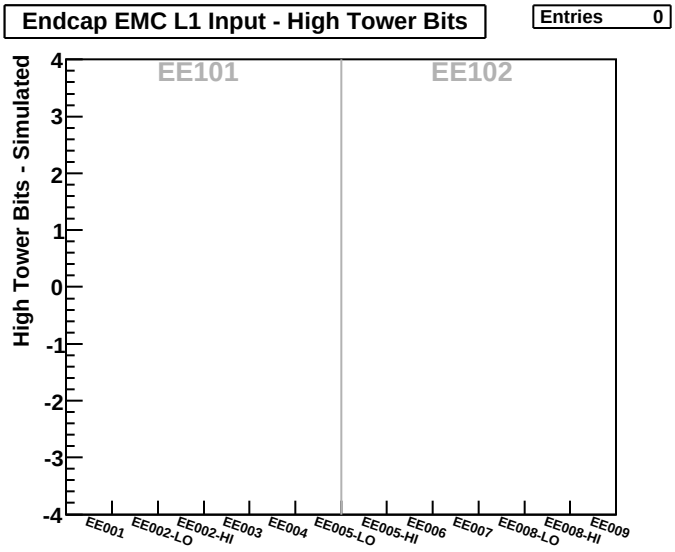

Endcap EMC L1 Input - Low Eta Sum
Entries 4421460
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

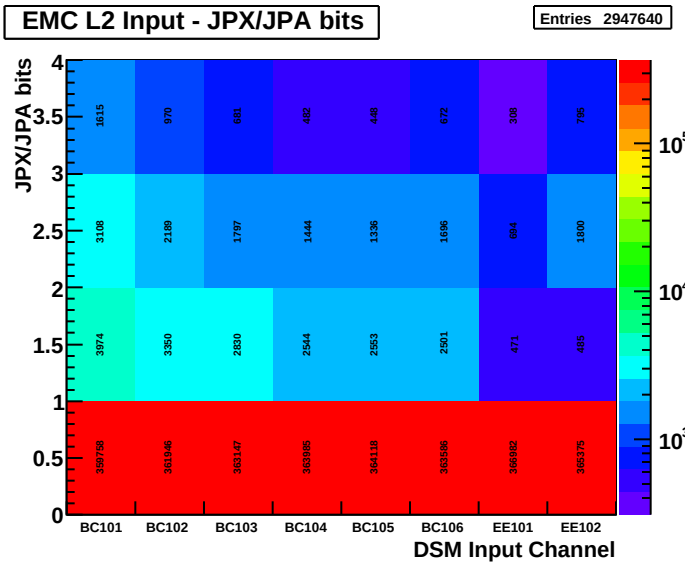

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

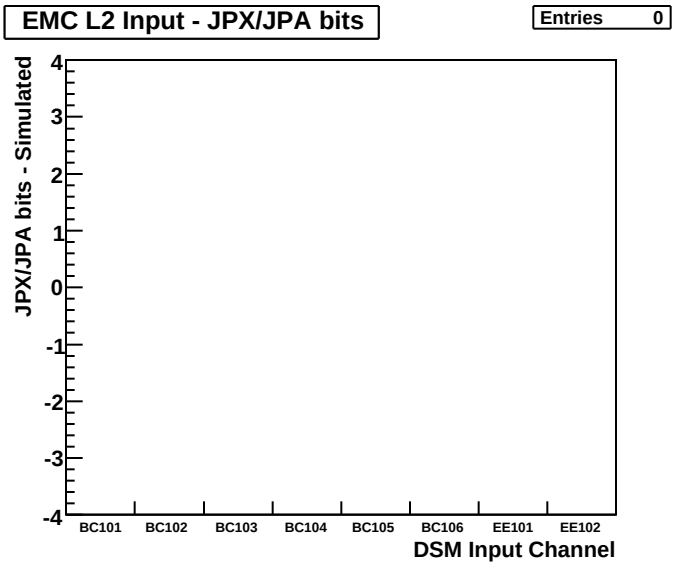

Endcap EMC L1 Input - High Eta Sum
Entries 4421460
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

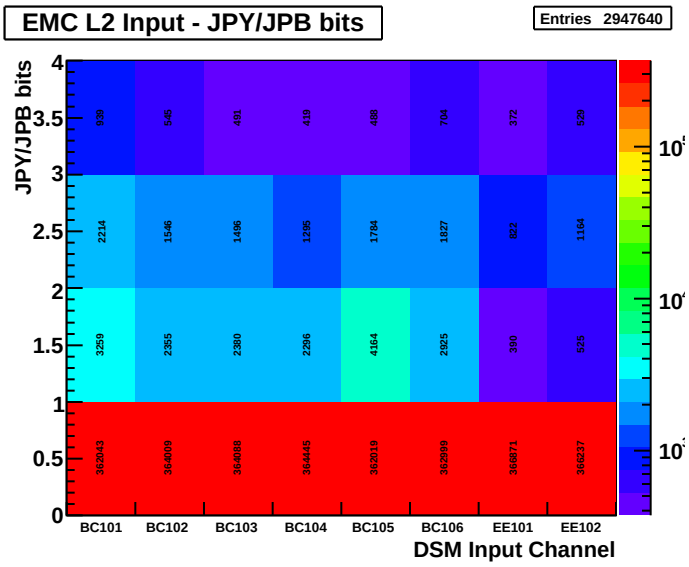

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

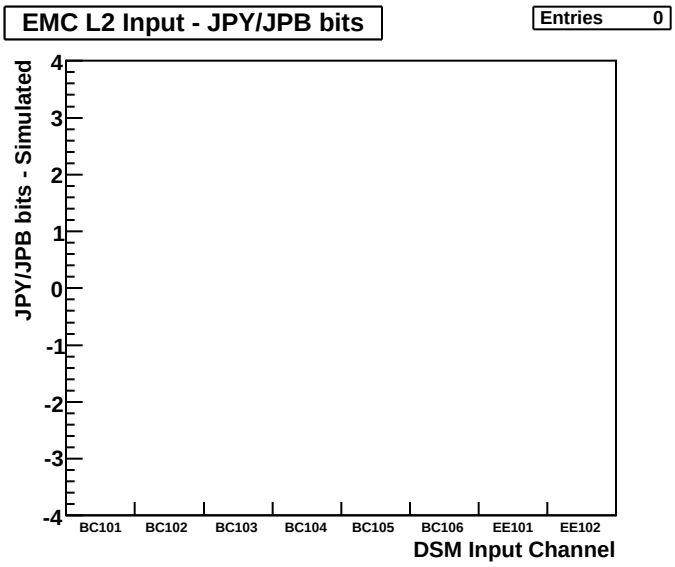

Endcap EMC L1 Input - High Tower Bits
Entries 4421460
High Tower Bits
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

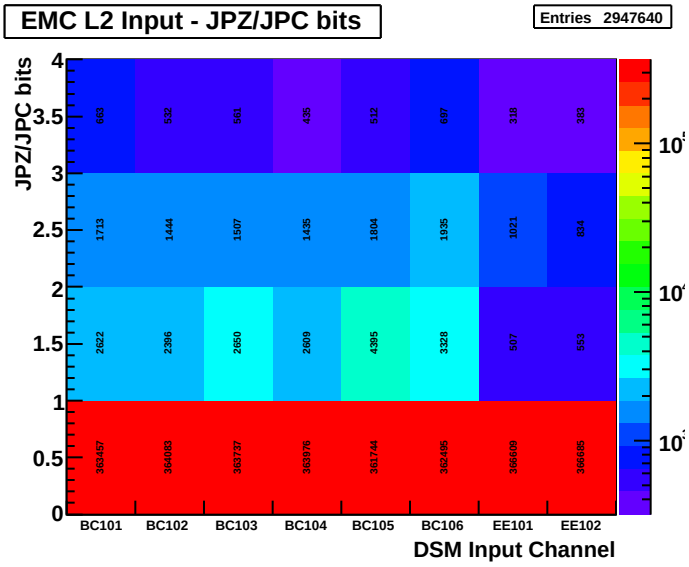

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

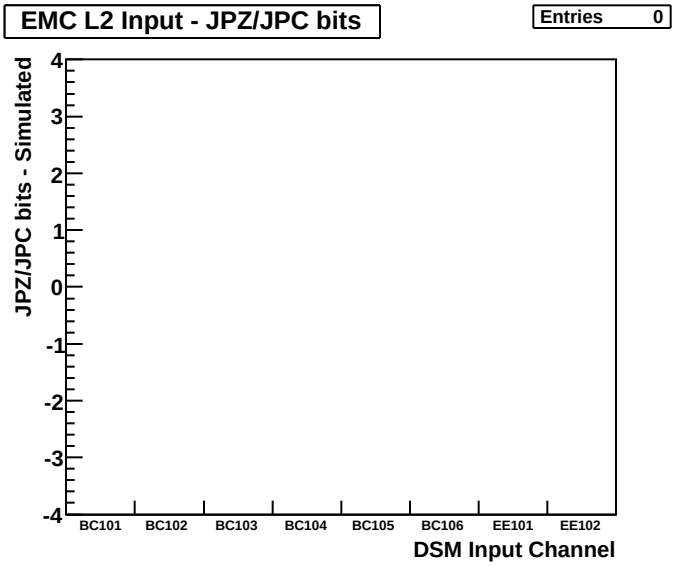

EMC L2 Input - JPX/JPA bits
Entries 2947640
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
1615 970 681 482 448 672 308 795
3108 2189 1797 1444 1336 1696 694 1800
3974 3350 2830 2544 2553 2501 471 485
359758 361946 362147 363985 364118 363586 366932 365375

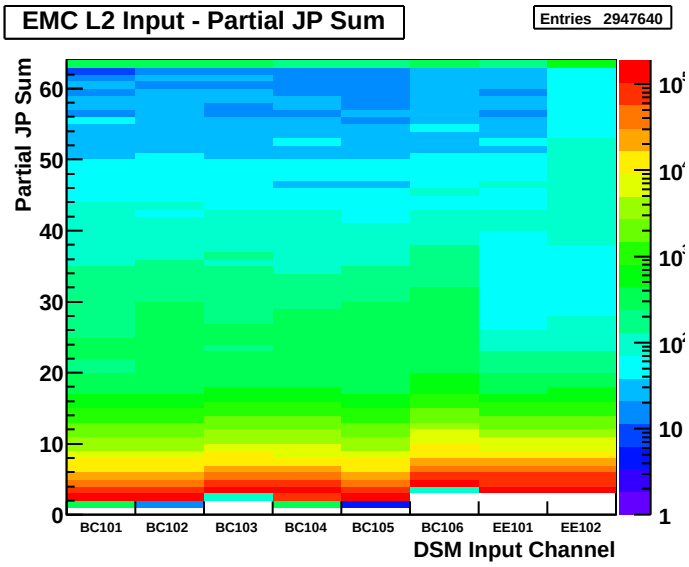

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

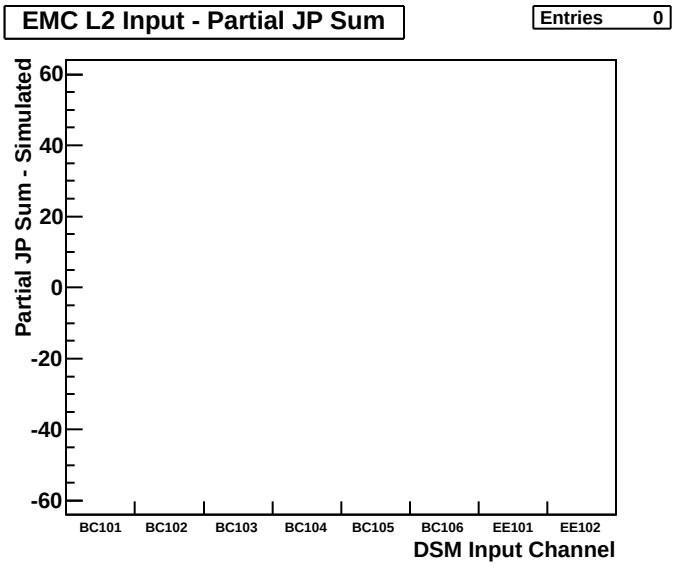

EMC L2 Input - JPY/JPB bits
Entries 2947640
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
938 545 491 419 488 704 372 529
2214 1546 1496 1295 1784 1827 822 1164
3259 2355 2380 2296 4164 2925 390 525
362043 364009 364088 364445 362019 362999 366871 366237

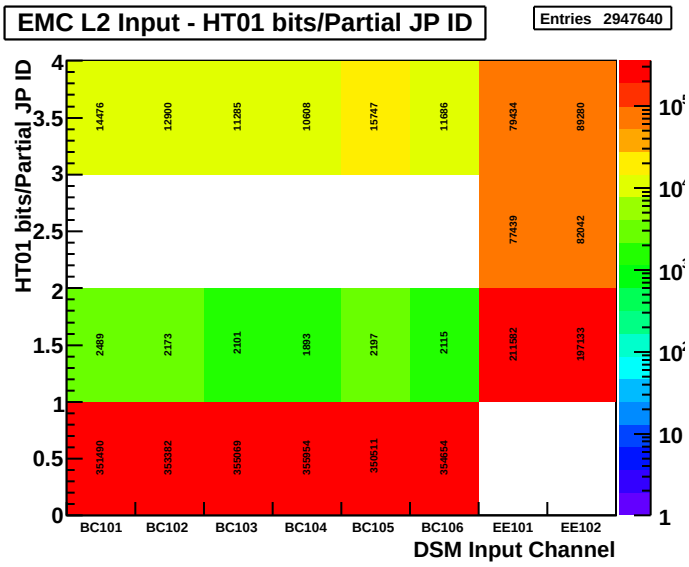

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

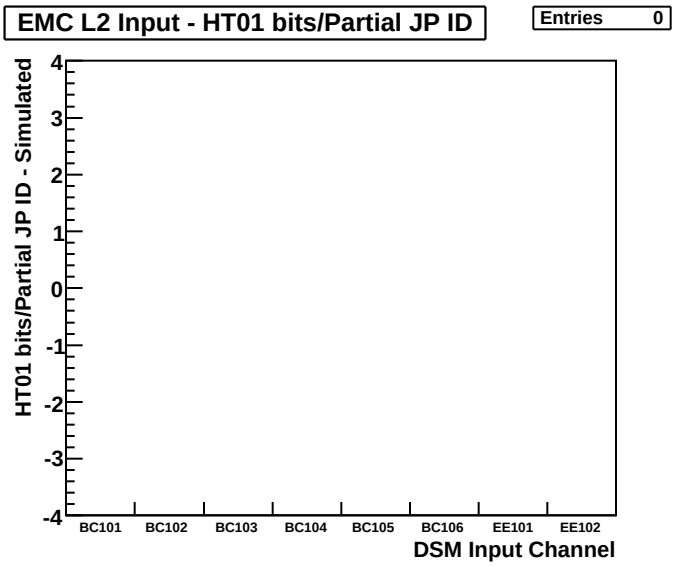

EMC L2 Input - JPZ/JPC bits
Entries 2947640
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
683 532 561 435 512 697 318 383
1713 1444 1507 1435 1804 1935 1021 834
2622 2396 2650 2609 4395 3328 507 553
363457 364083 363737 363976 361744 362495 366609 366685

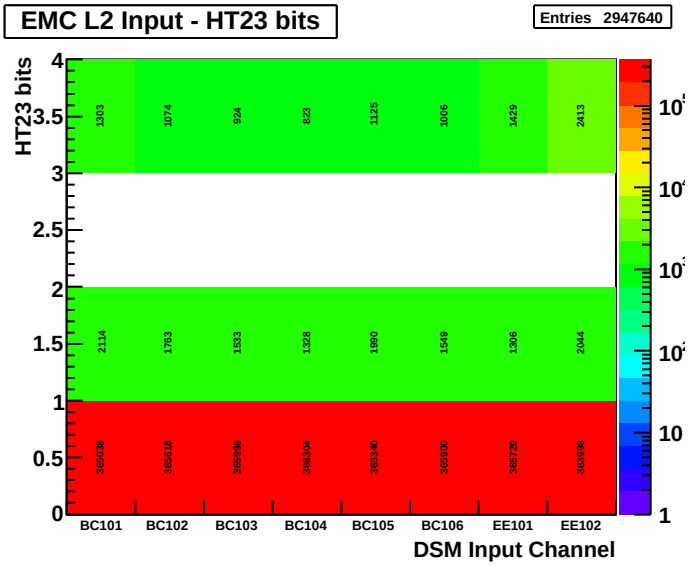

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

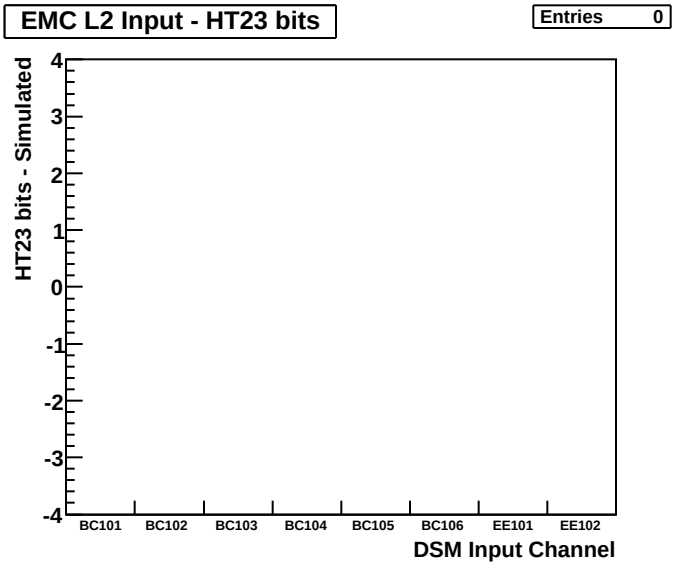

EMC L2 Input - Partial JP Sum
Entries 2947640
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

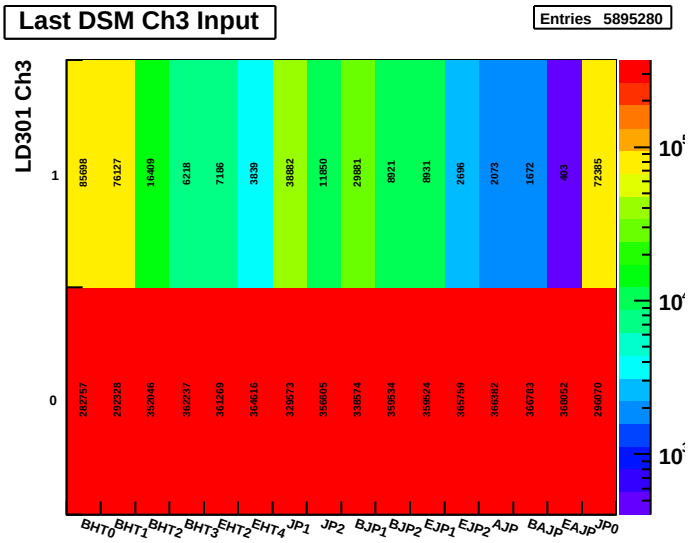

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

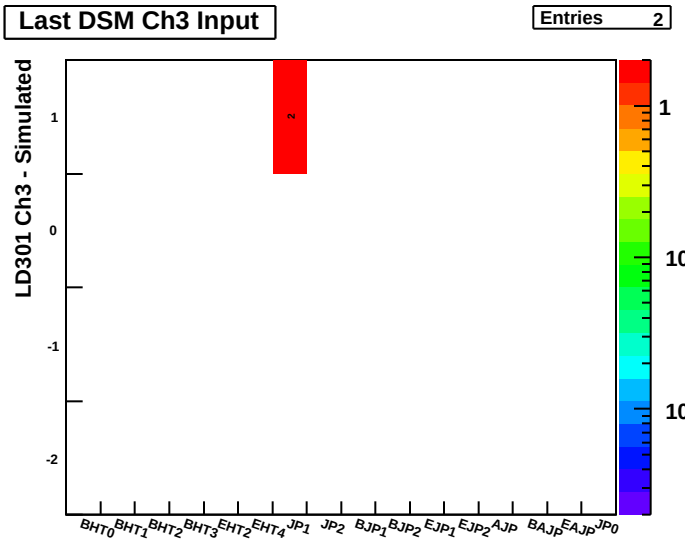

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2947640
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

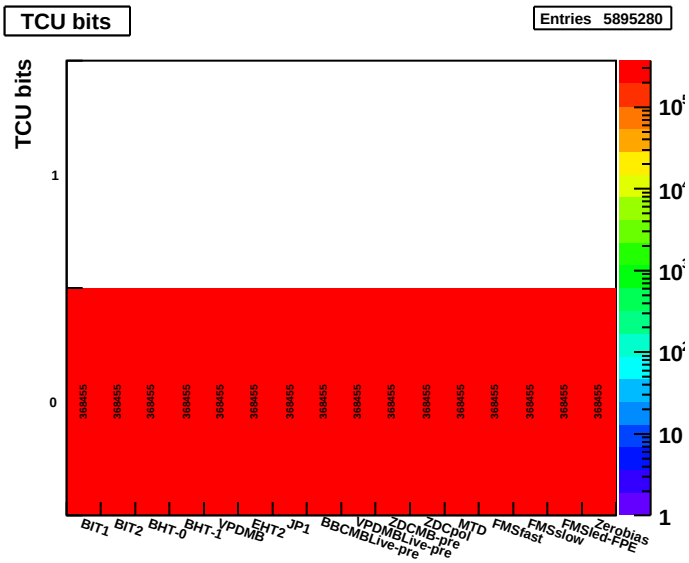

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

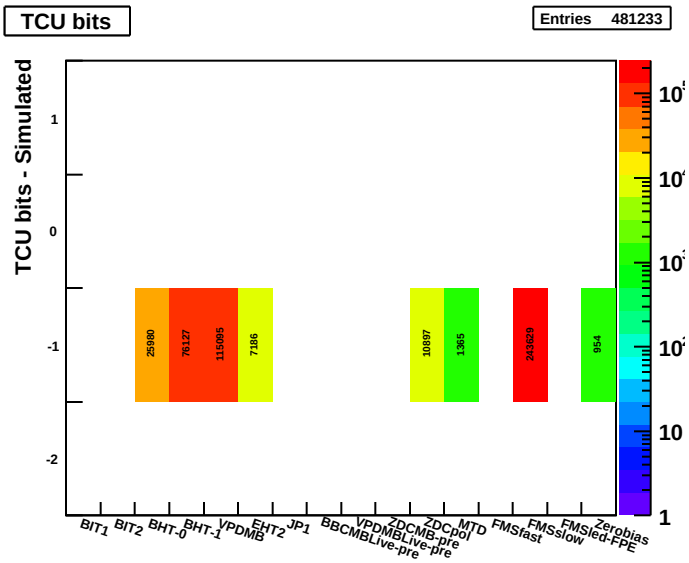

EMC L2 Input - HT23 bits
Entries 2947640
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

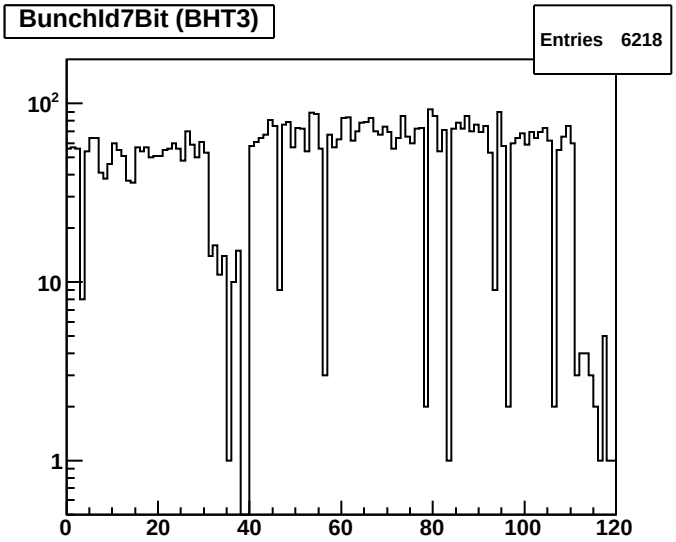

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

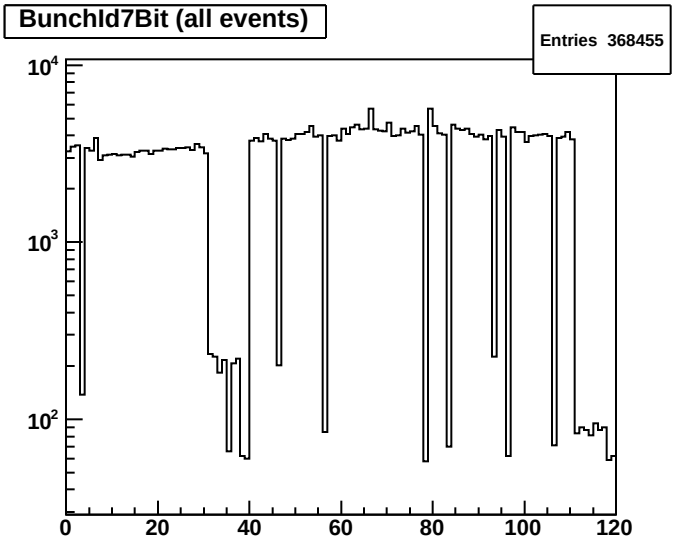

Last DSM Ch3 Input
Entries 5895280
LD301 Ch3
85698 76127 16409 6218 7186 3839 38882 11850 29981 8921 8931 2696 2073 1672 403 72395
282757 292328 352046 362237 361269 364616 329573 356605 338574 359534 359524 365759 366382 366783 368052 296070
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

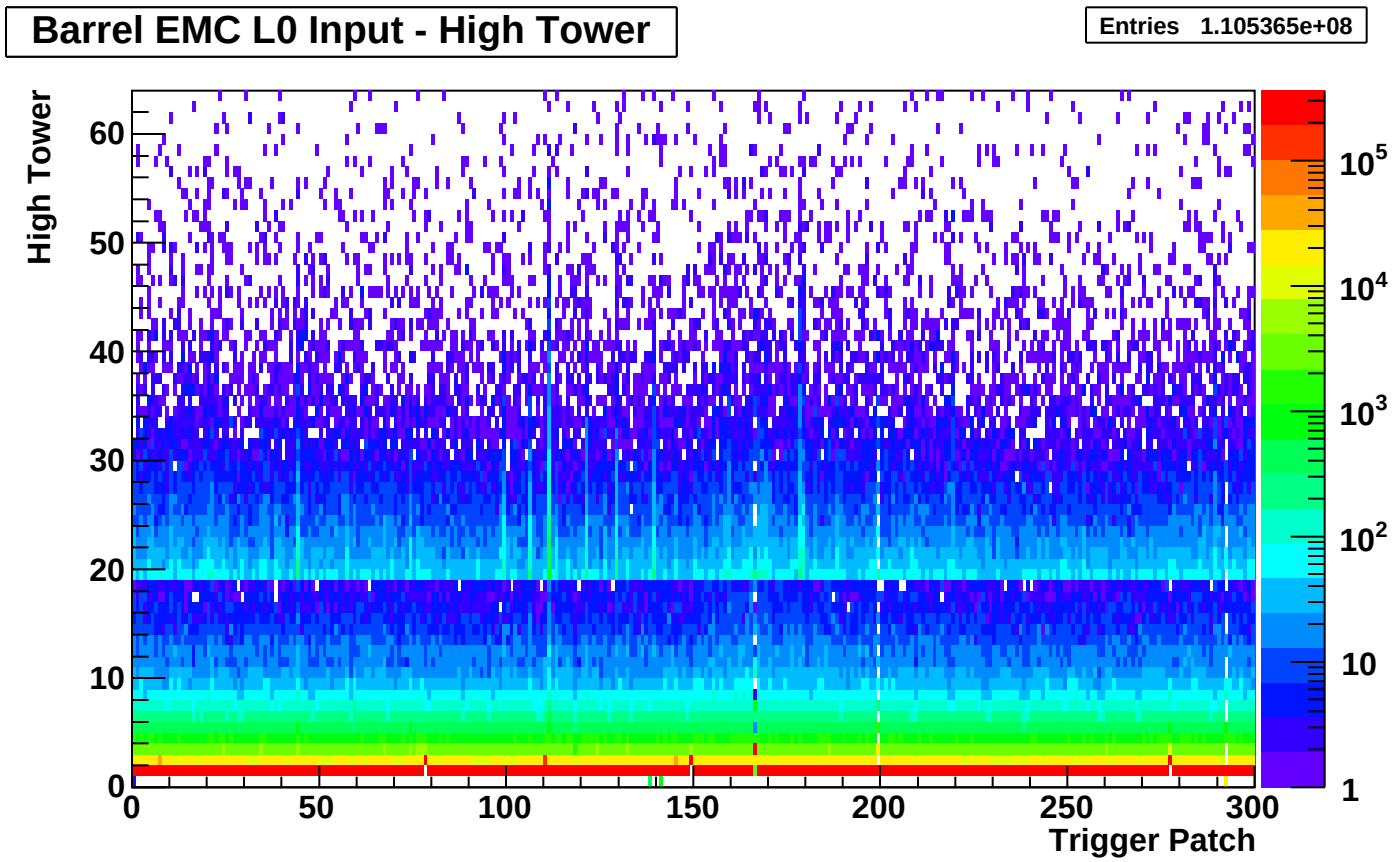

Last DSM Ch3 Input
Entries 2
LD301 Ch3 - Simulated
2
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

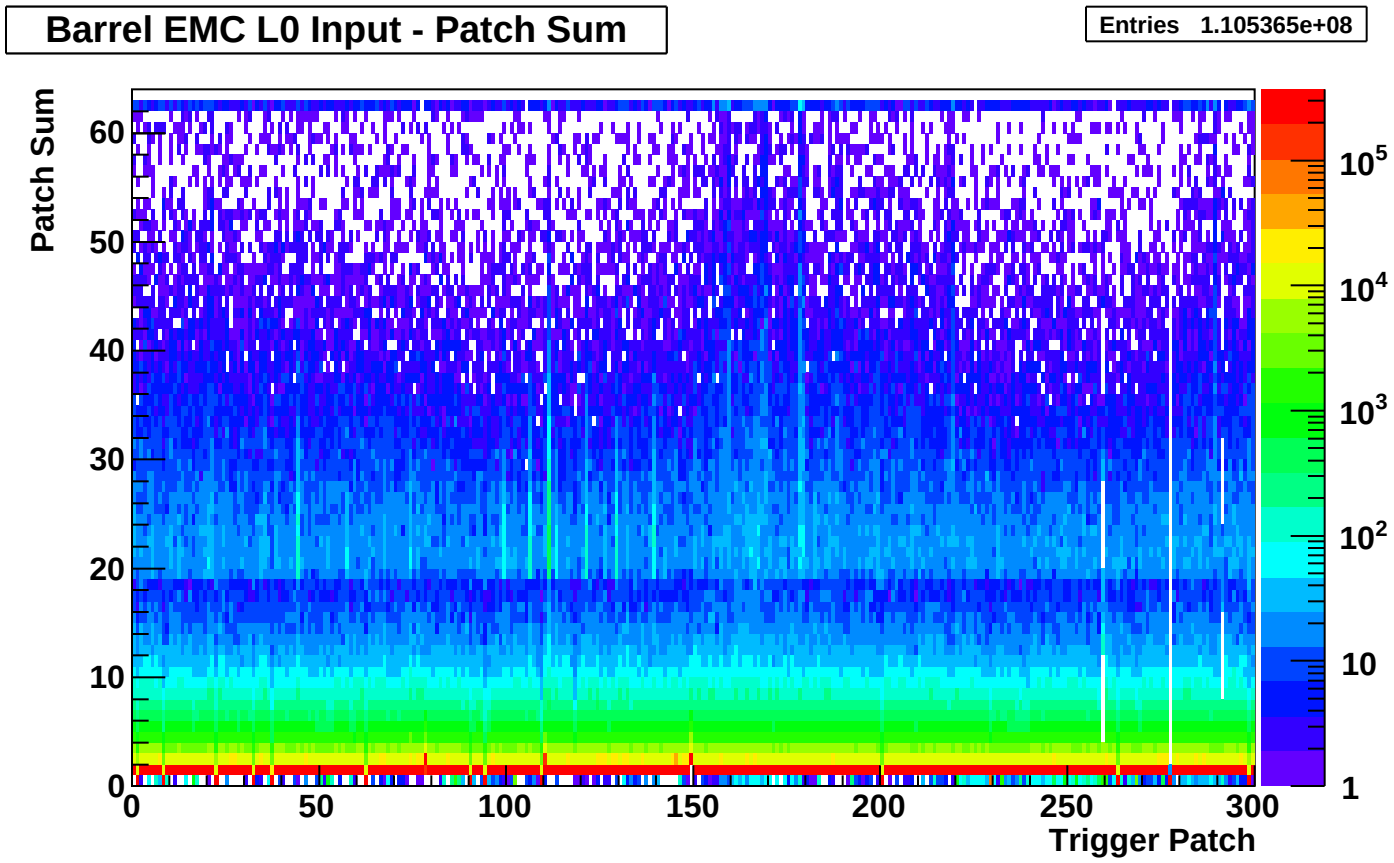

TCU bits
Entries 5895280
TCU bits
368455
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

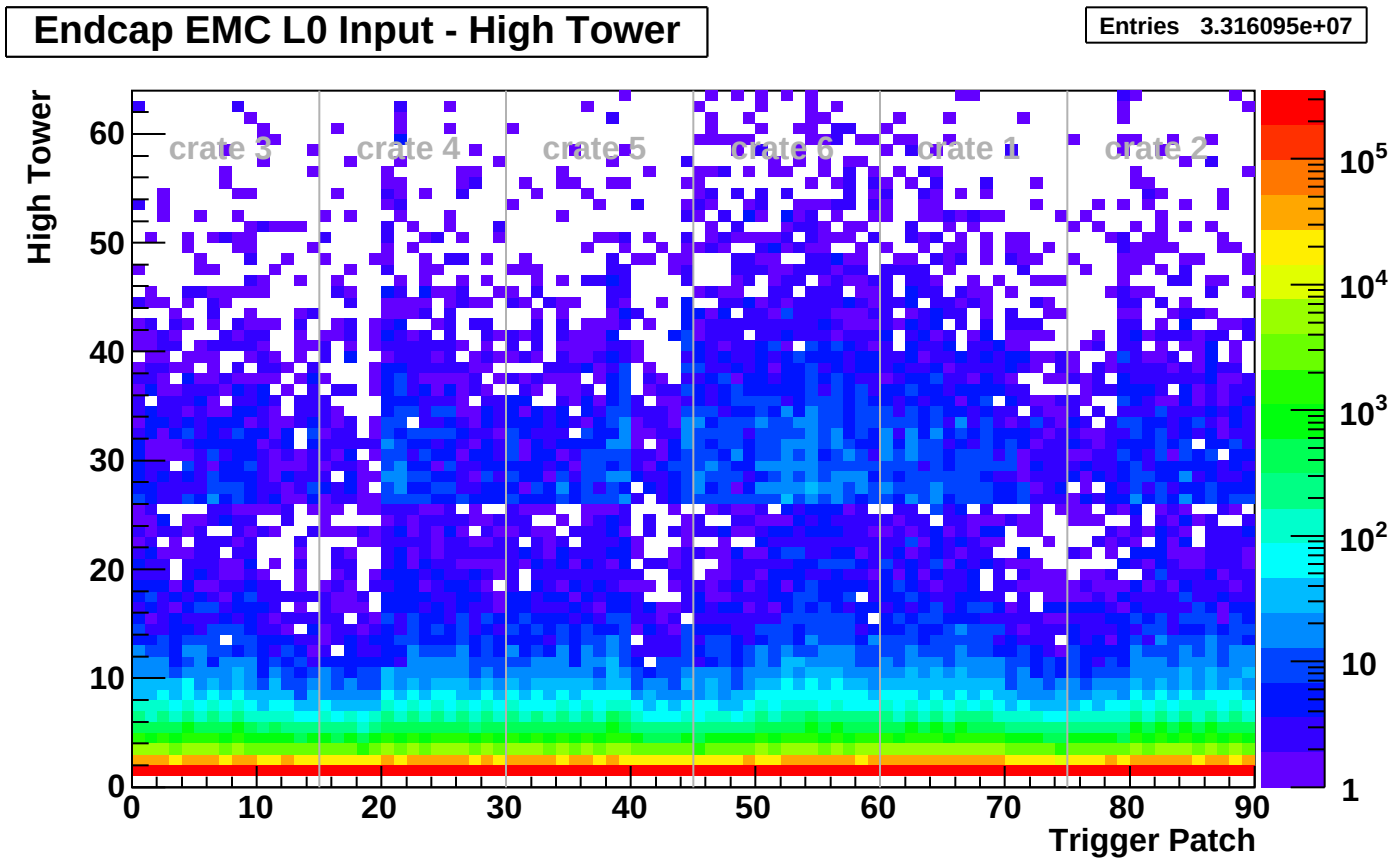

TCU bits
Entries 481233
TCU bits - Simulated
25980 76127 115095 7186 10897 1365 243629 954
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDC-pol MTD FMSfast FMSslow FMSled-FPE Zerobias

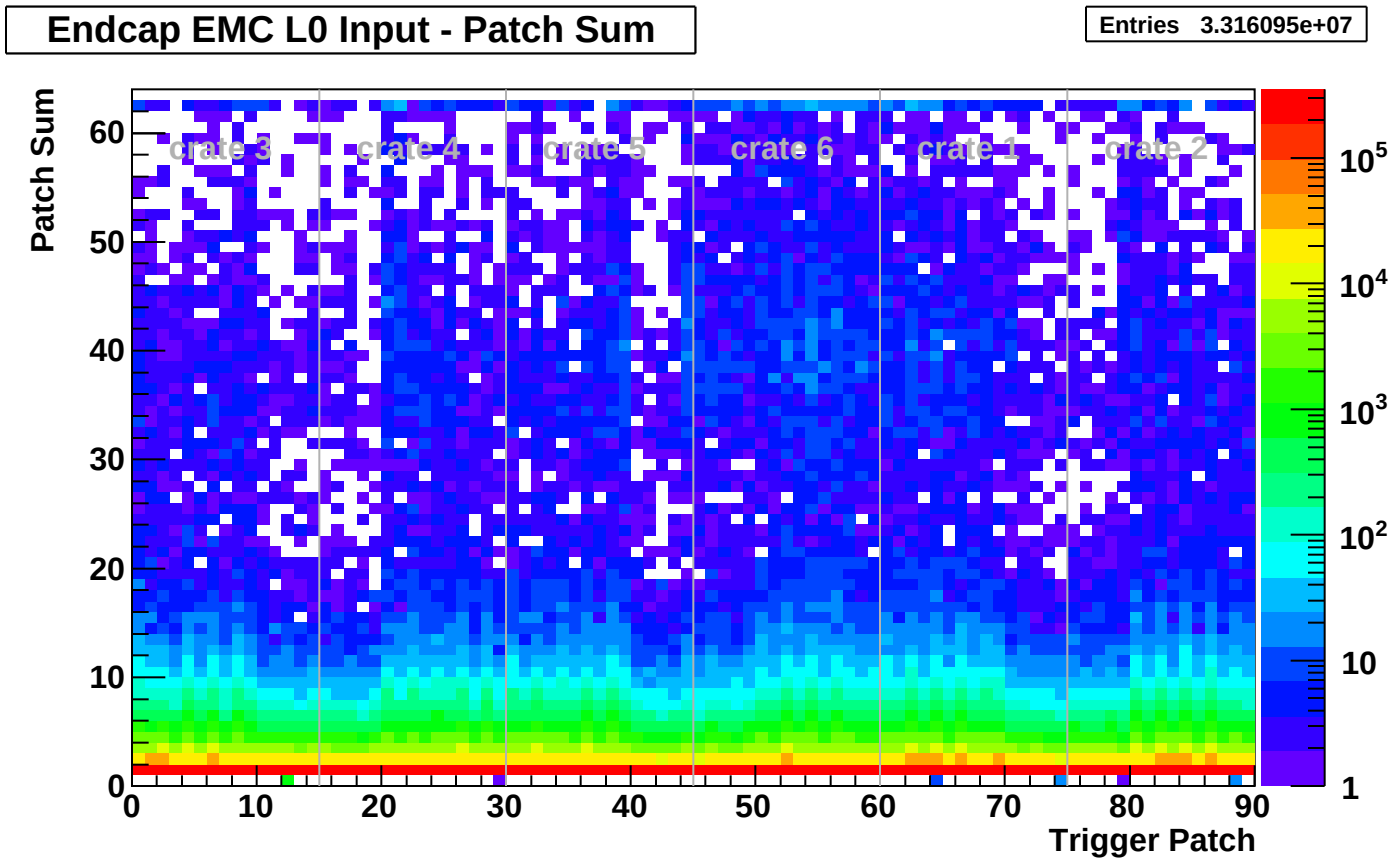

BunchId7Bit (BHT3)
Entries 6218

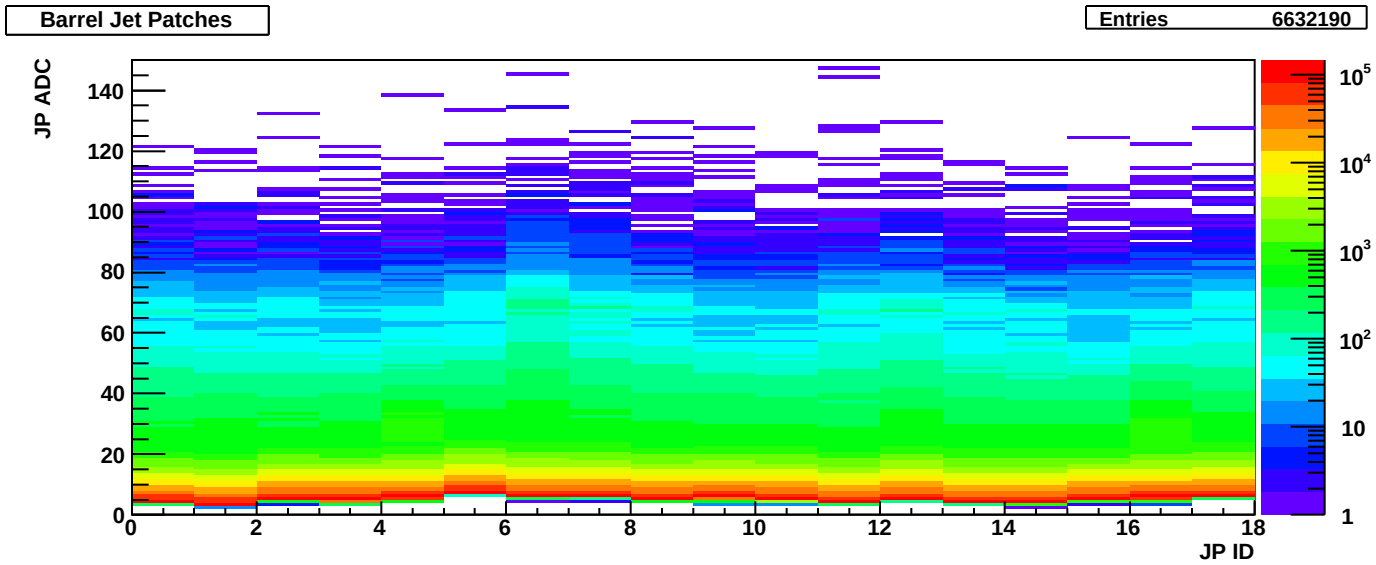

BunchId7Bit (all events)
Entries 368455

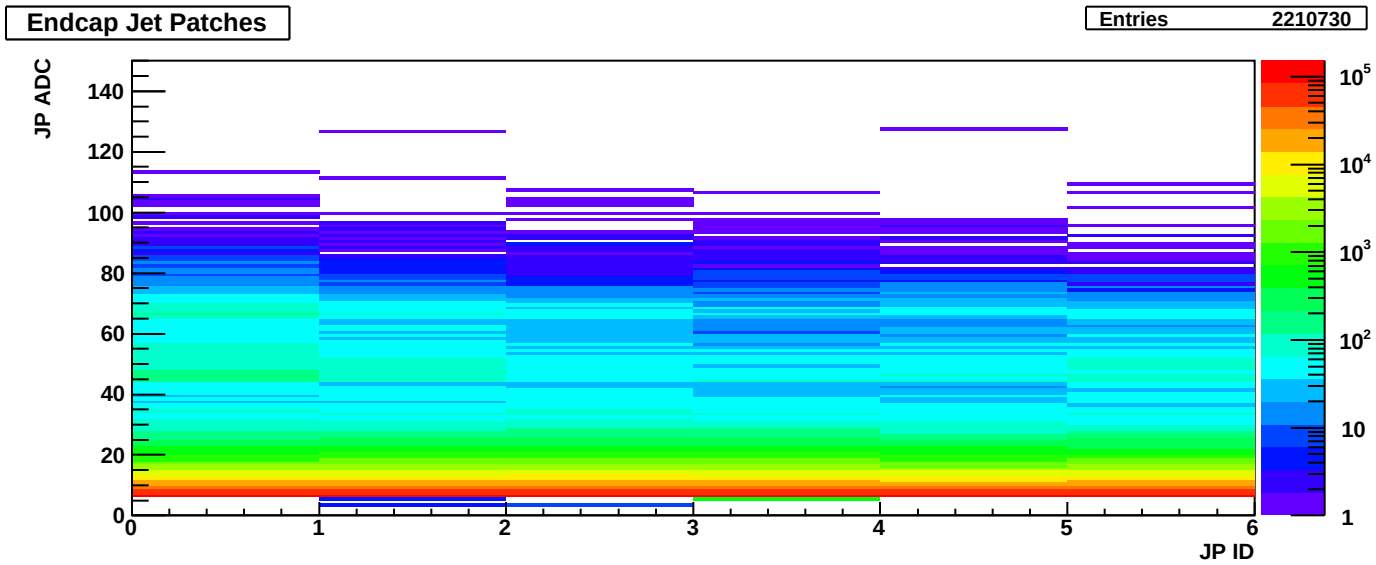

Barrel EMC L0 Input - High Tower
Entries 1.105365e+08
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

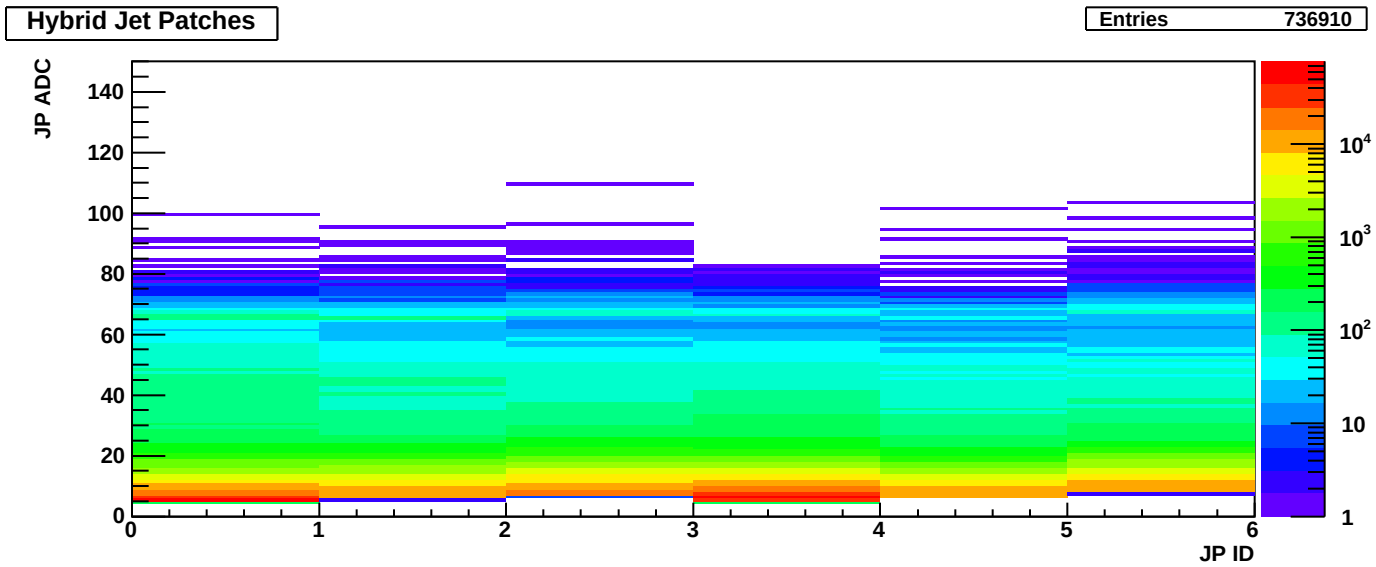

Barrel EMC L0 Input - Patch Sum
Entries 1.105365e+08
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
$10^2$
$10^3$
$10^4$
$10^5$

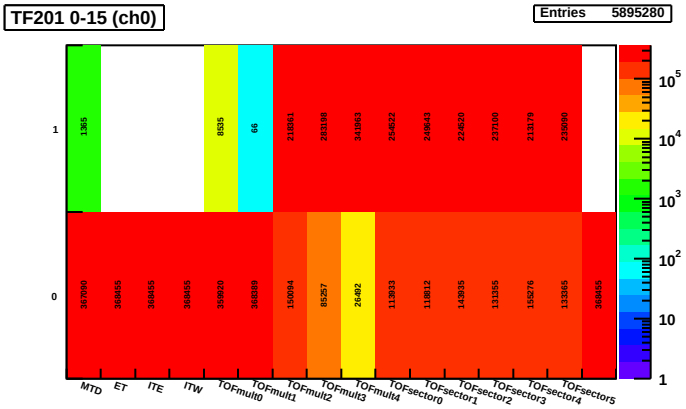

Endcap EMC L0 Input - High Tower
Entries 3.316095e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

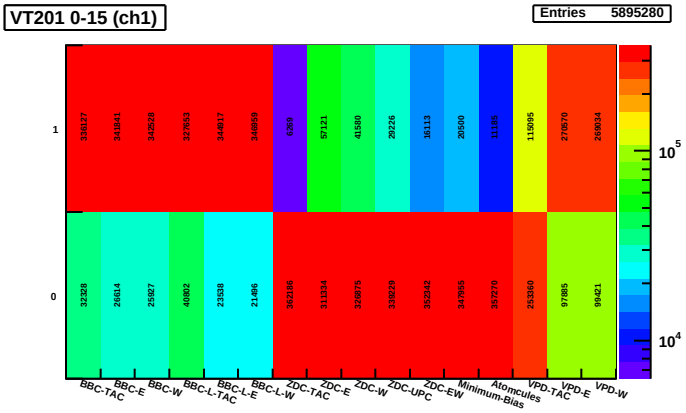

Endcap EMC L0 Input - Patch Sum
Entries 3.316095e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

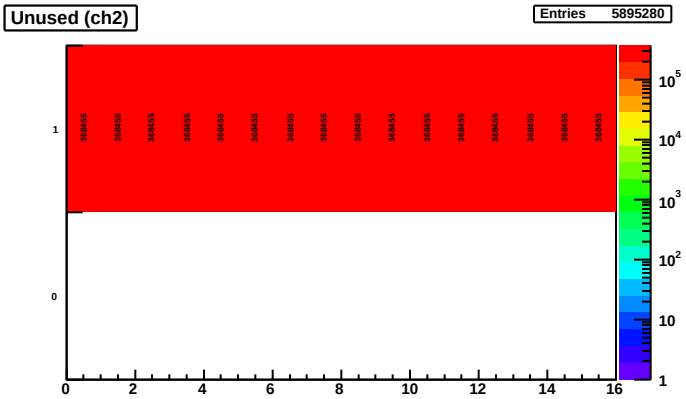

Barrel Jet Patches
Entries 6632190
JP ADC
JP ID

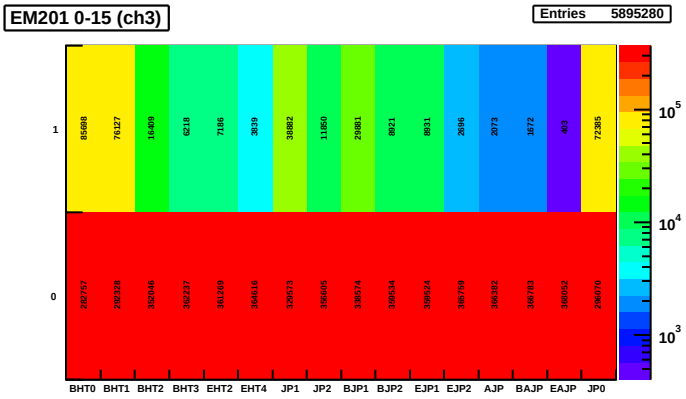

Endcap Jet Patches
Entries 2210730
JP ADC
JP ID

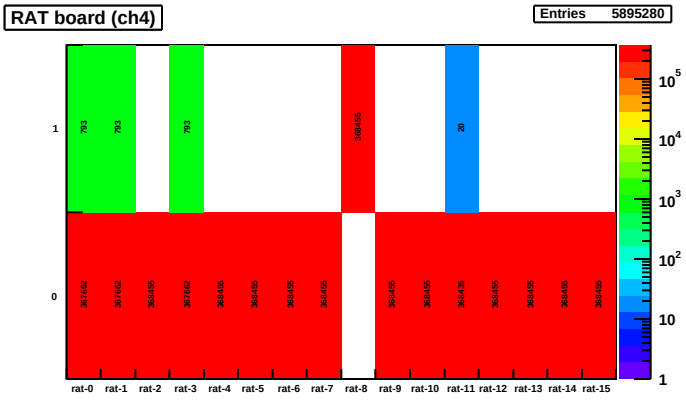

Hybrid Jet Patches
Entries 736910
JP ADC
JP ID

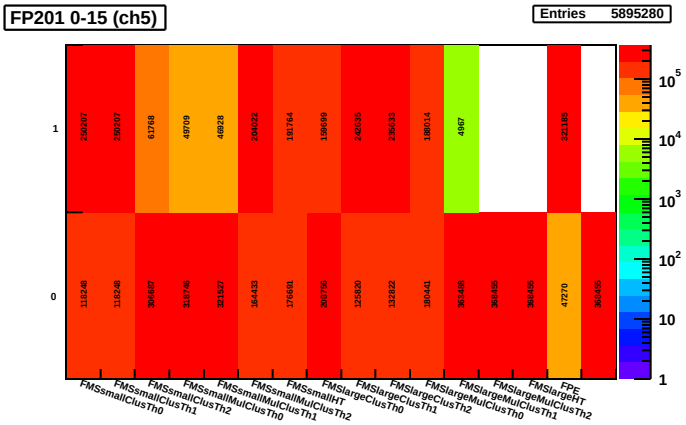

TF201 0-15 (ch0)
Entries 5895280
MTD
ET
ITE
ITW
TOFmult0
TOFmult1
TOFmult2
TOFmult3
TOFmult4
TOFsector0
TOFsector1
TOFsector2
TOFsector3
TOFsector4
TOFsector5

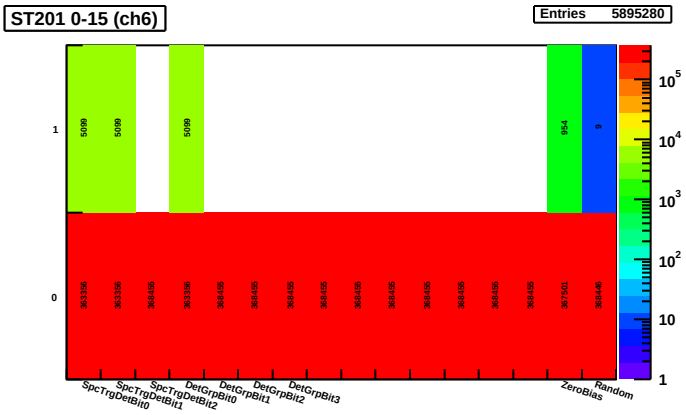

VT201 0-15 (ch1)
Entries 5895280
BBC-TAC
BBC-E
BBC-W
BBC-L-TAC
BBC-L-E
BBC-L-W
ZDC-TAC
ZDC-E
ZDC-W
ZDC-UPC
ZDC-EW
Minimum-Bias
Atomcules
VPD-TAC
VPD-E
VPD-W

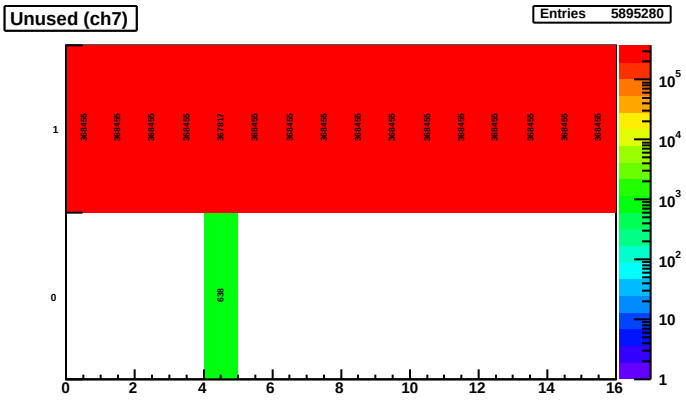

Unused (ch2)
Entries 5895280

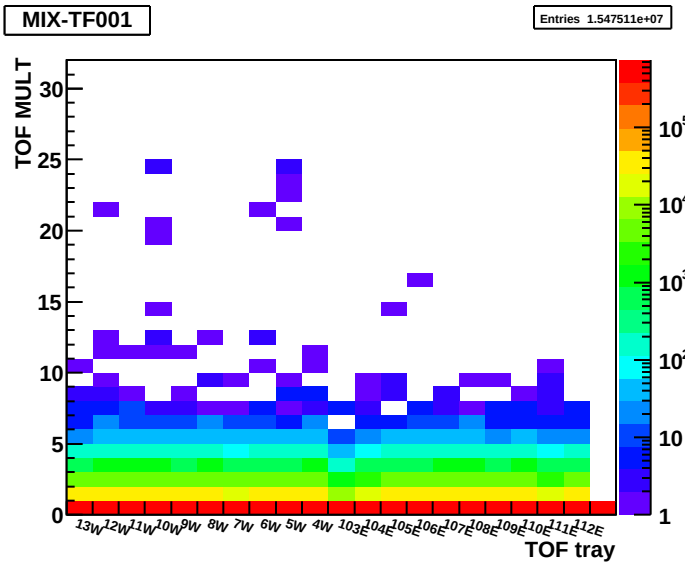

EM201 0-15 (ch3)
Entries 5895280
BHT0
BHT1
BHT2
BHT3
EHT2
EHT4
JP1
JP2
BJP1
BJP2
EJP1
EJP2
AJP
BAJP
EAJP
JP0

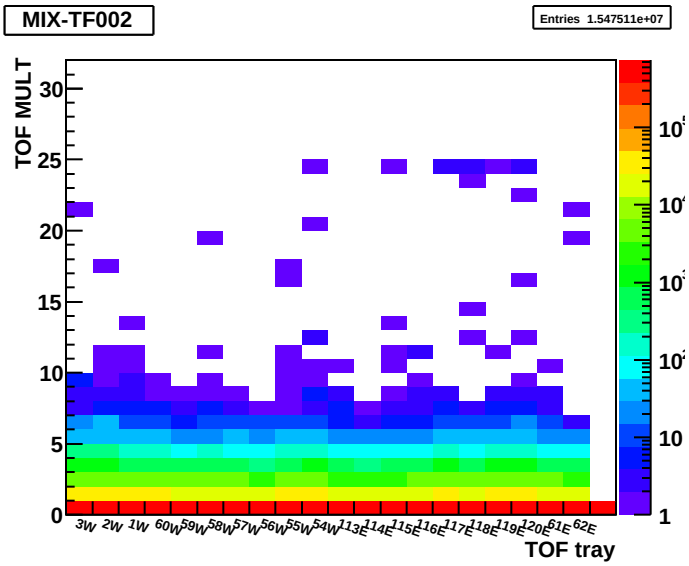

RAT board (ch4)
Entries 5895280
rat-0
rat-1
rat-2
rat-3
rat-4
rat-5
rat-6
rat-7
rat-8
rat-9
rat-10
rat-11
rat-12
rat-13
rat-14
rat-15

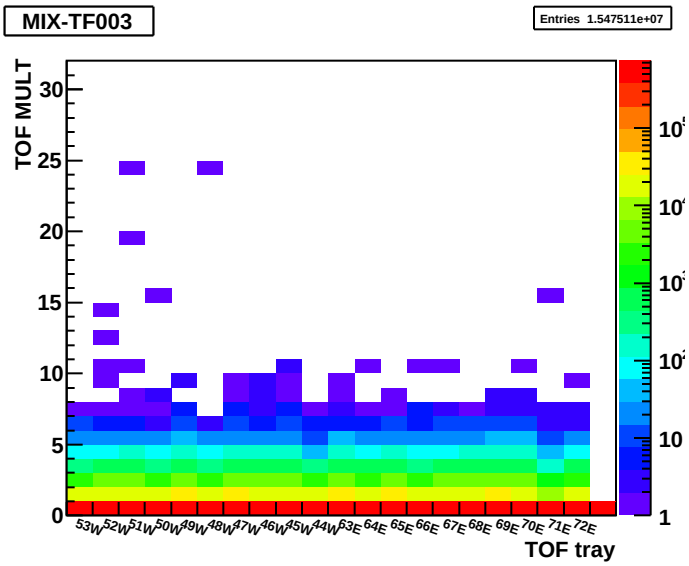

FP201 0-15 (ch5)
Entries 5895280
FMSsmallClusTh0
FMSsmallClusTh1
FMSsmallClusTh2
FMSsmallMulClusTh0
FMSsmallMulClusTh1
FMSsmallMulClusTh2
FMSsmallHT
FMSlargeClusTh0
FMSlargeClusTh1
FMSlargeClusTh2
FMSlargeMulClusTh0
FMSlargeMulClusTh1
FMSlargeMulClusTh2
FMSlargeHT
FPE

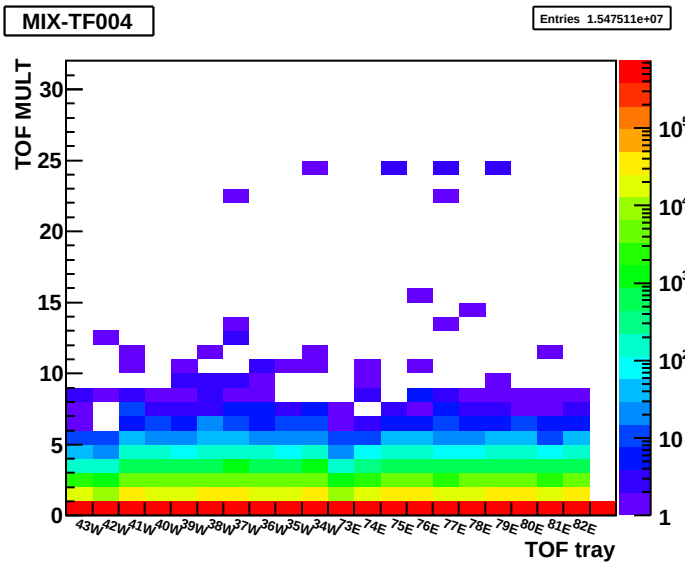

ST201 0-15 (ch6)
Entries 5895280
SpcTrgDetBit0
SpcTrgDetBit1
SpcTrgDetBit2
DetCrpBit0
DetCrpBit1
DetCrpBit2
DetCrpBit3
ZeroBias
Random

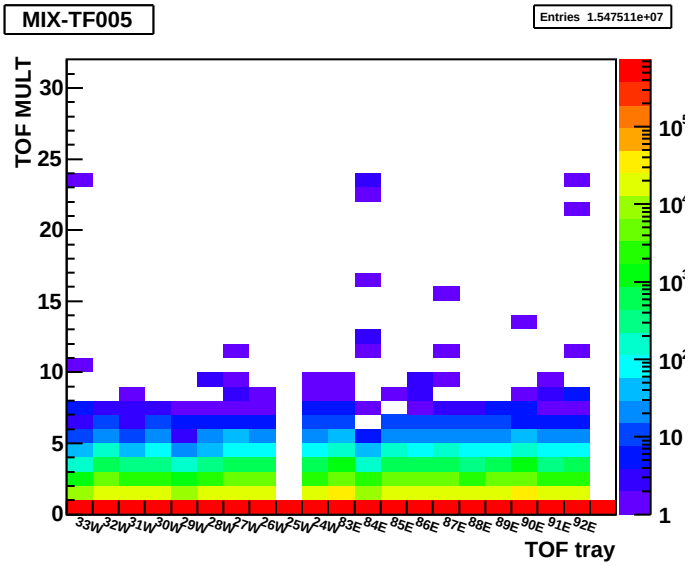

Unused (ch7)
Entries 5895280

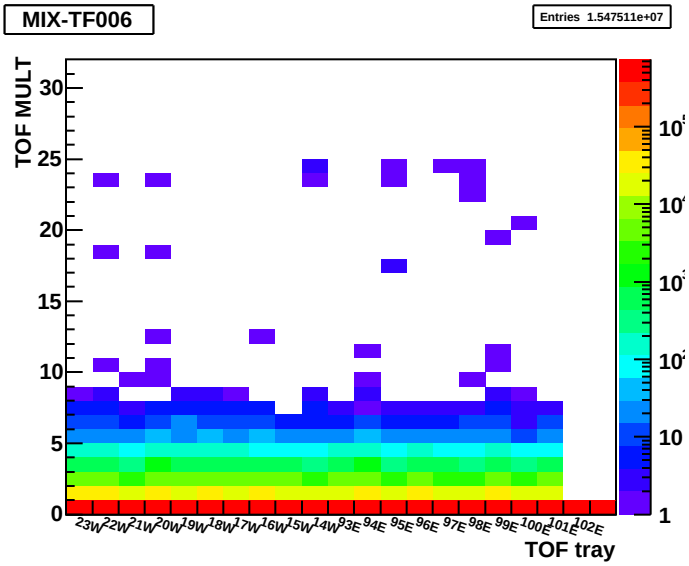

MIX-TF001
Entries 1.547511e+07
TOF MULT
TOF tray

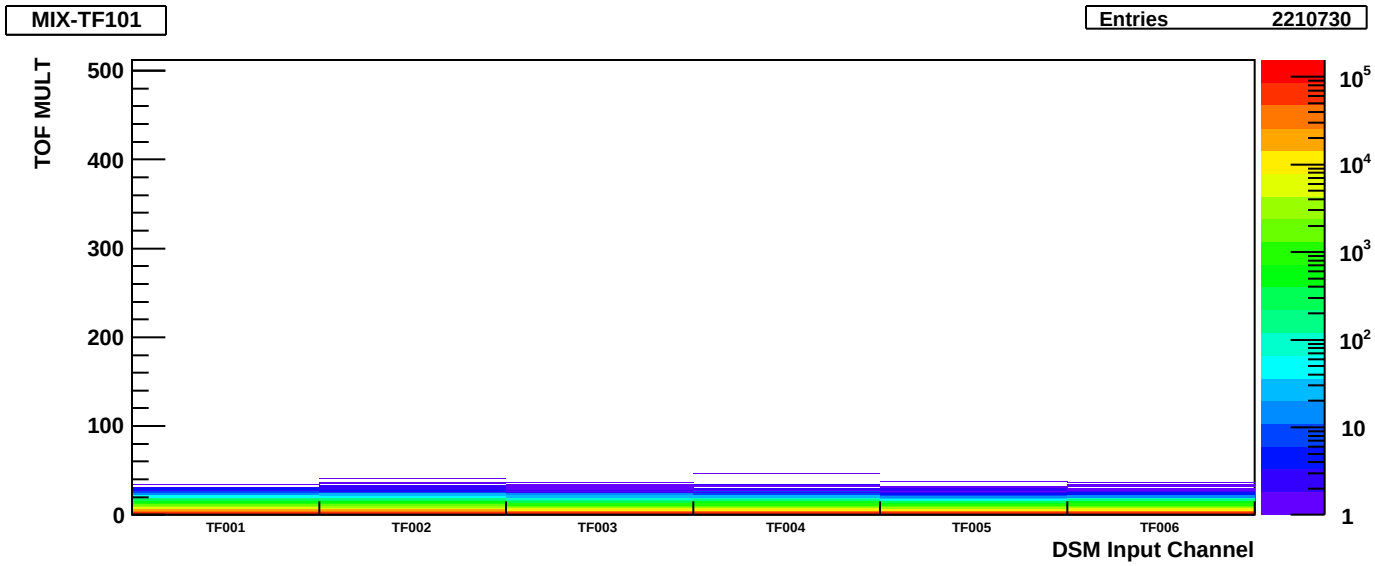

MIX-TF002
Entries 1.547511e+07
TOF MULT
TOF tray

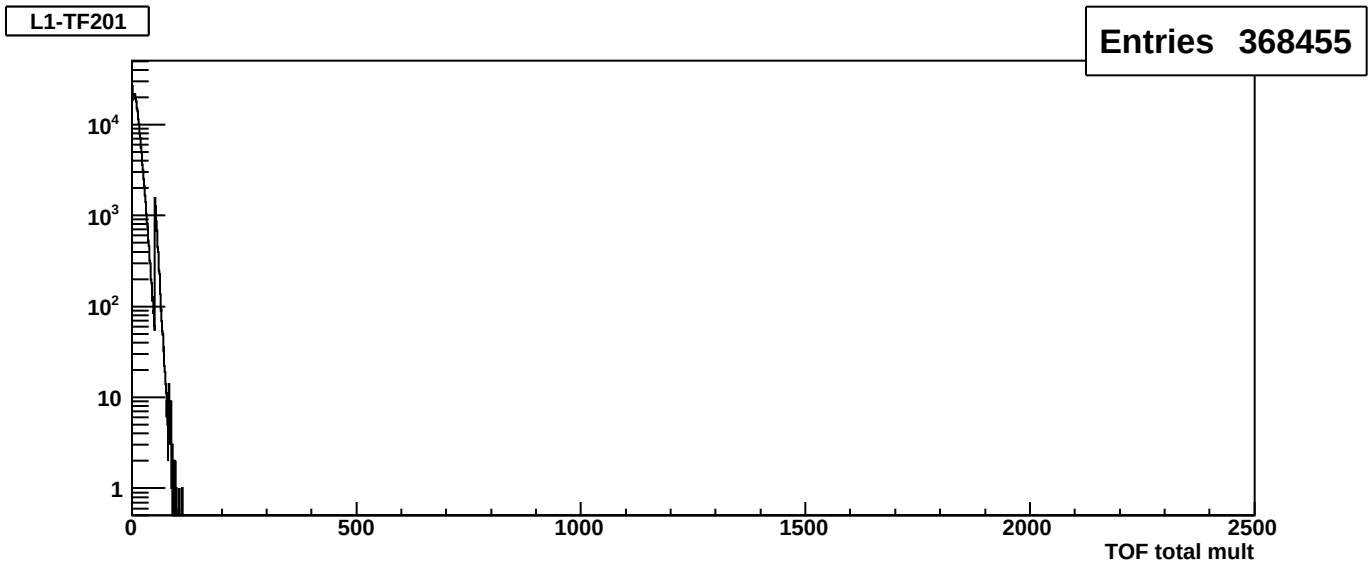

MIX-TF003
Entries 1.547511e+07
TOF MULT
TOF tray

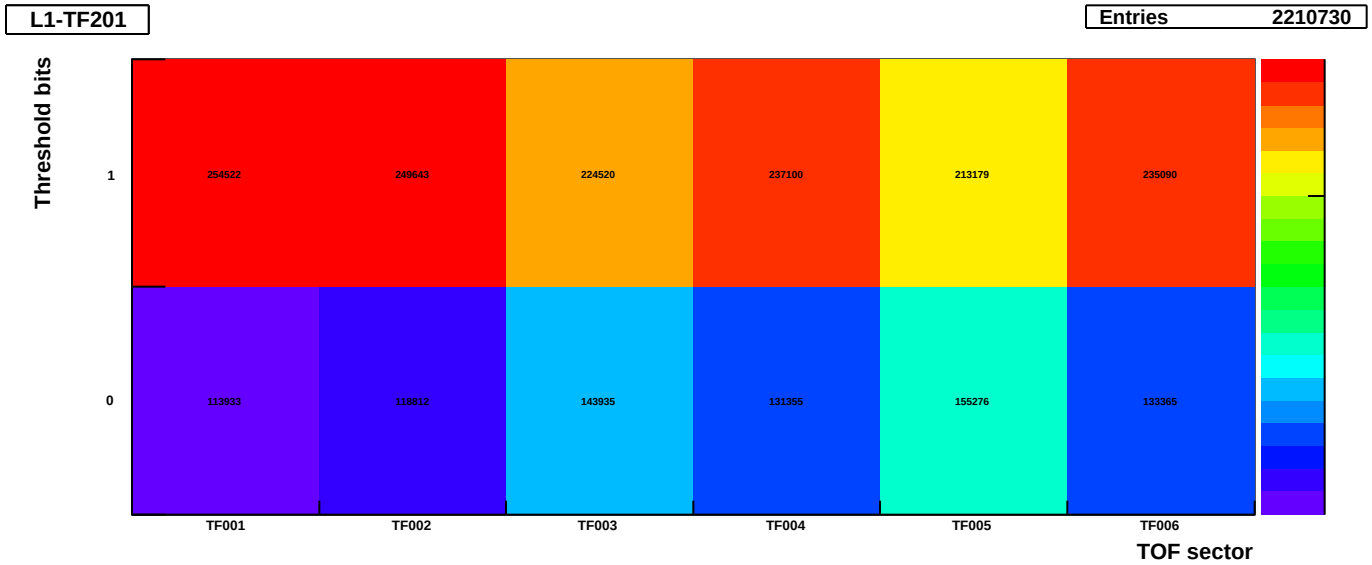

MIX-TF004
Entries 1.547511e+07
TOF MULT
TOF tray

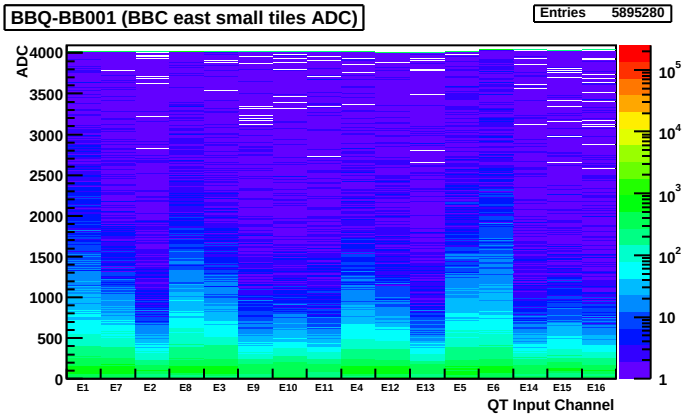

MIX-TF005
Entries 1.547511e+07
TOF MULT
TOF tray

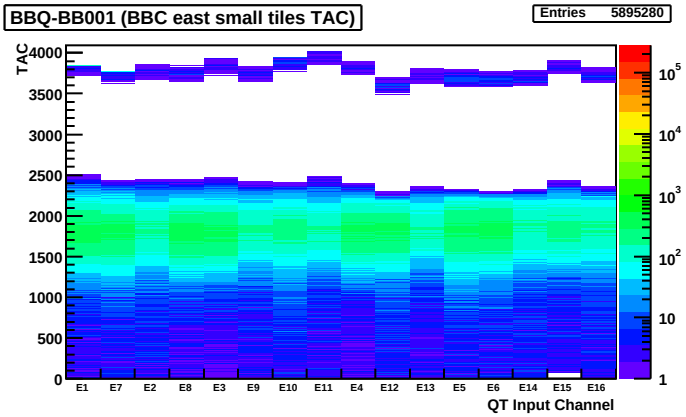

MIX-TF006
Entries 1.547511e+07
TOF MULT
TOF tray

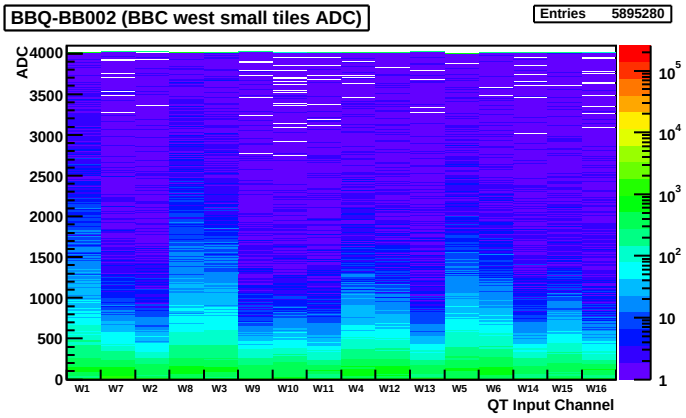

MIX-TF101
Entries 2210730
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
$10^5$
$10^4$
$10^3$
$10^2$
10
1

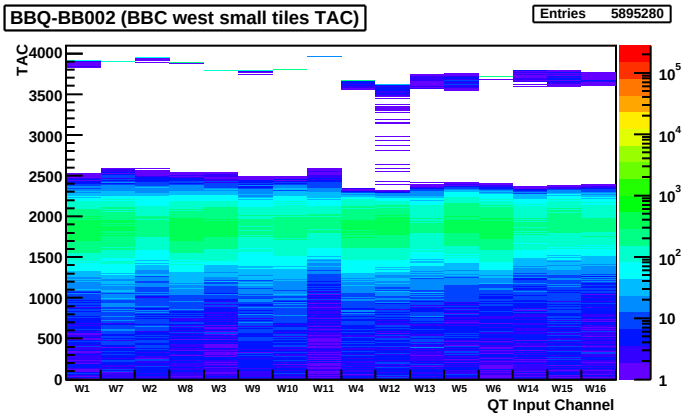

L1-TF201
Entries 368455
$10^4$
$10^3$
$10^2$
10
1
0
500
1000
1500
2000
2500
TOF total mult

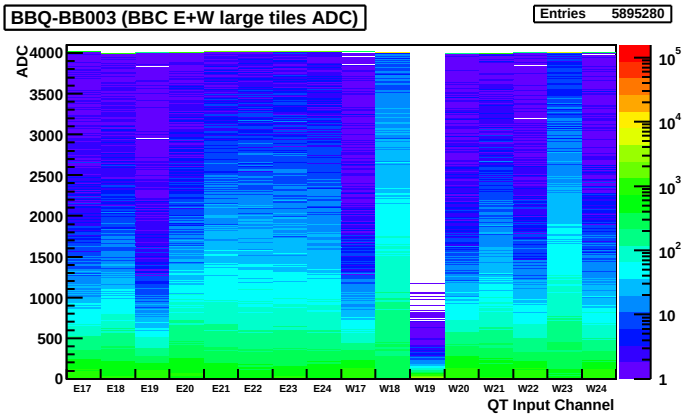

L1-TF201
Entries 2210730
Threshold bits
1
0
254522
249643
224520
237100
213179
235090
113933
118812
143935
131355
155276
133365
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector

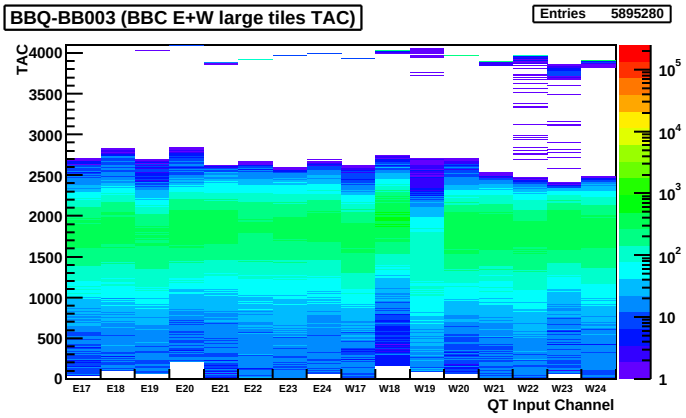

BBQ-BB001 (BBC east small tiles ADC)
Entries 5895280
ADC
QT Input Channel

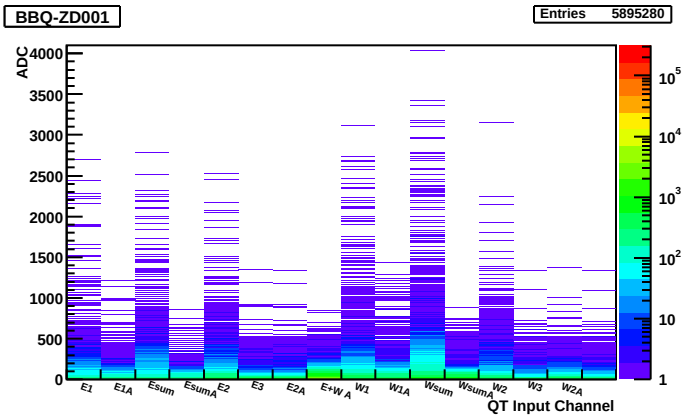

BBQ-BB001 (BBC east small tiles TAC)
Entries 5895280
TAC
QT Input Channel

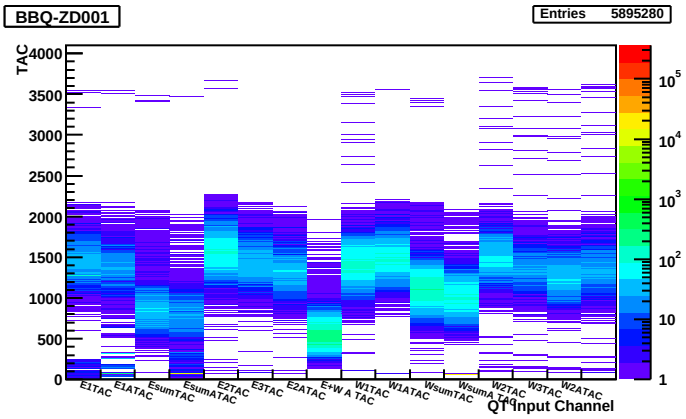

BBQ-BB002 (BBC west small tiles ADC)
Entries 5895280
ADC
QT Input Channel

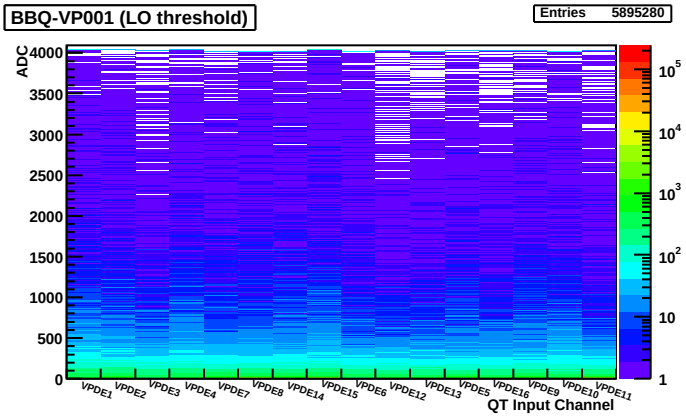

BBQ-BB002 (BBC west small tiles TAC)
Entries 5895280
TAC
QT Input Channel

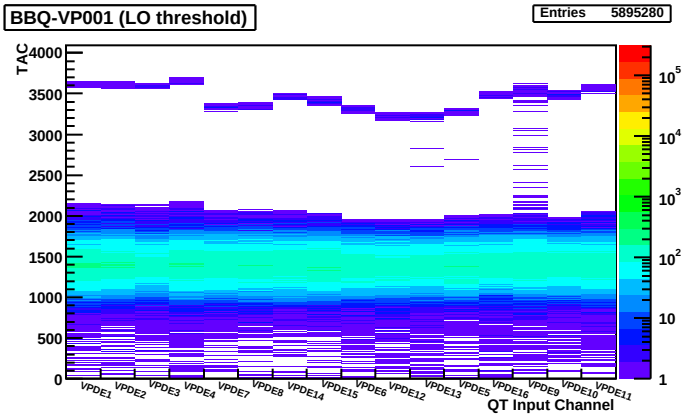

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 5895280
ADC
QT Input Channel

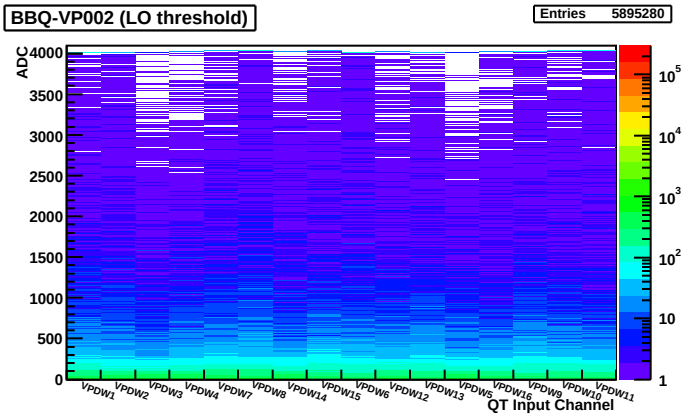

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 5895280
TAC
QT Input Channel

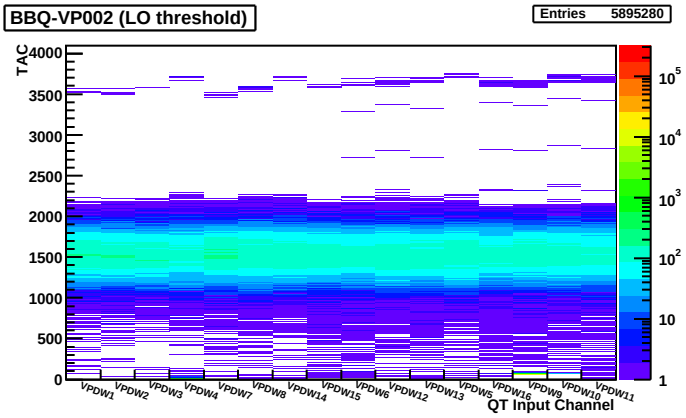

BBQ-ZD001
Entries 5895280
ADC
QT Input Channel

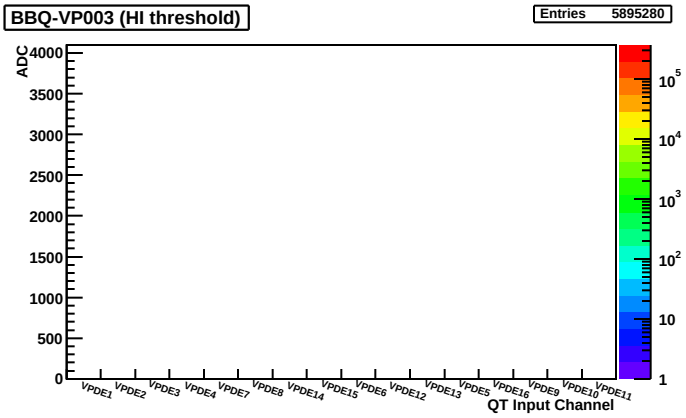

BBQ-ZD001
Entries 5895280
TAC
QT Input Channel

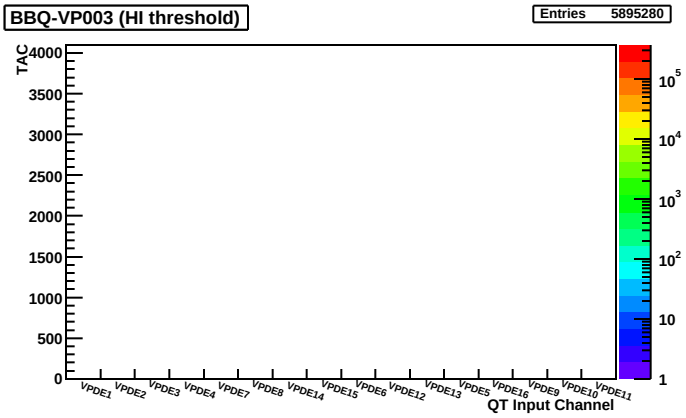

BBQ-VP001 (LO threshold)
Entries 5895280
ADC
QT Input Channel

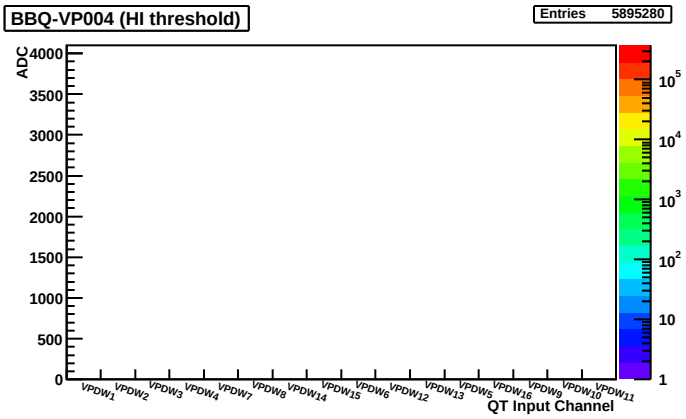

BBQ-VP001 (LO threshold)
Entries 5895280
TAC
QT Input Channel

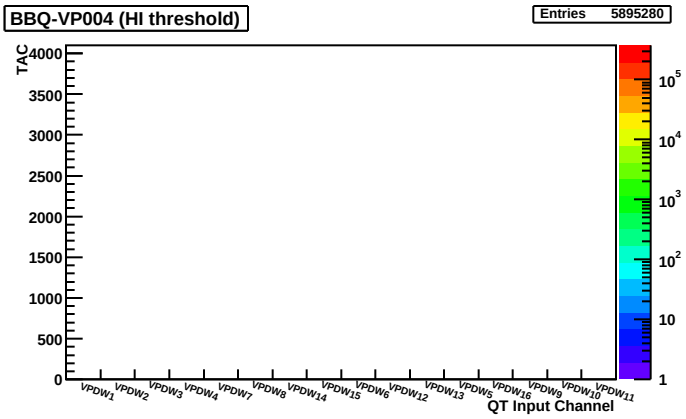

BBQ-VP002 (LO threshold)
Entries 5895280
ADC
QT Input Channel

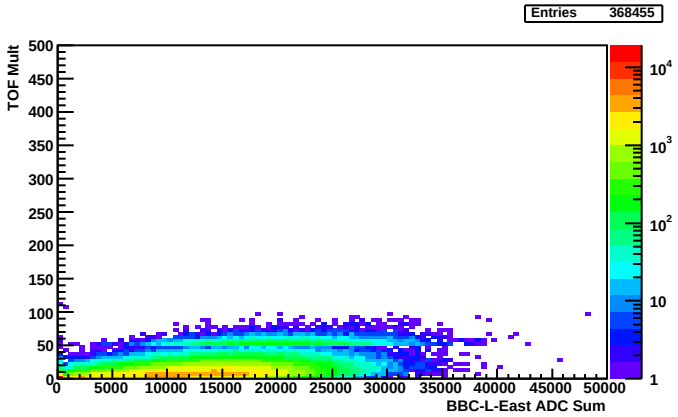

BBQ-VP002 (LO threshold)
Entries 5895280
TAC
QT Input Channel

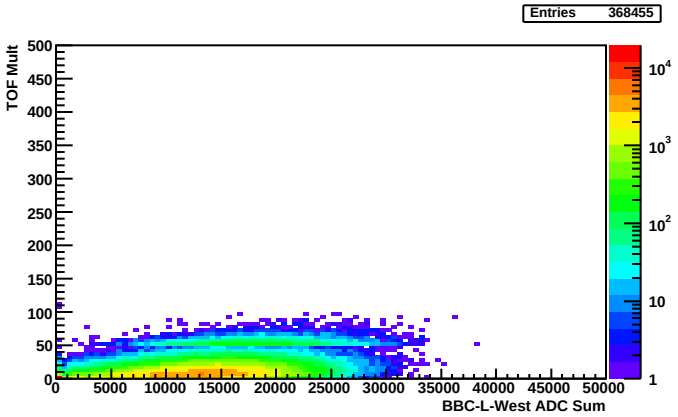

BBQ-VP003 (HI threshold)
Entries 5895280
ADC
QT Input Channel

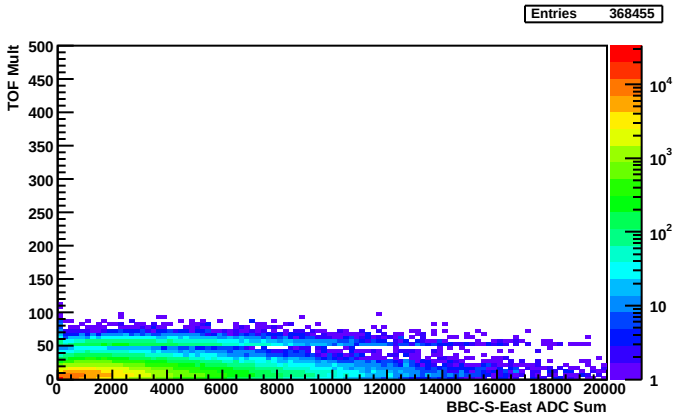

BBQ-VP003 (HI threshold)
Entries 5895280
TAC
QT Input Channel

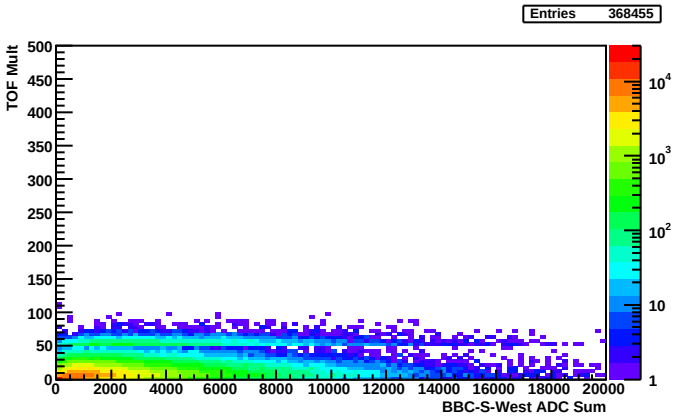

BBQ-VP004 (HI threshold)
Entries 5895280
ADC
QT Input Channel

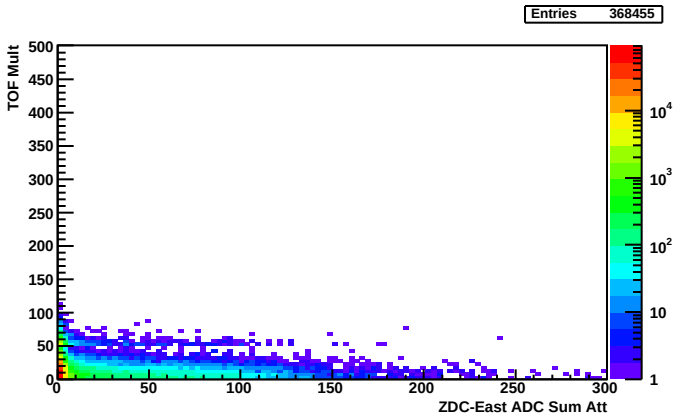

BBQ-VP004 (HI threshold)
Entries 5895280
TAC
QT Input Channel

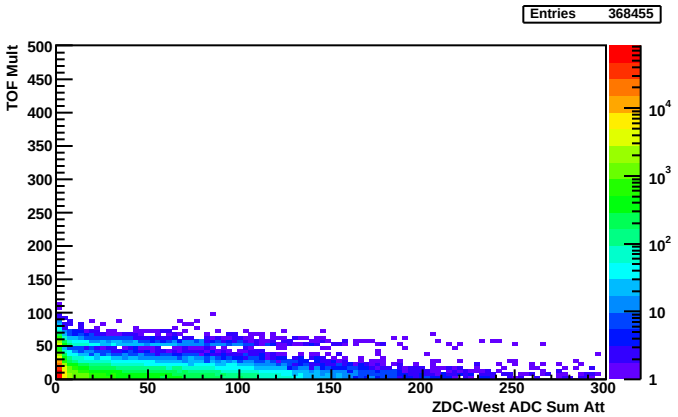

Entries 368455
TOF Mult
BBC-L-East ADC Sum

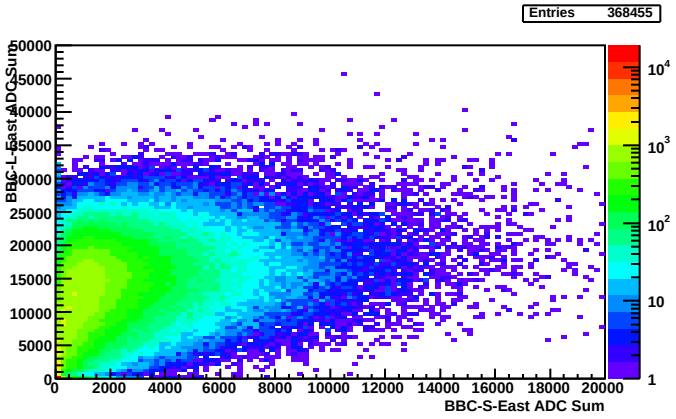

Entries 368455
TOF Mult
BBC-L-West ADC Sum

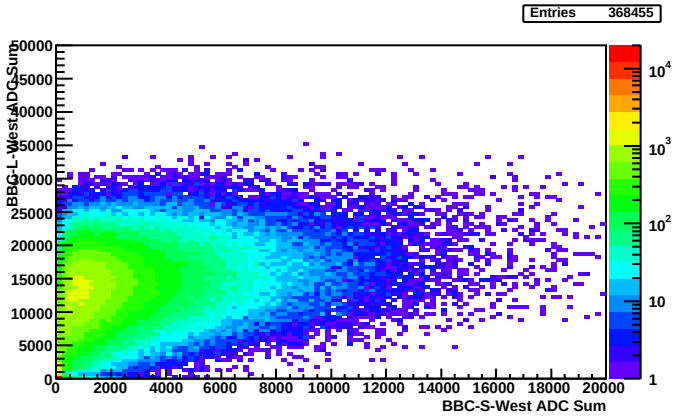

Entries 368455
TOF Mult
BBC-S-East ADC Sum

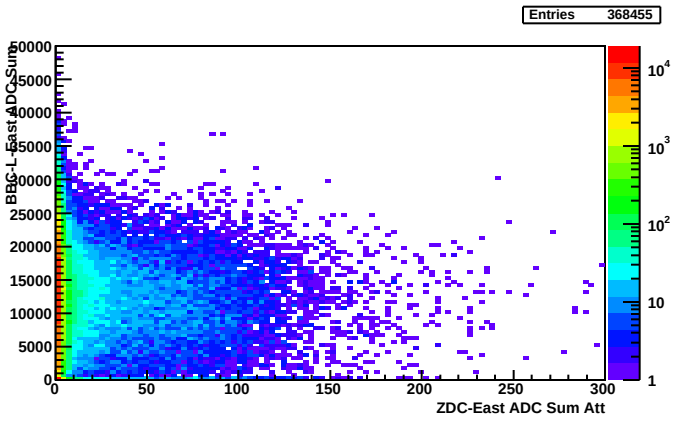

Entries 368455
TOF Mult
BBC-S-West ADC Sum

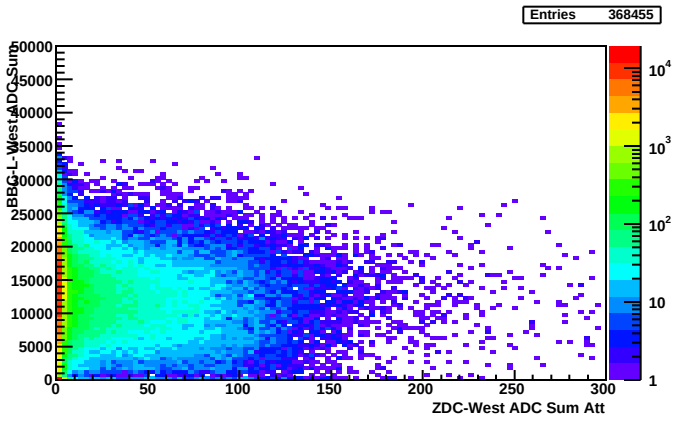

Entries 368455
TOF Mult
ZDC-East ADC Sum Att

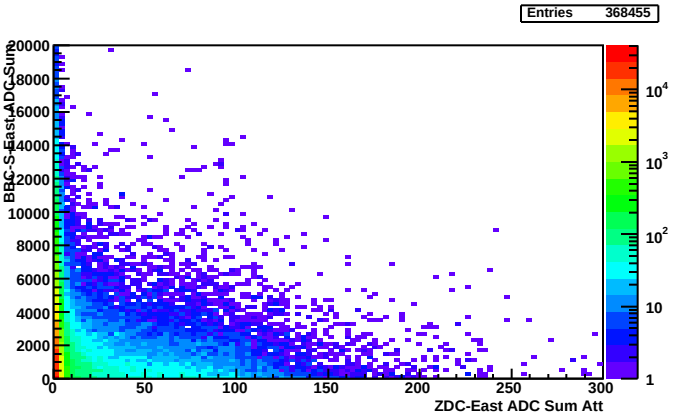

Entries 368455
TOF Mult
ZDC-West ADC Sum Att

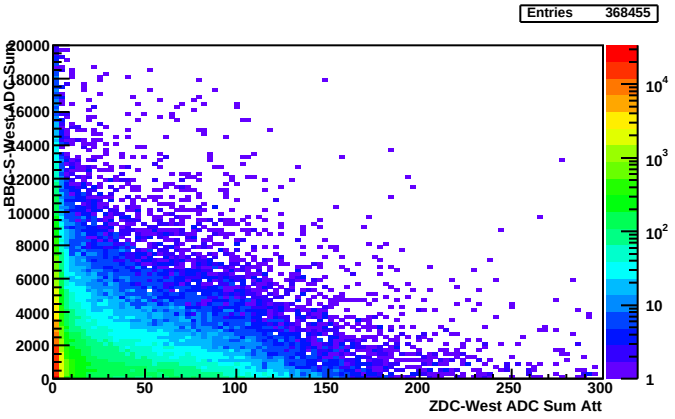

Entries 368455
BBC-L-East ADC Sum
BBC-S-East ADC Sum

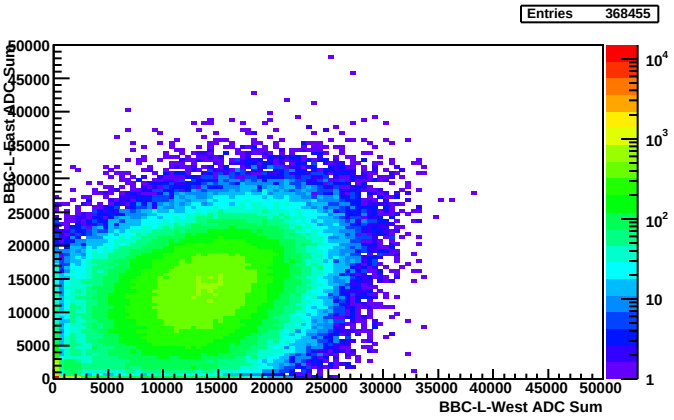

Entries 368455
BBC-L-West ADC Sum
BBC-S-West ADC Sum

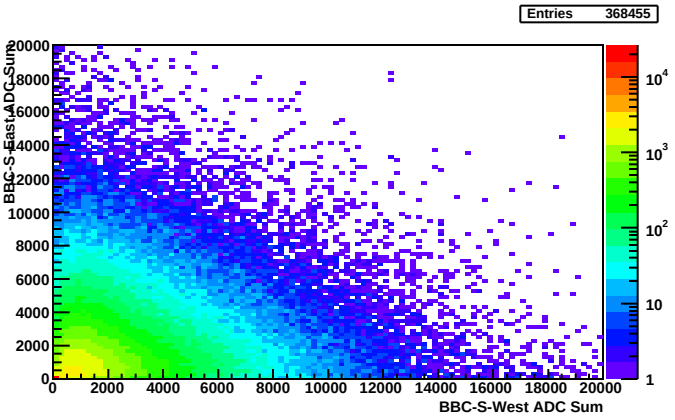

Entries 368455
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

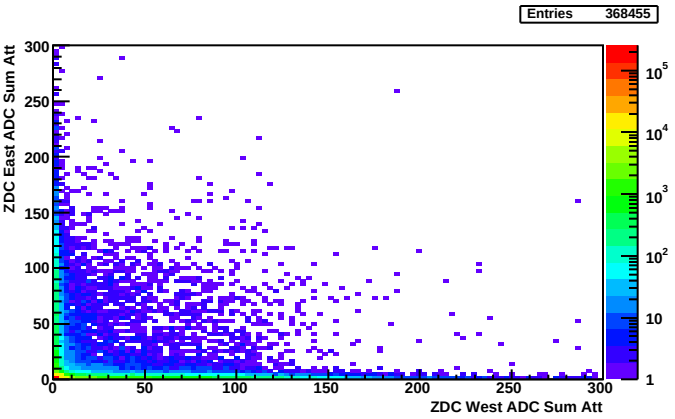

Entries 368455
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

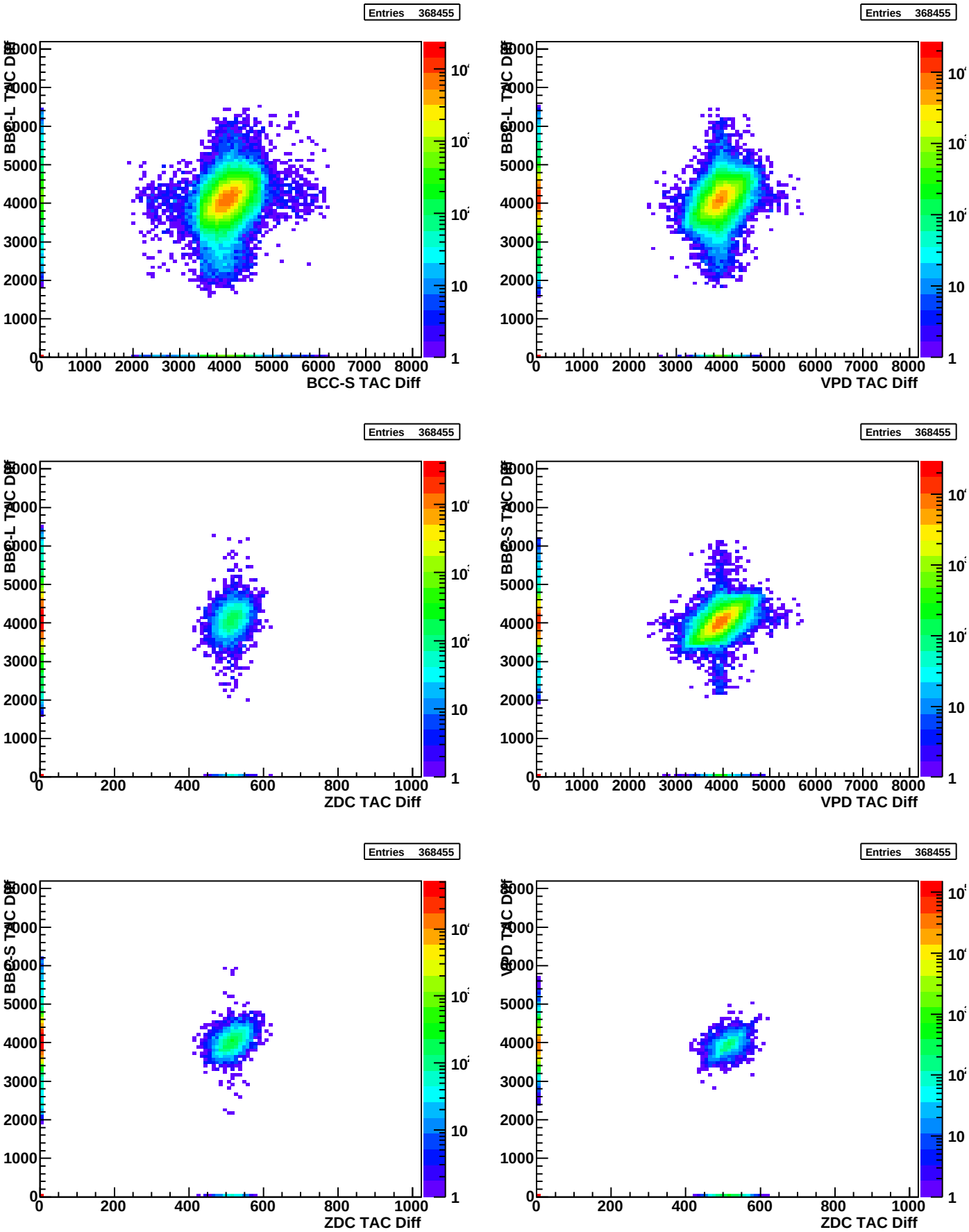

Entries 368455
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

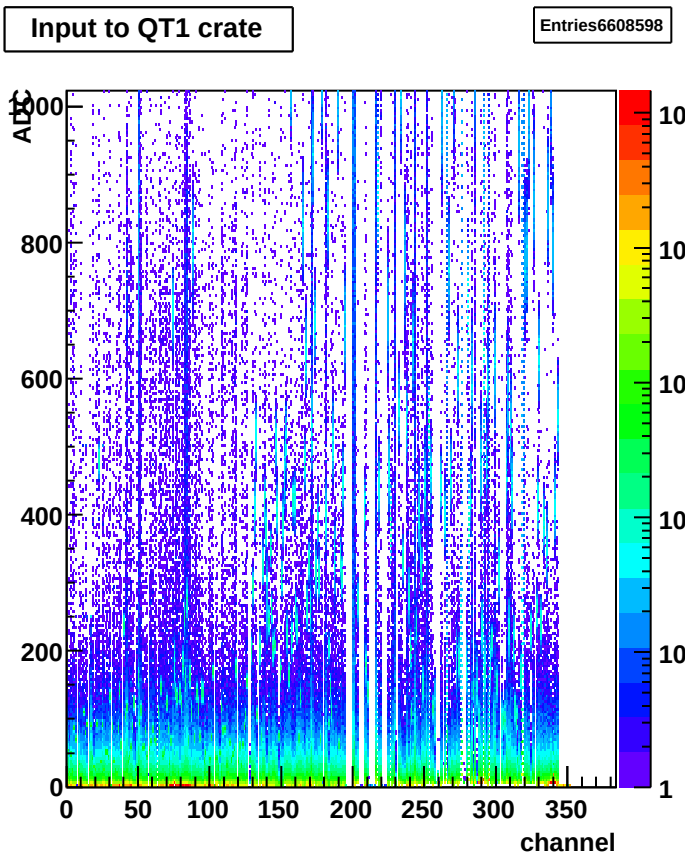

Entries 368455
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

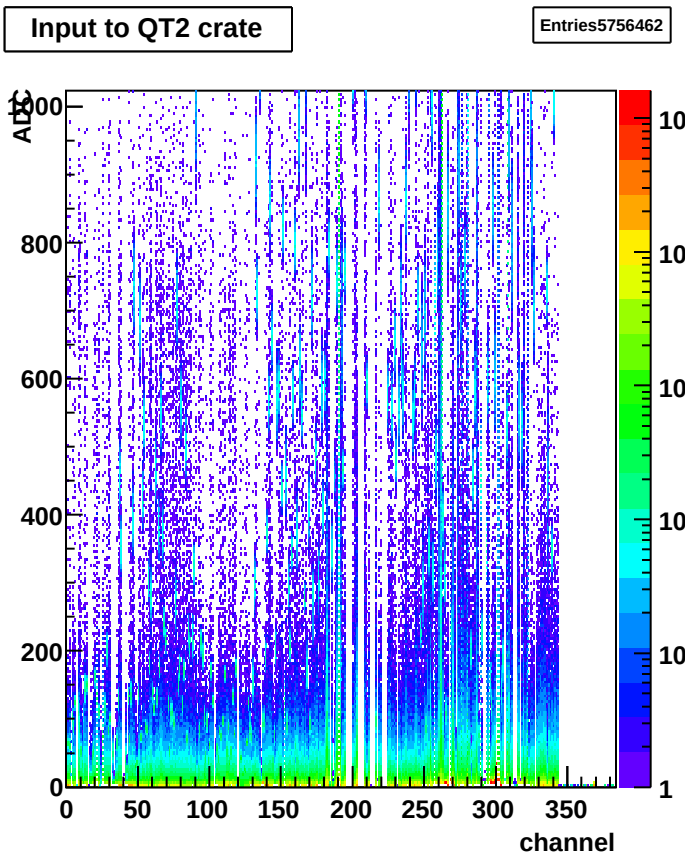

Entries 368455
BBC-L-East ADC Sum
BBC-L-West ADC Sum

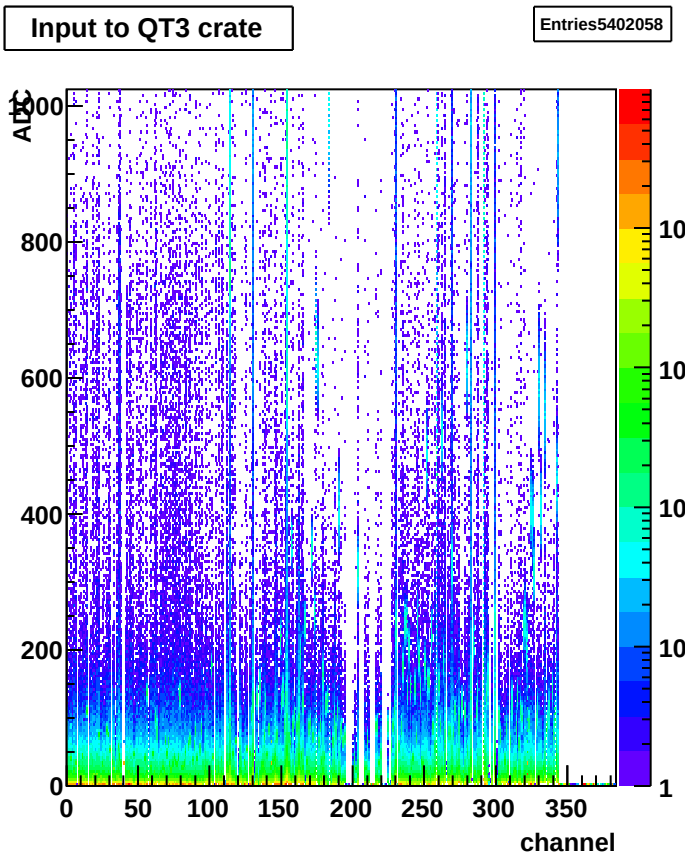

Entries 368455
BBC-S-East ADC Sum
BBC-S-West ADC Sum

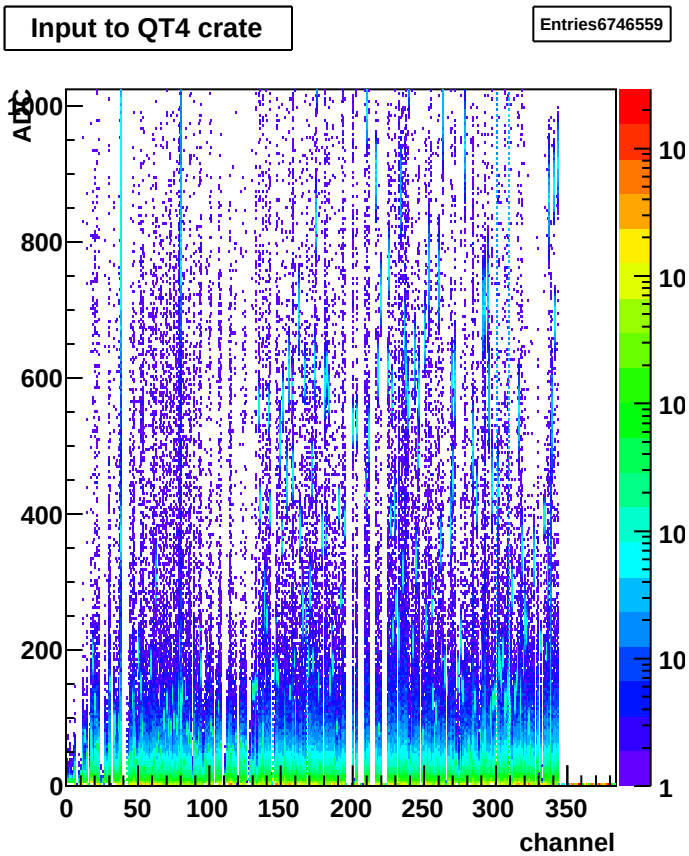

Entries 368455
ZDC East ADC Sum Att
ZDC West ADC Sum Att

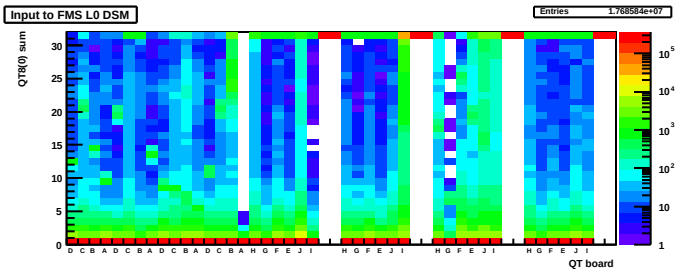

Entries 368455
BBC-L TAC Diff
BCC-S TAC Diff
Entries 368455
BBC-L TAC Diff
VPD TAC Diff
Entries 368455
BBC-L TAC Diff
ZDC TAC Diff
Entries 368455
BBC-S TAC Diff
VPD TAC Diff
Entries 368455
BBC-S TAC Diff
ZDC TAC Diff
Entries 368455
VPD TAC Diff
ZDC TAC Diff

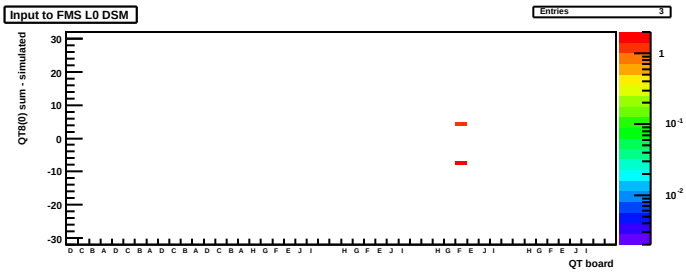

Input to QT1 crate
Entries6608598
ADC
channel

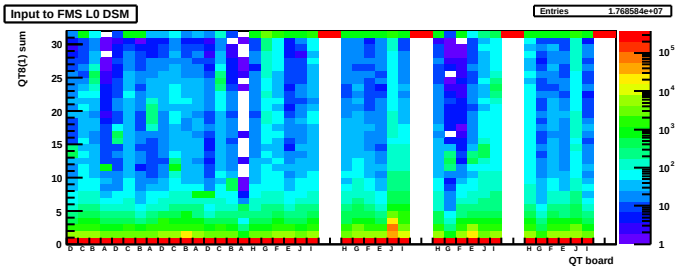

Input to QT2 crate
Entries5756462
ADC
channel

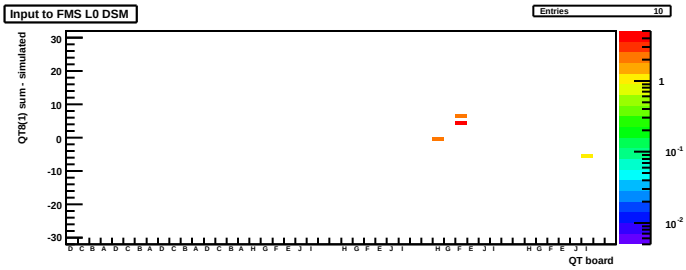

Input to QT3 crate
Entries5402058
ADC
channel

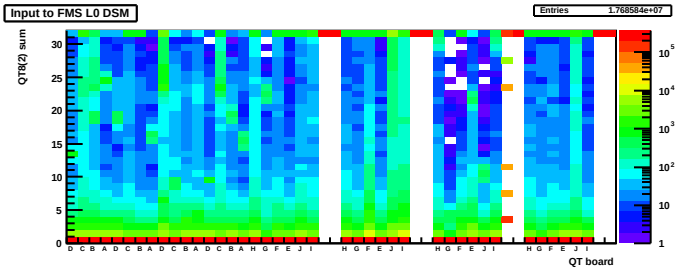

Input to QT4 crate
Entries6746559
ADC
channel

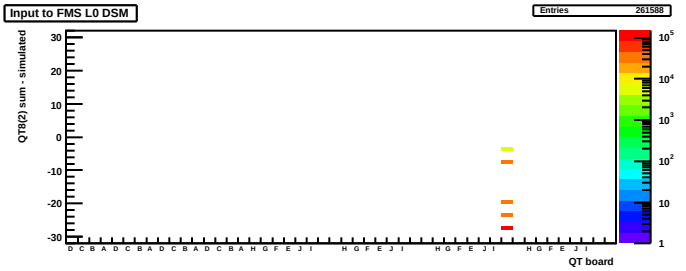

Input to FMS L0 DSM
Entries 1.768584e+07
QT8(0) sum
QT board

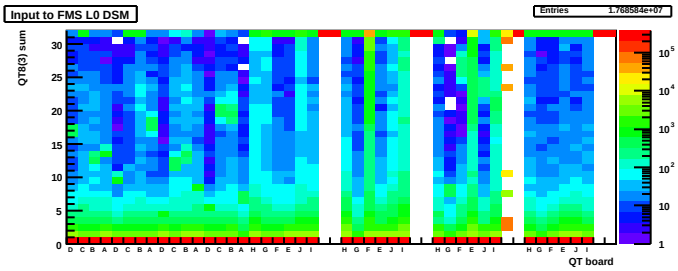

Input to FMS L0 DSM
Entries 3
QT8(0) sum - simulated
QT board

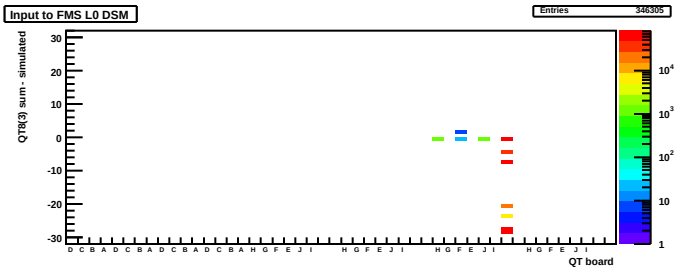

Input to FMS L0 DSM
Entries 1.768584e+07
QT8(1) sum
QT board

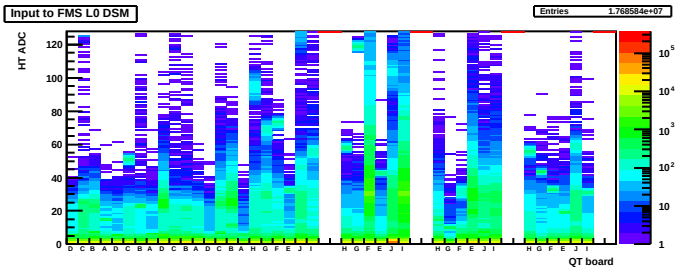

Input to FMS L0 DSM
Entries 10
QT8(1) sum - simulated
QT board

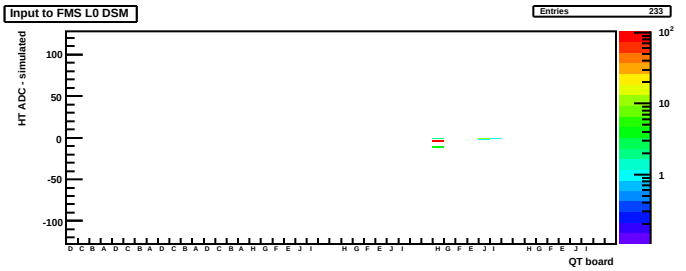

Input to FMS L0 DSM
Entries 1.768584e+07
QT8(2) sum
QT board

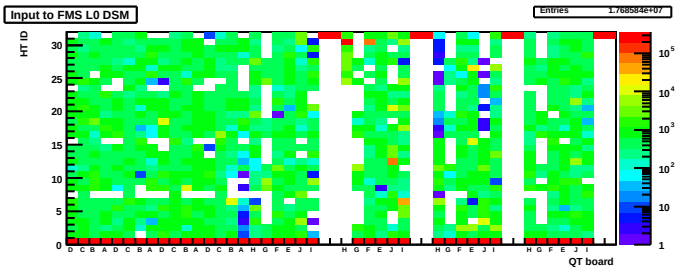

Input to FMS L0 DSM
Entries 261588
QT8(2) sum - simulated
QT board

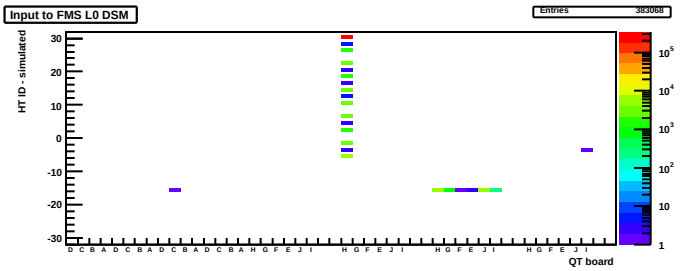

Input to FMS L0 DSM
Entries 1.768584e+07
QT8(3) sum
QT board

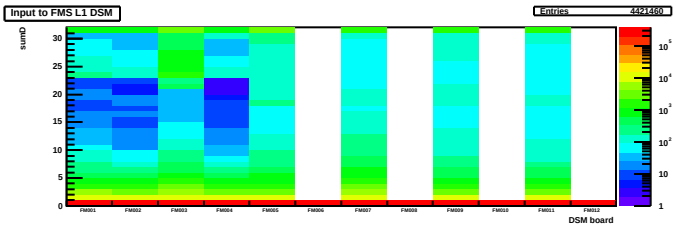

Input to FMS L0 DSM
Entries 346305
QT8(3) sum - simulated
QT board

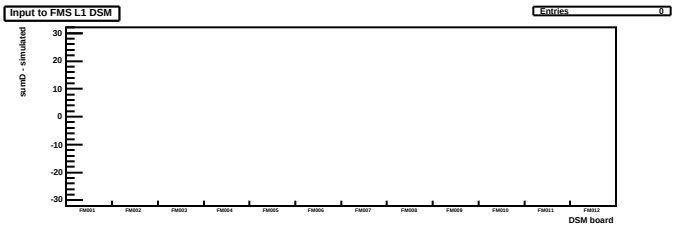

Input to FMS L0 DSM
Entries 1.768584e+07
HT ADC
QT board

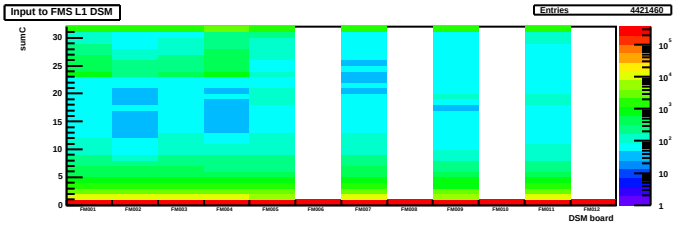

Input to FMS L0 DSM
Entries 233
HT ADC - simulated
QT board

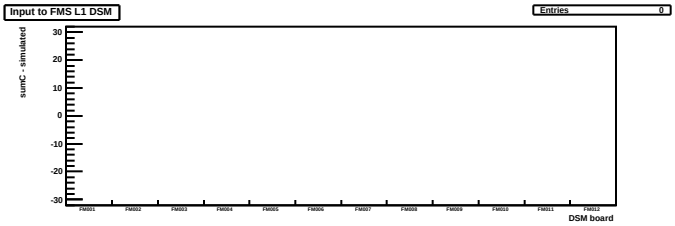

Input to FMS L0 DSM
Entries 1.768584e+07
HT ID
QT board

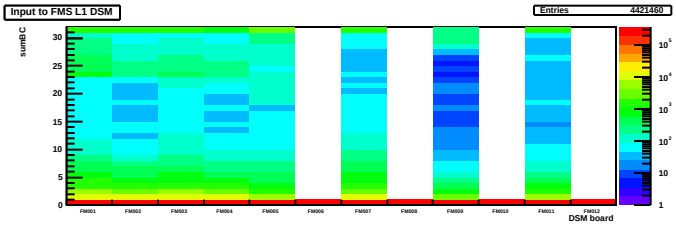

Input to FMS L0 DSM
Entries 383068
HT ID - simulated
QT board

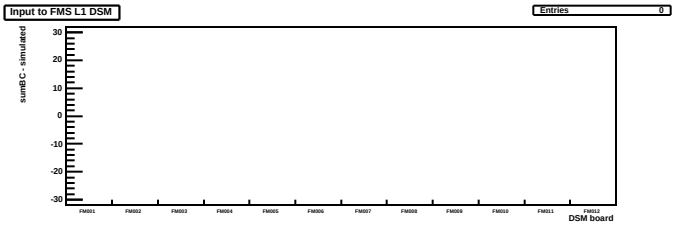

Input to FMS L1 DSM
Entries 4421460
sumD
DSM board

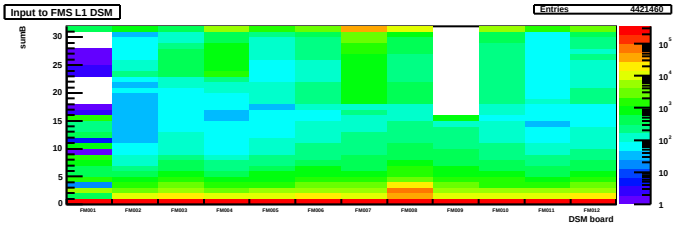

Input to FMS L1 DSM
Entries 0
sumD - simulated
DSM board

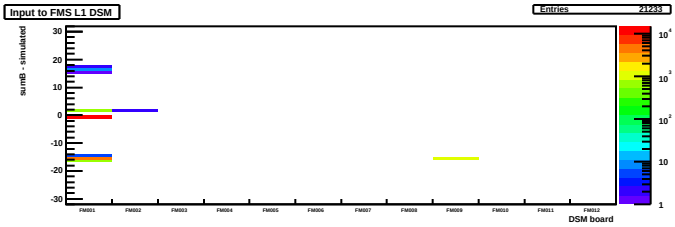

Input to FMS L1 DSM
Entries 4421460
sumC
DSM board

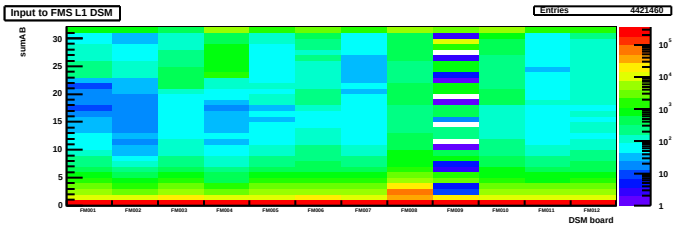

Input to FMS L1 DSM
Entries 0
sumC - simulated
DSM board

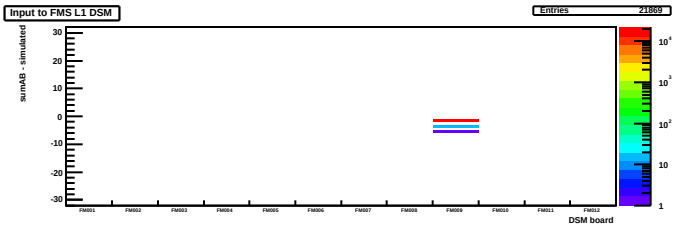

Input to FMS L1 DSM
Entries 4421460
sumBC
DSM board

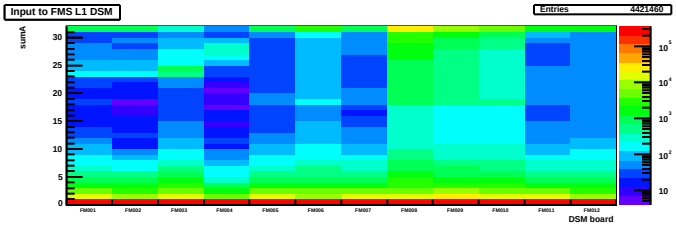

Input to FMS L1 DSM
Entries 0
sumBC - simulated
DSM board

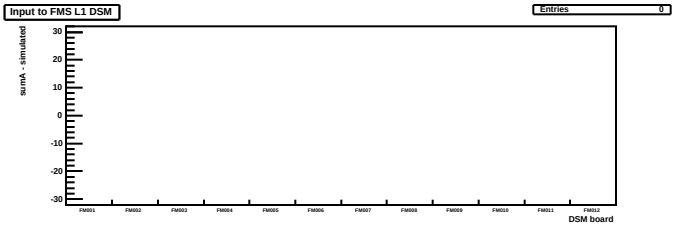

Input to FMS L1 DSM
Entries 4421460
sumB
DSM board

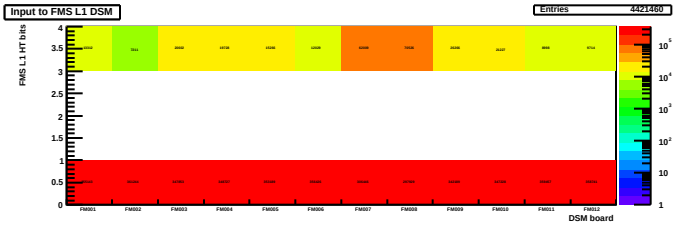

Input to FMS L1 DSM
Entries 21233
sumB - simulated
DSM board

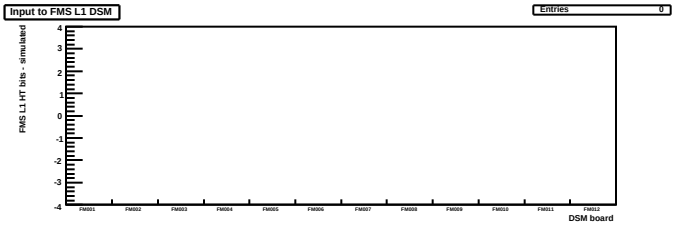

Input to FMS L1 DSM
Entries 4421460
sumAB
DSM board

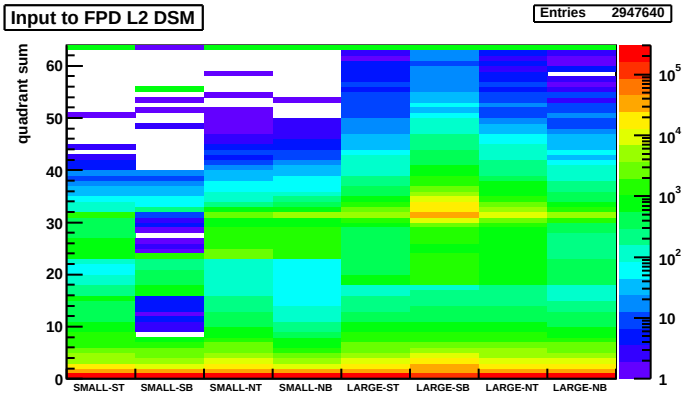

Input to FMS L1 DSM
Entries 21869
sumAB - simulated
DSM board

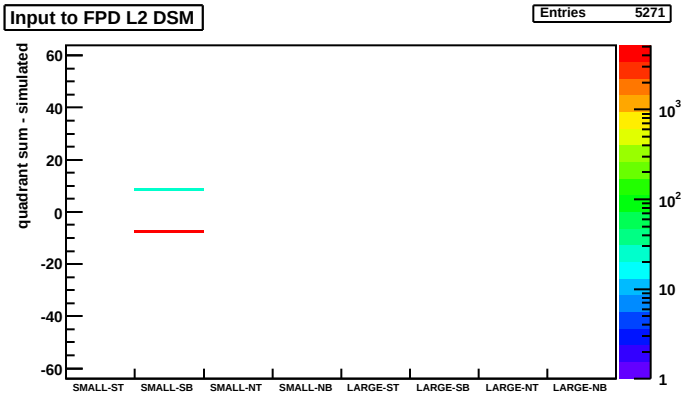

Input to FMS L1 DSM
Entries 4421460
sumA
DSM board

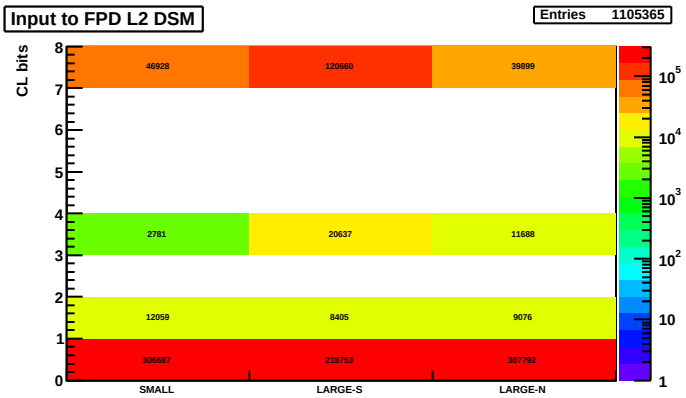

Input to FMS L1 DSM
Entries 0
sumA - simulated
DSM board

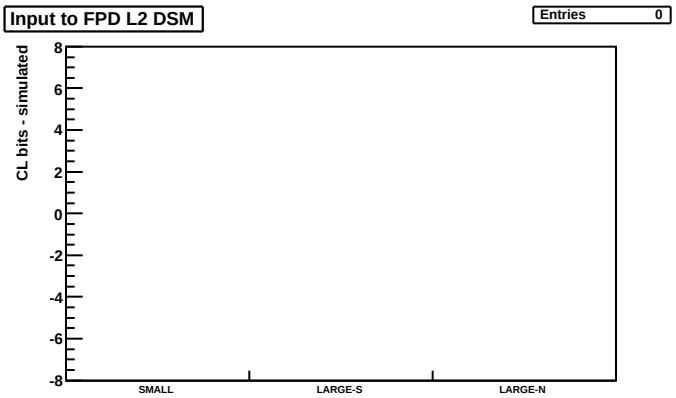

Input to FMS L1 DSM
Entries 4421460
FMS L1 HT bits
DSM board

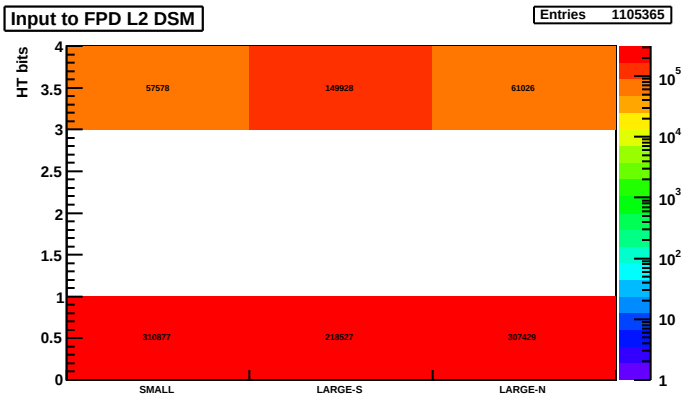

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

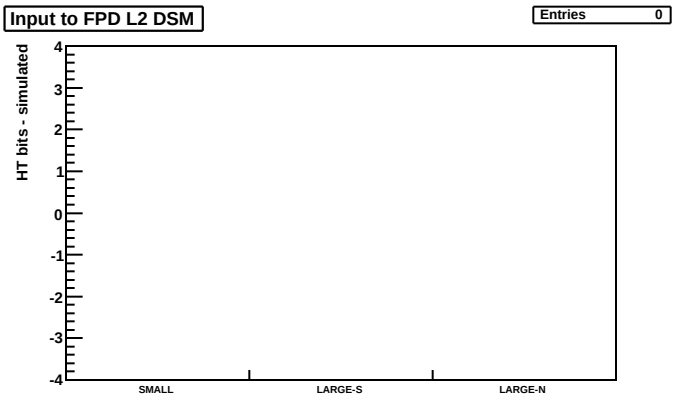

Input to FPD L2 DSM
Entries 2947640
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

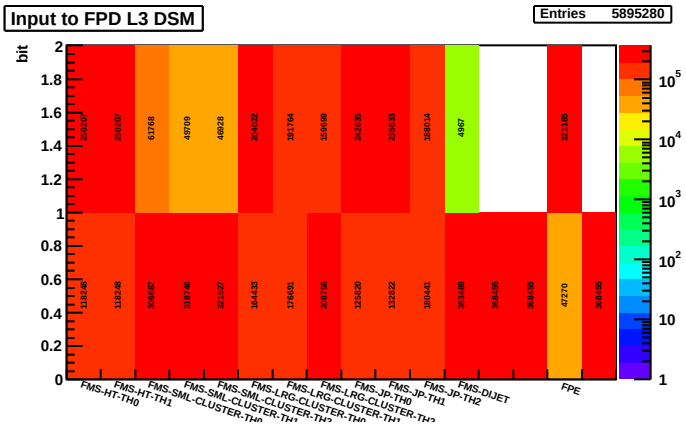

Input to FPD L2 DSM
Entries 5271
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

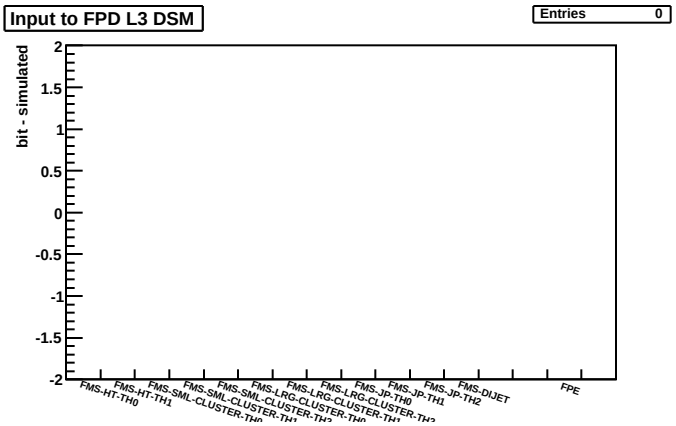

Input to FPD L2 DSM
Entries 1105365
CL bits
46928
120660
39899
2781
20637
11688
12059
8405
9076
306687
218753
307792
SMALL
LARGE-S
LARGE-N

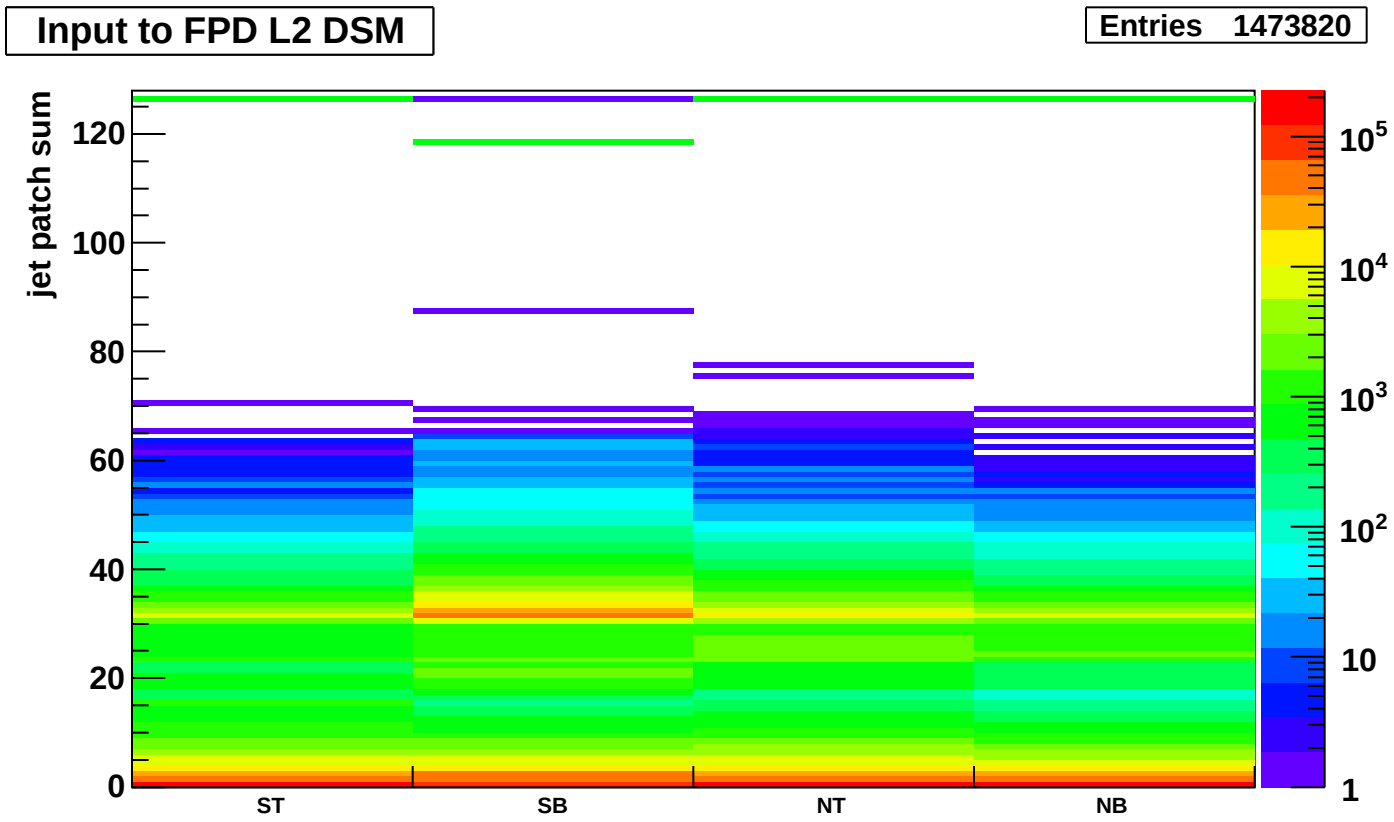

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

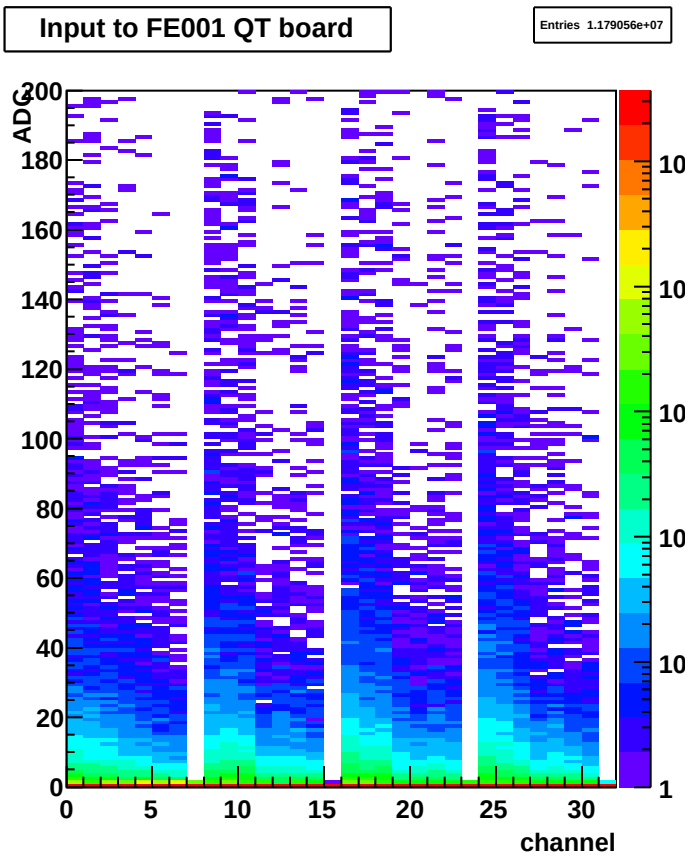

Input to FPD L2 DSM
Entries 1105365
HT bits
57578
149928
61026
310877
218527
307429
SMALL
LARGE-S
LARGE-N

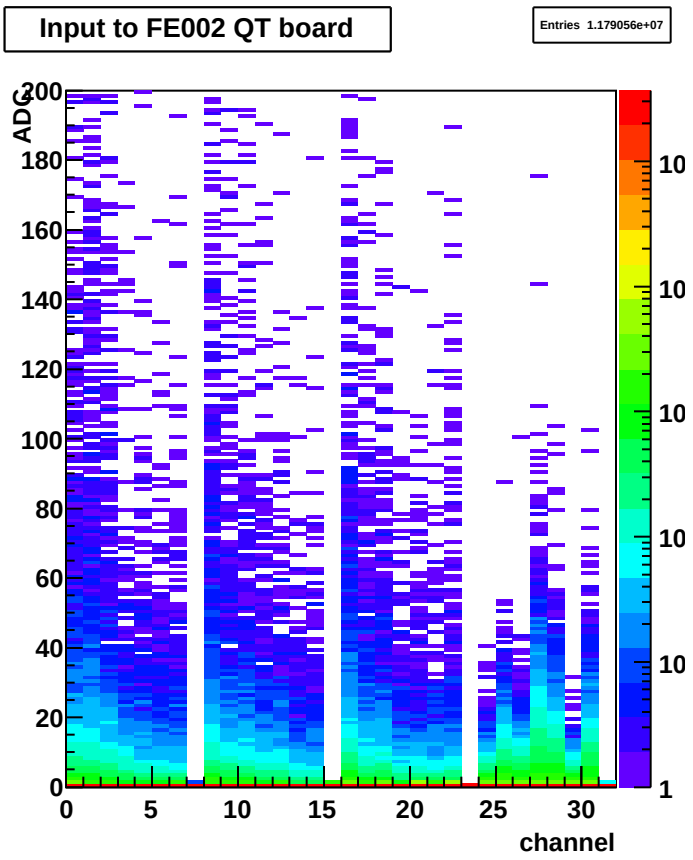

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

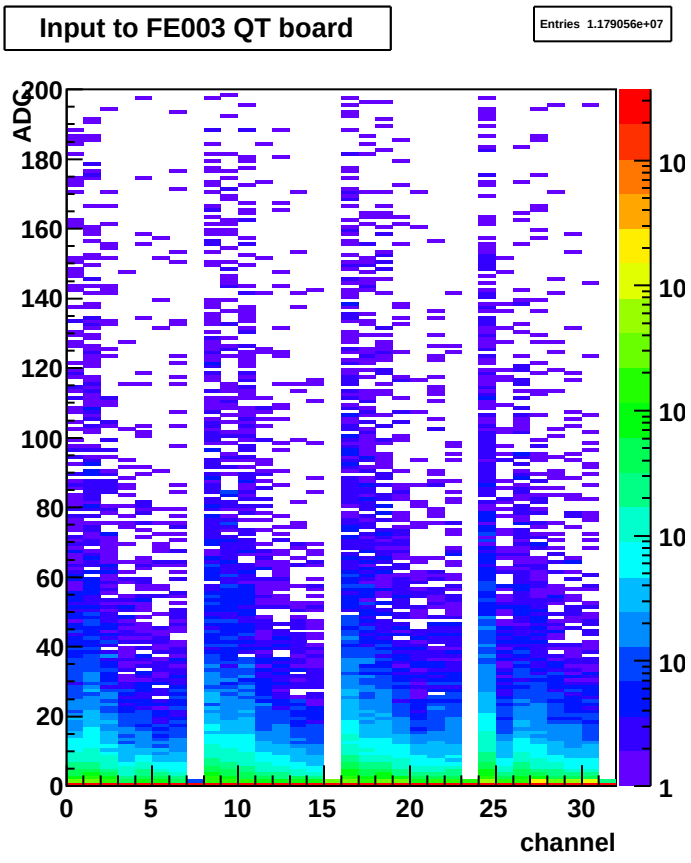

Input to FPD L3 DSM
Entries 5895280
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

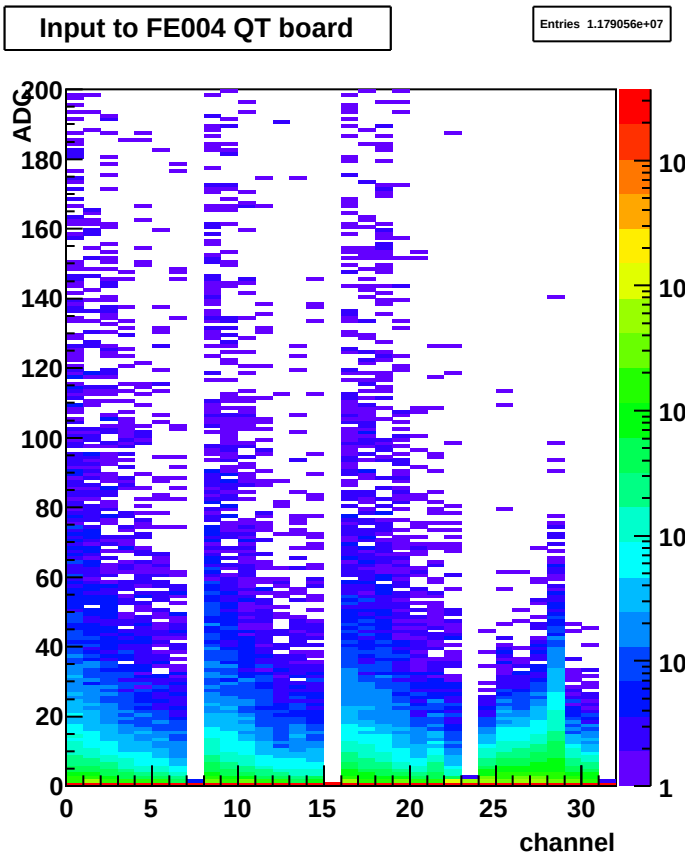

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

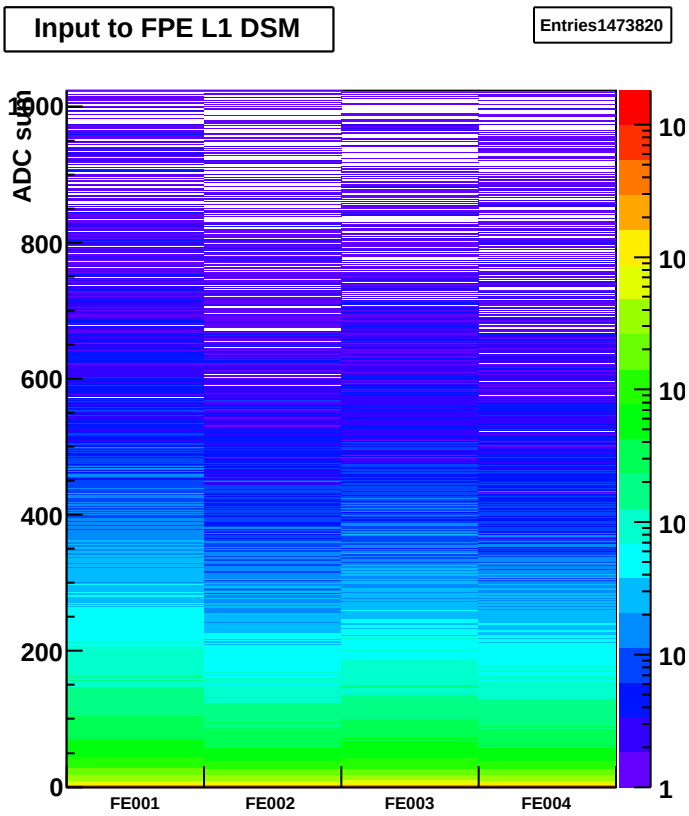

Input to FPD L2 DSM
Entries 1473820
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
$10^2$
$10^3$
$10^4$
$10^5$

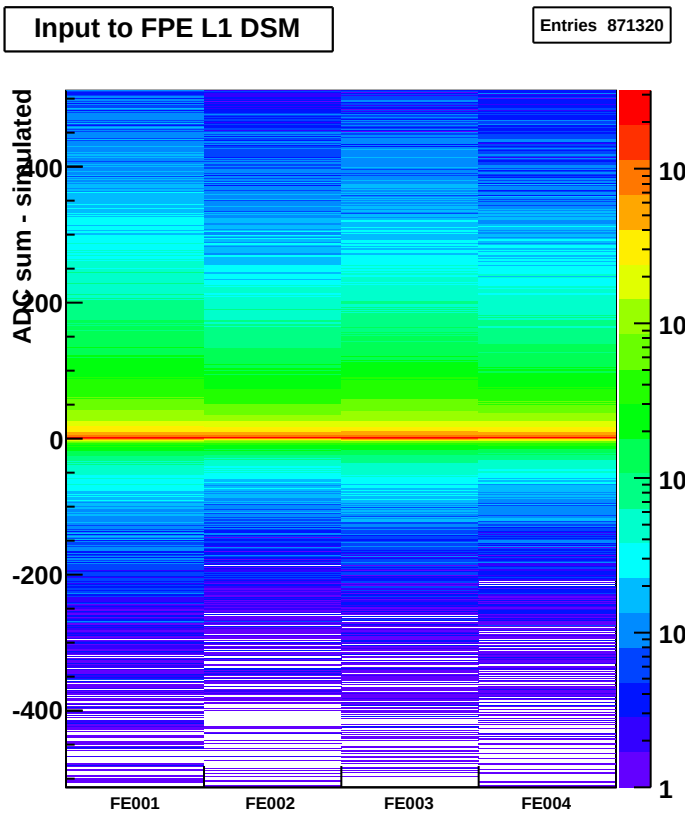

Input to FE001 QT board
Entries 1.179056e+07
ADC
channel

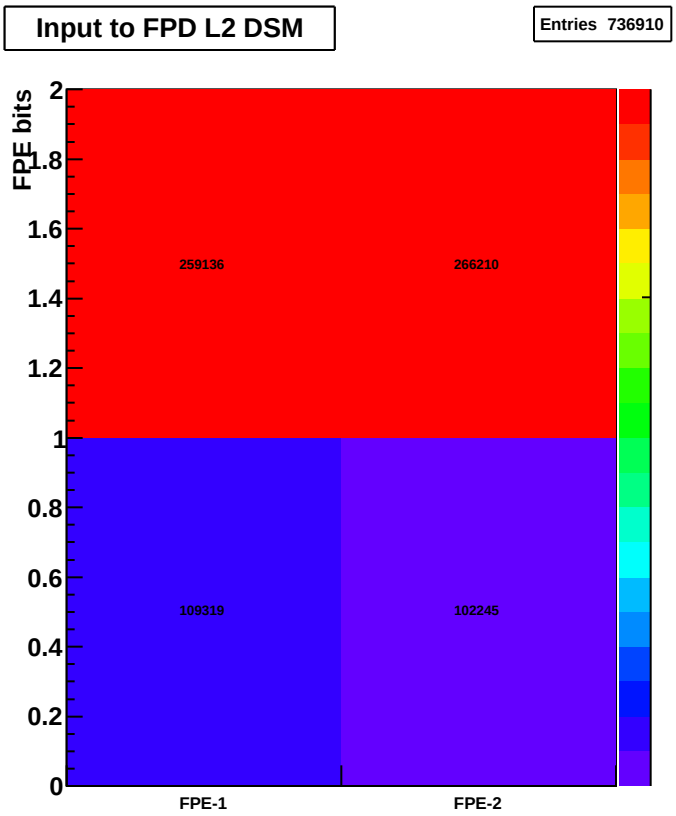

Input to FE002 QT board
Entries 1.179056e+07
ADC
channel

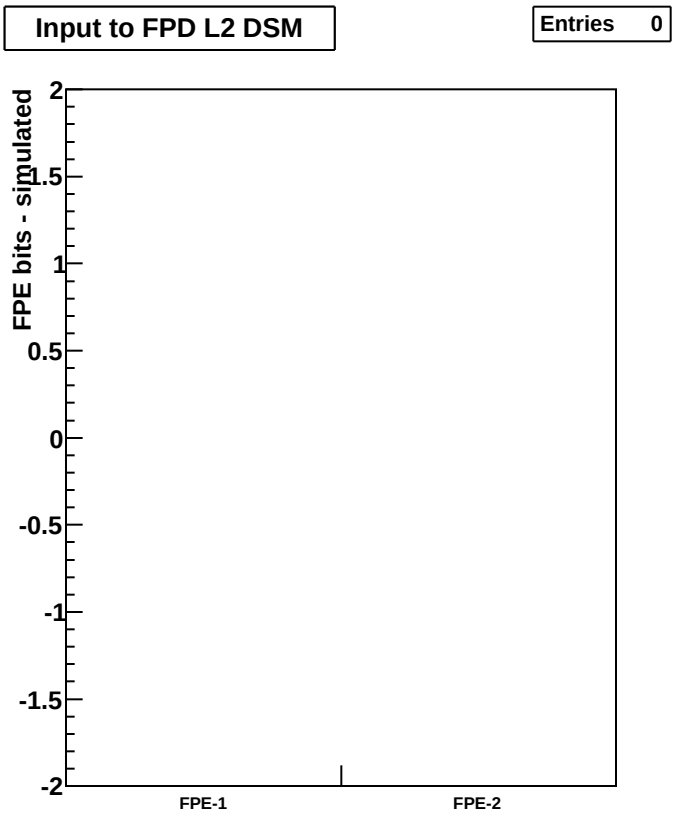

Input to FE003 QT board
Entries 1.179056e+07
ADC
channel


Input to FE004 QT board
Entries 1.179056e+07
ADC
channel


Input to FPE L1 DSM
Entries1473820
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
10
1


Input to FPE L1 DSM
Entries 871320
ADC sum - simulated
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 736910
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
259136
266210
109319
102245
FPE-1
FPE-2


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2