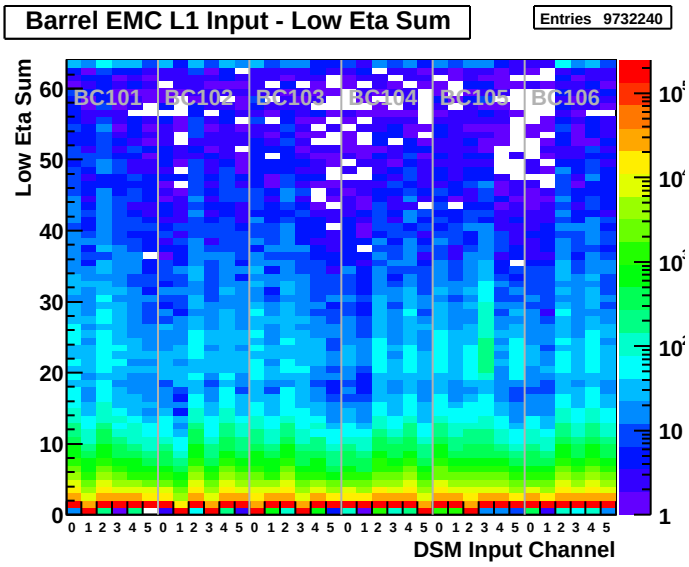
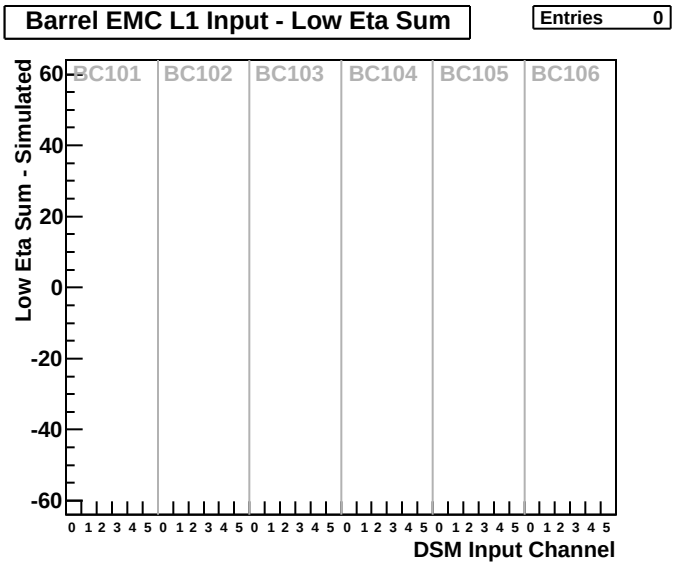
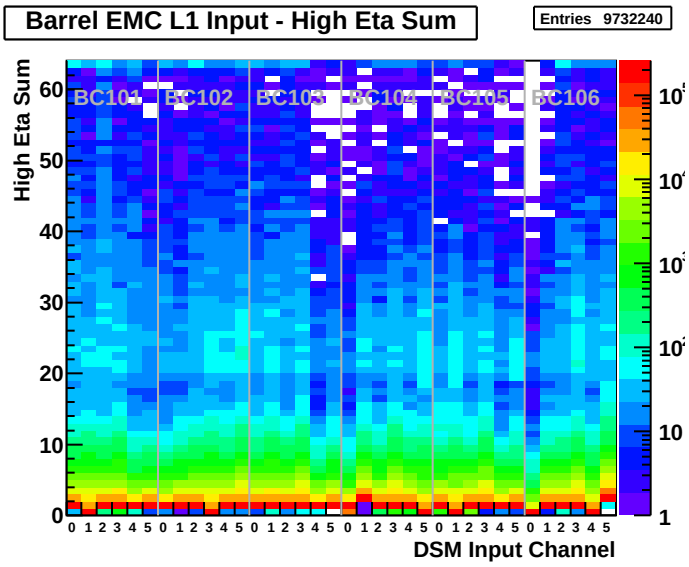
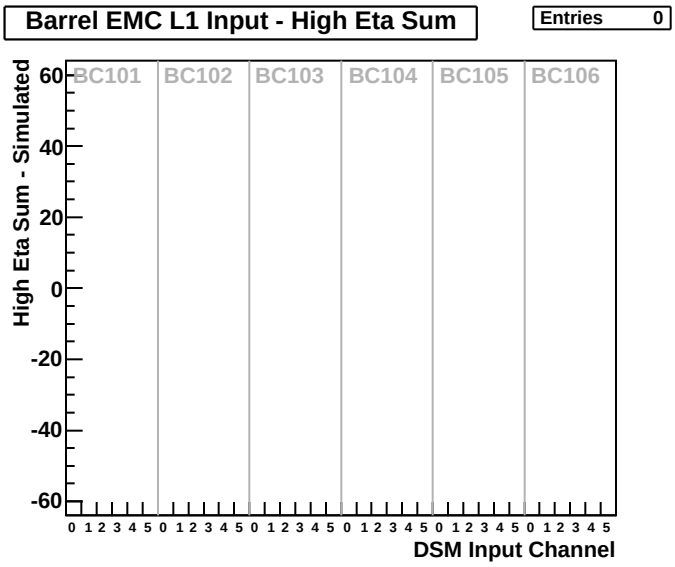
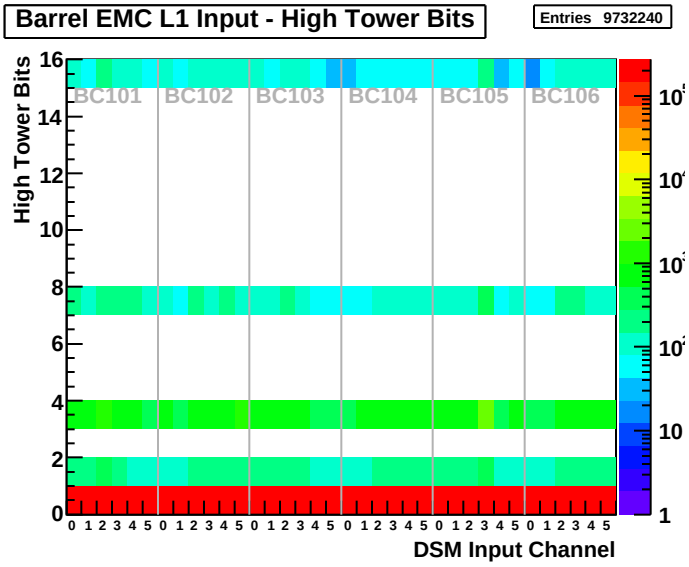
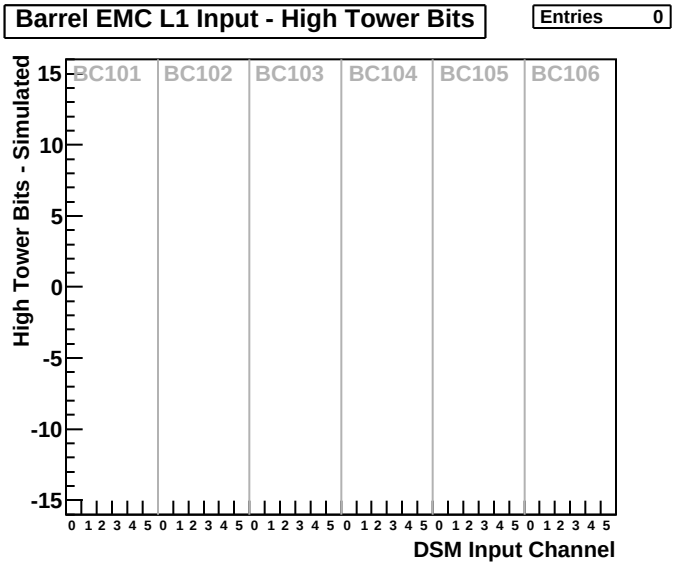

Barrel EMC L1 Input - Low Eta Sum
Entries 9732240
Low Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

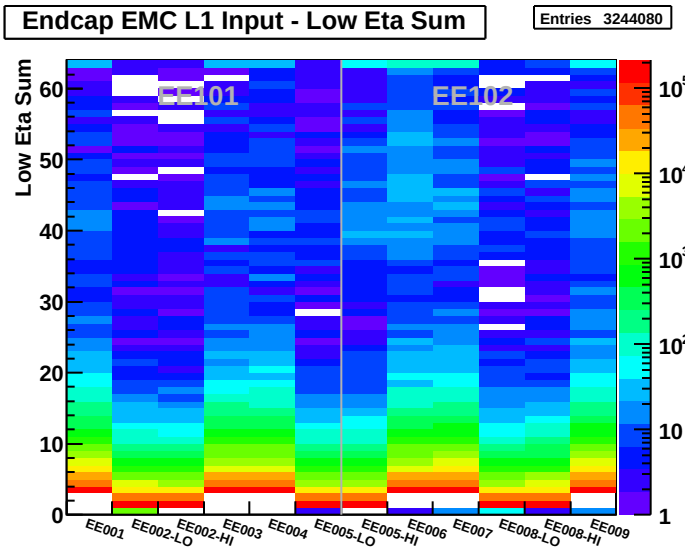

Barrel EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

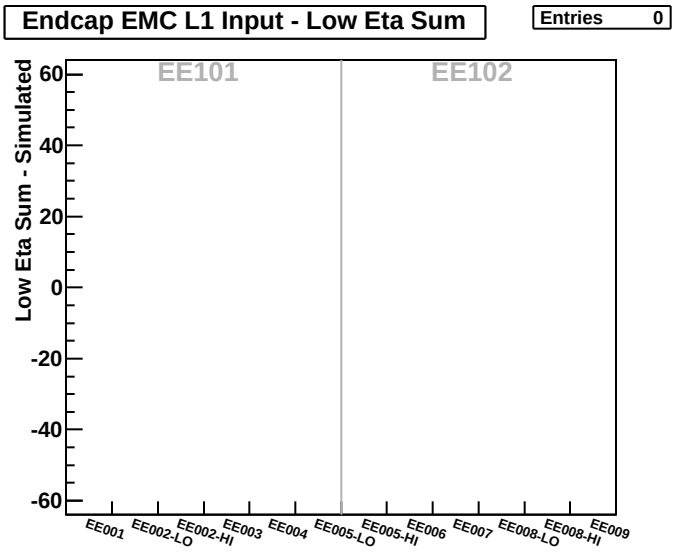

Barrel EMC L1 Input - High Eta Sum
Entries 9732240
High Eta Sum
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

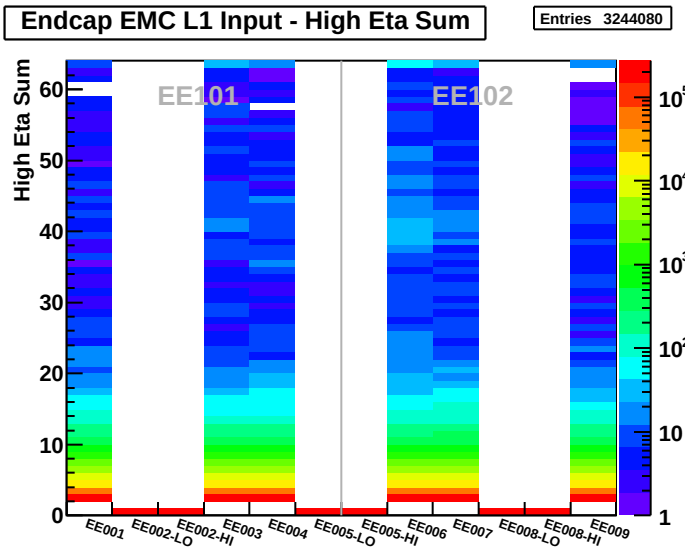

Barrel EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

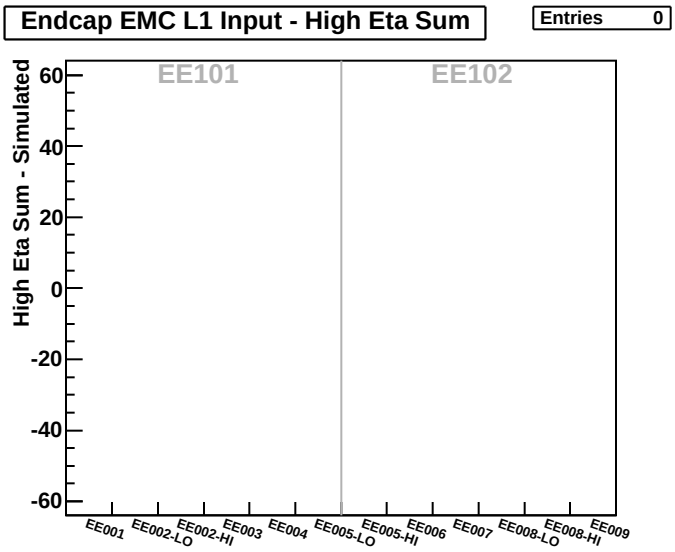

Barrel EMC L1 Input - High Tower Bits
Entries 9732240
High Tower Bits
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

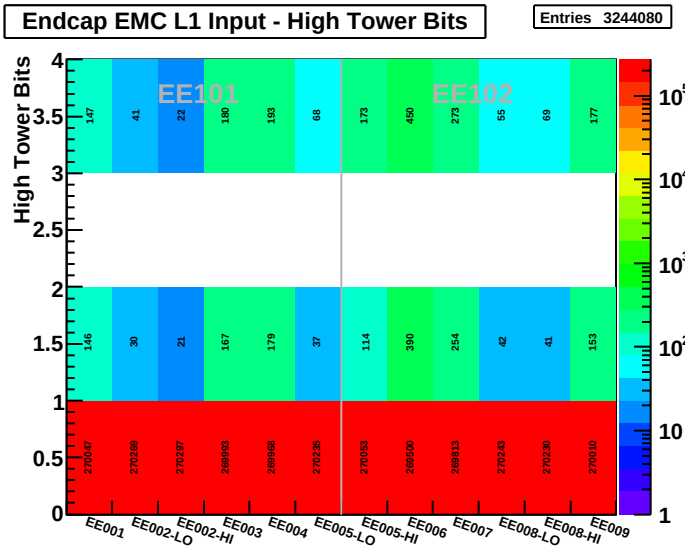

Barrel EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
BC101
BC102
BC103
BC104
BC105
BC106
DSM Input Channel

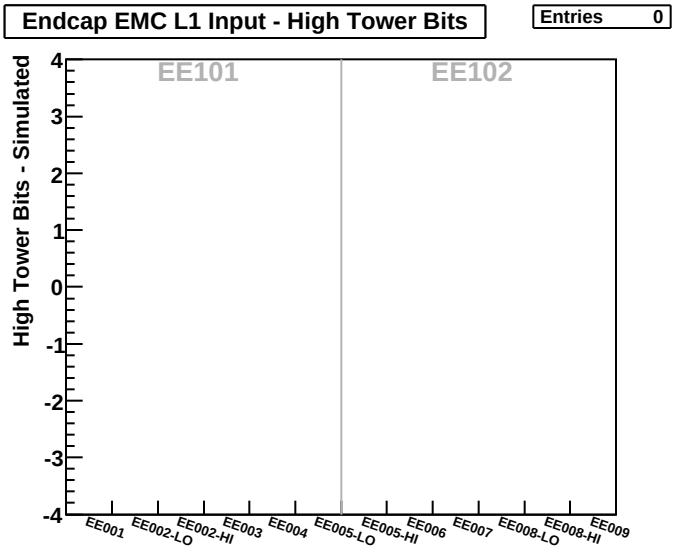

Endcap EMC L1 Input - Low Eta Sum
Entries 3244080
Low Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

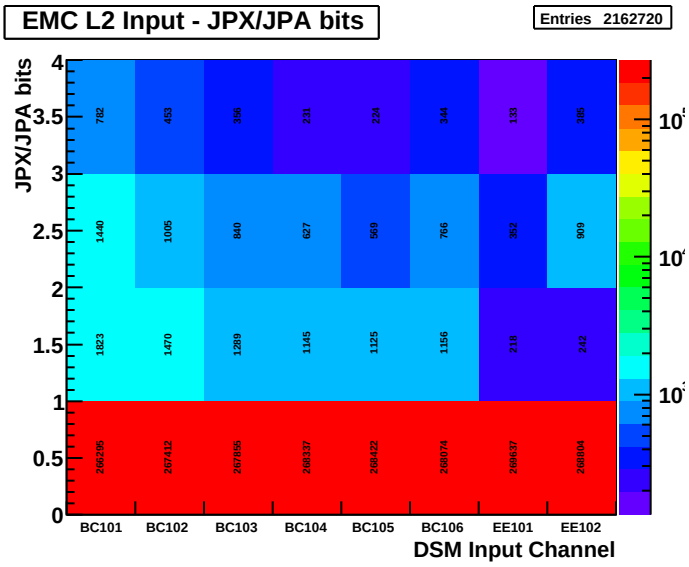

Endcap EMC L1 Input - Low Eta Sum
Entries 0
Low Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

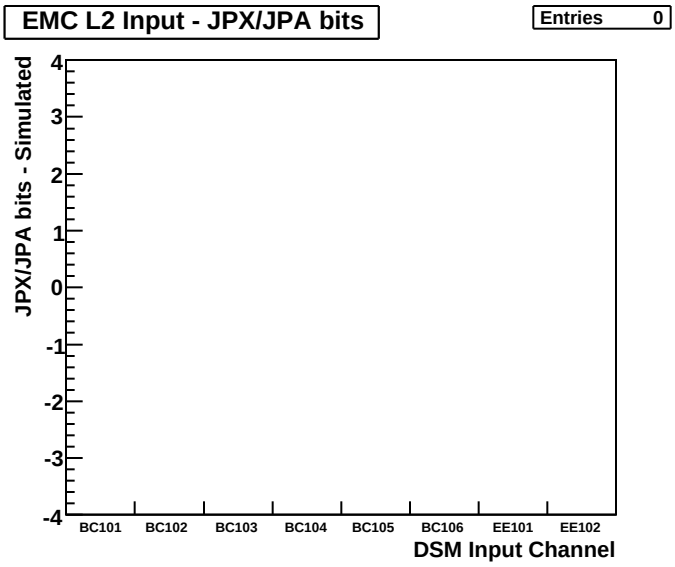

Endcap EMC L1 Input - High Eta Sum
Entries 3244080
High Eta Sum
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

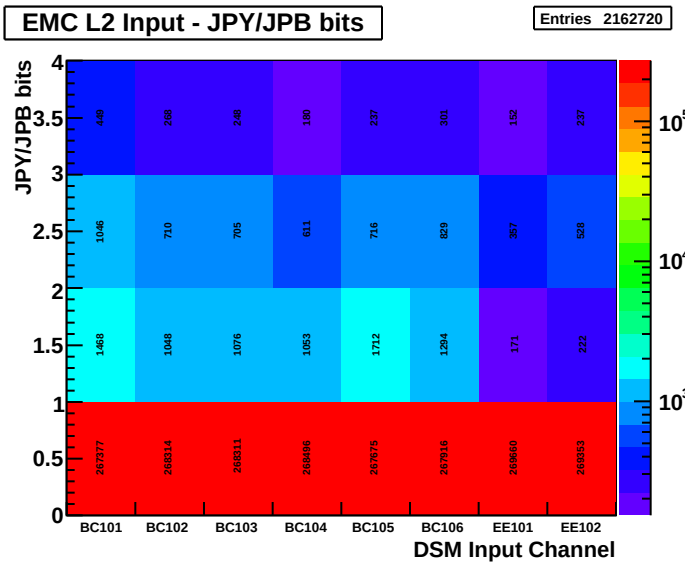

Endcap EMC L1 Input - High Eta Sum
Entries 0
High Eta Sum - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

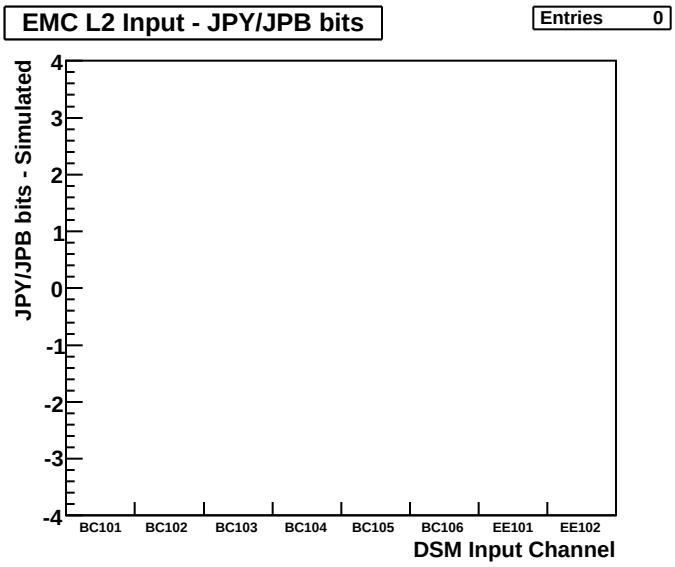

Endcap EMC L1 Input - High Tower Bits
Entries 3244080
High Tower Bits
EE101
EE102
147 41 22 180 193 68 173 450 273 55 69 177
146 30 21 167 179 37 114 390 254 42 41 153
270047 270269 270297 269993 269988 270235 270053 269500 269813 270243 270230 270010
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

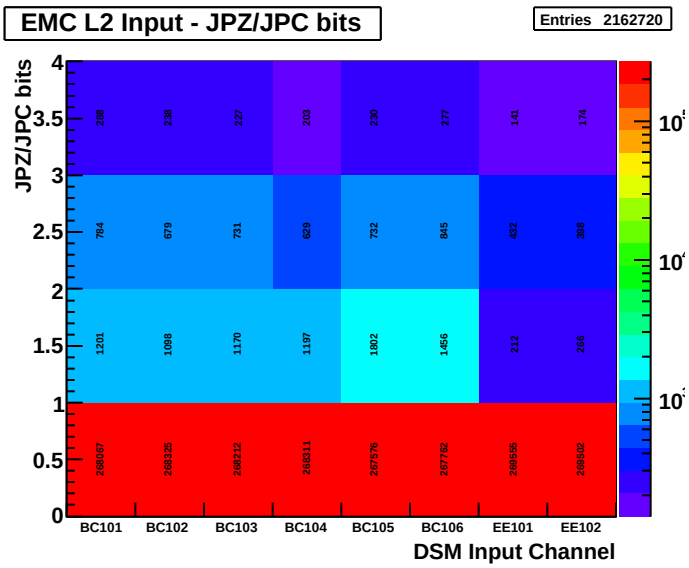

Endcap EMC L1 Input - High Tower Bits
Entries 0
High Tower Bits - Simulated
EE101
EE102
EE001 EE002-LO EE002-HI EE003 EE004 EE005-LO EE005-HI EE006 EE007 EE008-LO EE008-HI EE009

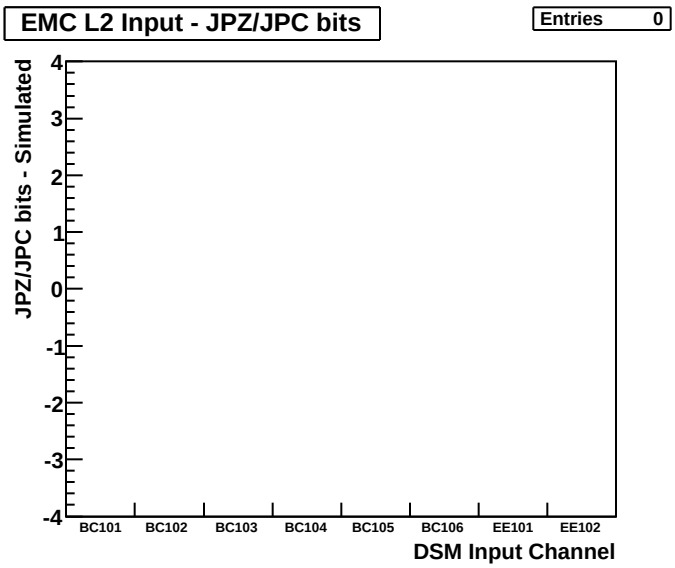

EMC L2 Input - JPX/JPA bits
Entries 2162720
JPX/JPA bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
782 453 356 231 224 344 133 385
1440 1005 840 627 569 766 382 909
1823 1470 1289 1145 1125 1156 218 242
266295 267412 267855 268337 268422 268074 269637 269804

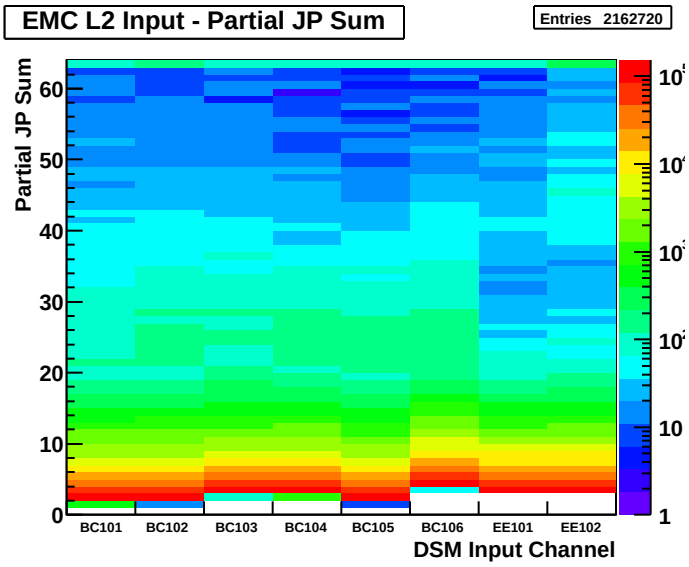

EMC L2 Input - JPX/JPA bits
Entries 0
JPX/JPA bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

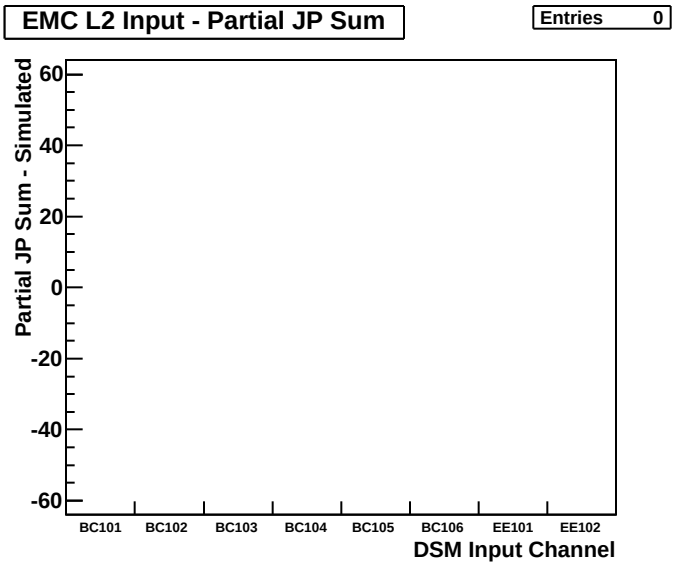

EMC L2 Input - JPY/JPB bits
Entries 2162720
JPY/JPB bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
446 268 248 180 237 301 152 237
1046 710 705 611 716 829 357 528
1468 1048 1076 1053 1712 1294 171 222
267377 268314 268311 268496 267675 267916 269660 269353

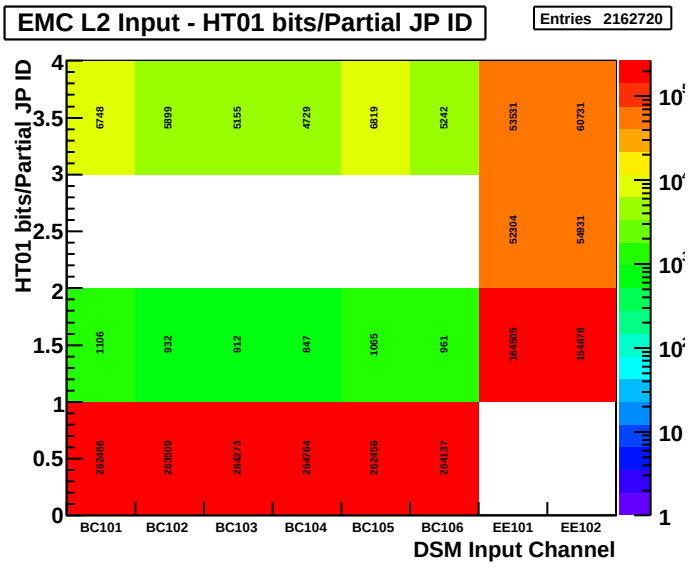

EMC L2 Input - JPY/JPB bits
Entries 0
JPY/JPB bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

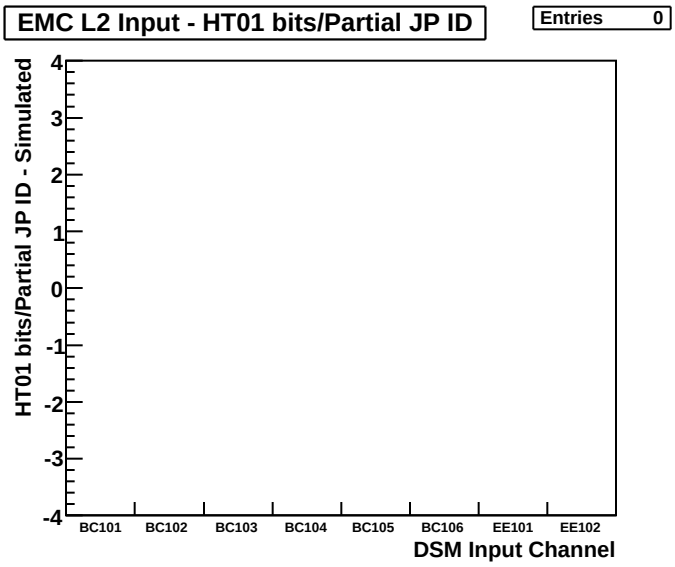

EMC L2 Input - JPZ/JPC bits
Entries 2162720
JPZ/JPC bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102
238 238 227 203 230 277 141 174
784 679 731 629 732 845 432 398
1201 1098 1170 1197 1802 1456 212 266
268067 268325 268212 268311 267576 267762 269555 269502

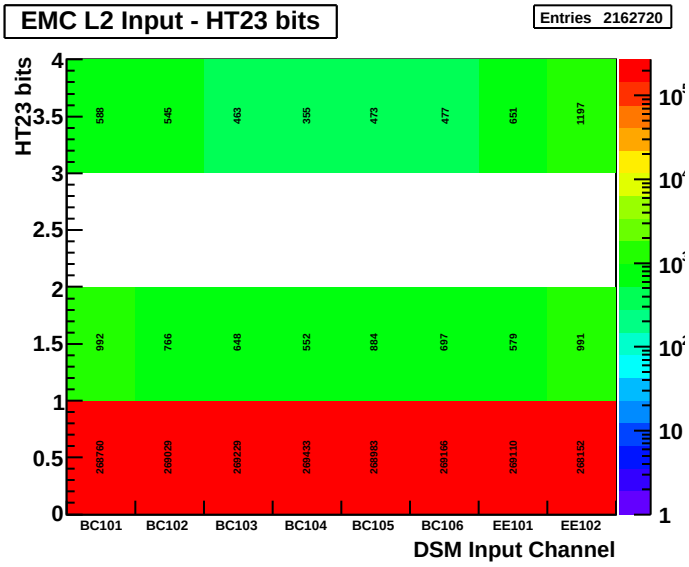

EMC L2 Input - JPZ/JPC bits
Entries 0
JPZ/JPC bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

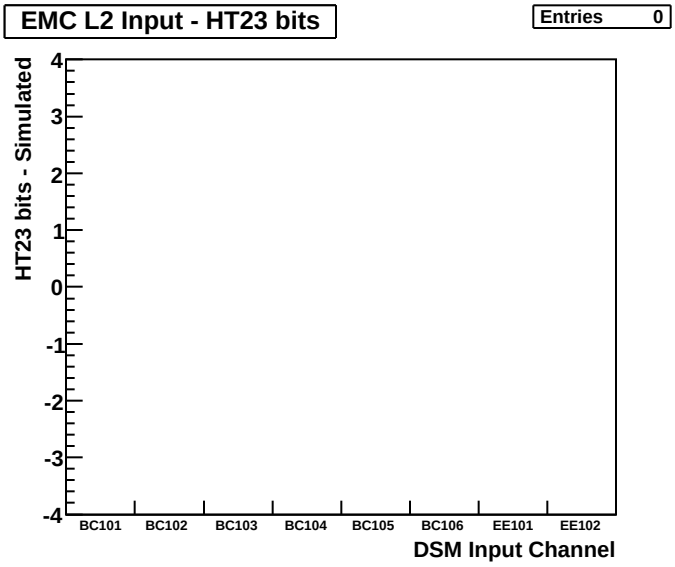

EMC L2 Input - Partial JP Sum
Entries 2162720
Partial JP Sum
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

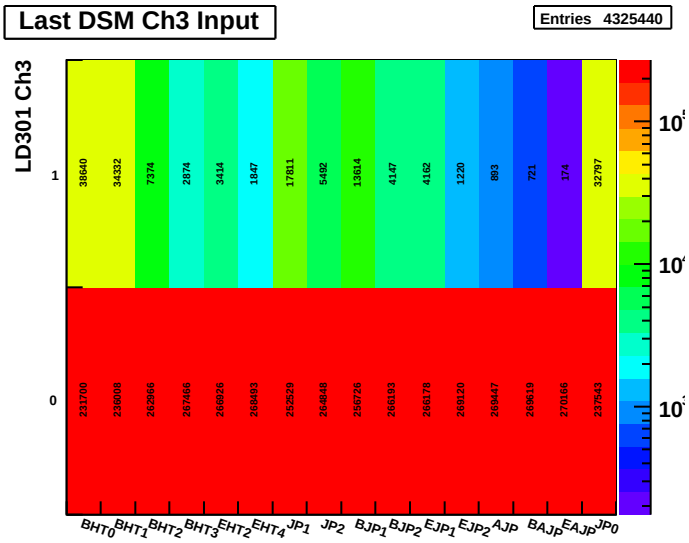

EMC L2 Input - Partial JP Sum
Entries 0
Partial JP Sum - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

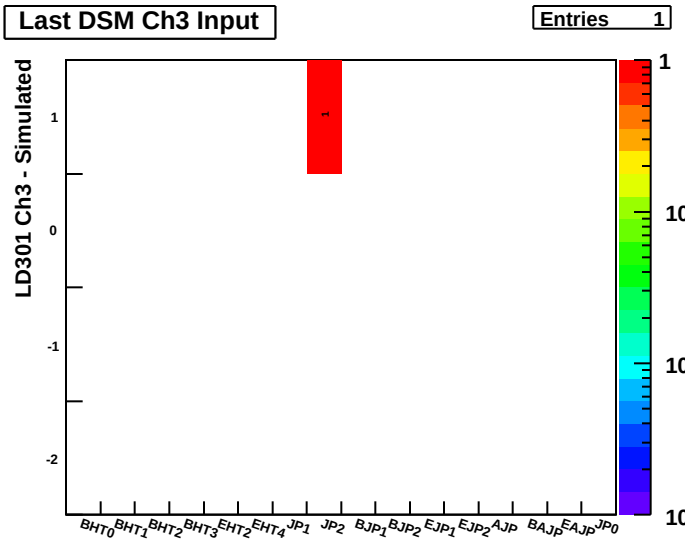

EMC L2 Input - HT01 bits/Partial JP ID
Entries 2162720
HT01 bits/Partial JP ID
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

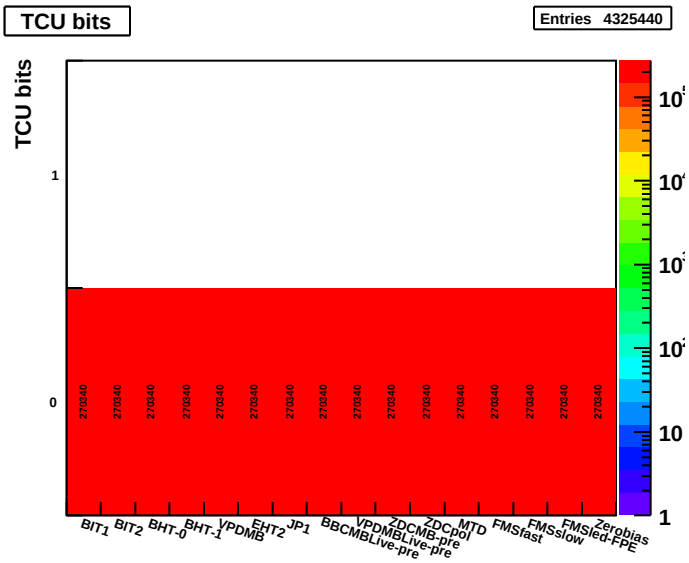

EMC L2 Input - HT01 bits/Partial JP ID
Entries 0
HT01 bits/Partial JP ID - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

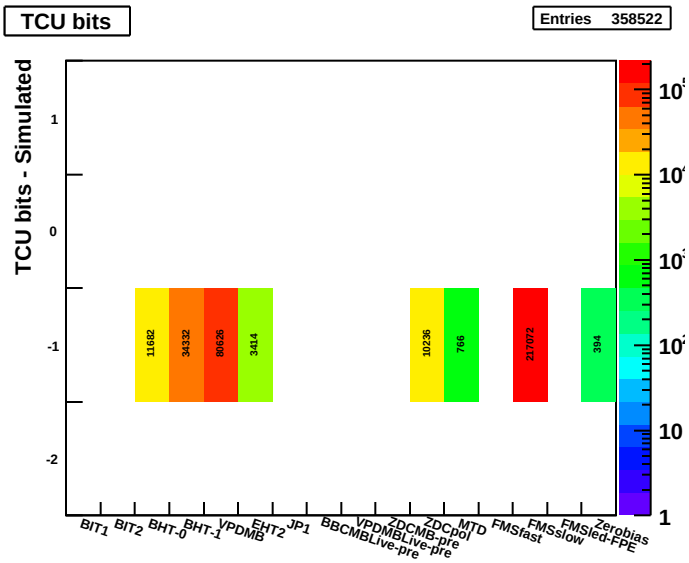

EMC L2 Input - HT23 bits
Entries 2162720
HT23 bits
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

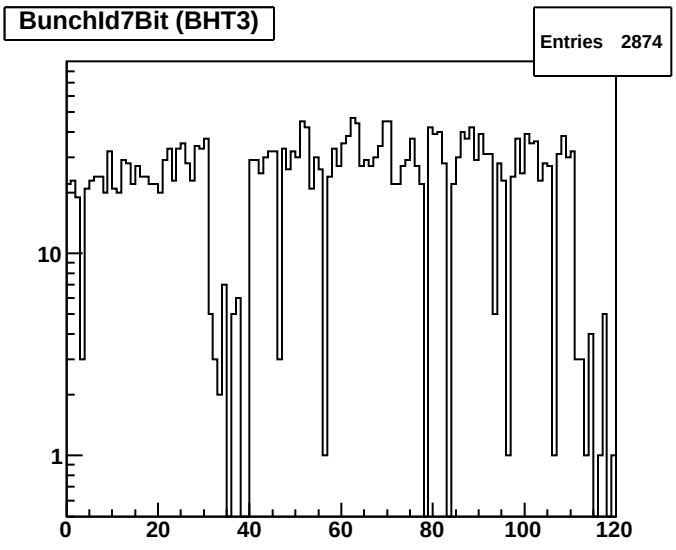

EMC L2 Input - HT23 bits
Entries 0
HT23 bits - Simulated
DSM Input Channel
BC101 BC102 BC103 BC104 BC105 BC106 EE101 EE102

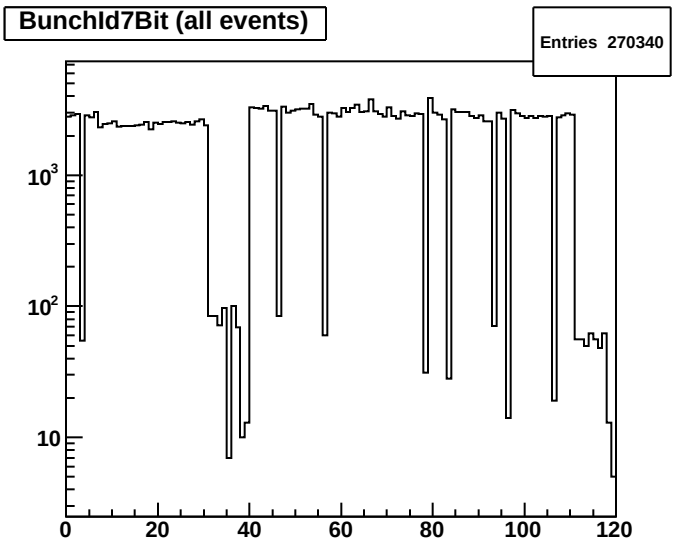

Last DSM Ch3 Input
Entries 4325440
LD301 Ch3
38640 34332 7374 2874 3414 1847 17811 5492 13614 4147 4162 1220 893 721 174 32797
231700 236008 262966 267466 268926 268493 252529 264848 256726 266183 266178 269120 269447 269619 270156 237543
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

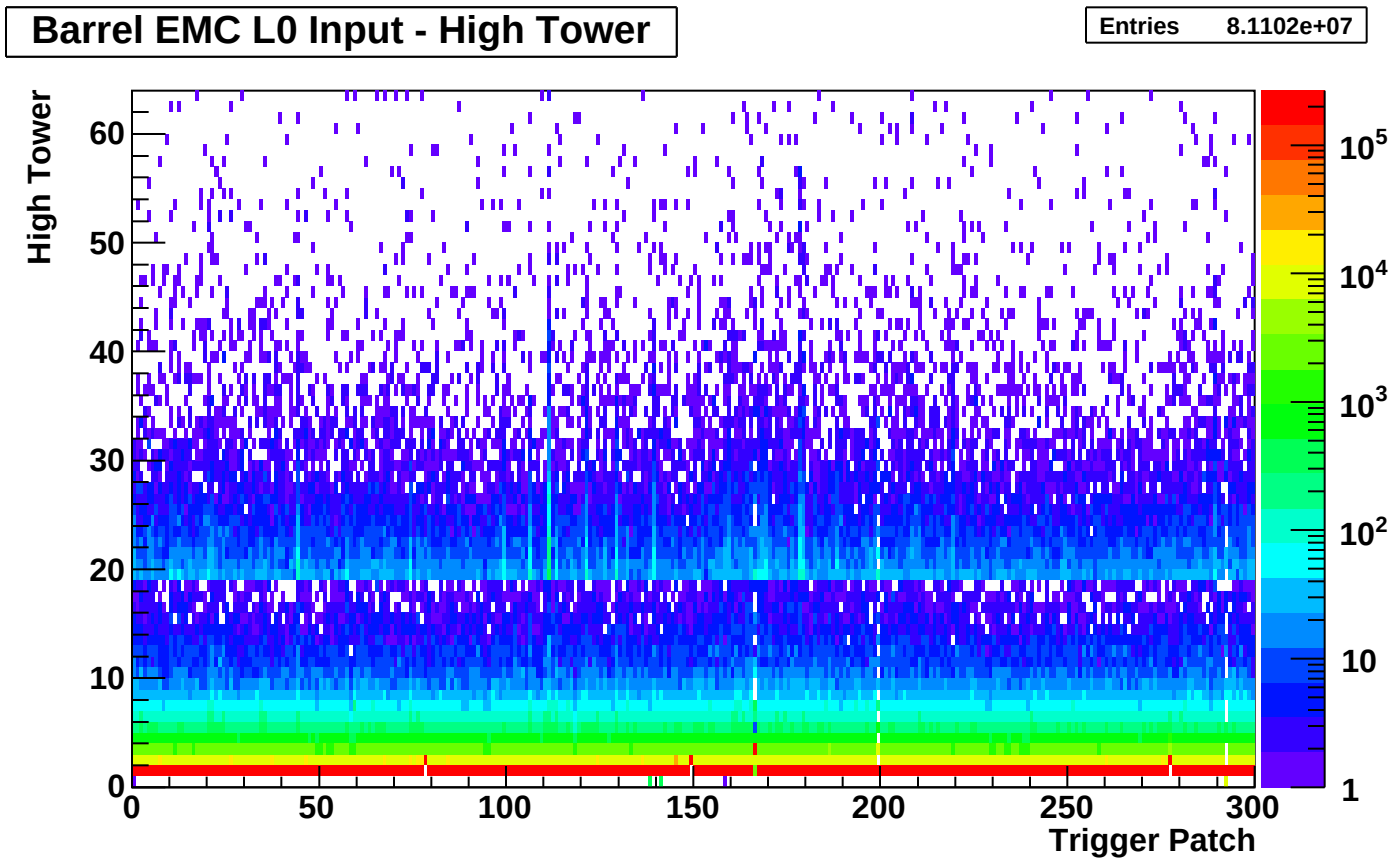

Last DSM Ch3 Input
Entries 1
LD301 Ch3 - Simulated
1
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

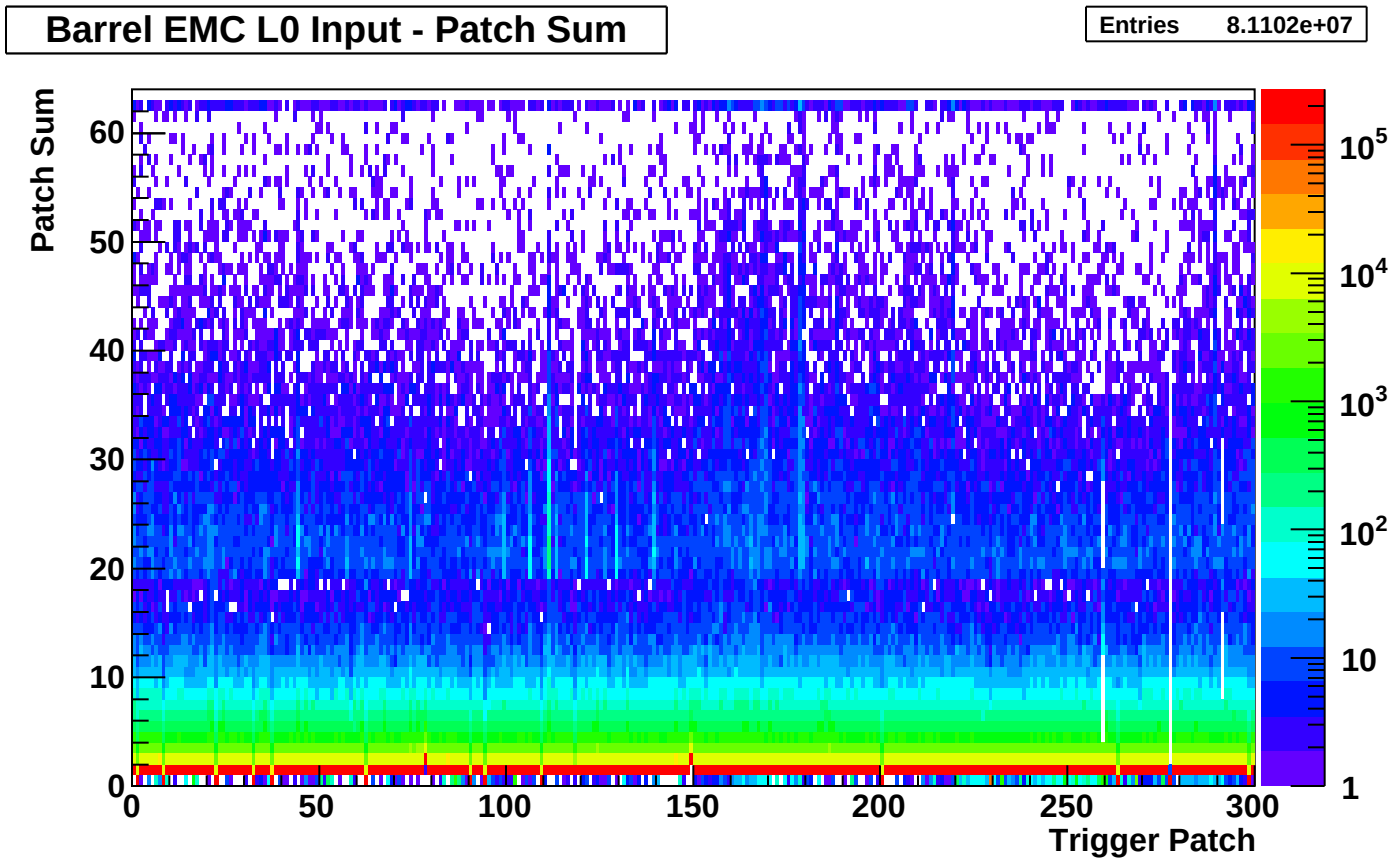

TCU bits
Entries 4325440
TCU bits
270340 270340 270340 270340 270340 270340 270340 270340 270340 270340 270340 270340 270340 270340 270340 270340
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

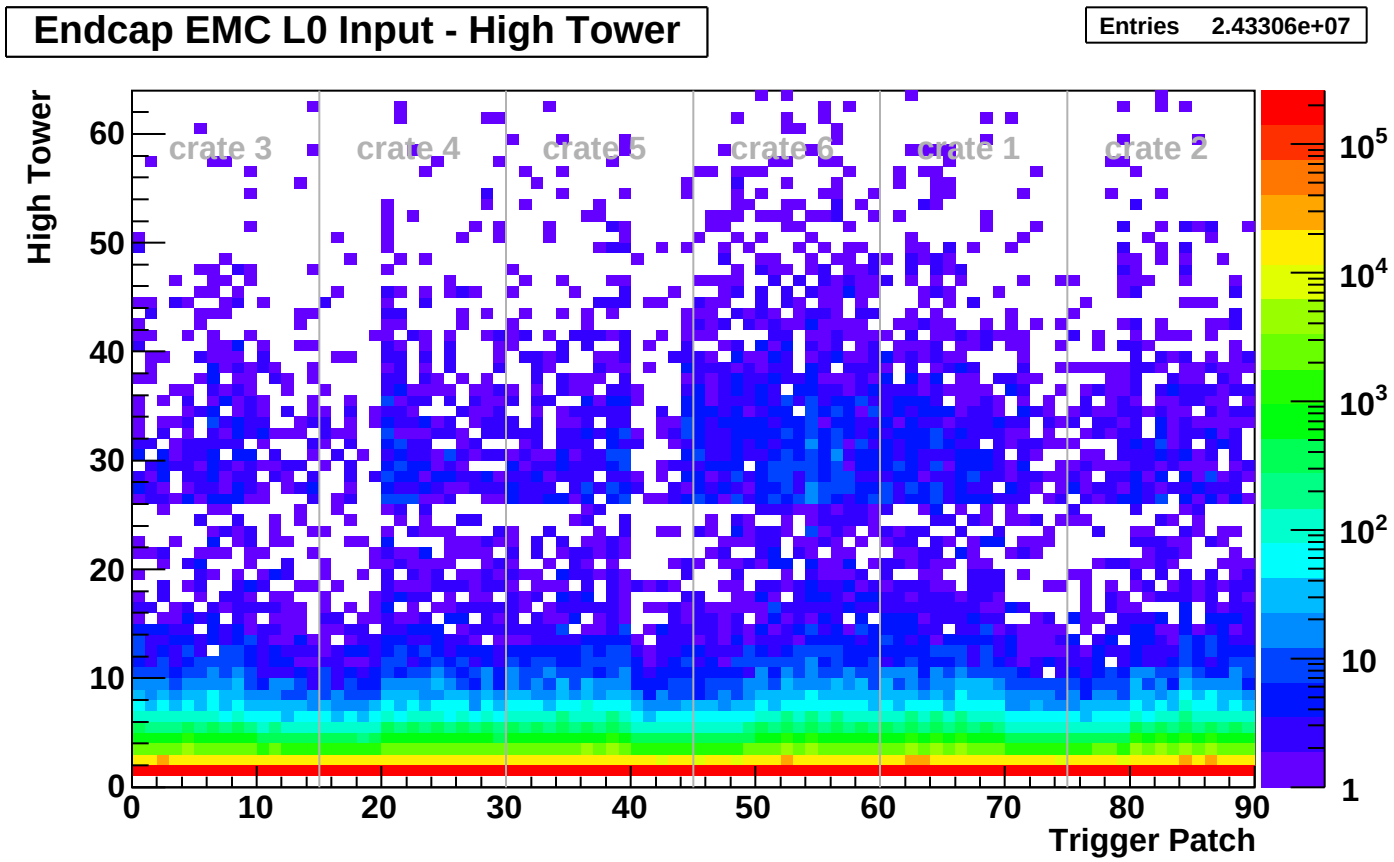

TCU bits
Entries 358522
TCU bits - Simulated
11682 34332 80626 3414 10236 766 217072 394
BIT1 BIT2 BHT-0 BHT-1 VPDMB EHT2 JP1 BBCMBLive-pre VPDMBLive-pre ZDCMB-pre ZDCpol MTD FMSfast FMSslow FMSled-FPE Zerobias

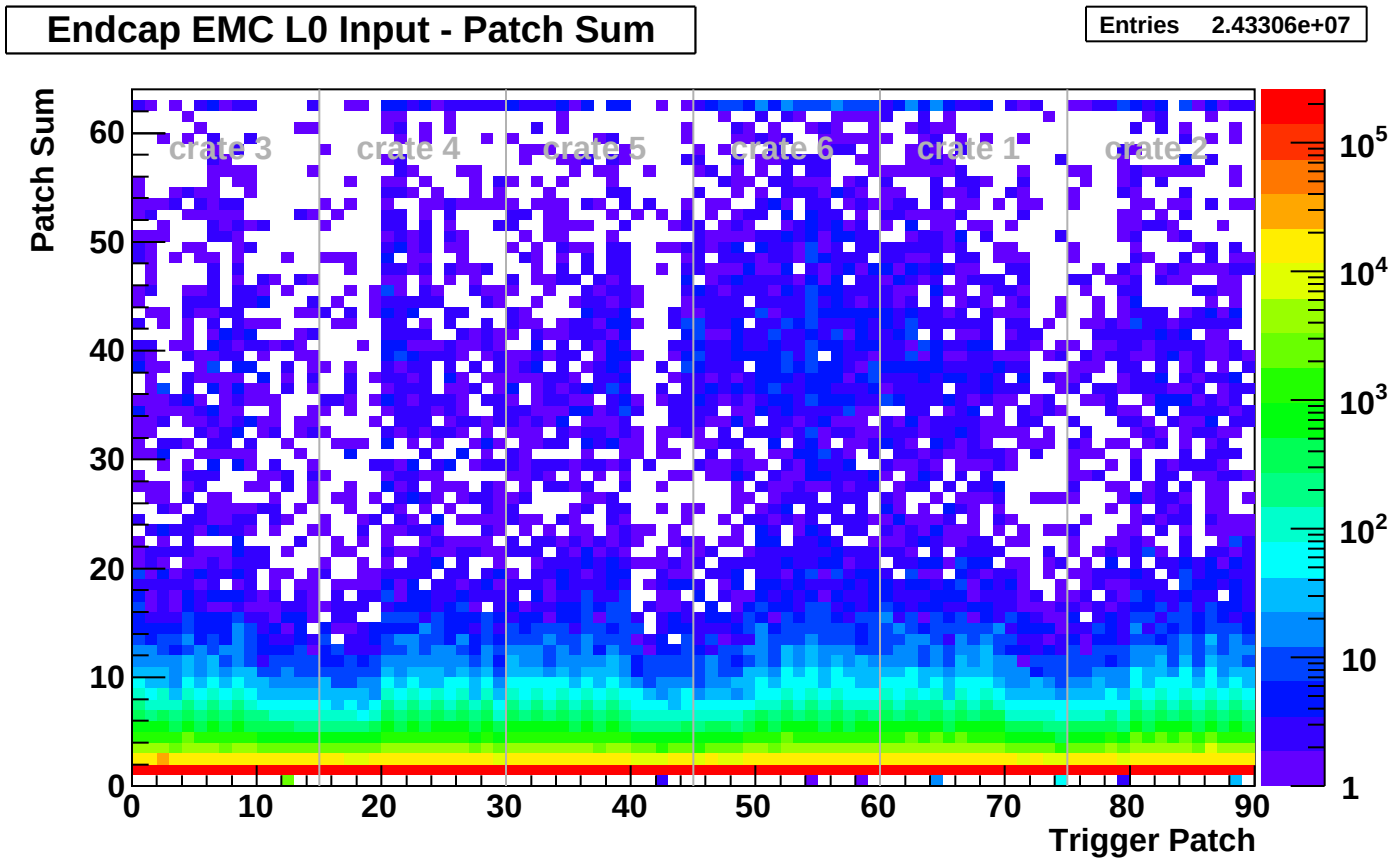

BunchId7Bit (BHT3)
Entries 2874

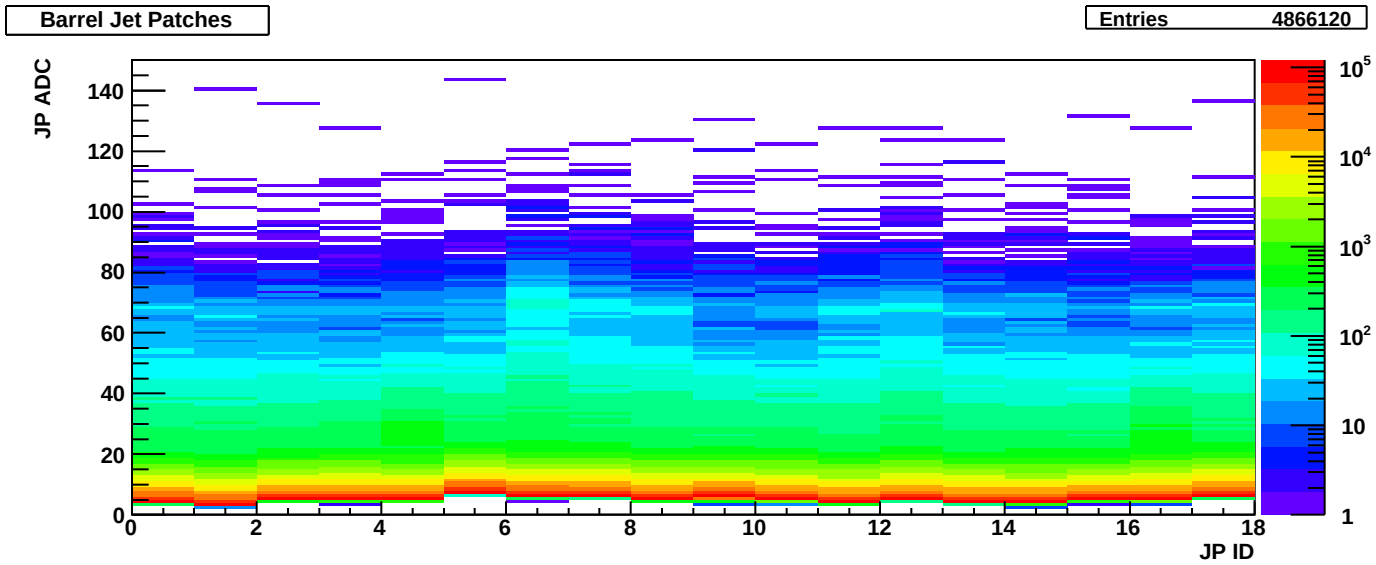

BunchId7Bit (all events)
Entries 270340

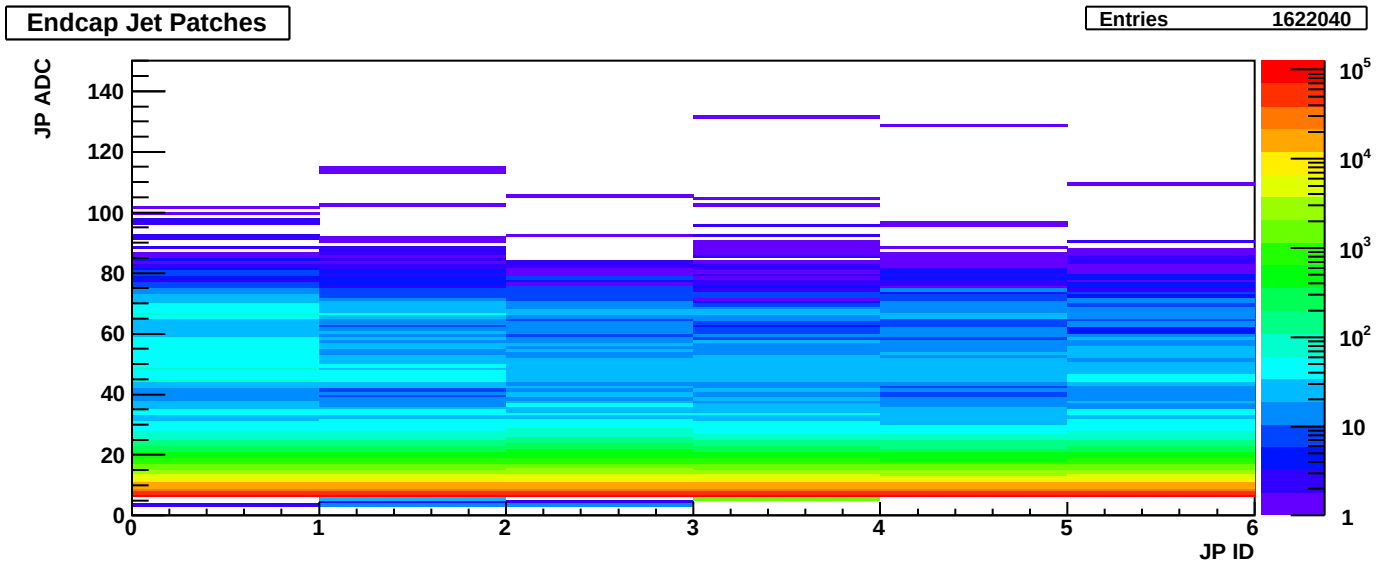

Barrel EMC L0 Input - High Tower
Entries 8.1102e+07
High Tower
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

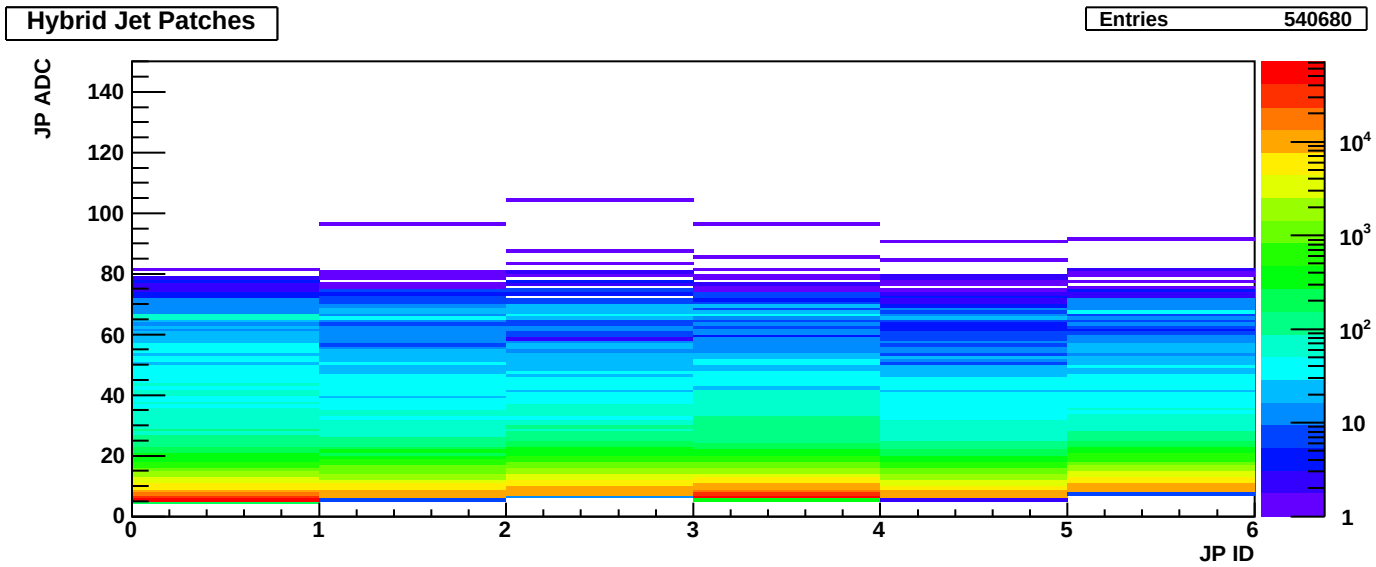

Barrel EMC L0 Input - Patch Sum
Entries 8.1102e+07
Patch Sum
Trigger Patch
0
10
20
30
40
50
60
0
50
100
150
200
250
300
1
10
10^2
10^3
10^4
10^5

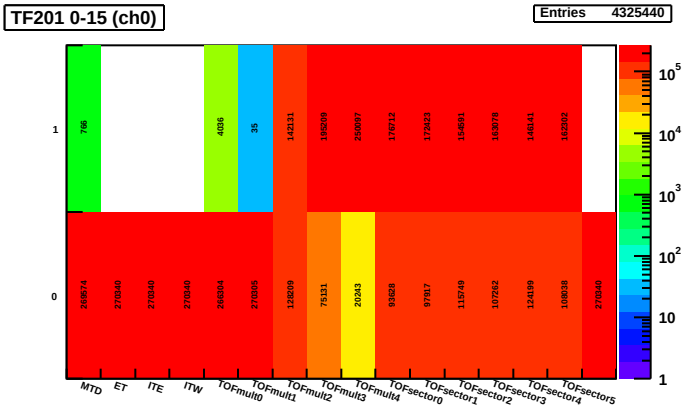

Endcap EMC L0 Input - High Tower
Entries 2.43306e+07
High Tower
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

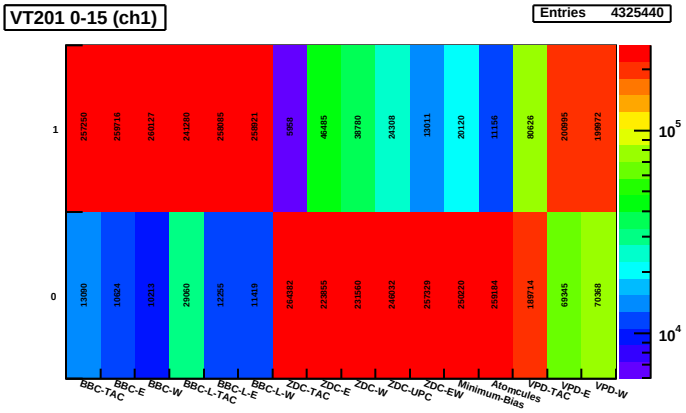

Endcap EMC L0 Input - Patch Sum
Entries 2.43306e+07
Patch Sum
Trigger Patch
crate 3
crate 4
crate 5
crate 6
crate 1
crate 2

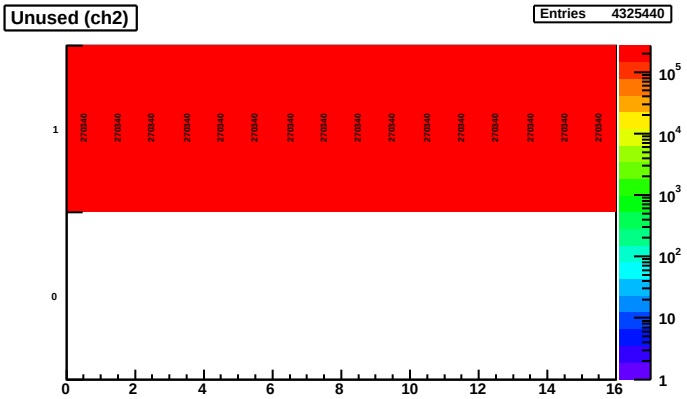

Barrel Jet Patches
Entries 4866120
JP ADC
JP ID

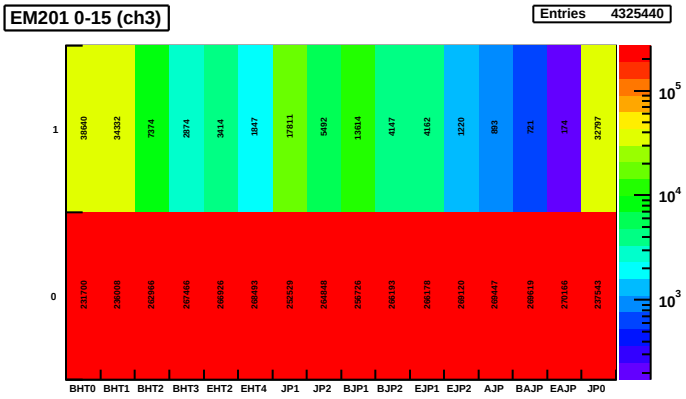

Endcap Jet Patches
Entries 1622040
JP ADC
JP ID

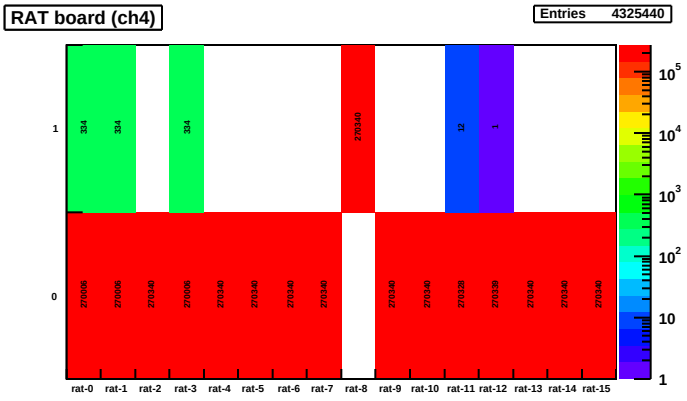

Hybrid Jet Patches
Entries 540680
JP ADC
JP ID

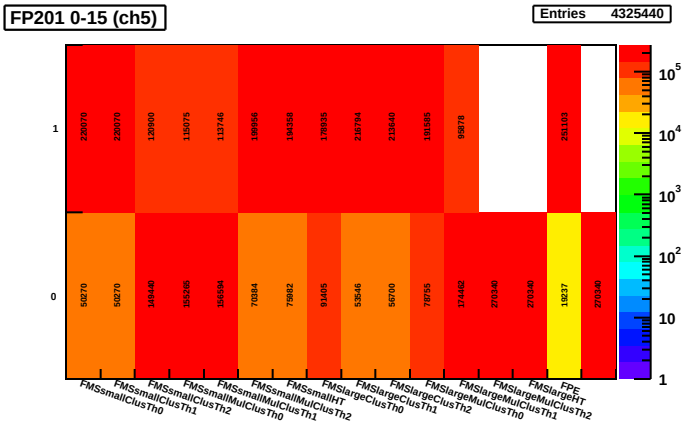

TF201 0-15 (ch0)
Entries 4325440
MTD ET ITE ITW TOFmult0 TOFmult1 TOFmult2 TOFmult3 TOFmult4 TOFsector0 TOFsector1 TOFsector2 TOFsector3 TOFsector4 TOFsector5

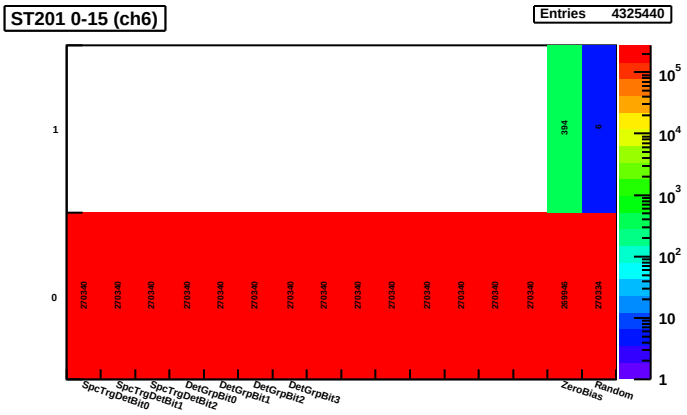

VT201 0-15 (ch1)
Entries 4325440
BBC-TAC BBC-E BBC-W BBC-L-TAC BBC-L-E BBC-L-W ZDC-TAC ZDC-E ZDC-W ZDC-UPC ZDC-EW Minimum-Bias Atomcules VPD-TAC VPD-E VPD-W

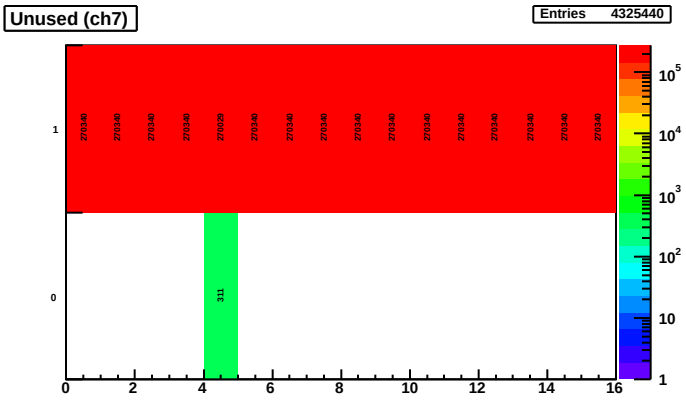

Unused (ch2)
Entries 4325440

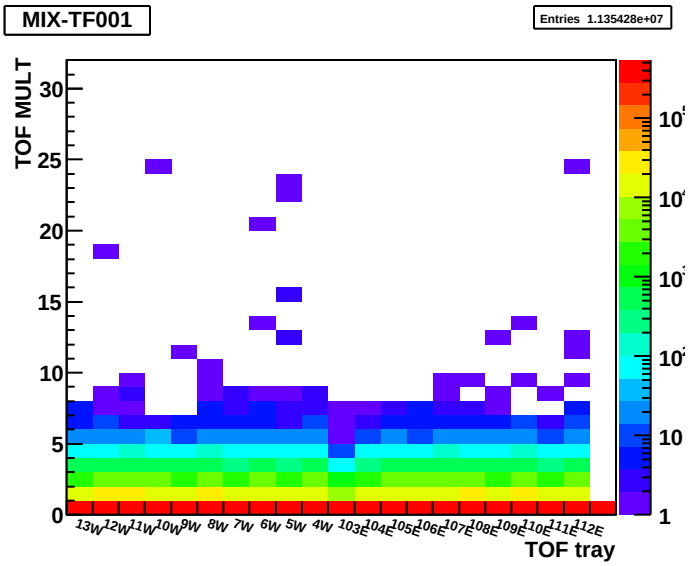

EM201 0-15 (ch3)
Entries 4325440
BHT0 BHT1 BHT2 BHT3 EHT2 EHT4 JP1 JP2 BJP1 BJP2 EJP1 EJP2 AJP BAJP EAJP JP0

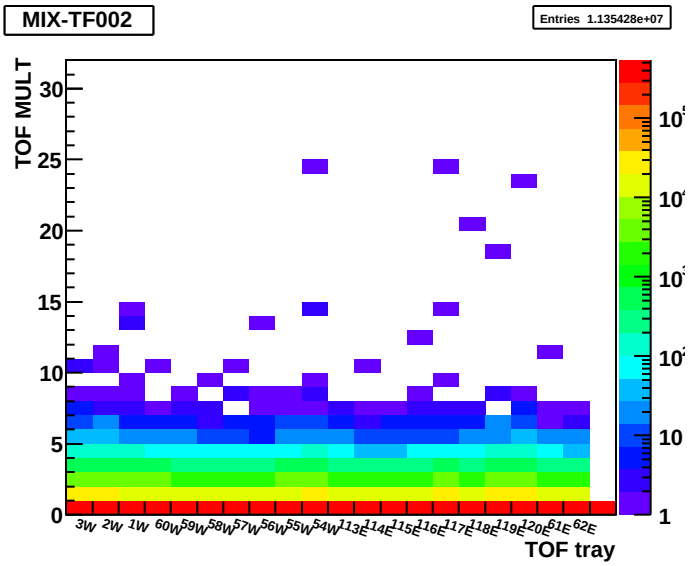

RAT board (ch4)
Entries 4325440
rat-0 rat-1 rat-2 rat-3 rat-4 rat-5 rat-6 rat-7 rat-8 rat-9 rat-10 rat-11 rat-12 rat-13 rat-14 rat-15

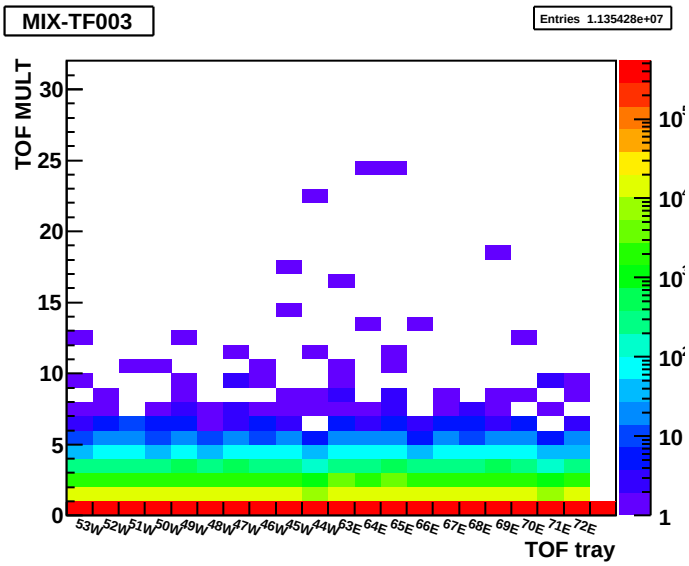

FP201 0-15 (ch5)
Entries 4325440
FMSsmallClusTh0 FMSsmallClusTh1 FMSsmallClusTh2 FMSsmallMulClusTh0 FMSsmallMulClusTh1 FMSsmallMulClusTh2 FMSsmallHT FMSlargeClusTh0 FMSlargeClusTh1 FMSlargeClusTh2 FMSlargeMulClusTh0 FMSlargeMulClusTh1 FMSlargeMulClusTh2 FMSlargeHT FPE

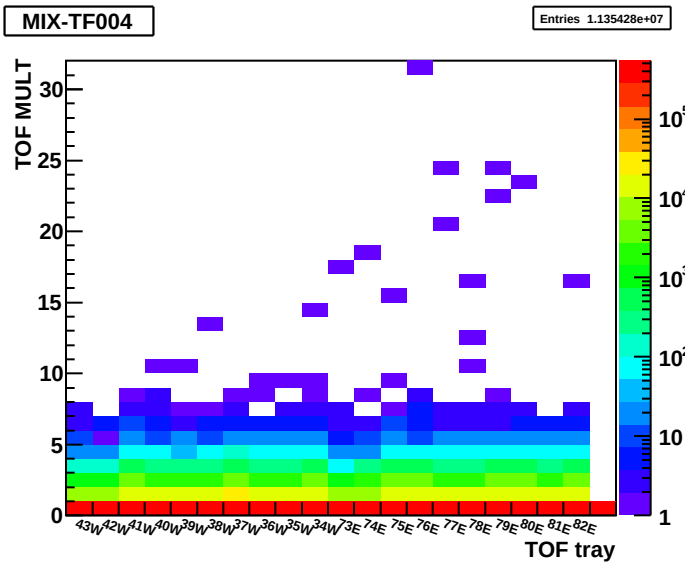

ST201 0-15 (ch6)
Entries 4325440
SpcTrgDetBit0 SpcTrgDetBit1 SpcTrgDetBit2 DetCrpBit0 DetCrpBit1 DetCrpBit2 DetCrpBit3 ZeroBias Random

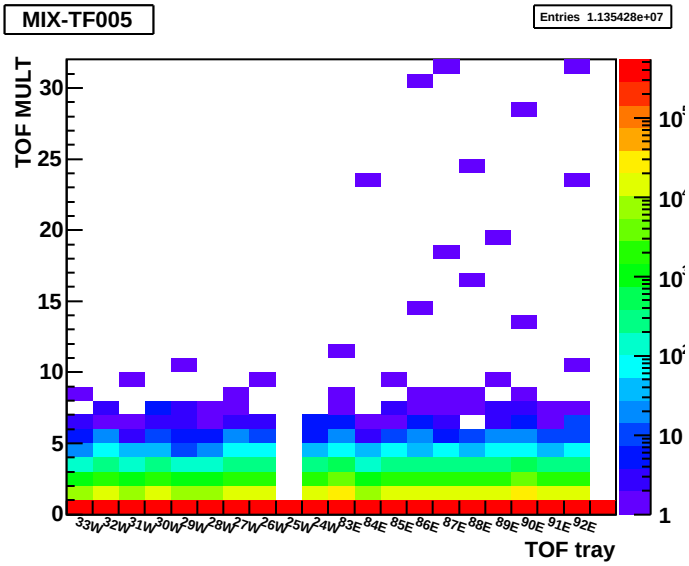

Unused (ch7)
Entries 4325440

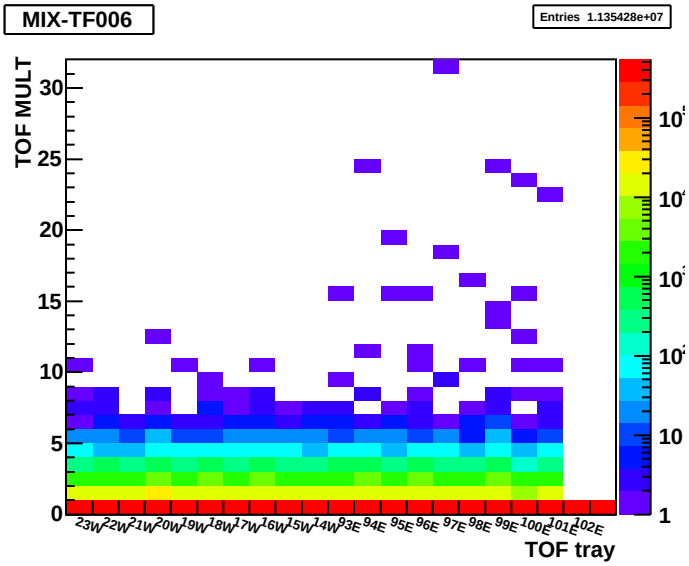

MIX-TF001
Entries 1.135428e+07
TOF MULT
TOF tray

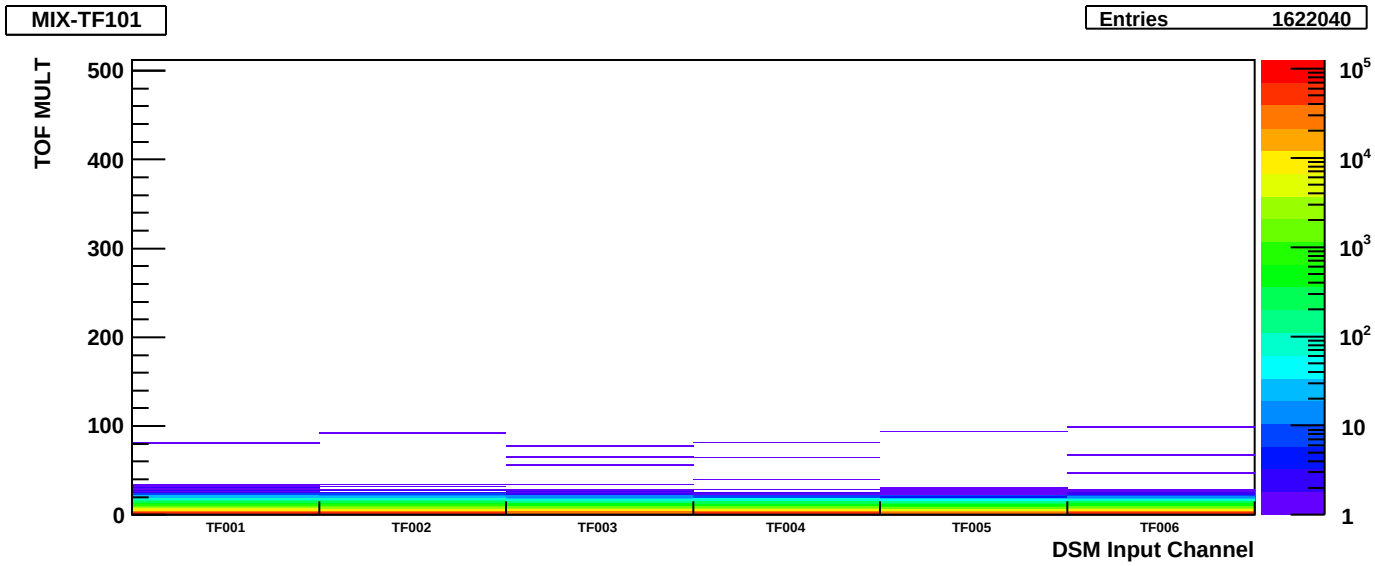

MIX-TF002
Entries 1.135428e+07
TOF MULT
TOF tray

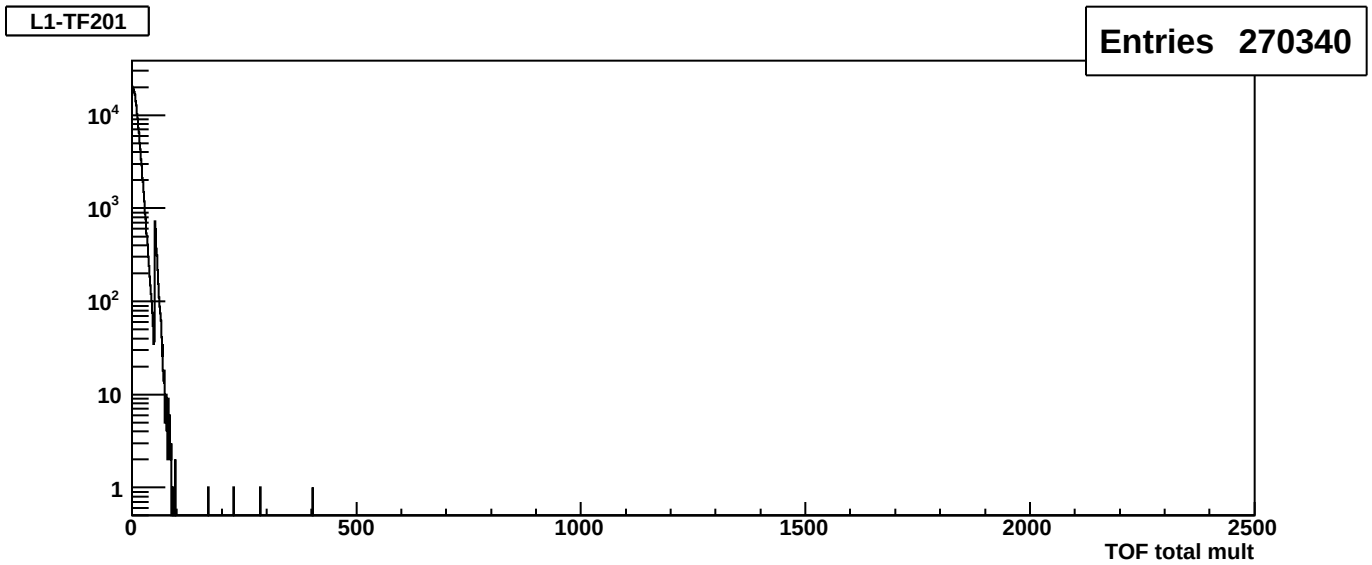

MIX-TF003
Entries 1.135428e+07
TOF MULT
TOF tray

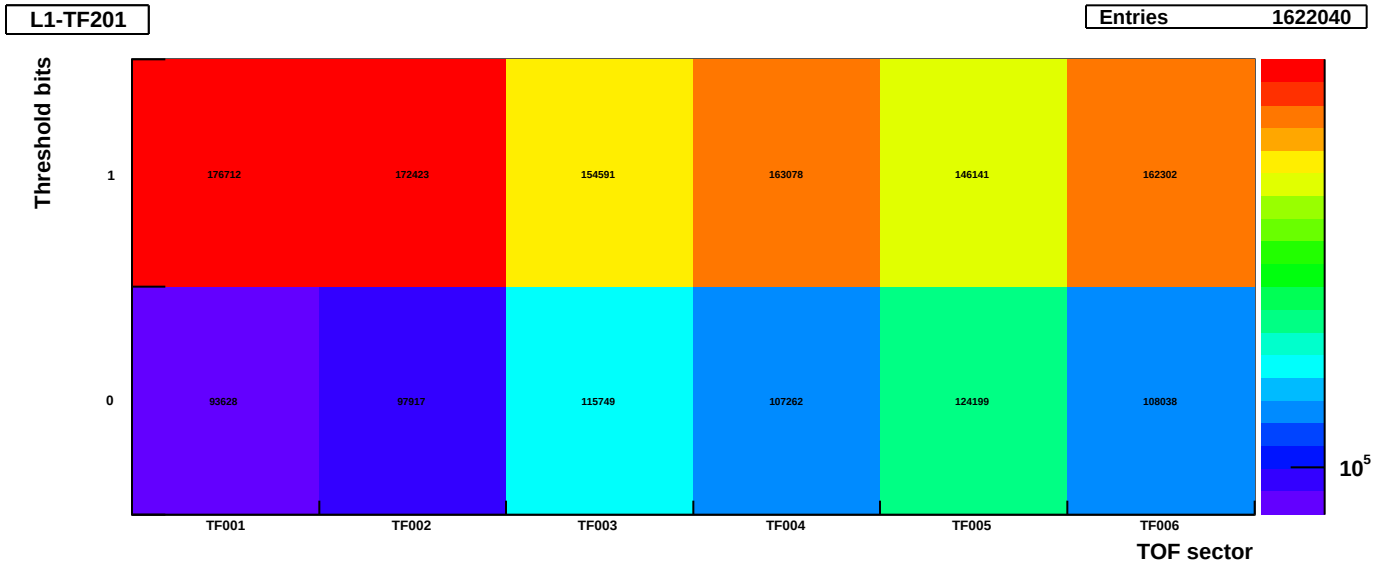

MIX-TF004
Entries 1.135428e+07
TOF MULT
TOF tray

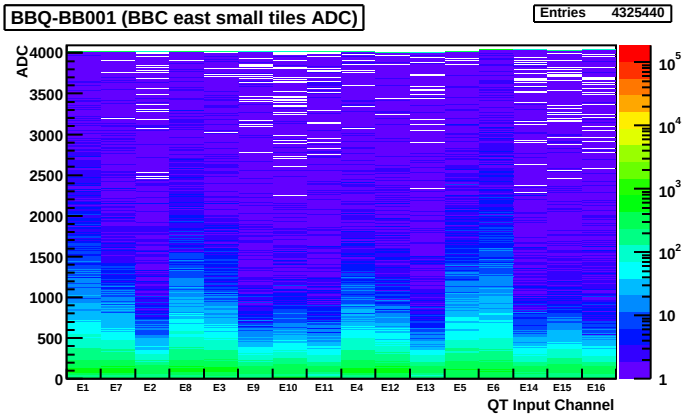

MIX-TF005
Entries 1.135428e+07
TOF MULT
TOF tray

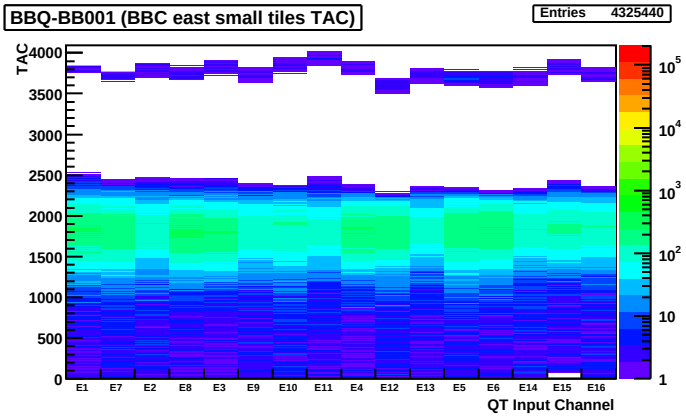

MIX-TF006
Entries 1.135428e+07
TOF MULT
TOF tray

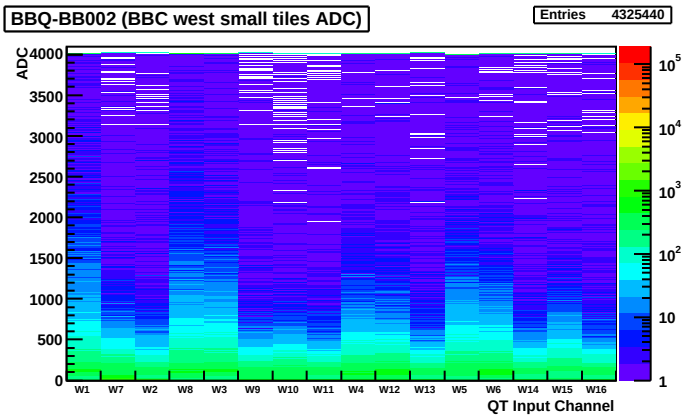

MIX-TF101
Entries 1622040
TOF MULT
500
400
300
200
100
0
TF001
TF002
TF003
TF004
TF005
TF006
DSM Input Channel
10^5
10^4
10^3
10^2
10
1

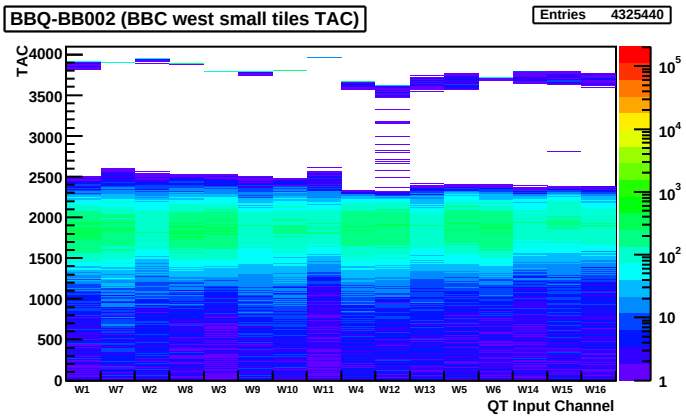

L1-TF201
Entries 270340
10^4
10^3
10^2
10
1
0
500
1000
1500
2000
2500
TOF total mult

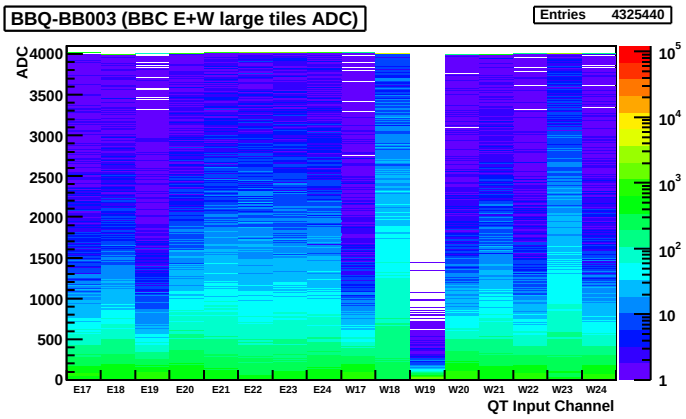

L1-TF201
Entries 1622040
Threshold bits
1
0
176712
172423
154591
163078
146141
162302
93628
97917
115749
107262
124199
108038
TF001
TF002
TF003
TF004
TF005
TF006
TOF sector
10^5

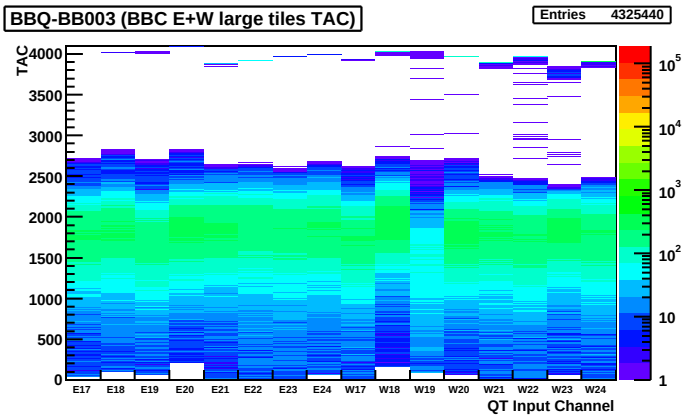

BBQ-BB001 (BBC east small tiles ADC)
Entries 4325440
ADC
QT Input Channel

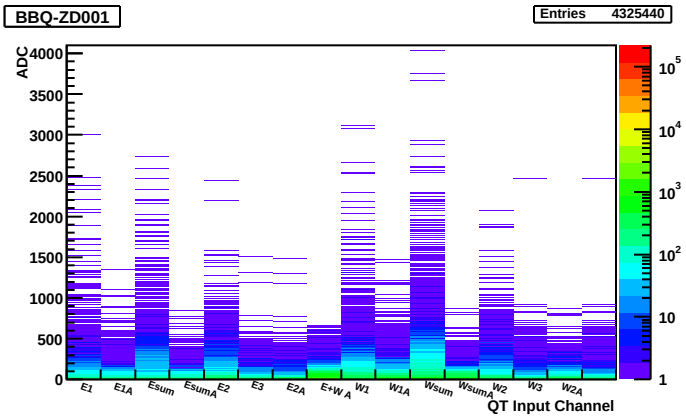

BBQ-BB001 (BBC east small tiles TAC)
Entries 4325440
TAC
QT Input Channel

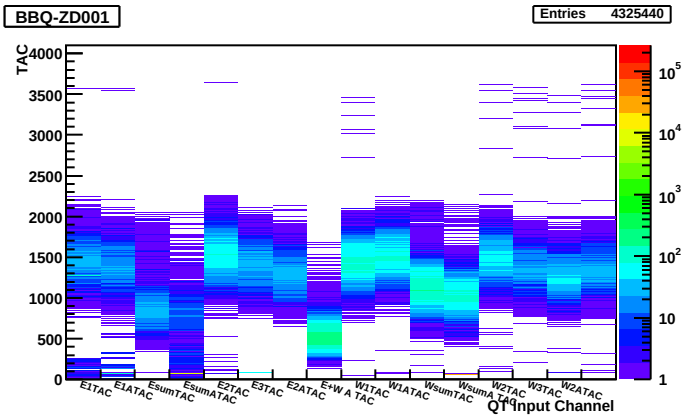

BBQ-BB002 (BBC west small tiles ADC)
Entries 4325440
ADC
QT Input Channel

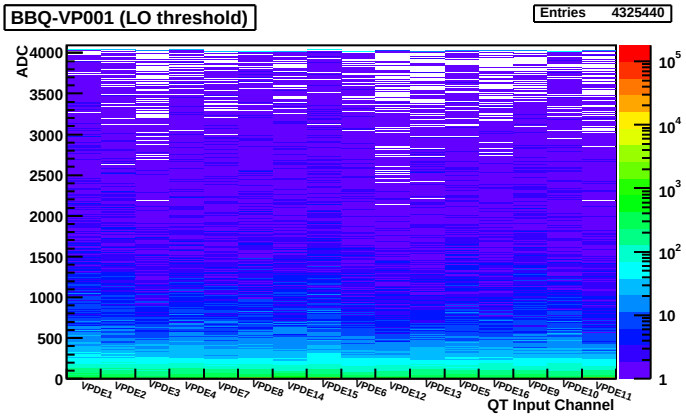

BBQ-BB002 (BBC west small tiles TAC)
Entries 4325440
TAC
QT Input Channel

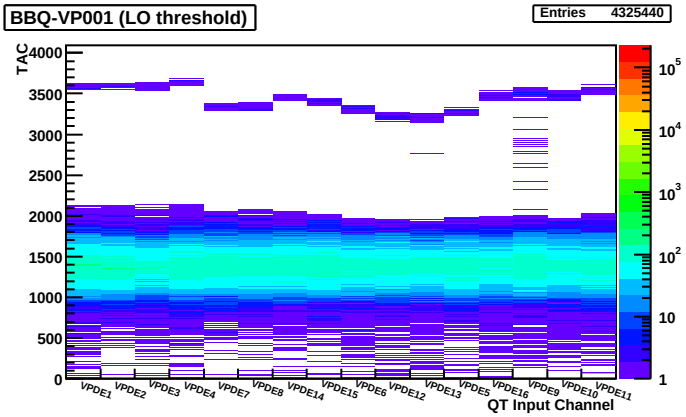

BBQ-BB003 (BBC E+W large tiles ADC)
Entries 4325440
ADC
QT Input Channel

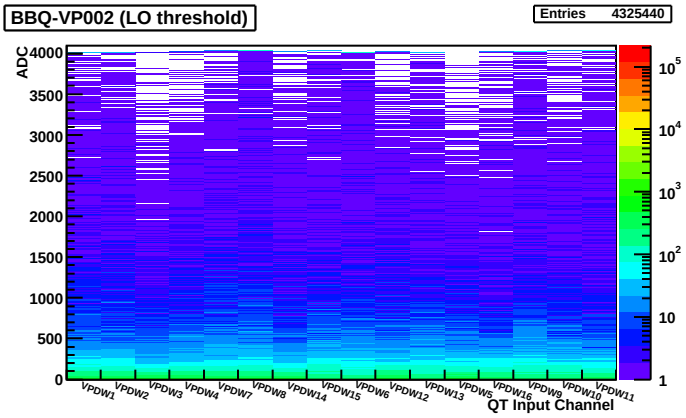

BBQ-BB003 (BBC E+W large tiles TAC)
Entries 4325440
TAC
QT Input Channel

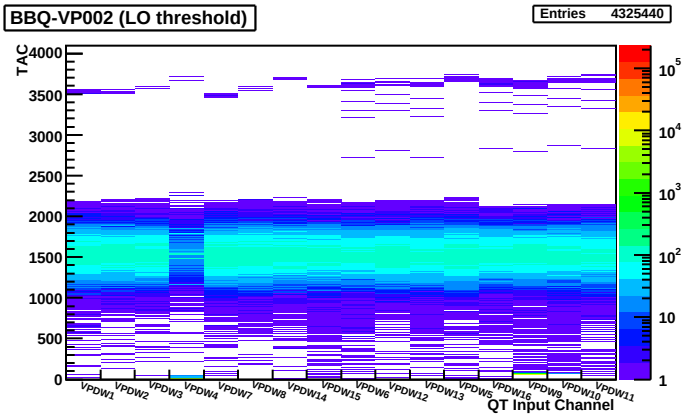

BBQ-ZD001
Entries 4325440
ADC
QT Input Channel

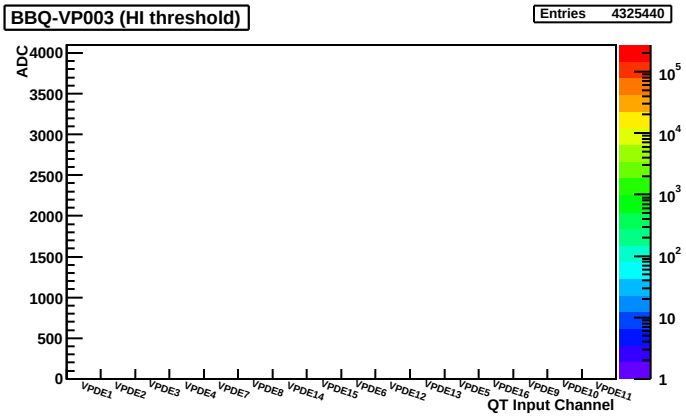

BBQ-ZD001
Entries 4325440
TAC
QT Input Channel

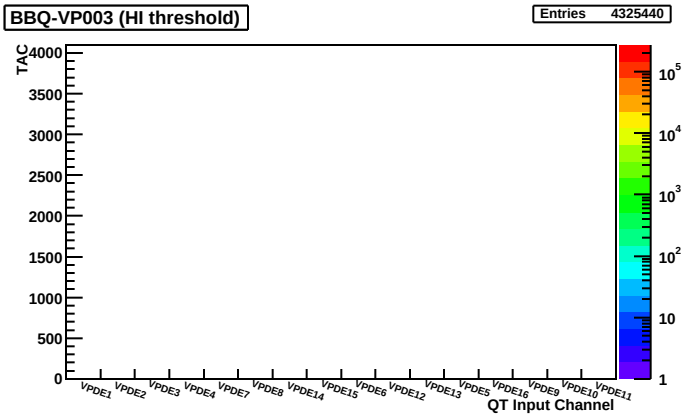

BBQ-VP001 (LO threshold)
Entries 4325440
ADC
QT Input Channel

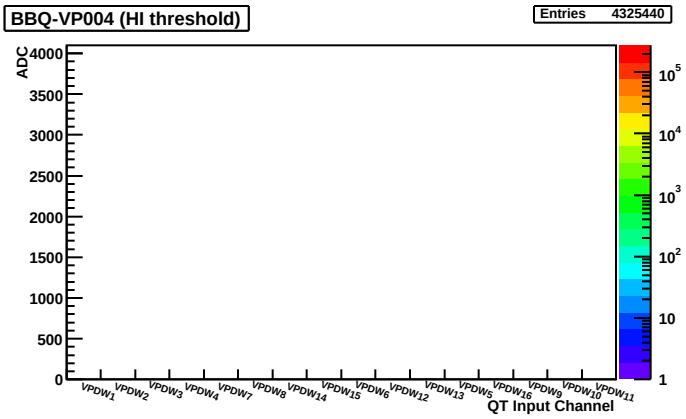

BBQ-VP001 (LO threshold)
Entries 4325440
TAC
QT Input Channel

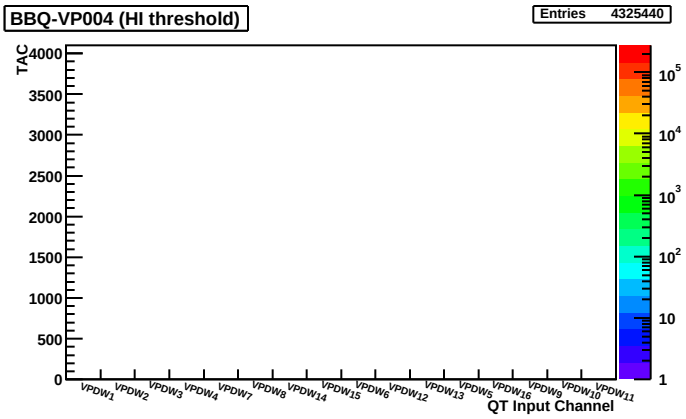

BBQ-VP002 (LO threshold)
Entries 4325440
ADC
QT Input Channel

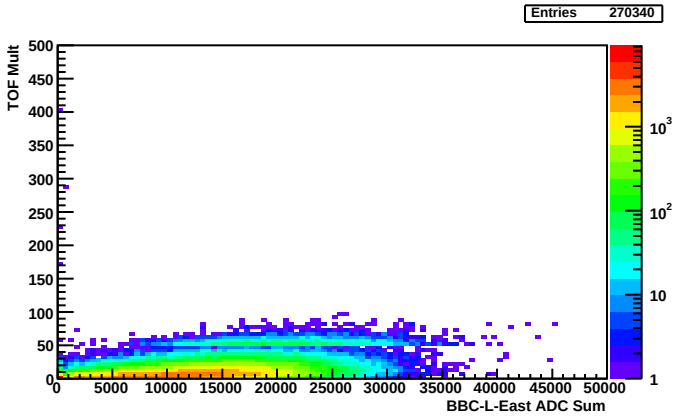

BBQ-VP002 (LO threshold)
Entries 4325440
TAC
QT Input Channel

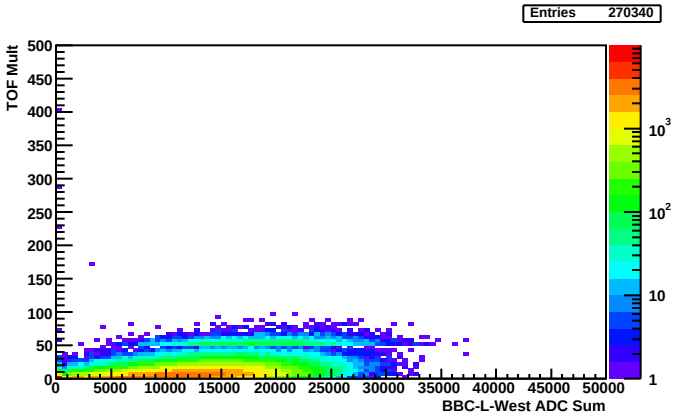

BBQ-VP003 (HI threshold)
Entries 4325440
ADC
QT Input Channel

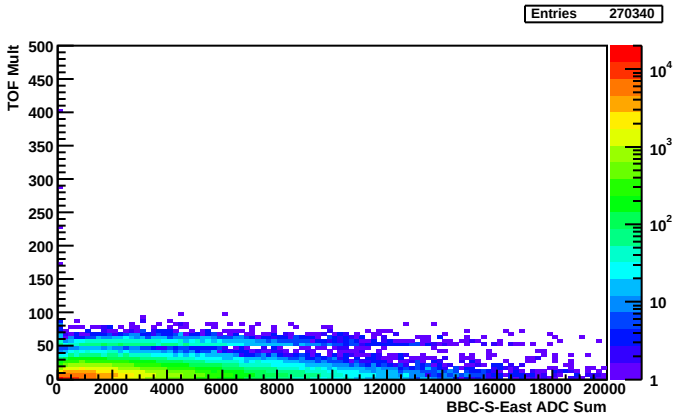

BBQ-VP003 (HI threshold)
Entries 4325440
TAC
QT Input Channel

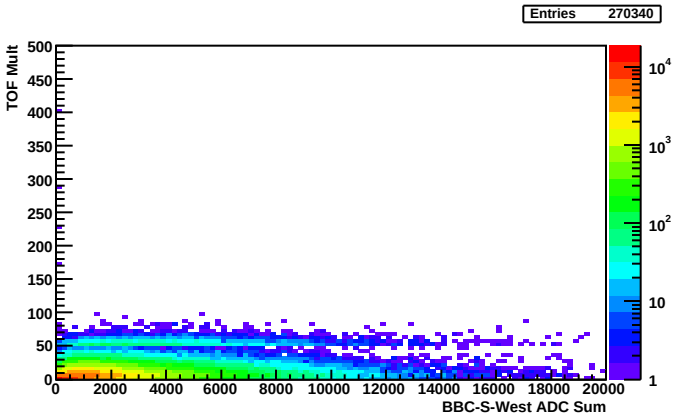

BBQ-VP004 (HI threshold)
Entries 4325440
ADC
QT Input Channel

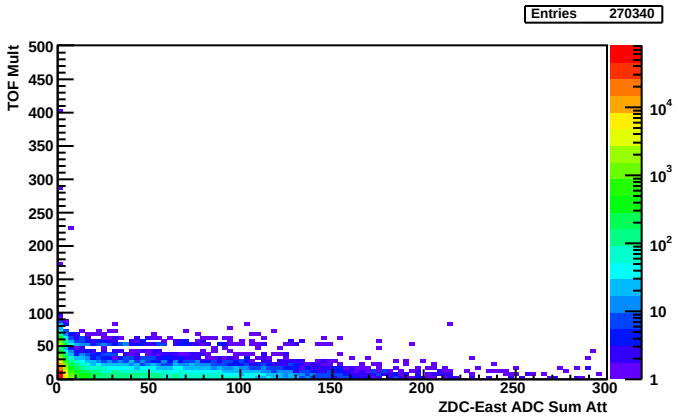

BBQ-VP004 (HI threshold)
Entries 4325440
TAC
QT Input Channel

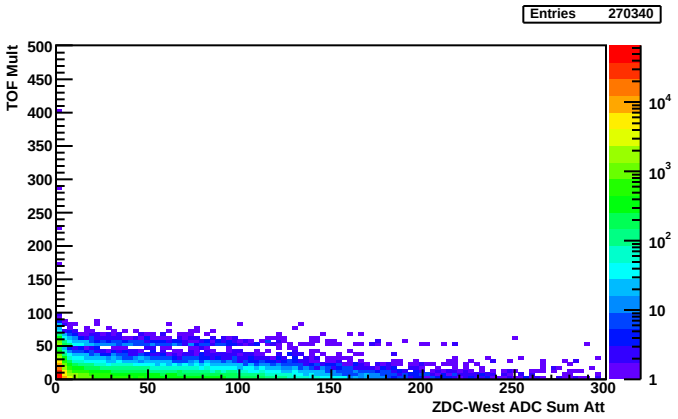

Entries 270340
TOF Mult
BBC-L-East ADC Sum

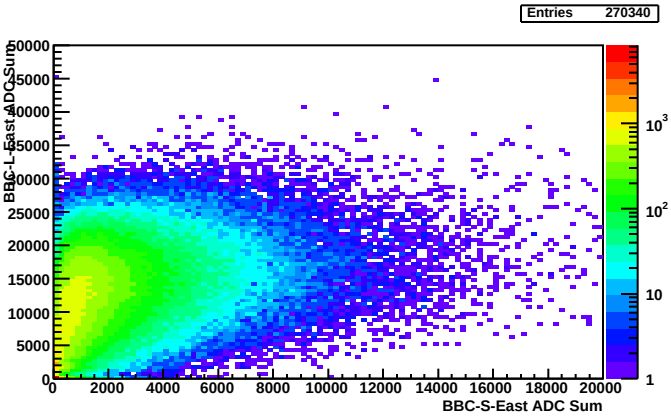

Entries 270340
TOF Mult
BBC-L-West ADC Sum

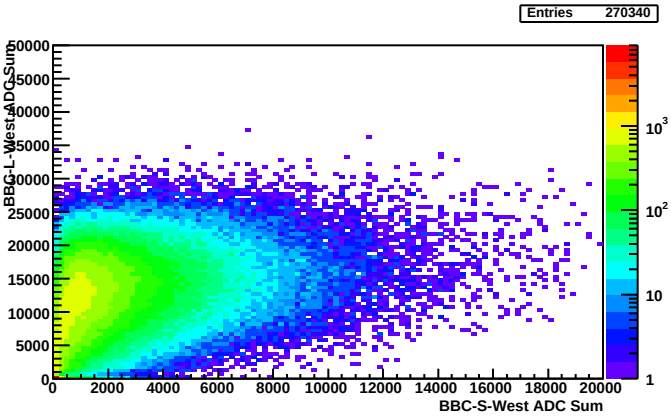

Entries 270340
TOF Mult
BBC-S-East ADC Sum

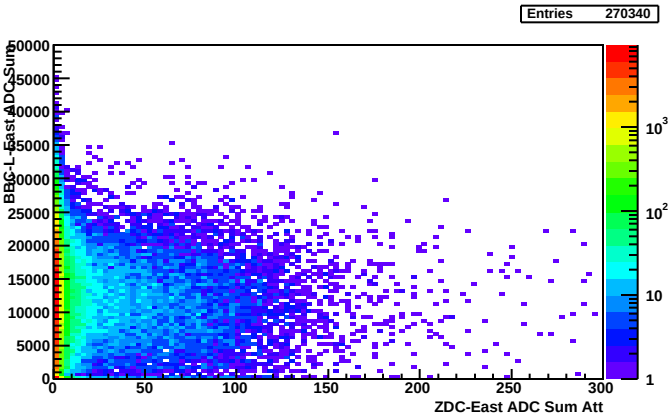

Entries 270340
TOF Mult
BBC-S-West ADC Sum

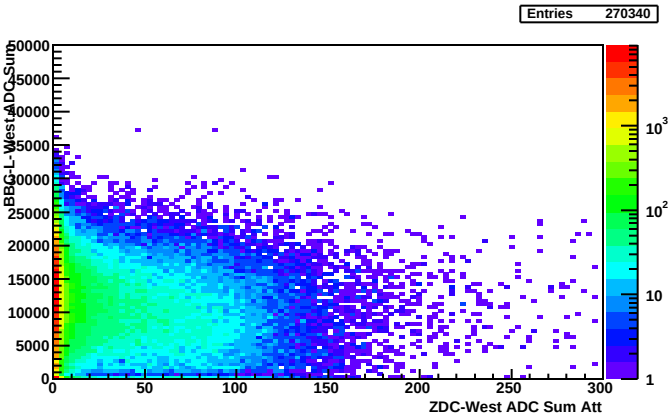

Entries 270340
TOF Mult
ZDC-East ADC Sum Att

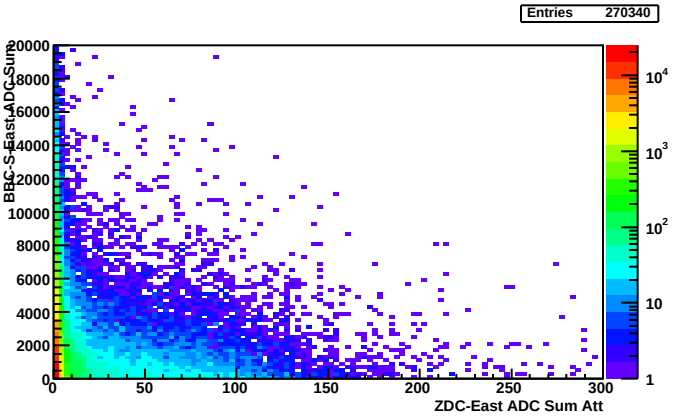

Entries 270340
TOF Mult
ZDC-West ADC Sum Att

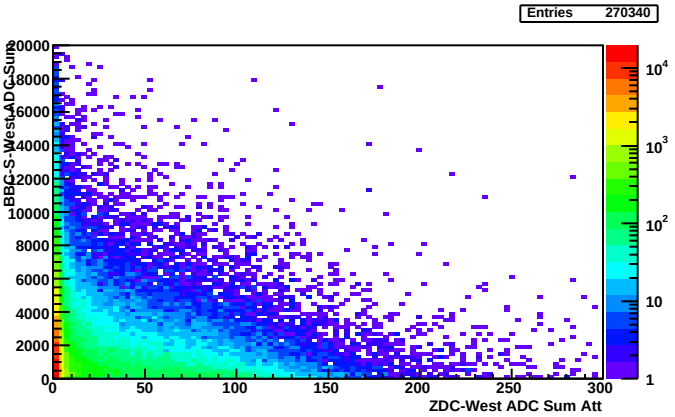

Entries 270340
BBC-L-East ADC Sum
BBC-S-East ADC Sum

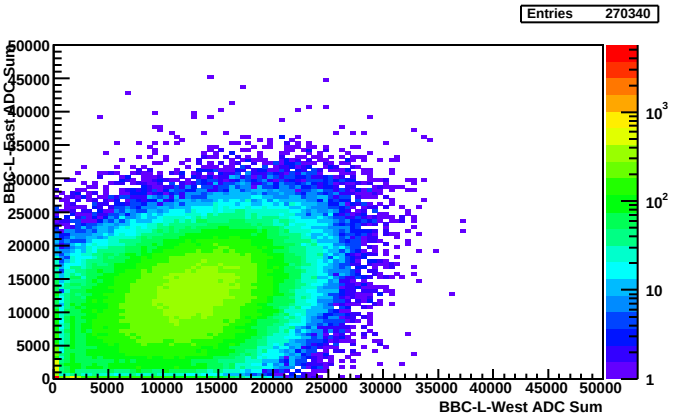

Entries 270340
BBC-L-West ADC Sum
BBC-S-West ADC Sum

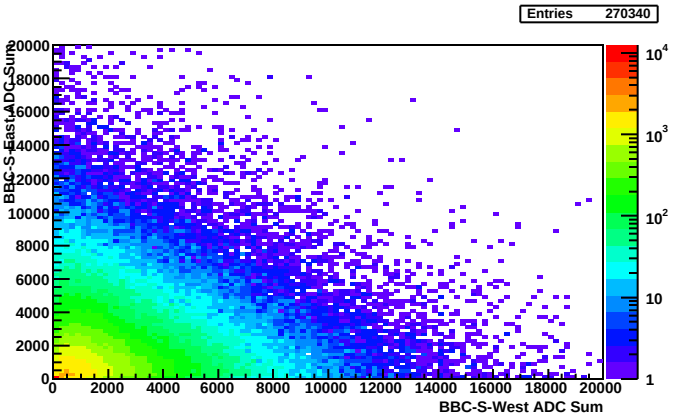

Entries 270340
BBC-L-East ADC Sum
ZDC-East ADC Sum Att

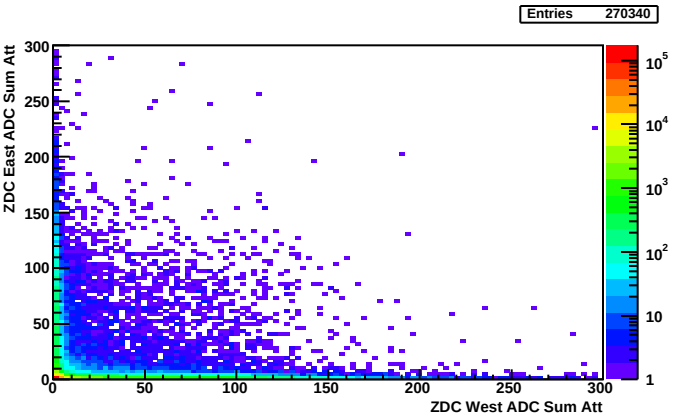

Entries 270340
BBC-L-West ADC Sum
ZDC-West ADC Sum Att

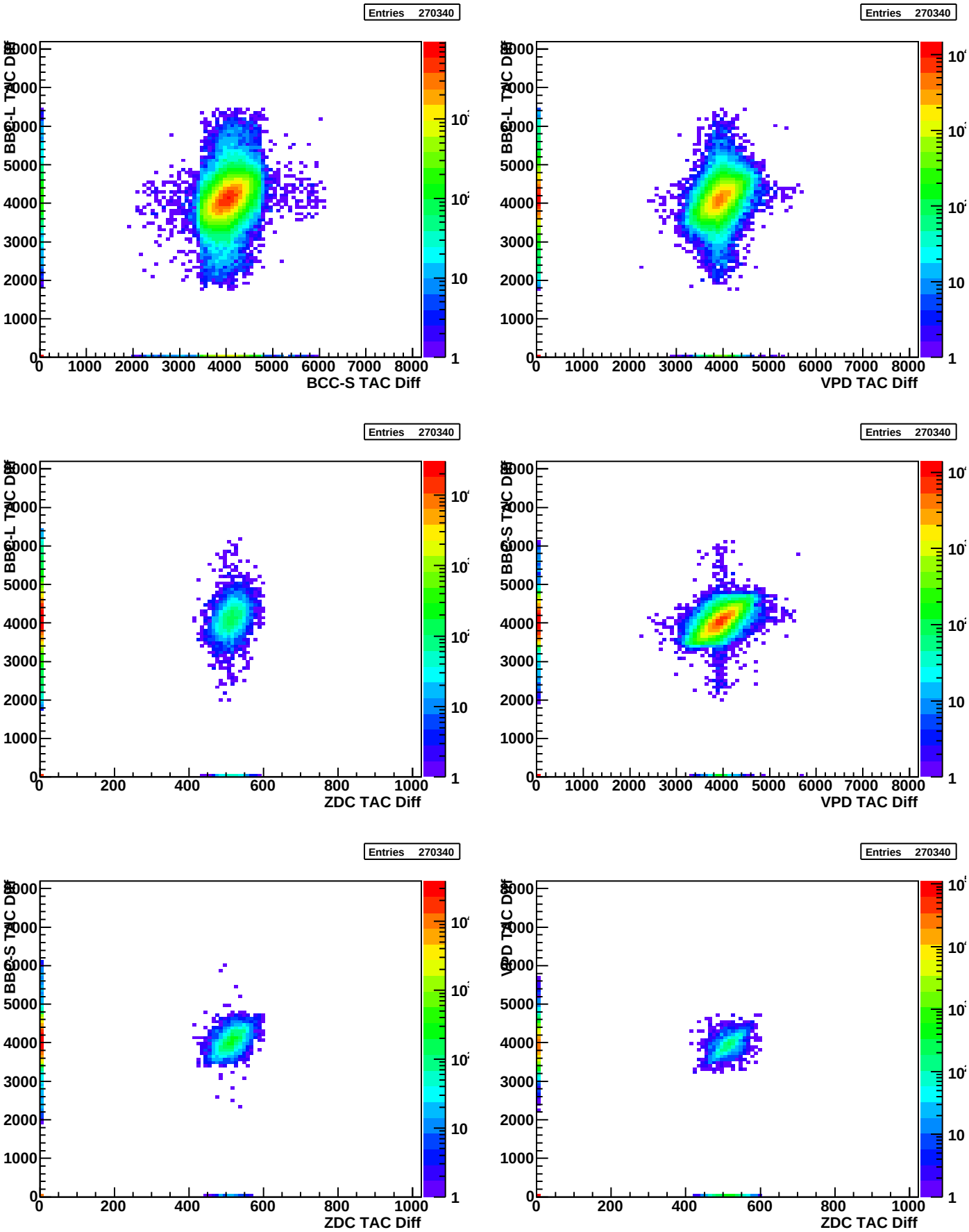

Entries 270340
BBC-S-East ADC Sum
ZDC-East ADC Sum Att

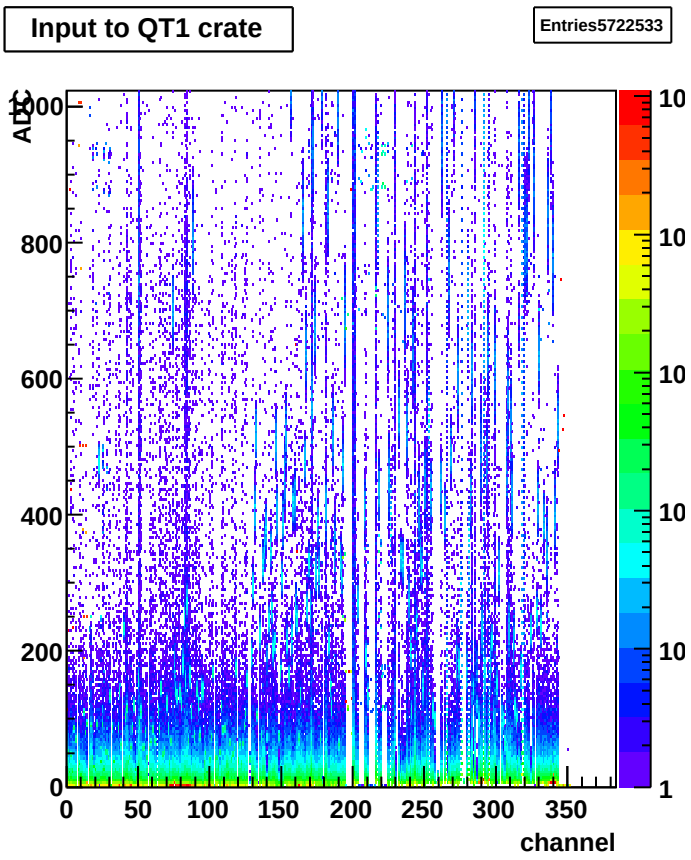

Entries 270340
BBC-S-West ADC Sum
ZDC-West ADC Sum Att

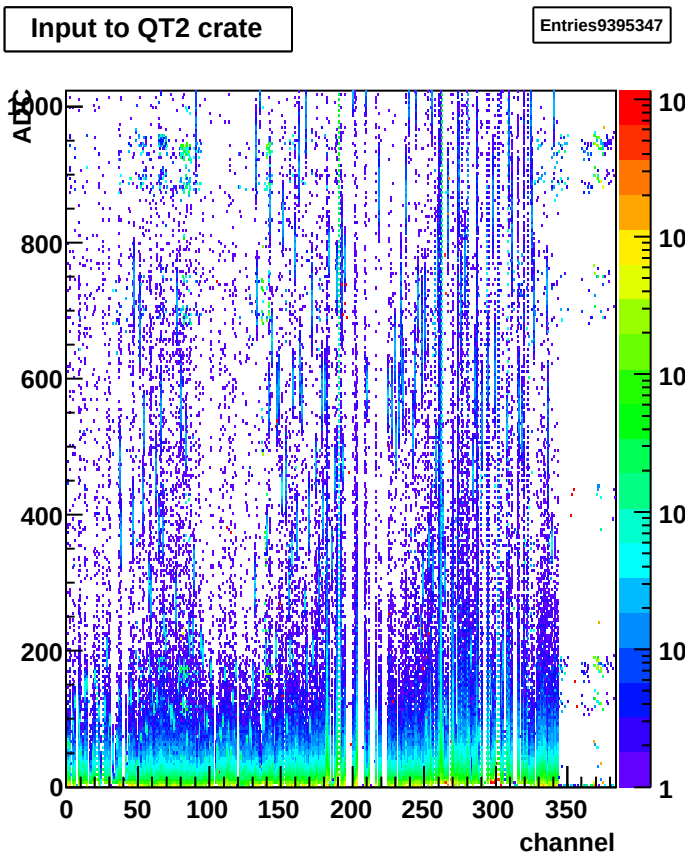

Entries 270340
BBC-L-East ADC Sum
BBC-L-West ADC Sum

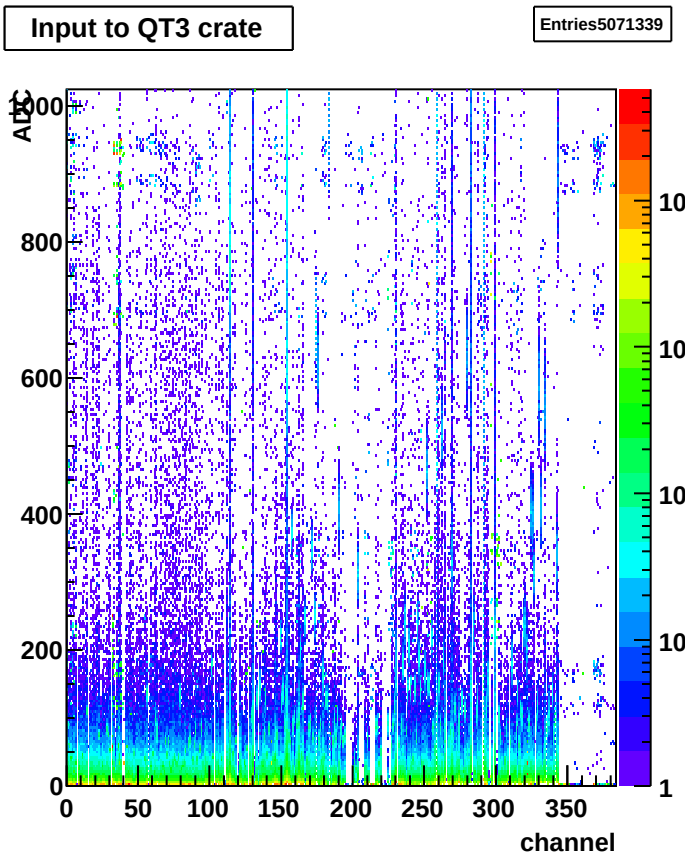

Entries 270340
BBC-S-East ADC Sum
BBC-S-West ADC Sum

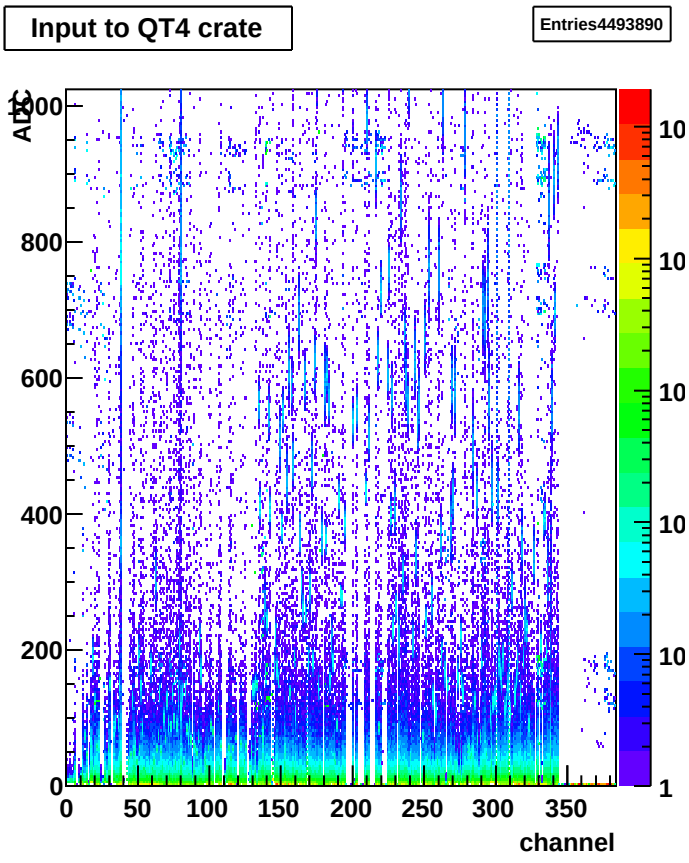

Entries 270340
ZDC East ADC Sum Att
ZDC West ADC Sum Att

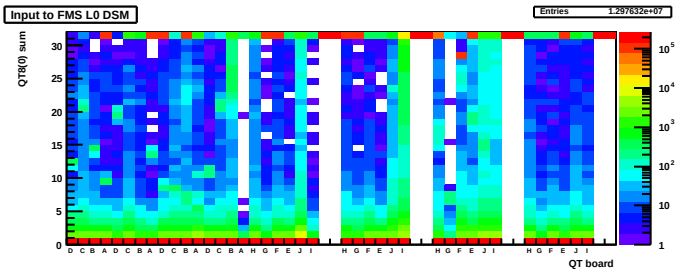

Entries 270340
BBC-L TAC Diff
BCC-S TAC Diff
Entries 270340
BBC-L TAC Diff
VPD TAC Diff
Entries 270340
BBC-L TAC Diff
ZDC TAC Diff
Entries 270340
BBC-S TAC Diff
VPD TAC Diff
Entries 270340
BBC-S TAC Diff
ZDC TAC Diff
Entries 270340
VPD TAC Diff
ZDC TAC Diff

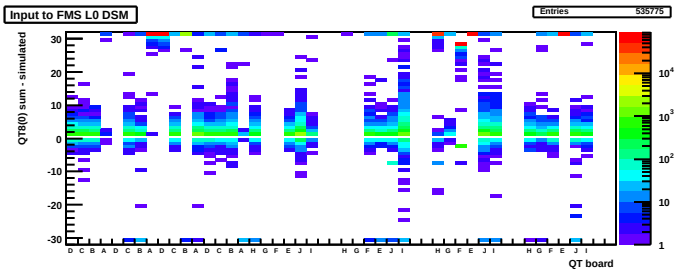

Input to QT1 crate
Entries5722533
ADC
channel

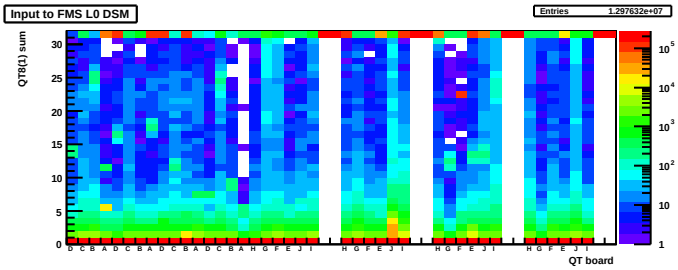

Input to QT2 crate
Entries9395347
ADC
channel

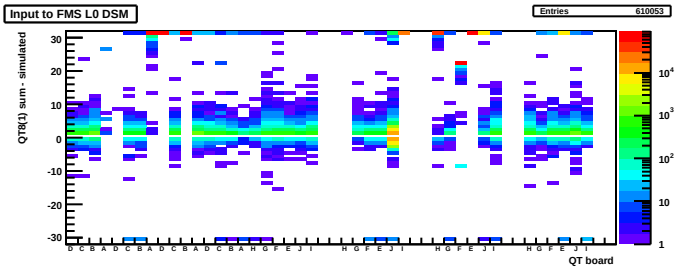

Input to QT3 crate
Entries5071339
ADC
channel

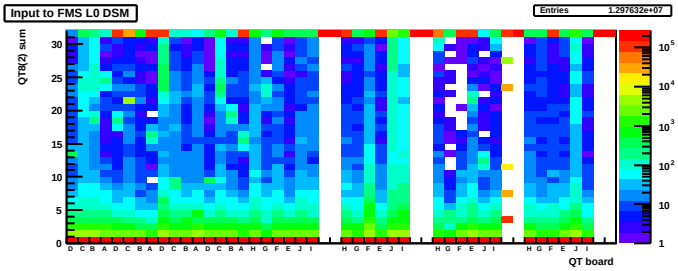

Input to QT4 crate
Entries4493890
ADC
channel

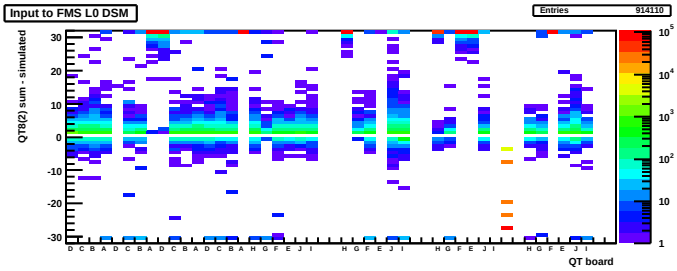

Input to FMS L0 DSM
Entries 1.297632e+07
QT8(0) sum
QT board

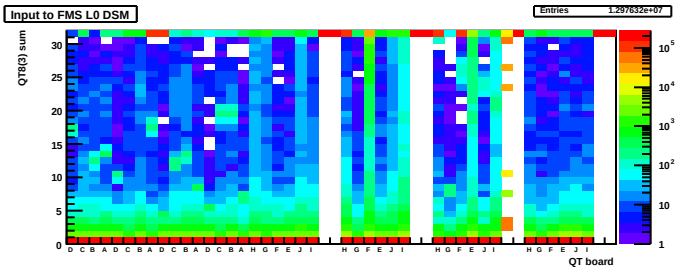

Input to FMS L0 DSM
Entries 535775
QT8(0) sum - simulated
QT board

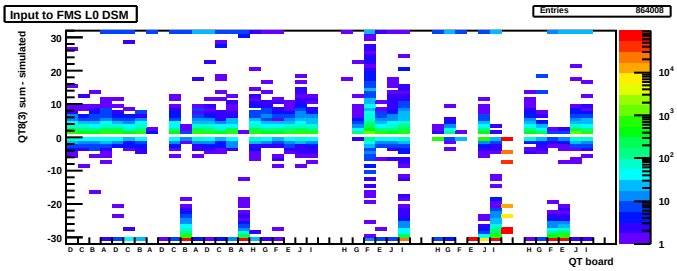

Input to FMS L0 DSM
Entries 1.297632e+07
QT8(1) sum
QT board

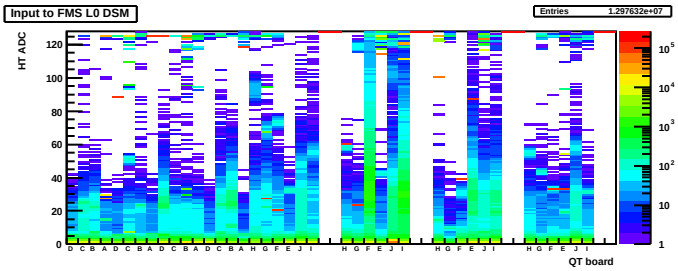

Input to FMS L0 DSM
Entries 610053
QT8(1) sum - simulated
QT board

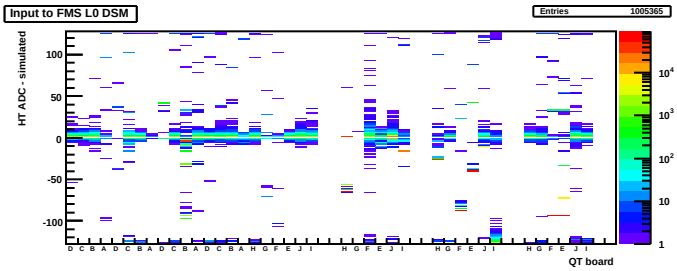

Input to FMS L0 DSM
Entries 1.297632e+07
QT8(2) sum
QT board

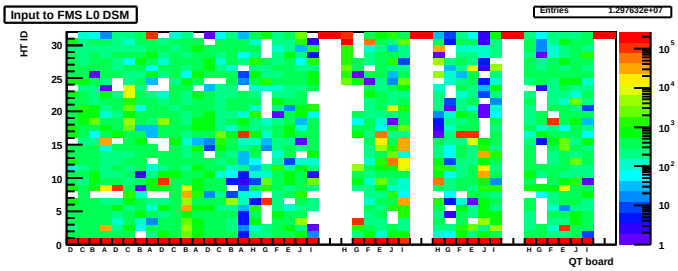

Input to FMS L0 DSM
Entries 914110
QT8(2) sum - simulated
QT board

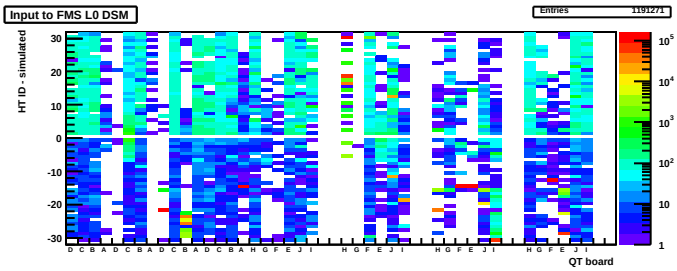

Input to FMS L0 DSM
Entries 1.297632e+07
QT8(3) sum
QT board

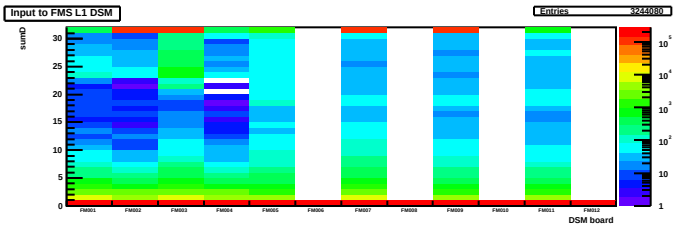

Input to FMS L0 DSM
Entries 864008
QT8(3) sum - simulated
QT board

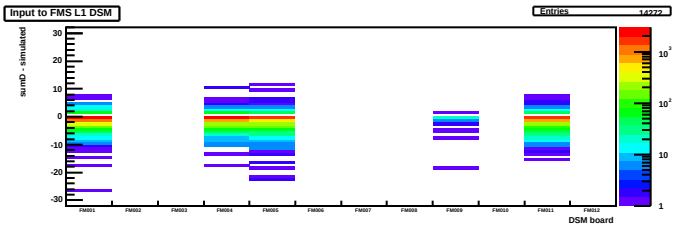

Input to FMS L0 DSM
Entries 1.297632e+07
HT ADC
QT board

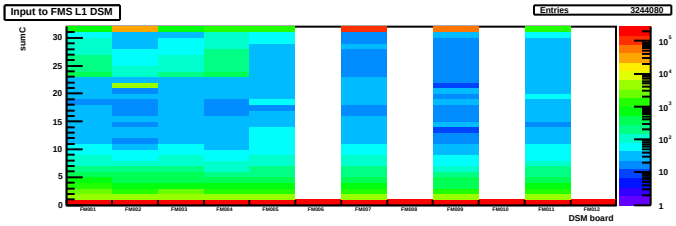

Input to FMS L0 DSM
Entries 1005365
HT ADC - simulated
QT board

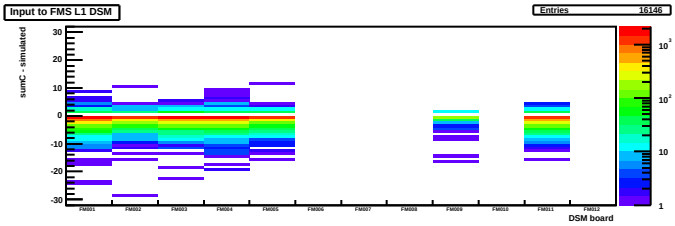

Input to FMS L0 DSM
Entries 1.297632e+07
HT ID
QT board

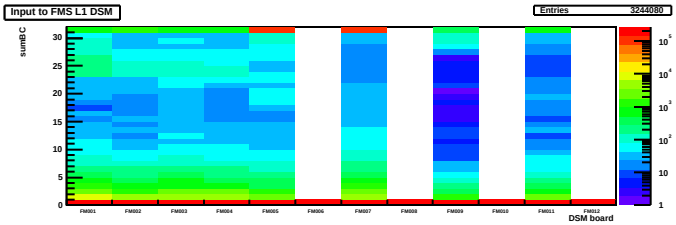

Input to FMS L0 DSM
Entries 1191271
HT ID - simulated
QT board

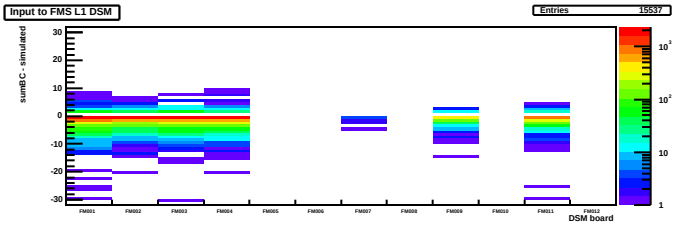

Input to FMS L1 DSM
Entries 3244080
sumD
DSM board

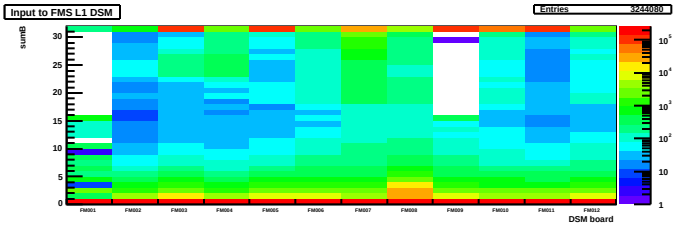

Input to FMS L1 DSM
Entries 14222
sumD - simulated
DSM board

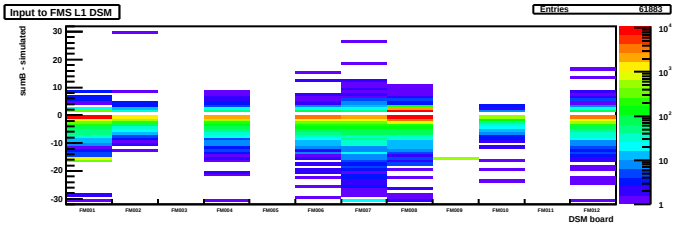

Input to FMS L1 DSM
Entries 3244080
sumC
DSM board

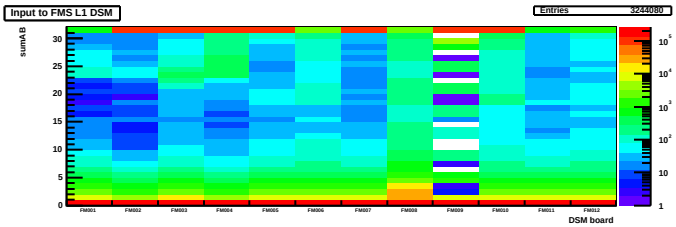

Input to FMS L1 DSM
Entries 16146
sumC - simulated
DSM board

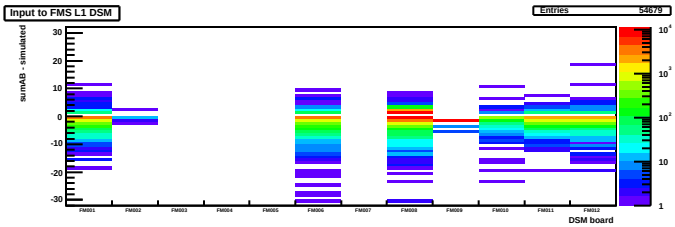

Input to FMS L1 DSM
Entries 3244080
sumBC
DSM board

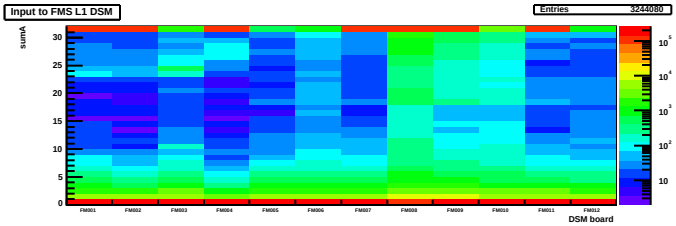

Input to FMS L1 DSM
Entries 15537
sumBC - simulated
DSM board

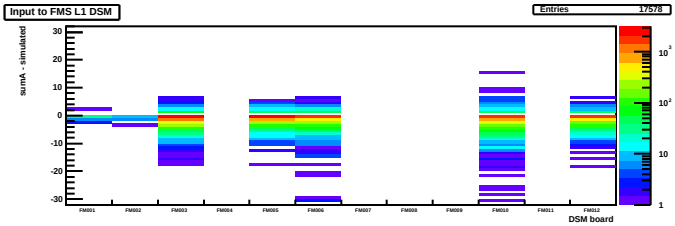

Input to FMS L1 DSM
Entries 3244080
sumB
DSM board

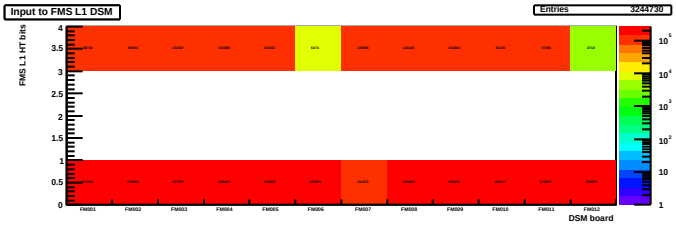

Input to FMS L1 DSM
Entries 61883
sumB - simulated
DSM board

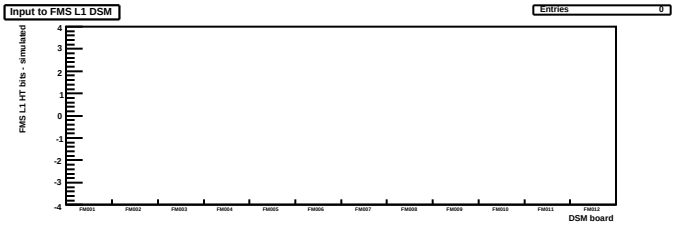

Input to FMS L1 DSM
Entries 3244080
sumAB
DSM board

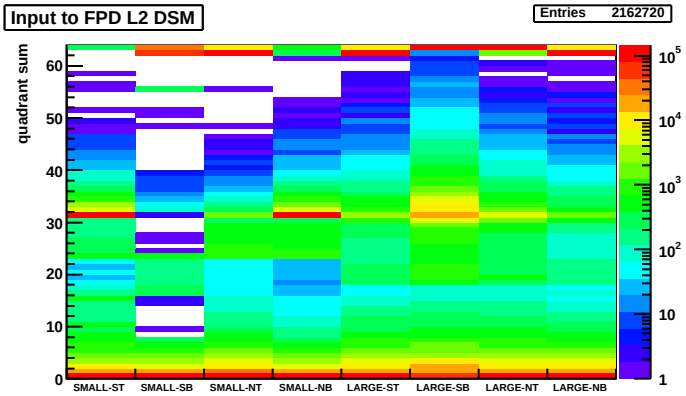

Input to FMS L1 DSM
Entries 54679
sumAB - simulated
DSM board

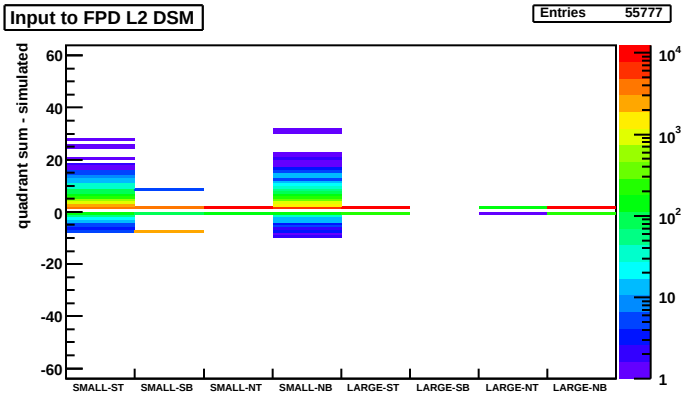

Input to FMS L1 DSM
Entries 3244080
sumA
DSM board

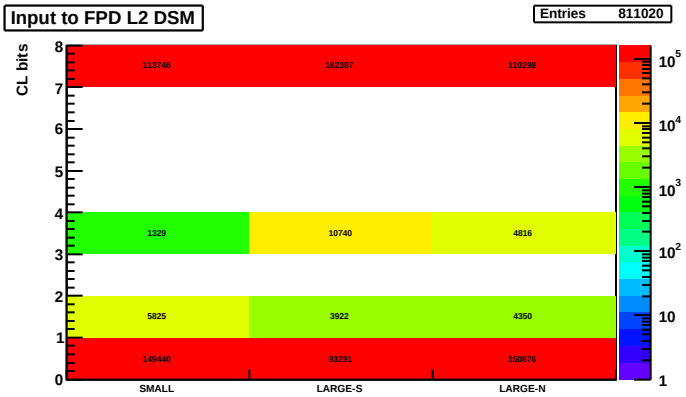

Input to FMS L1 DSM
Entries 17578
sumA - simulated
DSM board

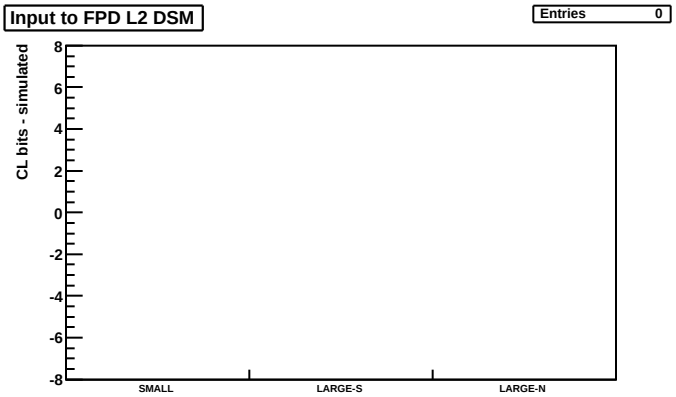

Input to FMS L1 DSM
Entries 3244730
FMS L1 HT bits
DSM board

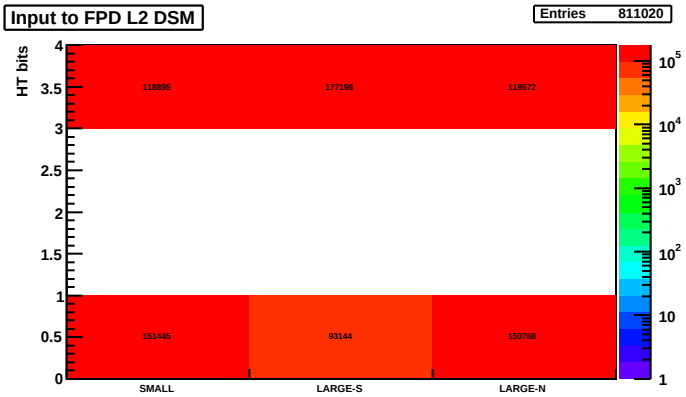

Input to FMS L1 DSM
Entries 0
FMS L1 HT bits - simulated
DSM board

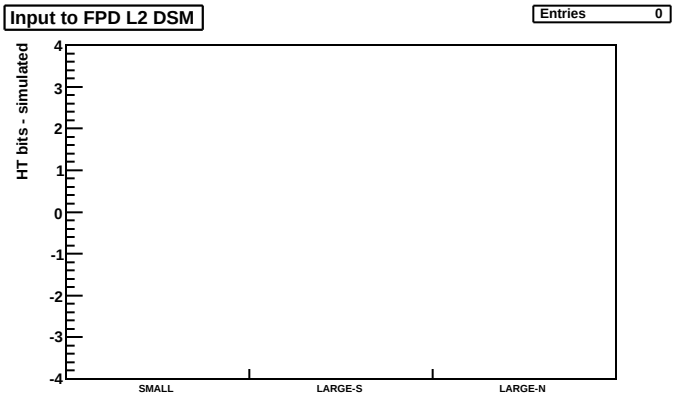

Input to FPD L2 DSM
Entries 2162720
quadrant sum
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

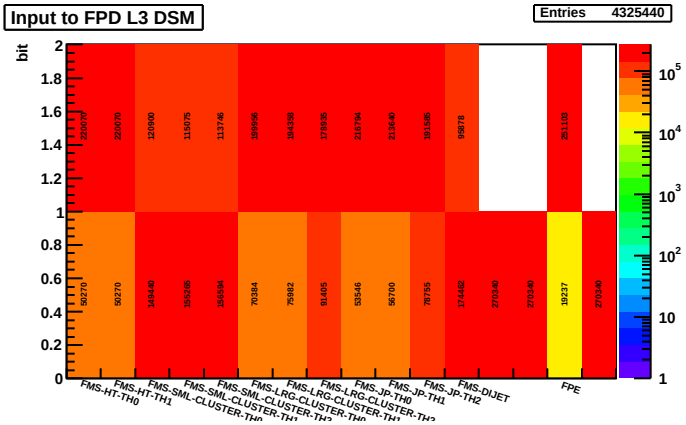

Input to FPD L2 DSM
Entries 55777
quadrant sum - simulated
SMALL-ST
SMALL-SB
SMALL-NT
SMALL-NB
LARGE-ST
LARGE-SB
LARGE-NT
LARGE-NB

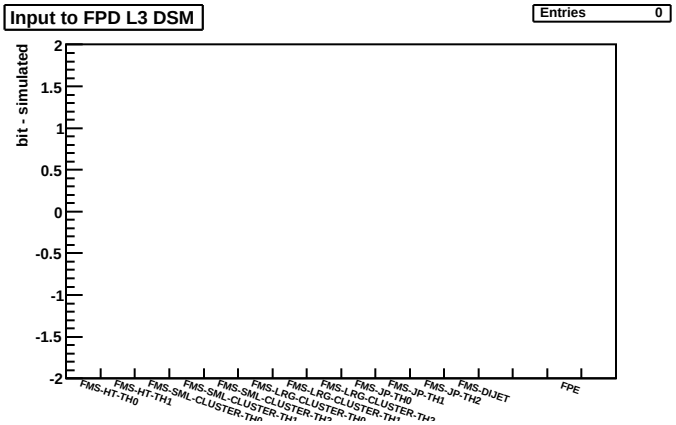

Input to FPD L2 DSM
Entries 811020
CL bits
113746
162387
110298
1329
10740
4816
5825
3922
4350
149440
93291
150876
SMALL
LARGE-S
LARGE-N

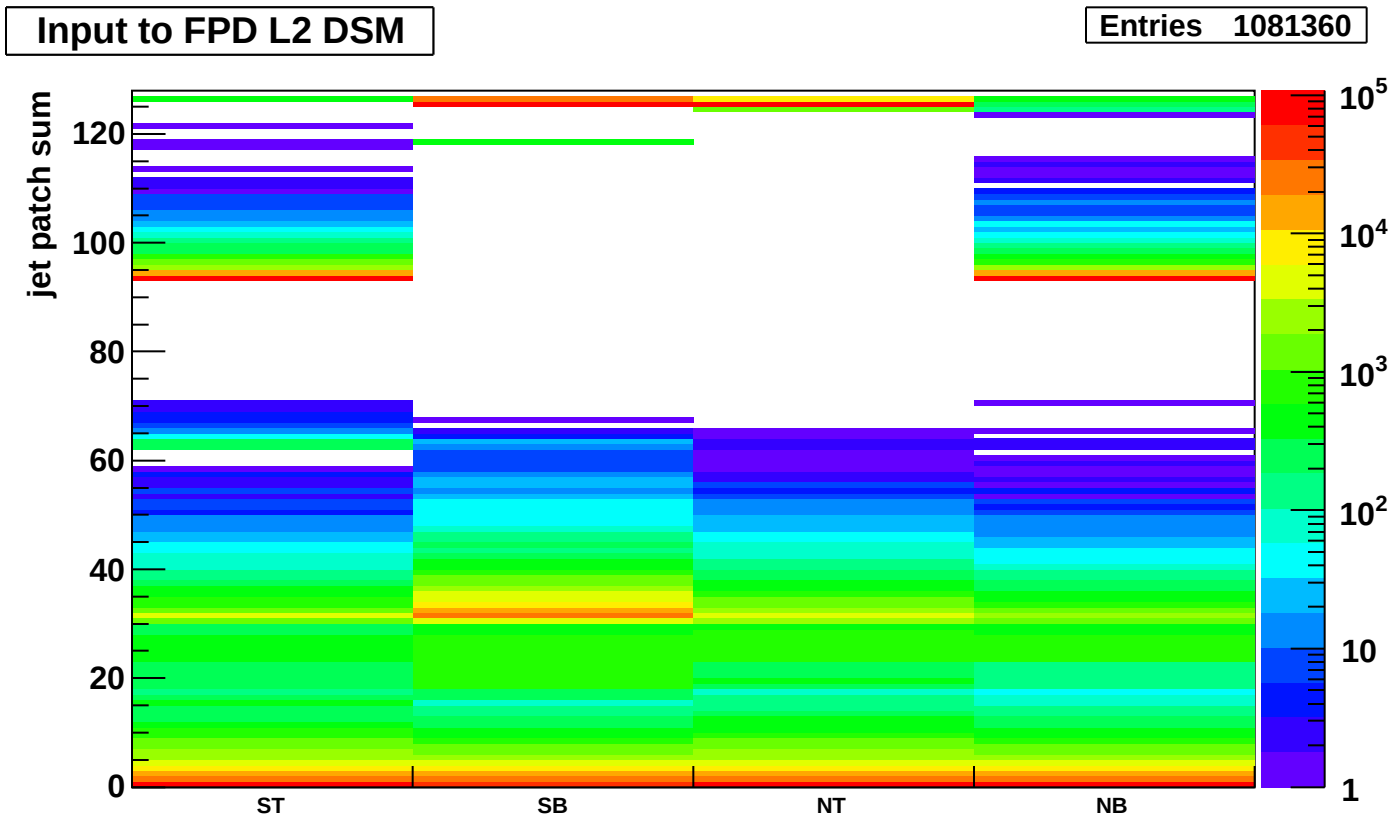

Input to FPD L2 DSM
Entries 0
CL bits - simulated
SMALL
LARGE-S
LARGE-N

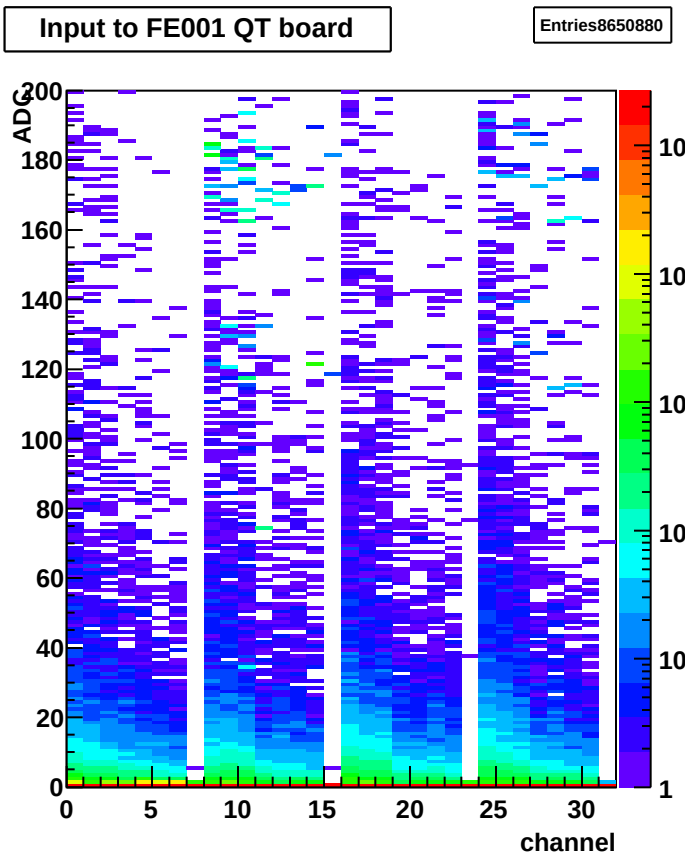

Input to FPD L2 DSM
Entries 811020
HT bits
118895
177196
119572
151445
93144
150768
SMALL
LARGE-S
LARGE-N

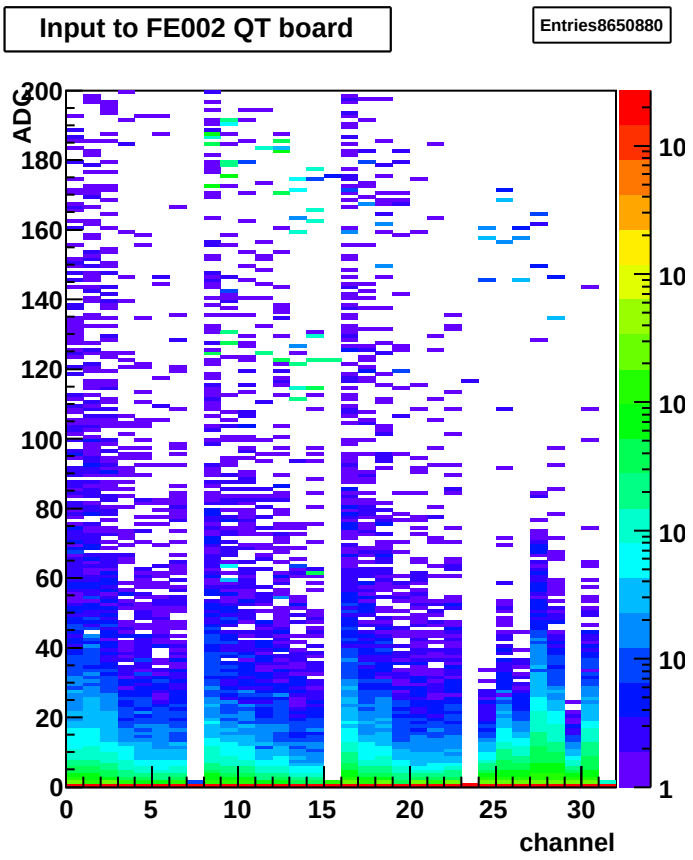

Input to FPD L2 DSM
Entries 0
HT bits - simulated
SMALL
LARGE-S
LARGE-N

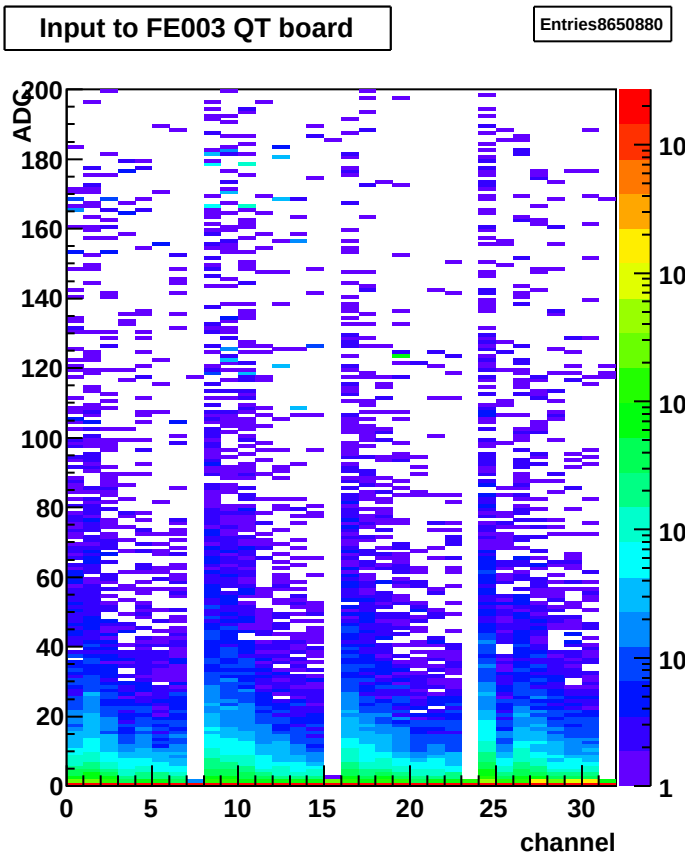

Input to FPD L3 DSM
Entries 4325440
bit
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

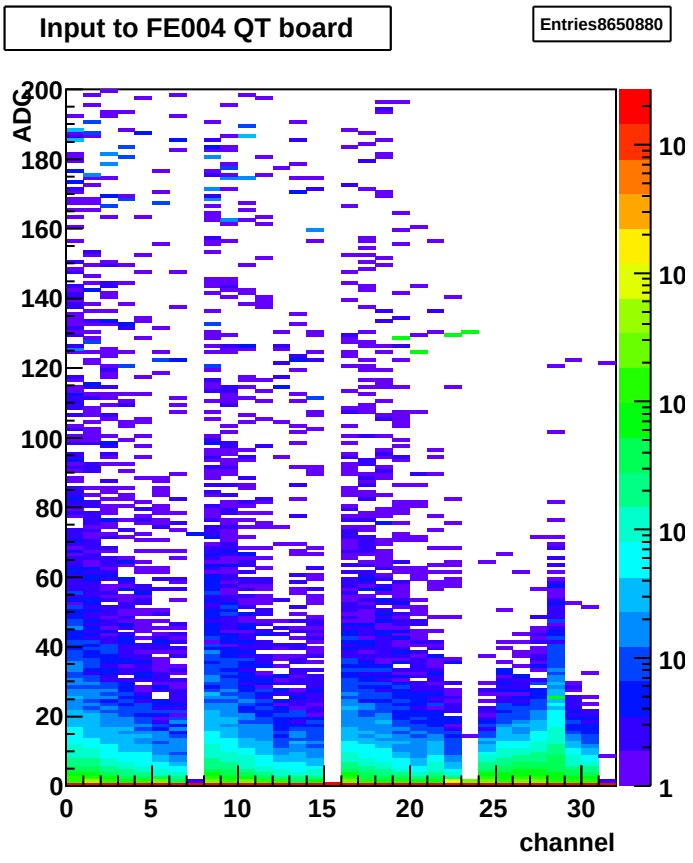

Input to FPD L3 DSM
Entries 0
bit - simulated
FMS-HT-TH0
FMS-HT-TH1
FMS-SML-CLUSTER-TH0
FMS-SML-CLUSTER-TH1
FMS-SML-CLUSTER-TH2
FMS-LRG-CLUSTER-TH0
FMS-LRG-CLUSTER-TH1
FMS-LRG-CLUSTER-TH2
FMS-JP-TH0
FMS-JP-TH1
FMS-JP-TH2
FMS-DIJET
FPE

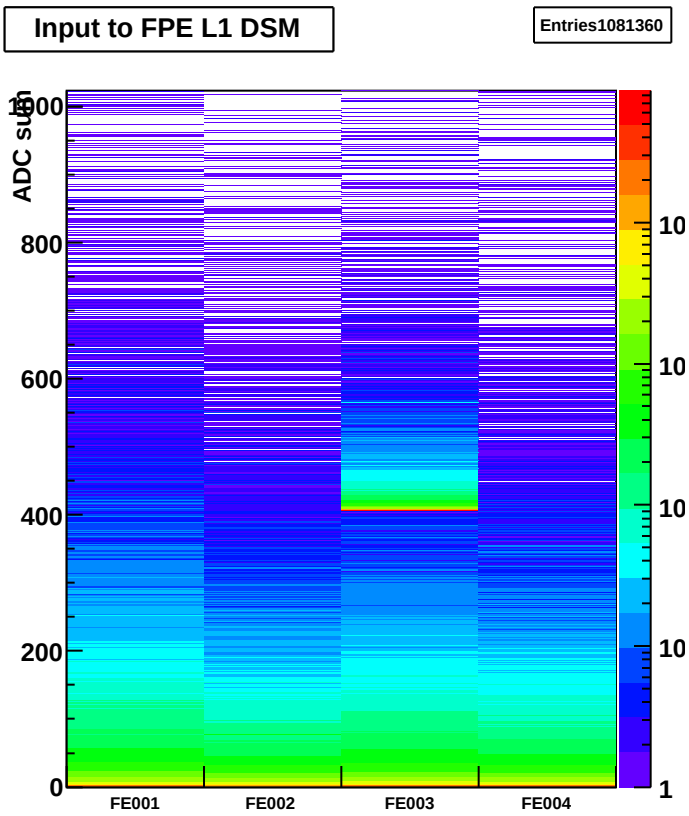

Input to FPD L2 DSM
Entries 1081360
jet patch sum
0
20
40
60
80
100
120
ST
SB
NT
NB
1
10
10^2
10^3
10^4
10^5

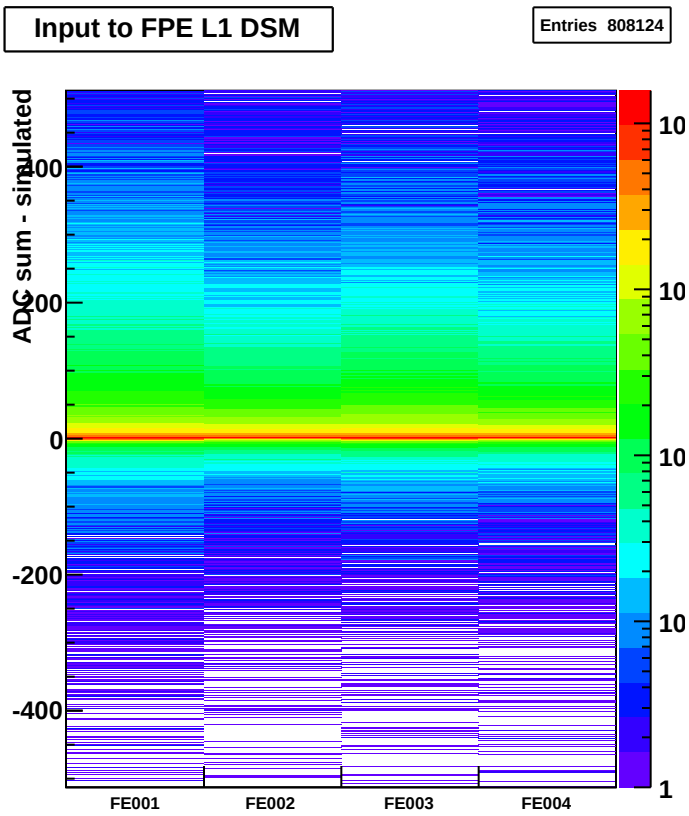

Input to FE001 QT board
Entries8650880
ADC
channel

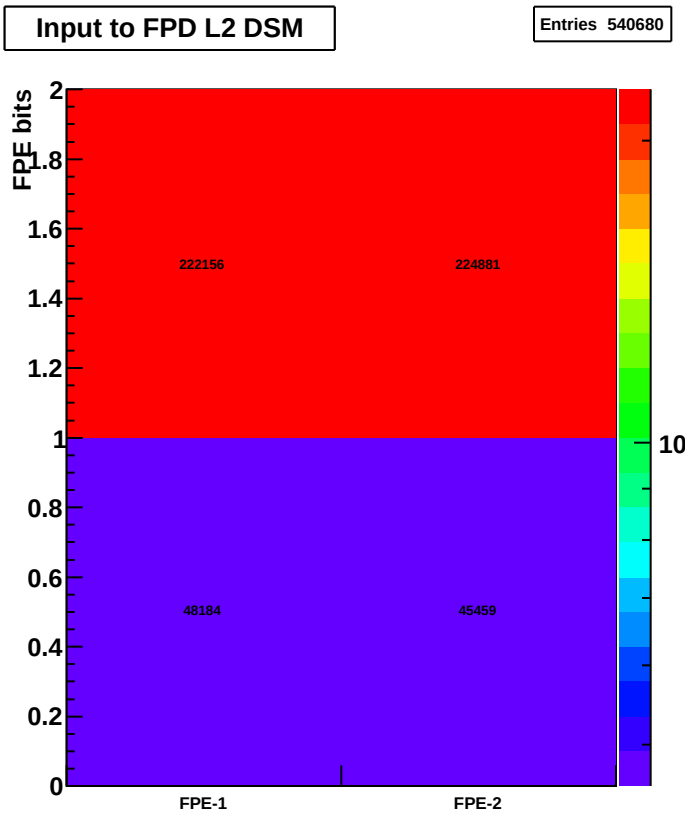

Input to FE002 QT board
Entries8650880
ADC
channel

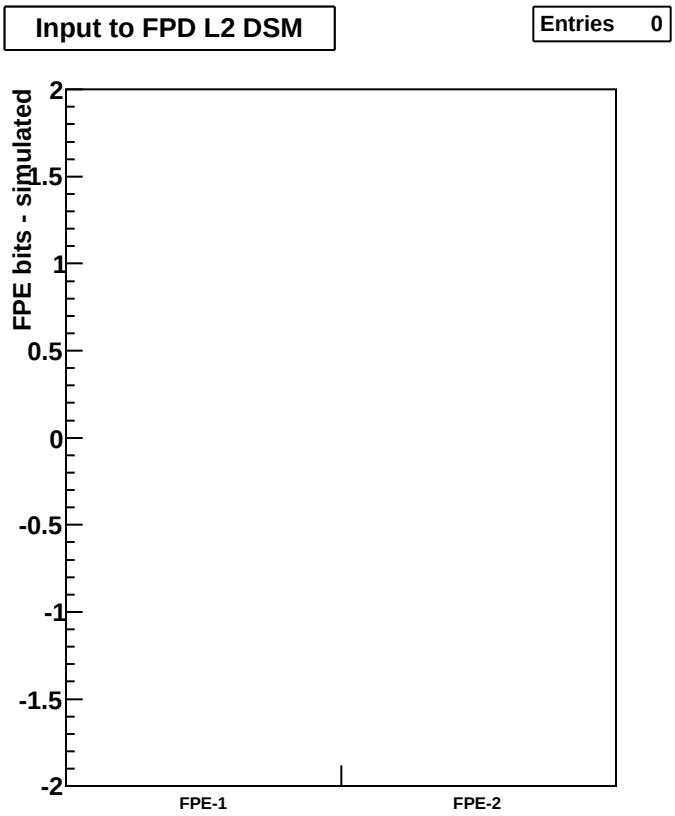

Input to FE003 QT board
Entries8650880
ADC
channel


Input to FE004 QT board
Entries8650880
ADC
channel


Input to FPE L1 DSM
Entries1081360
ADC sum
1000
800
600
400
200
0
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPE L1 DSM
Entries 808124
ADC sum - simulated
400
200
0
-200
-400
FE001
FE002
FE003
FE004
10
10
10
10
1


Input to FPD L2 DSM
Entries 540680
FPE bits
2
1.8
1.6
1.4
1.2
1
0.8
0.6
0.4
0.2
0
222156
224881
48184
45459
FPE-1
FPE-2
10


Input to FPD L2 DSM
Entries 0
FPE bits - simulated
2
1.5
1
0.5
0
-0.5
-1
-1.5
-2
FPE-1
FPE-2